

Effective (Isotropic) Radiated Power Output Data for ENDC

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Gain (dBi)	E(I)RP (dBm)	Limit (dBm)	Verdict
DC_2A-n7A	15	5+5	DFT-BPSK	M+L	Inner_1RB_Left	22.38	2.2	24.58	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+L	Inner_1RB_Right	22.48	2.2	24.68	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+L	Inner_Full	22.46	2.2	24.66	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+L	Outer_Full	22.59	2.2	24.79	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+L	Inner_1RB_Left	22.42	2.2	24.62	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+L	Inner_1RB_Right	22.48	2.2	24.68	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+L	Inner_Full	22.41	2.2	24.61	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+L	Outer_Full	22.48	2.2	24.68	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+L	Inner_1RB_Left	21.81	2.2	24.01	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+L	Inner_1RB_Right	21.93	2.2	24.13	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+L	Inner_Full	21.96	2.2	24.16	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+L	Outer_Full	19.46	2.2	21.66	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+L	Inner_1RB_Left	20.67	2.2	22.87	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+L	Inner_1RB_Right	20.74	2.2	22.94	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+L	Inner_Full	20.58	2.2	22.78	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+L	Outer_Full	19.02	2.2	21.22	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+L	Inner_1RB_Left	17.93	2.2	20.13	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+L	Inner_1RB_Right	18.02	2.2	20.22	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+L	Inner_Full	18.51	2.2	20.71	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+L	Outer_Full	18.45	2.2	20.65	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+M	Inner_1RB_Left	22.73	2.2	24.93	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+M	Inner_1RB_Right	22.76	2.2	24.96	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+M	Inner_Full	22.83	2.2	25.03	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+M	Outer_Full	22.85	2.2	25.05	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+M	Inner_1RB_Left	22.69	2.2	24.89	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+M	Inner_1RB_Right	22.72	2.2	24.92	33	PASS

DC_2A-n7A	15	5+5	DFT-QPSK	M+M	Inner_Full	22.73	2.2	24.93	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+M	Outer_Full	22.77	2.2	24.97	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+M	Inner_1RB_Left	22.1	2.2	24.3	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+M	Inner_1RB_Right	22.22	2.2	24.42	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+M	Inner_Full	22.18	2.2	24.38	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+M	Outer_Full	19.78	2.2	21.98	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+M	Inner_1RB_Left	20.81	2.2	23.01	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+M	Inner_1RB_Right	20.91	2.2	23.11	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+M	Inner_Full	20.8	2.2	23	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+M	Outer_Full	19.25	2.2	21.45	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+M	Inner_1RB_Left	18.24	2.2	20.44	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+M	Inner_1RB_Right	18.31	2.2	20.51	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+M	Inner_Full	18.76	2.2	20.96	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+M	Outer_Full	18.67	2.2	20.87	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+H	Inner_1RB_Left	22.72	2.2	24.92	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+H	Inner_1RB_Right	22.79	2.2	24.99	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+H	Inner_Full	22.76	2.2	24.96	33	PASS
DC_2A-n7A	15	5+5	DFT-BPSK	M+H	Outer_Full	22.78	2.2	24.98	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+H	Inner_1RB_Left	22.72	2.2	24.92	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+H	Inner_1RB_Right	22.76	2.2	24.96	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+H	Inner_Full	22.7	2.2	24.9	33	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+H	Outer_Full	22.77	2.2	24.97	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+H	Inner_1RB_Left	22.15	2.2	24.35	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+H	Inner_1RB_Right	22.24	2.2	24.44	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+H	Inner_Full	22.27	2.2	24.47	33	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+H	Outer_Full	19.85	2.2	22.05	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+H	Inner_1RB_Left	20.98	2.2	23.18	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+H	Inner_1RB_Right	20.95	2.2	23.15	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+H	Inner_Full	20.78	2.2	22.98	33	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+H	Outer_Full	19.3	2.2	21.5	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+H	Inner_1RB_Left	18.15	2.2	20.35	33	PASS

DC_2A-n7A	15	5+5	DFT-256QAM	M+H	Inner_1RB_Right	18.35	2.2	20.55	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+H	Inner_Full	18.8	2.2	21	33	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+H	Outer_Full	18.79	2.2	20.99	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+L	Inner_1RB_Left	22.39	2.2	24.59	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+L	Inner_1RB_Right	22.58	2.2	24.78	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+L	Inner_Full	22.5	2.2	24.7	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+L	Outer_Full	22.43	2.2	24.63	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+L	Inner_1RB_Left	22.37	2.2	24.57	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+L	Inner_1RB_Right	22.57	2.2	24.77	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+L	Inner_Full	22.43	2.2	24.63	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+L	Outer_Full	22.45	2.2	24.65	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+L	Inner_1RB_Left	21.8	2.2	24	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+L	Inner_1RB_Right	21.95	2.2	24.15	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+L	Inner_Full	22.05	2.2	24.25	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+L	Outer_Full	19.53	2.2	21.73	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+L	Inner_1RB_Left	20.59	2.2	22.79	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+L	Inner_1RB_Right	20.69	2.2	22.89	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+L	Inner_Full	20.45	2.2	22.65	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+L	Outer_Full	19.08	2.2	21.28	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+L	Inner_1RB_Left	17.93	2.2	20.13	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+L	Inner_1RB_Right	18.03	2.2	20.23	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+L	Inner_Full	18.39	2.2	20.59	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+L	Outer_Full	18.43	2.2	20.63	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+M	Inner_1RB_Left	22.66	2.2	24.86	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+M	Inner_1RB_Right	22.7	2.2	24.9	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+M	Inner_Full	22.69	2.2	24.89	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+M	Outer_Full	22.74	2.2	24.94	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+M	Inner_1RB_Left	22.63	2.2	24.83	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+M	Inner_1RB_Right	22.78	2.2	24.98	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+M	Inner_Full	22.75	2.2	24.95	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+M	Outer_Full	22.77	2.2	24.97	33	PASS

DC_2A-n7A	15	5+10	DFT-16QAM	M+M	Inner_1RB_Left	22.09	2.2	24.29	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+M	Inner_1RB_Right	22.13	2.2	24.33	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+M	Inner_Full	22.3	2.2	24.5	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+M	Outer_Full	19.75	2.2	21.95	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+M	Inner_1RB_Left	20.81	2.2	23.01	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+M	Inner_1RB_Right	20.95	2.2	23.15	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+M	Inner_Full	20.77	2.2	22.97	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+M	Outer_Full	19.29	2.2	21.49	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+M	Inner_1RB_Left	18.2	2.2	20.4	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+M	Inner_1RB_Right	18.24	2.2	20.44	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+M	Inner_Full	18.69	2.2	20.89	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+M	Outer_Full	18.72	2.2	20.92	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+H	Inner_1RB_Left	22.73	2.2	24.93	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+H	Inner_1RB_Right	22.74	2.2	24.94	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+H	Inner_Full	22.79	2.2	24.99	33	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+H	Outer_Full	22.81	2.2	25.01	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+H	Inner_1RB_Left	22.78	2.2	24.98	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+H	Inner_1RB_Right	22.79	2.2	24.99	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+H	Inner_Full	22.77	2.2	24.97	33	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+H	Outer_Full	22.79	2.2	24.99	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+H	Inner_1RB_Left	22.16	2.2	24.36	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+H	Inner_1RB_Right	22.16	2.2	24.36	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+H	Inner_Full	22.26	2.2	24.46	33	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+H	Outer_Full	19.84	2.2	22.04	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+H	Inner_1RB_Left	20.91	2.2	23.11	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+H	Inner_1RB_Right	20.95	2.2	23.15	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+H	Inner_Full	20.85	2.2	23.05	33	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+H	Outer_Full	19.39	2.2	21.59	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+H	Inner_1RB_Left	18.19	2.2	20.39	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+H	Inner_1RB_Right	18.23	2.2	20.43	33	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+H	Inner_Full	18.79	2.2	20.99	33	PASS

DC_2A-n7A	15	5+10	DFT-256QAM	M+H	Outer_Full	18.78	2.2	20.98	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+L	Inner_1RB_Left	22.65	2.2	24.85	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+L	Inner_1RB_Right	22.81	2.2	25.01	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+L	Inner_Full	22.8	2.2	25	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+L	Outer_Full	22.75	2.2	24.95	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+L	Inner_1RB_Left	22.7	2.2	24.9	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+L	Inner_1RB_Right	22.79	2.2	24.99	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+L	Inner_Full	22.82	2.2	25.02	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+L	Outer_Full	22.87	2.2	25.07	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+L	Inner_1RB_Left	21.96	2.2	24.16	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+L	Inner_1RB_Right	22.16	2.2	24.36	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+L	Inner_Full	22.34	2.2	24.54	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+L	Outer_Full	19.8	2.2	22	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+L	Inner_1RB_Left	20.84	2.2	23.04	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+L	Inner_1RB_Right	20.94	2.2	23.14	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+L	Inner_Full	20.69	2.2	22.89	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+L	Outer_Full	19.32	2.2	21.52	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+L	Inner_1RB_Left	18.22	2.2	20.42	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+L	Inner_1RB_Right	18.35	2.2	20.55	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+L	Inner_Full	18.66	2.2	20.86	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+L	Outer_Full	18.67	2.2	20.87	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+M	Inner_1RB_Left	22.51	2.2	24.71	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+M	Inner_1RB_Right	22.66	2.2	24.86	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+M	Inner_Full	22.66	2.2	24.86	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+M	Outer_Full	22.7	2.2	24.9	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+M	Inner_1RB_Left	22.62	2.2	24.82	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+M	Inner_1RB_Right	22.67	2.2	24.87	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+M	Inner_Full	22.72	2.2	24.92	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+M	Outer_Full	22.69	2.2	24.89	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+M	Inner_1RB_Left	22.01	2.2	24.21	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+M	Inner_1RB_Right	22.12	2.2	24.32	33	PASS

DC_2A-n7A	15	5+15	DFT-16QAM	M+M	Inner_Full	22.18	2.2	24.38	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+M	Outer_Full	19.8	2.2	22	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+M	Inner_1RB_Left	20.75	2.2	22.95	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+M	Inner_1RB_Right	20.85	2.2	23.05	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+M	Inner_Full	20.7	2.2	22.9	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+M	Outer_Full	19.32	2.2	21.52	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+M	Inner_1RB_Left	18.06	2.2	20.26	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+M	Inner_1RB_Right	18.23	2.2	20.43	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+M	Inner_Full	18.66	2.2	20.86	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+M	Outer_Full	18.71	2.2	20.91	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+H	Inner_1RB_Left	22.69	2.2	24.89	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+H	Inner_1RB_Right	22.79	2.2	24.99	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+H	Inner_Full	22.91	2.2	25.11	33	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+H	Outer_Full	22.82	2.2	25.02	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+H	Inner_1RB_Left	22.73	2.2	24.93	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+H	Inner_1RB_Right	22.81	2.2	25.01	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+H	Inner_Full	22.86	2.2	25.06	33	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+H	Outer_Full	22.83	2.2	25.03	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+H	Inner_1RB_Left	22.11	2.2	24.31	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+H	Inner_1RB_Right	22.06	2.2	24.26	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+H	Inner_Full	22.24	2.2	24.44	33	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+H	Outer_Full	19.87	2.2	22.07	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+H	Inner_1RB_Left	20.88	2.2	23.08	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+H	Inner_1RB_Right	21	2.2	23.2	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+H	Inner_Full	20.74	2.2	22.94	33	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+H	Outer_Full	19.43	2.2	21.63	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+H	Inner_1RB_Left	18.25	2.2	20.45	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+H	Inner_1RB_Right	18.39	2.2	20.59	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+H	Inner_Full	18.75	2.2	20.95	33	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+H	Outer_Full	18.77	2.2	20.97	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+L	Inner_1RB_Left	22.6	2.2	24.8	33	PASS

DC_2A-n7A	15	5+20	DFT-BPSK	M+L	Inner_1RB_Right	22.81	2.2	25.01	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+L	Inner_Full	22.8	2.2	25	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+L	Outer_Full	22.86	2.2	25.06	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+L	Inner_1RB_Left	22.66	2.2	24.86	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+L	Inner_1RB_Right	22.88	2.2	25.08	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+L	Inner_Full	22.83	2.2	25.03	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+L	Outer_Full	22.85	2.2	25.05	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+L	Inner_1RB_Left	22.01	2.2	24.21	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+L	Inner_1RB_Right	22.33	2.2	24.53	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+L	Inner_Full	22.26	2.2	24.46	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+L	Outer_Full	19.81	2.2	22.01	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+L	Inner_1RB_Left	20.89	2.2	23.09	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+L	Inner_1RB_Right	21.09	2.2	23.29	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+L	Inner_Full	20.86	2.2	23.06	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+L	Outer_Full	19.32	2.2	21.52	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+L	Inner_1RB_Left	18.24	2.2	20.44	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+L	Inner_1RB_Right	18.34	2.2	20.54	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+L	Inner_Full	18.81	2.2	21.01	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+L	Outer_Full	18.85	2.2	21.05	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+M	Inner_1RB_Left	22.52	2.2	24.72	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+M	Inner_1RB_Right	22.56	2.2	24.76	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+M	Inner_Full	22.63	2.2	24.83	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+M	Outer_Full	22.62	2.2	24.82	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Inner_1RB_Left	22.57	2.2	24.77	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Inner_1RB_Right	22.61	2.2	24.81	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Inner_Full	22.74	2.2	24.94	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_Full	22.76	2.2	24.96	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+M	Inner_1RB_Left	21.91	2.2	24.11	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+M	Inner_1RB_Right	21.97	2.2	24.17	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+M	Inner_Full	22.14	2.2	24.34	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+M	Outer_Full	19.72	2.2	21.92	33	PASS

DC_2A-n7A	15	5+20	DFT-64QAM	M+M	Inner_1RB_Left	20.69	2.2	22.89	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+M	Inner_1RB_Right	20.78	2.2	22.98	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+M	Inner_Full	20.76	2.2	22.96	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+M	Outer_Full	19.3	2.2	21.5	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+M	Inner_1RB_Left	18.11	2.2	20.31	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+M	Inner_1RB_Right	18.24	2.2	20.44	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+M	Inner_Full	18.67	2.2	20.87	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+M	Outer_Full	18.73	2.2	20.93	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+H	Inner_1RB_Left	22.63	2.2	24.83	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+H	Inner_1RB_Right	22.73	2.2	24.93	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+H	Inner_Full	22.76	2.2	24.96	33	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+H	Outer_Full	22.78	2.2	24.98	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+H	Inner_1RB_Left	22.69	2.2	24.89	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+H	Inner_1RB_Right	22.76	2.2	24.96	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+H	Inner_Full	22.89	2.2	25.09	33	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+H	Outer_Full	22.85	2.2	25.05	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+H	Inner_1RB_Left	22.06	2.2	24.26	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+H	Inner_1RB_Right	22.13	2.2	24.33	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+H	Inner_Full	22.23	2.2	24.43	33	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+H	Outer_Full	19.9	2.2	22.1	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+H	Inner_1RB_Left	20.81	2.2	23.01	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+H	Inner_1RB_Right	20.89	2.2	23.09	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+H	Inner_Full	20.87	2.2	23.07	33	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+H	Outer_Full	19.35	2.2	21.55	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+H	Inner_1RB_Left	18.26	2.2	20.46	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+H	Inner_1RB_Right	18.28	2.2	20.48	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+H	Inner_Full	18.78	2.2	20.98	33	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+H	Outer_Full	18.81	2.2	21.01	33	PASS

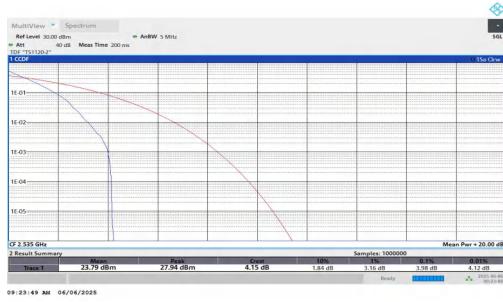
Peak-to-Average Ratio for ENDC

Peak-to-Average Ratio(CCDF)

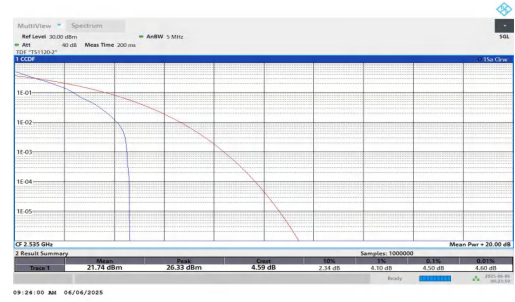
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result(dB)	Limit(dB)	Verdict
DC_2A-n7A	15	5+5	DFT-BPSK	M+M	Outer_Full	3.98	≤13	PASS
DC_2A-n7A	15	5+5	DFT-QPSK	M+M	Outer_Full	4.5	≤13	PASS
DC_2A-n7A	15	5+5	DFT-16QAM	M+M	Outer_Full	5.64	≤13	PASS
DC_2A-n7A	15	5+5	DFT-64QAM	M+M	Outer_Full	6.14	≤13	PASS
DC_2A-n7A	15	5+5	DFT-256QAM	M+M	Outer_Full	6.46	≤13	PASS
DC_2A-n7A	15	5+10	DFT-BPSK	M+M	Outer_Full	4.04	≤13	PASS
DC_2A-n7A	15	5+10	DFT-QPSK	M+M	Outer_Full	4.7	≤13	PASS
DC_2A-n7A	15	5+10	DFT-16QAM	M+M	Outer_Full	5.68	≤13	PASS
DC_2A-n7A	15	5+10	DFT-64QAM	M+M	Outer_Full	6.04	≤13	PASS
DC_2A-n7A	15	5+10	DFT-256QAM	M+M	Outer_Full	6.52	≤13	PASS
DC_2A-n7A	15	5+15	DFT-BPSK	M+M	Outer_Full	3.9	≤13	PASS
DC_2A-n7A	15	5+15	DFT-QPSK	M+M	Outer_Full	4.56	≤13	PASS
DC_2A-n7A	15	5+15	DFT-16QAM	M+M	Outer_Full	5.38	≤13	PASS
DC_2A-n7A	15	5+15	DFT-64QAM	M+M	Outer_Full	5.94	≤13	PASS
DC_2A-n7A	15	5+15	DFT-256QAM	M+M	Outer_Full	6.44	≤13	PASS
DC_2A-n7A	15	5+20	DFT-BPSK	M+M	Outer_Full	3.94	≤13	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_Full	4.6	≤13	PASS
DC_2A-n7A	15	5+20	DFT-16QAM	M+M	Outer_Full	5.52	≤13	PASS
DC_2A-n7A	15	5+20	DFT-64QAM	M+M	Outer_Full	5.94	≤13	PASS
DC_2A-n7A	15	5+20	DFT-256QAM	M+M	Outer_Full	6.66	≤13	PASS

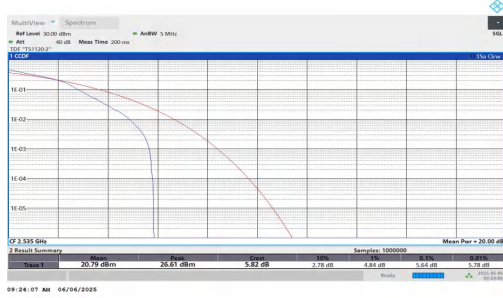
Test Graphs



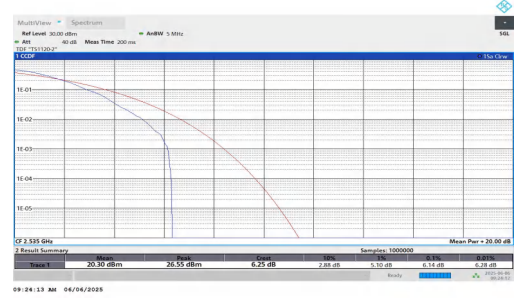
NTNV-DC_2A-n7A-PC3-15-5+5-M+M-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



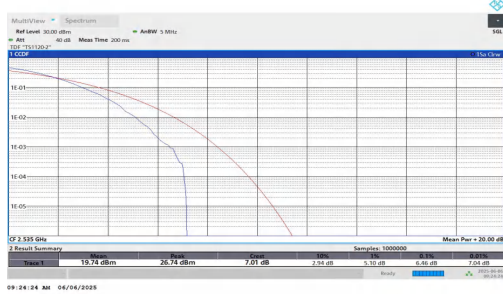
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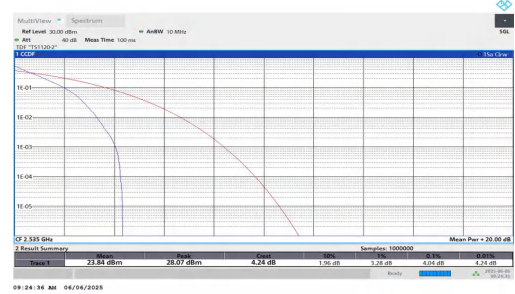
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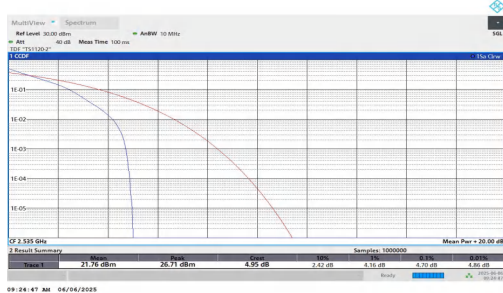
NTNV-DC_2A-n7A-PC3-15-5+5-M+M-QPSK-IDLE-DFT-64QAM-Outer_Full-PASS



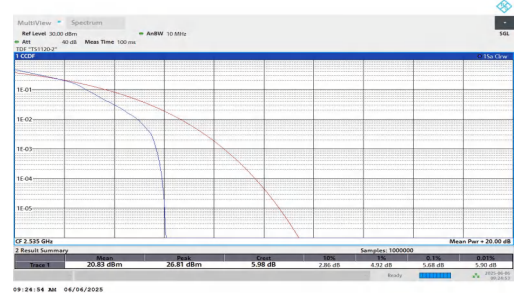
NTNV-DC_2A-n7A-PC3-15-5+5-M+M-QPSK-IDLE-DFT-256QAM-Outer_Full-PASS



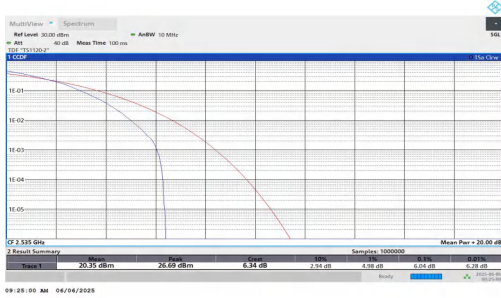
NTNV-DC_2A-n7A-PC3-15-5+10-M+M-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



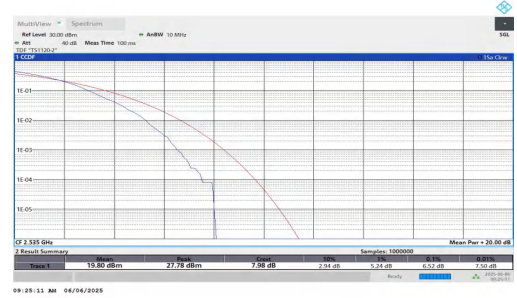
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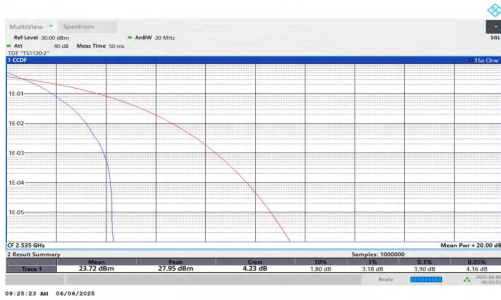
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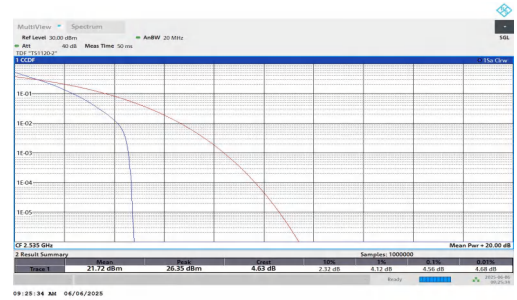
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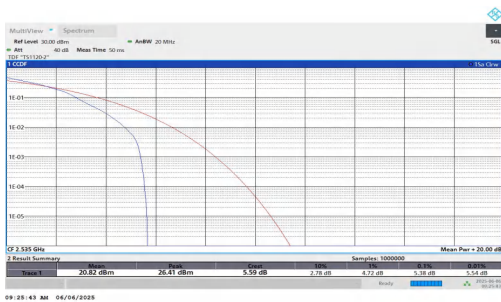
NTNV-DC_2A-n7A-PC3-15-5+10-M+M-QPSK-IDLE-DFT-256QAM-Outer_Full-PASS



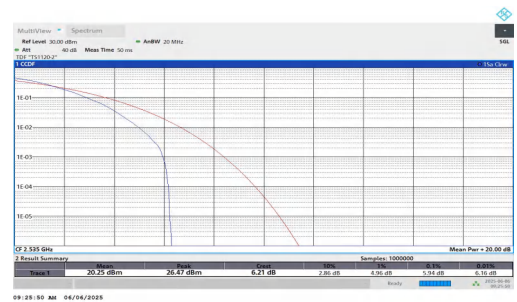
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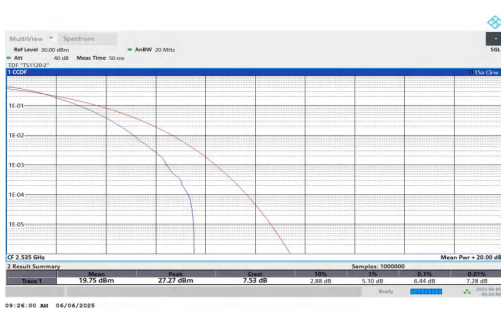
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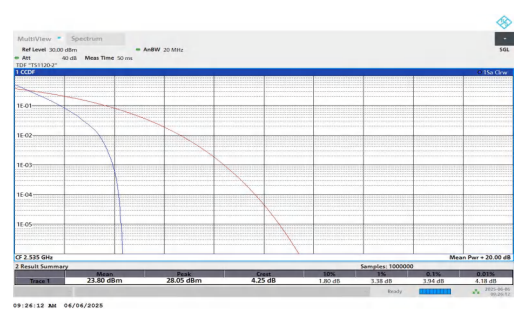
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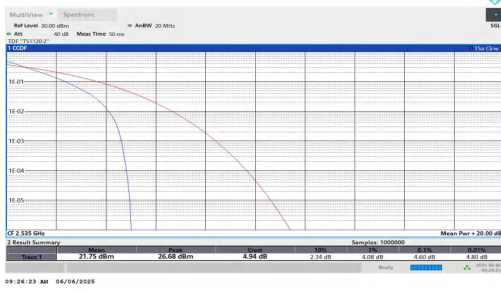
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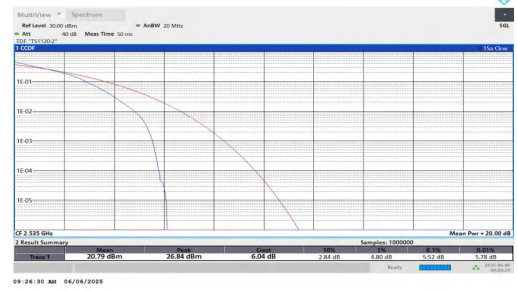
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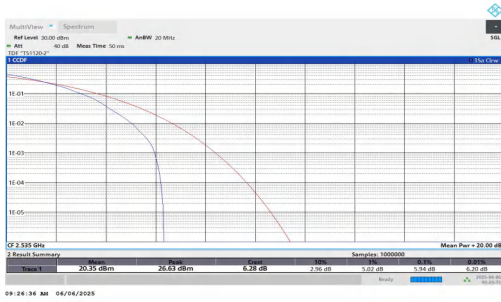
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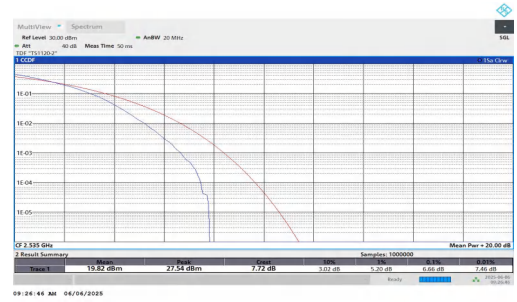
NTNV-DC_2A-n7A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-QPSK-Out_Full-PASS



NTNV-DC_2A-n7A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-16QAM-Out_Full-PASS



NTNV-DC_2A-n7A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-64QAM-Out_Full-PASS



NTNV-DC_2A-n7A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-256QAM-Out_Full-PASS

26dB Bandwidth and Occupied Bandwidth for ENDC

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A-n7A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4810	4.937	PASS
DC_2A-n7A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4887	4.977	PASS
DC_2A-n7A	15	5+5	CP-64QAM	M+M	Outer_Full	4.4764	4.939	PASS
DC_2A-n7A	15	5+5	CP-256QAM	M+M	Outer_Full	4.4836	4.931	PASS
DC_2A-n7A	15	5+10	CP-QPSK	M+M	Outer_Full	9.3112	10.02	PASS
DC_2A-n7A	15	5+10	CP-16QAM	M+M	Outer_Full	9.3127	9.920	PASS
DC_2A-n7A	15	5+10	CP-64QAM	M+M	Outer_Full	9.3383	12.97	PASS
DC_2A-n7A	15	5+10	CP-256QAM	M+M	Outer_Full	9.3201	10.03	PASS
DC_2A-n7A	15	5+15	CP-QPSK	M+M	Outer_Full	14.097	19.75	PASS
DC_2A-n7A	15	5+15	CP-16QAM	M+M	Outer_Full	14.115	14.99	PASS
DC_2A-n7A	15	5+15	CP-64QAM	M+M	Outer_Full	14.111	16.36	PASS
DC_2A-n7A	15	5+15	CP-256QAM	M+M	Outer_Full	14.114	14.99	PASS
DC_2A-n7A	15	5+20	CP-QPSK	M+M	Outer_Full	18.969	29.55	PASS
DC_2A-n7A	15	5+20	CP-16QAM	M+M	Outer_Full	18.946	19.98	PASS
DC_2A-n7A	15	5+20	CP-64QAM	M+M	Outer_Full	19.004	19.99	PASS
DC_2A-n7A	15	5+20	CP-256QAM	M+M	Outer_Full	18.941	20.05	PASS

Test Graphs



NTNV-DC_2A-n7A-PC3-15-5+5-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-Ant1-PASS



NTNV-DC_2A-n7A-PC3-15-5+5-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-Ant1-PASS



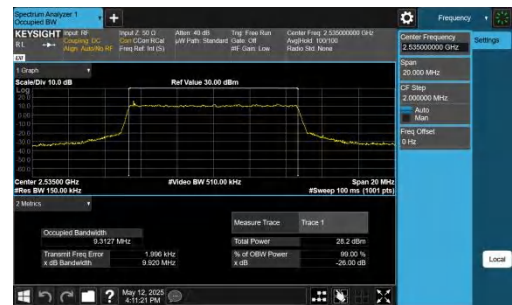
NTNV-DC_2A-n7A-PC3-15-5+5-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-Ant1-PASS



NTNV-DC_2A-n7A-PC3-15-5+5-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-Ant1-PASS



NTNV-DC_2A-n7A-PC3-15-5+10-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-Ant1-PASS



NTNV-DC_2A-n7A-PC3-15-5+10-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-Ant1-PASS



NTNV-DC_2A-n7A-PC3-15-5+10-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-Ant1-PASS



NTNV-DC_2A-n7A-PC3-15-5+10-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-Ant1-PASS



NTNV-DC_2A-n7A-PC3-15-5+15-M+M-QPSK-IDLE-CP-QPSK-Out er_Full-Ant1-PASS



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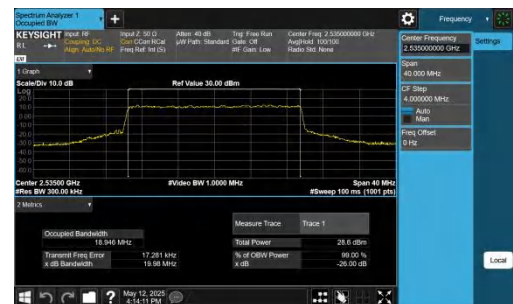
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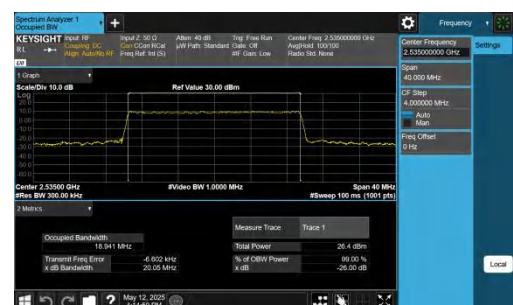
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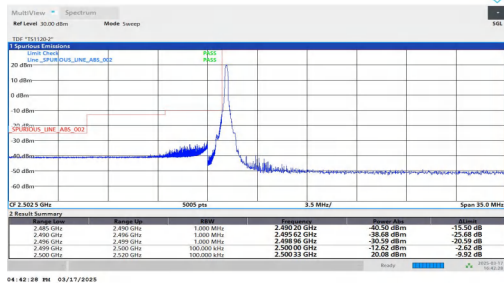
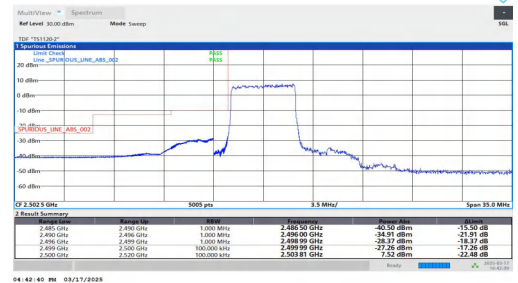
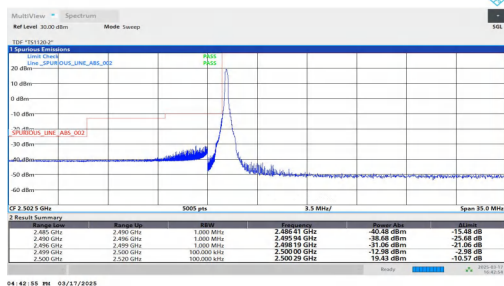
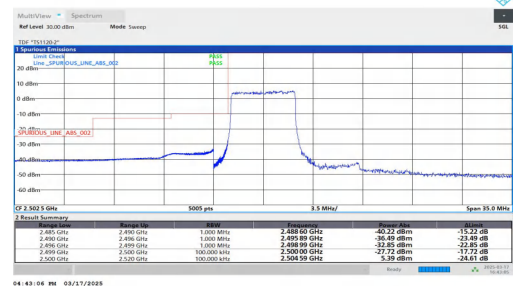
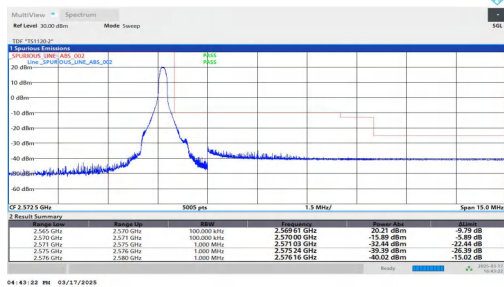
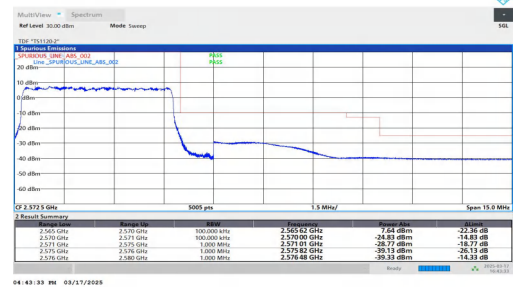
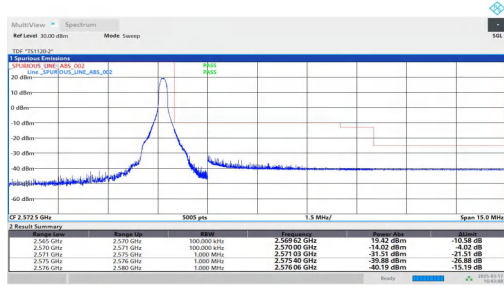
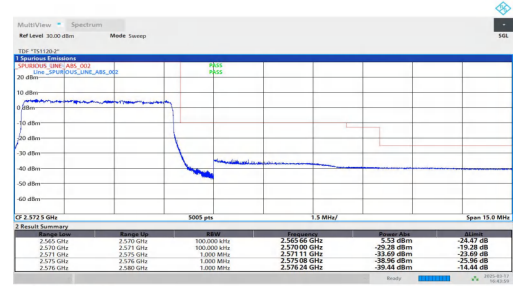
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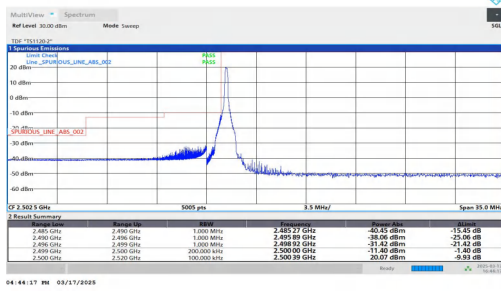


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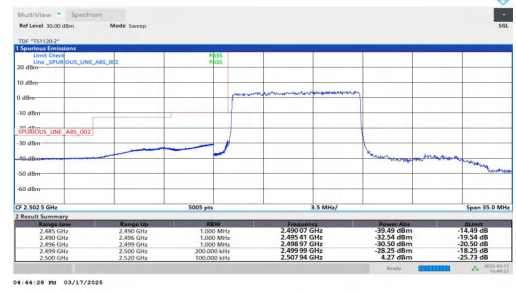
Band Edge for ENDC

Test Graphs

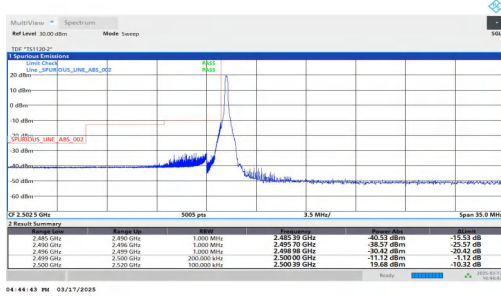
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uter_Full-PASSNTNV-DC_2A-n7A-PC3-15-5+5-M+L-QPSK-IDLE-DFT-QPSK-Edge_1
RB_Left-PASSNTNV-DC_2A-n7A-PC3-15-5+5-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASSNTNV-DC_2A-n7A-PC3-15-5+5-M+H-QPSK-IDLE-DFT-BPSK-Edge_1
RB_Right-PASSNTNV-DC_2A-n7A-PC3-15-5+5-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASSNTNV-DC_2A-n7A-PC3-15-5+5-M+H-QPSK-IDLE-DFT-QPSK-Edge_
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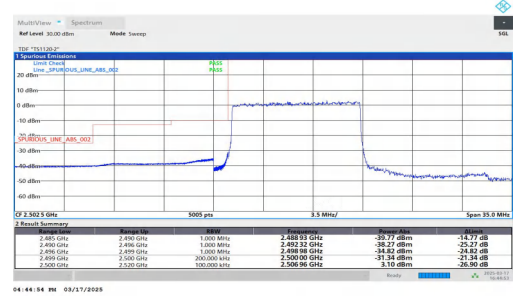
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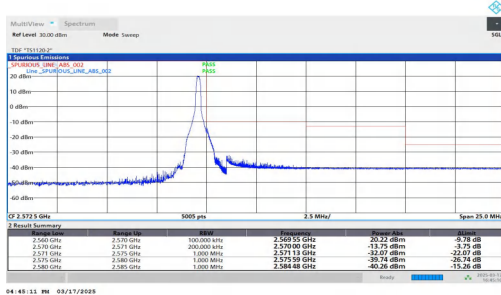
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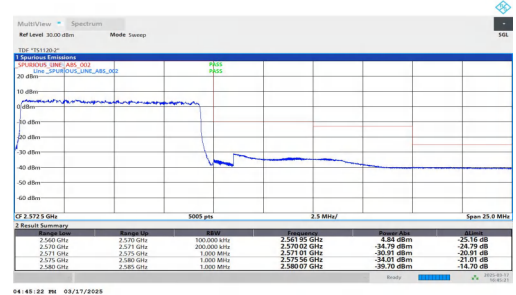
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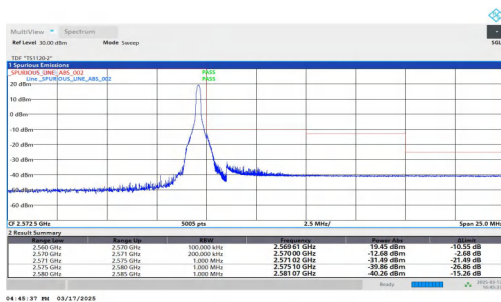
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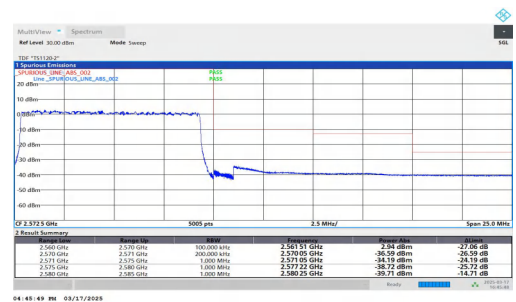
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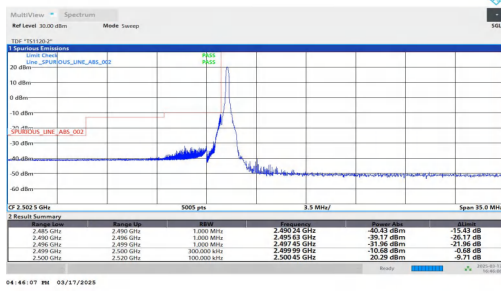
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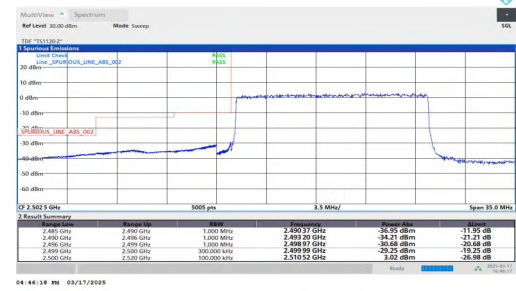
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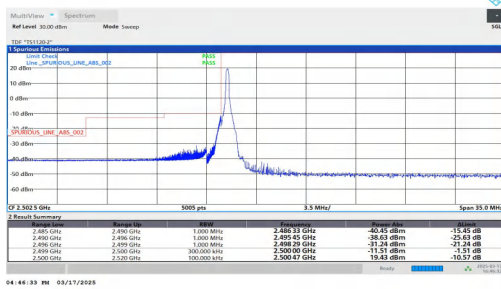
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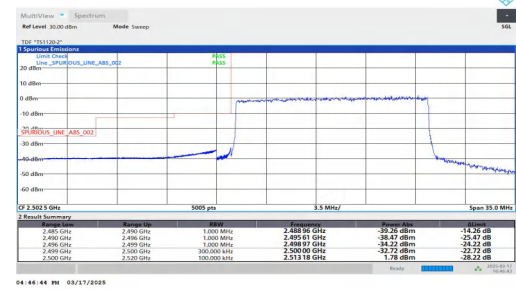
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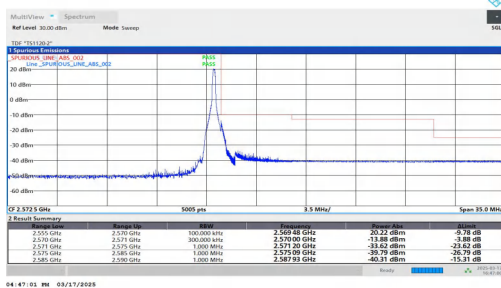
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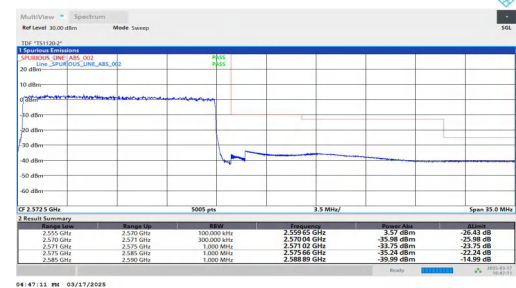
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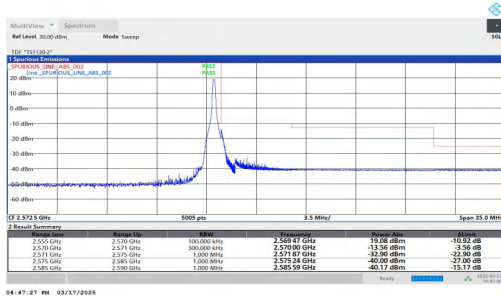
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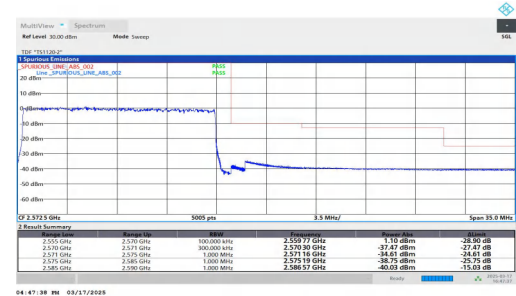
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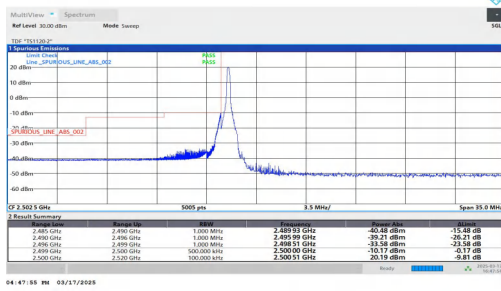
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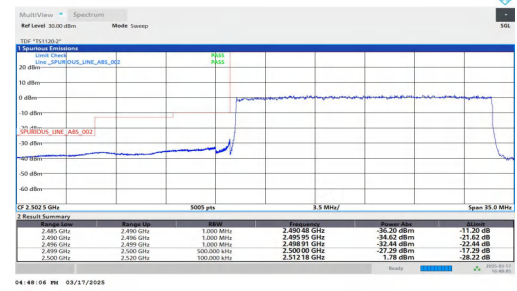
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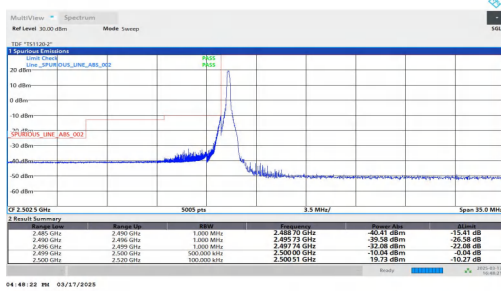
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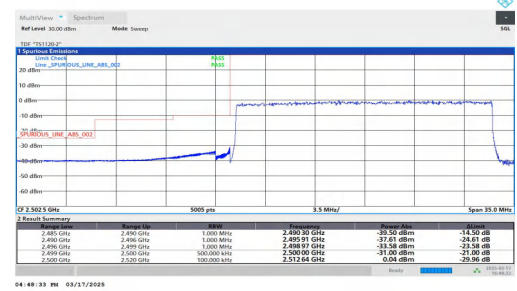
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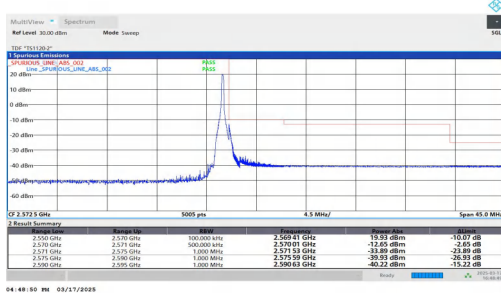
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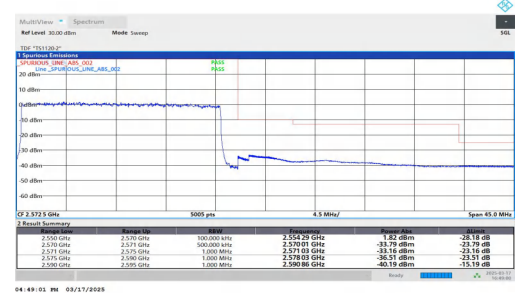
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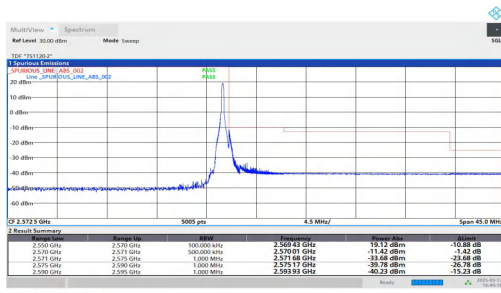
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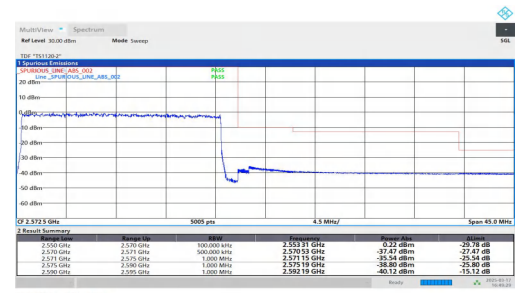
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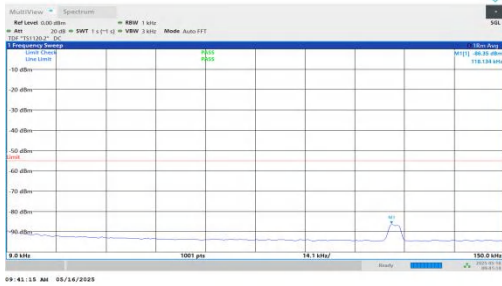
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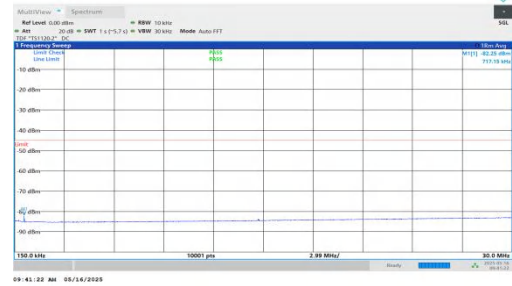
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Conducted Spurious Emission for ENDC

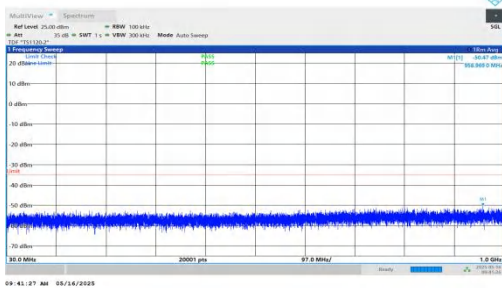
Test Graphs



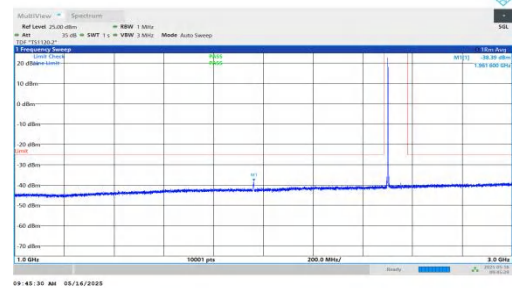
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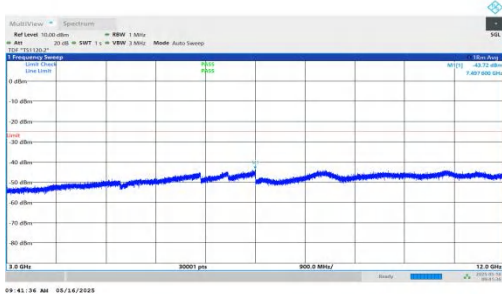
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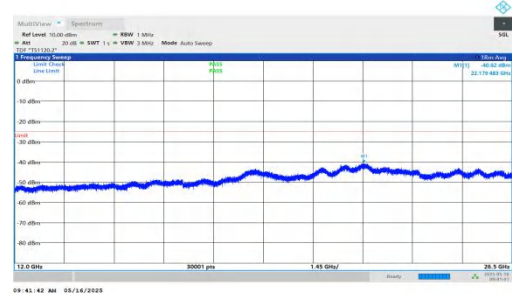
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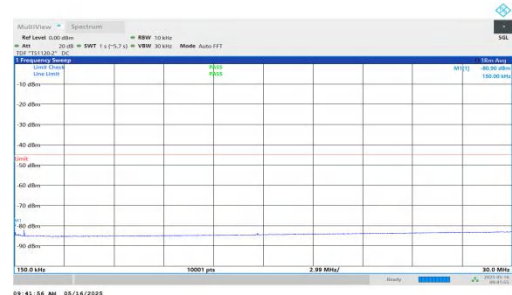
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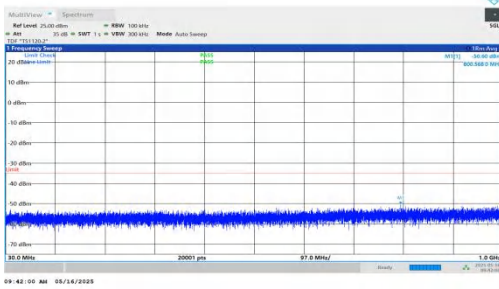
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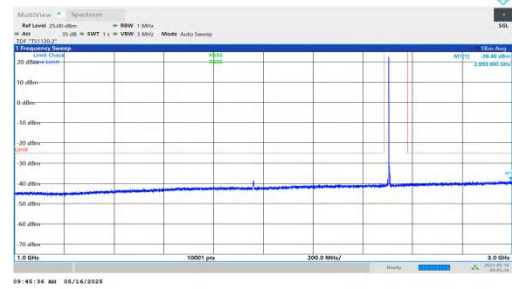
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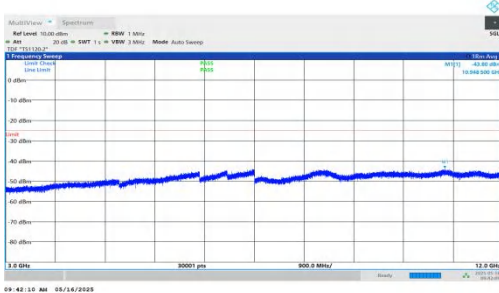
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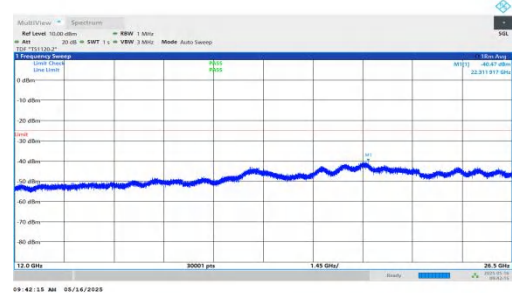
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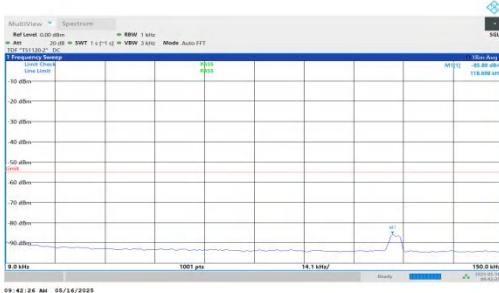
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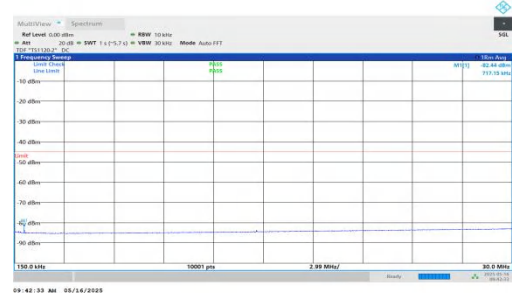
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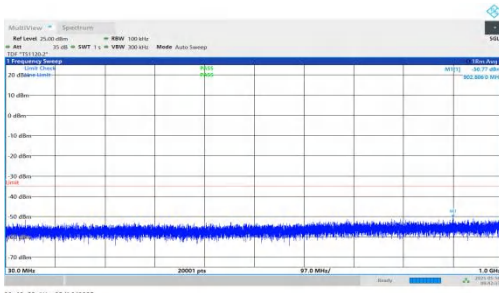
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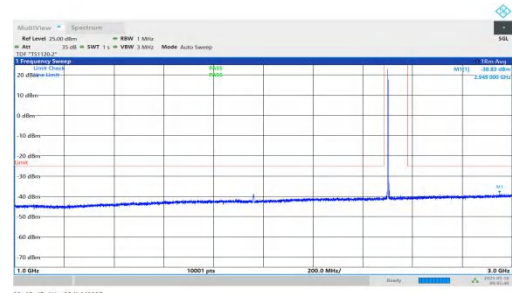
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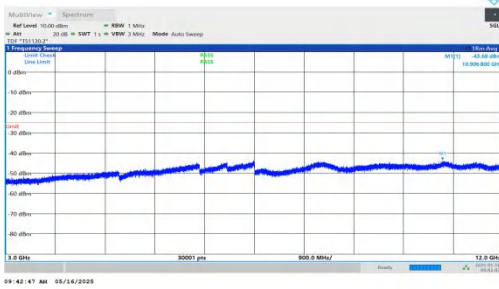
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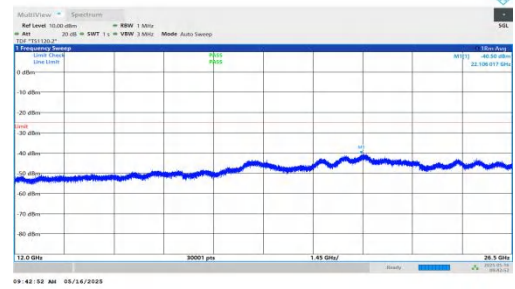
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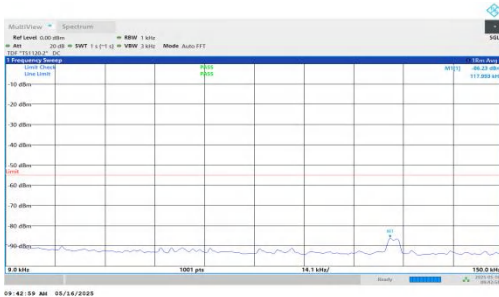
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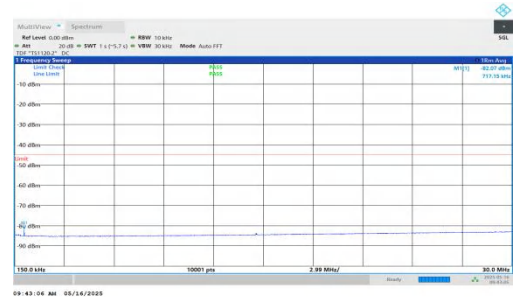
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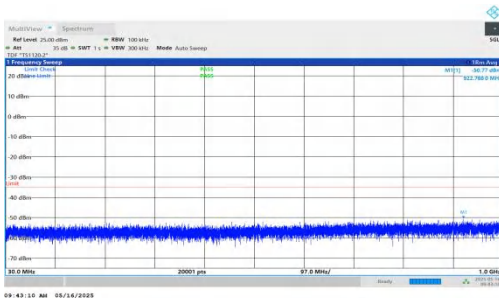
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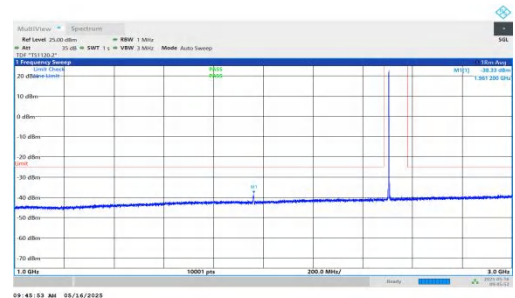
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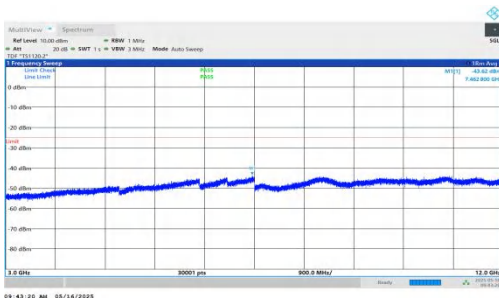
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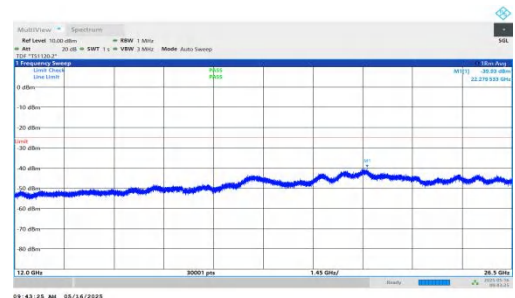
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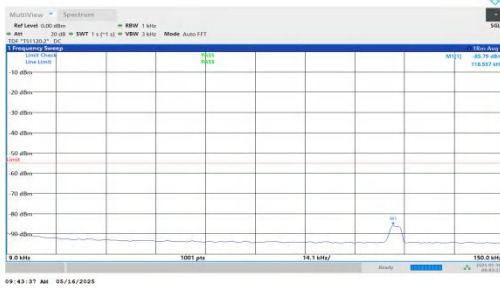
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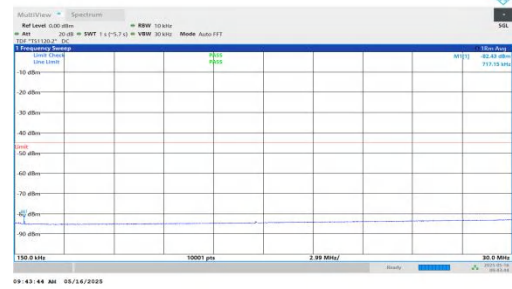
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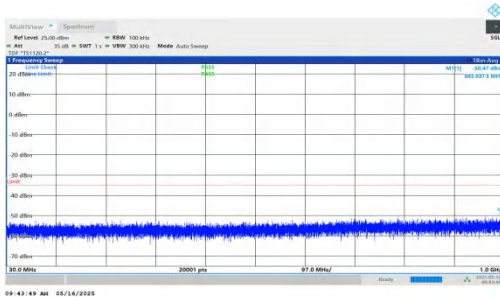
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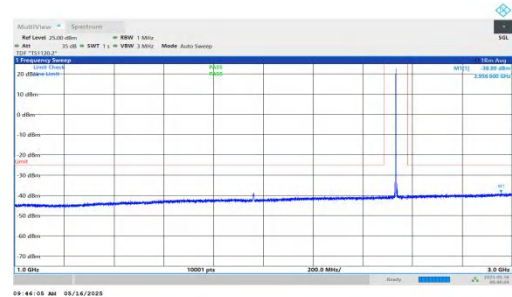
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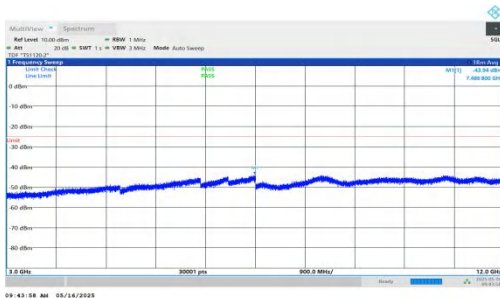
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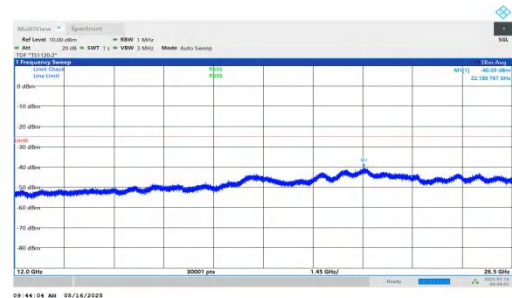
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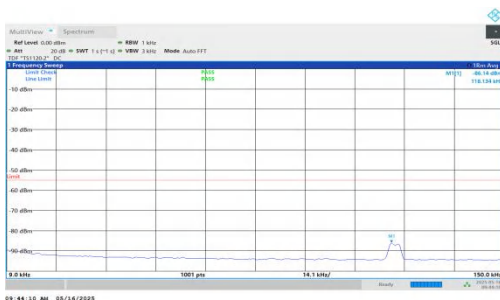
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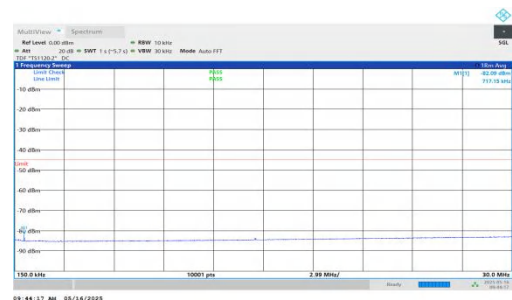
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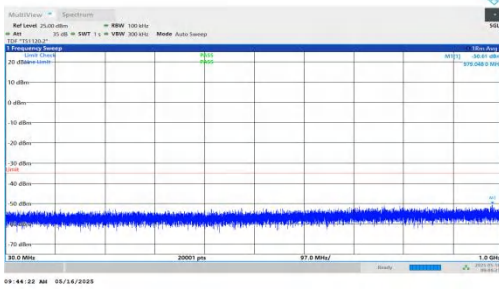
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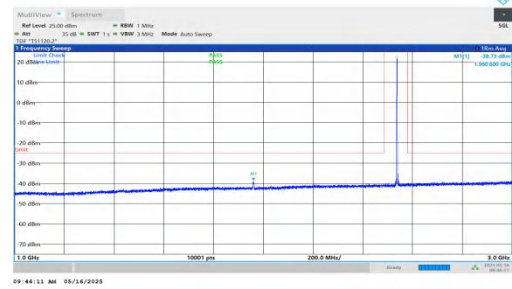
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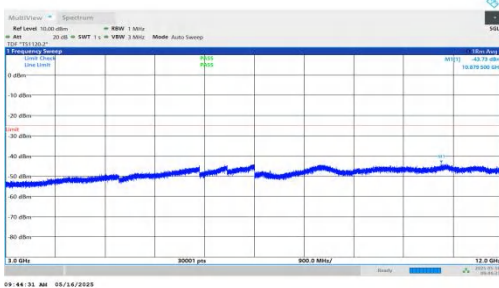
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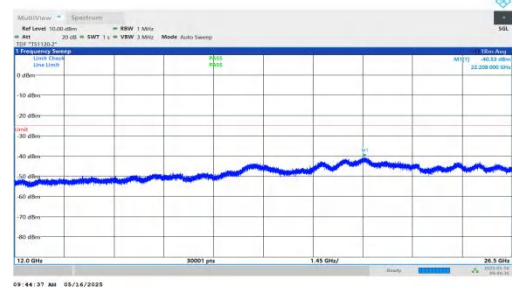
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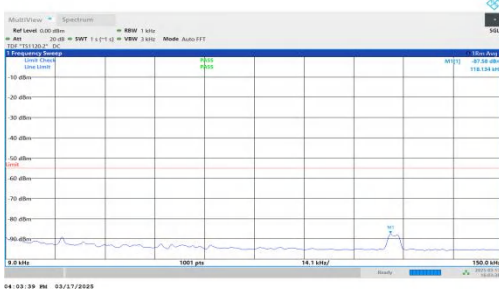
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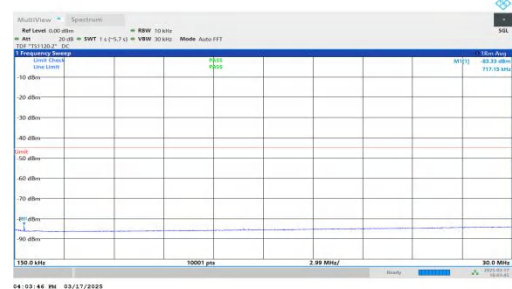
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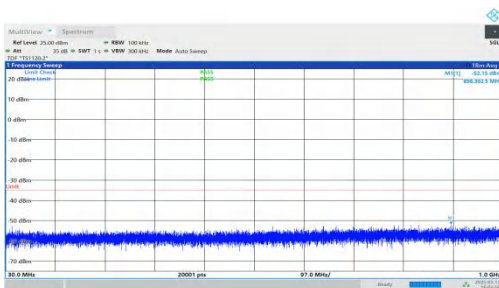
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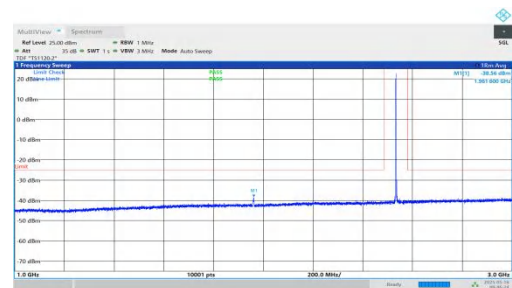
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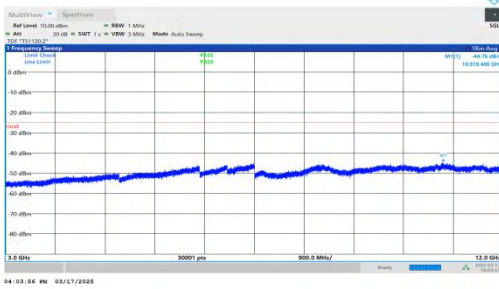
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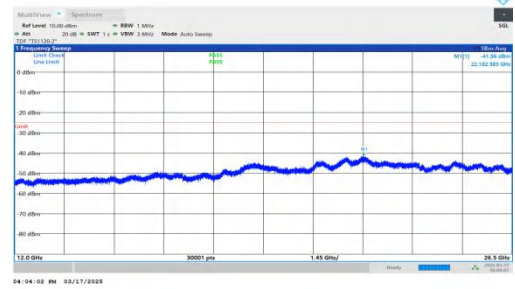
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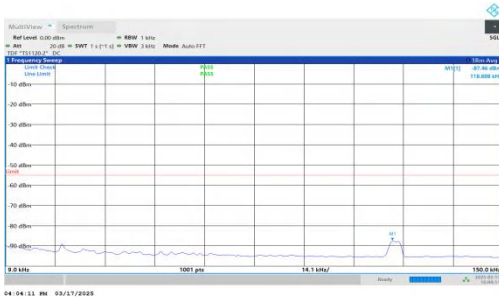
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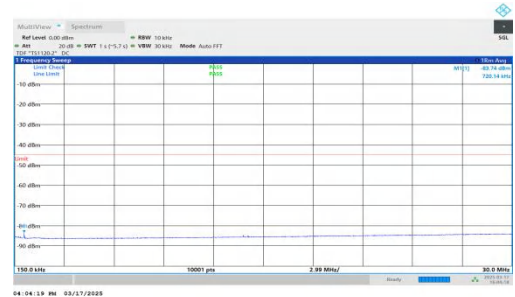
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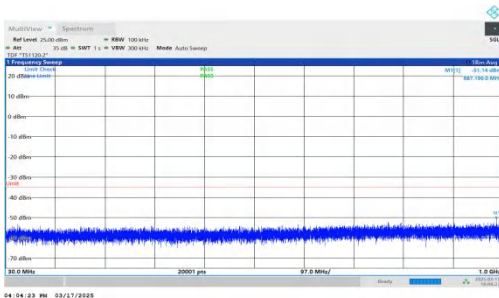
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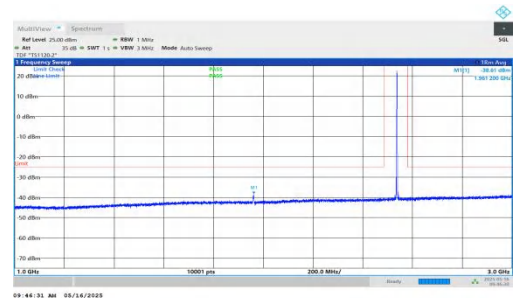
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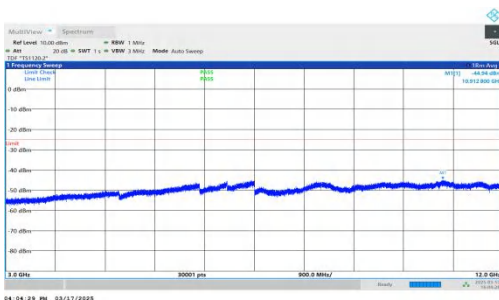
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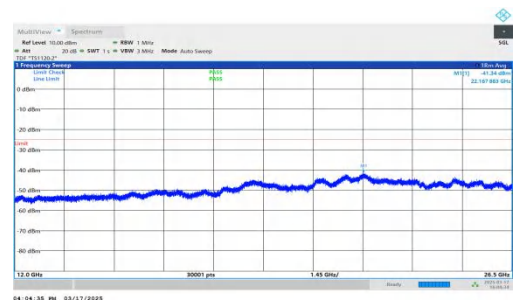
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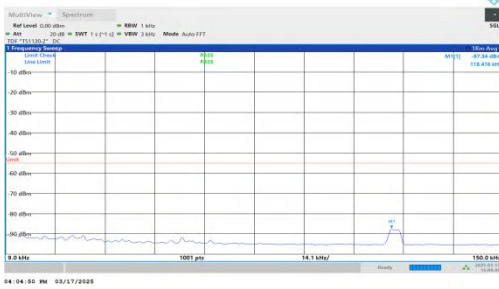
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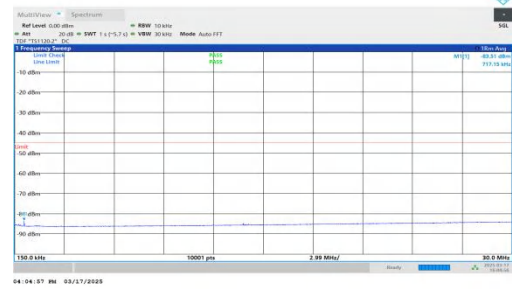
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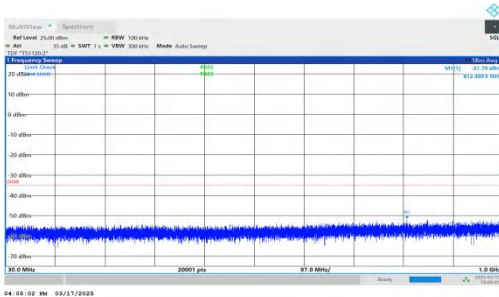
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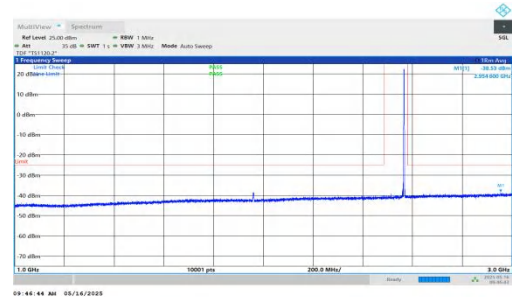
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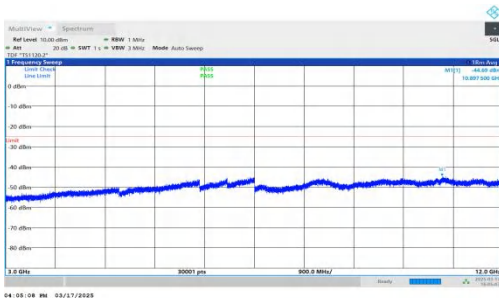
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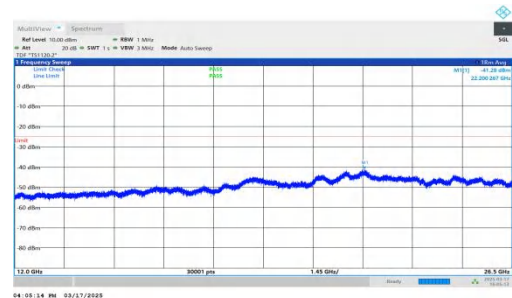
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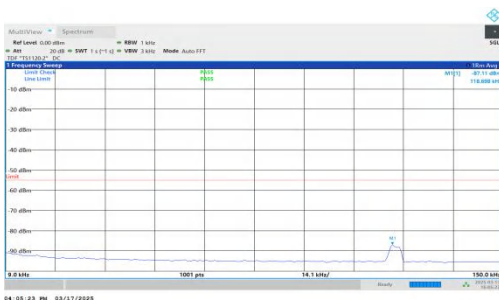
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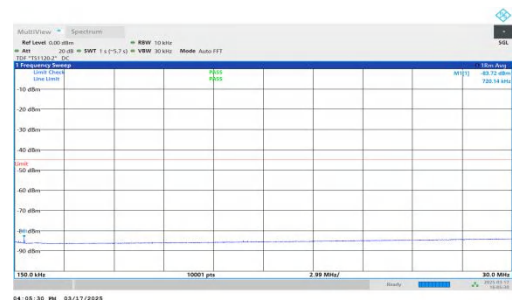
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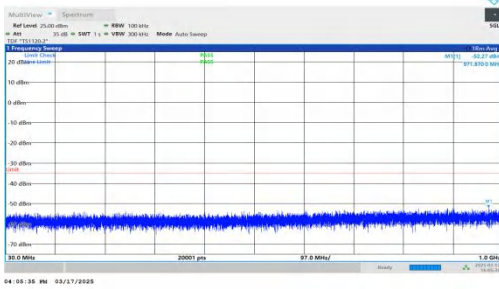
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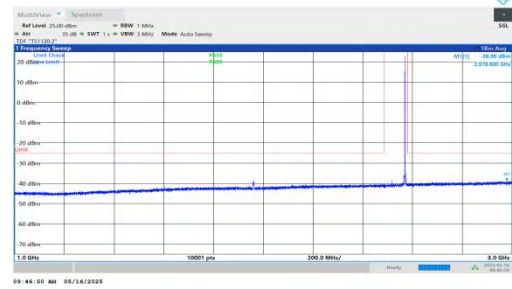
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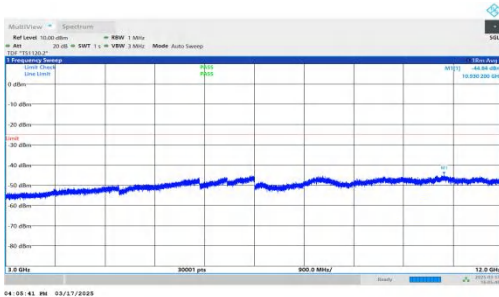
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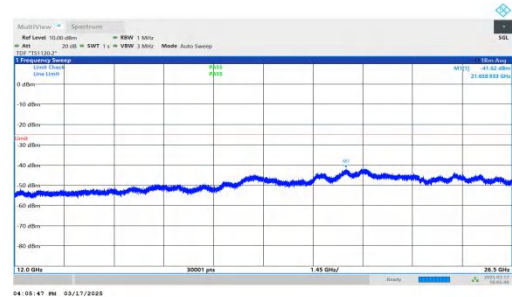
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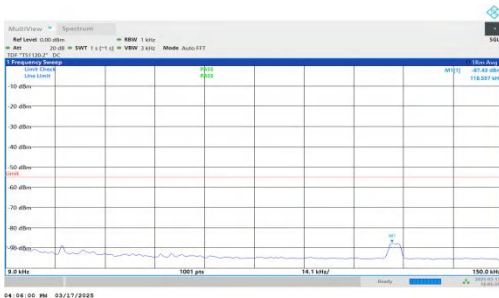
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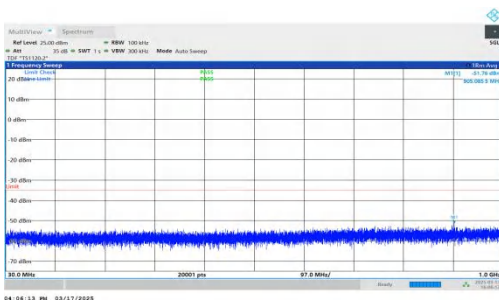
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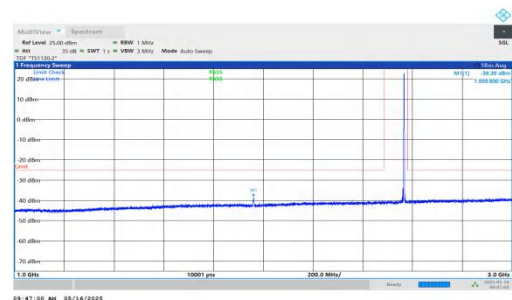
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