

Effective (Isotropic) Radiated Power Output Data for ENDC

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Gain (dBi)	E(I)RP (dBm)	Limit(dBm)	Verdict
DC_7A-n2A	15	5+5	DFT-BPSK	M+L	Inner_1RB_Left	22.76	1	23.76	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+L	Inner_1RB_Right	22.62	1	23.62	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+L	Inner_Full	22.71	1	23.71	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+L	Outer_Full	22.61	1	23.61	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+L	Inner_1RB_Left	22.71	1	23.71	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+L	Inner_1RB_Right	22.61	1	23.61	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+L	Inner_Full	22.69	1	23.69	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	22.26	1	23.26	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+L	Inner_1RB_Left	22.08	1	23.08	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+L	Inner_1RB_Right	21.9	1	22.9	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+L	Inner_Full	22.25	1	23.25	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	19.71	1	20.71	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+L	Inner_1RB_Left	20.91	1	21.91	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+L	Inner_1RB_Right	20.71	1	21.71	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+L	Inner_Full	20.64	1	21.64	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+L	Outer_Full	19.04	1	20.04	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+L	Inner_1RB_Left	18.1	1	19.1	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+L	Inner_1RB_Right	17.98	1	18.98	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+L	Inner_Full	18.58	1	19.58	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+L	Outer_Full	18.57	1	19.57	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+M	Inner_1RB_Left	22.78	1	23.78	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+M	Inner_1RB_Right	22.68	1	23.68	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+M	Inner_Full	22.82	1	23.82	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+M	Outer_Full	22.75	1	23.75	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+M	Inner_1RB_Left	22.84	1	23.84	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+M	Inner_1RB_Right	22.67	1	23.67	33	PASS

DC_7A-n2A	15	5+5	DFT-QPSK	M+M	Inner_Full	22.81	1	23.81	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	22.27	1	23.27	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+M	Inner_1RB_Left	22.26	1	23.26	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+M	Inner_1RB_Right	22.11	1	23.11	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+M	Inner_Full	22.23	1	23.23	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	19.76	1	20.76	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+M	Inner_1RB_Left	20.99	1	21.99	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+M	Inner_1RB_Right	20.86	1	21.86	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+M	Inner_Full	20.85	1	21.85	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+M	Outer_Full	19.34	1	20.34	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+M	Inner_1RB_Left	18.33	1	19.33	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+M	Inner_1RB_Right	18.14	1	19.14	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+M	Inner_Full	18.78	1	19.78	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+M	Outer_Full	18.76	1	19.76	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+H	Inner_1RB_Left	22.98	1	23.98	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+H	Inner_1RB_Right	22.96	1	23.96	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+H	Inner_Full	22.97	1	23.97	33	PASS
DC_7A-n2A	15	5+5	DFT-BPSK	M+H	Outer_Full	23	1	24	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+H	Inner_1RB_Left	23.01	1	24.01	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+H	Inner_1RB_Right	22.93	1	23.93	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+H	Inner_Full	22.99	1	23.99	33	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	22.29	1	23.29	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+H	Inner_1RB_Left	22.49	1	23.49	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+H	Inner_1RB_Right	22.39	1	23.39	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+H	Inner_Full	22.43	1	23.43	33	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	19.96	1	20.96	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+H	Inner_1RB_Left	21.17	1	22.17	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+H	Inner_1RB_Right	21.17	1	22.17	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+H	Inner_Full	21.07	1	22.07	33	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+H	Outer_Full	19.53	1	20.53	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+H	Inner_1RB_Left	18.48	1	19.48	33	PASS

DC_7A-n2A	15	5+5	DFT-256QAM	M+H	Inner_1RB_Right	18.41	1	19.41	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+H	Inner_Full	19	1	20	33	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+H	Outer_Full	18.99	1	19.99	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+L	Inner_1RB_Left	22.69	1	23.69	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+L	Inner_1RB_Right	22.7	1	23.7	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+L	Inner_Full	22.74	1	23.74	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+L	Outer_Full	22.73	1	23.73	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+L	Inner_1RB_Left	22.69	1	23.69	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+L	Inner_1RB_Right	22.68	1	23.68	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+L	Inner_Full	22.68	1	23.68	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	22.23	1	23.23	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+L	Inner_1RB_Left	22.27	1	23.27	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+L	Inner_1RB_Right	22.11	1	23.11	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+L	Inner_Full	22.2	1	23.2	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	19.65	1	20.65	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+L	Inner_1RB_Left	20.88	1	21.88	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+L	Inner_1RB_Right	20.8	1	21.8	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+L	Inner_Full	20.74	1	21.74	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+L	Outer_Full	19.3	1	20.3	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+L	Inner_1RB_Left	18.16	1	19.16	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+L	Inner_1RB_Right	18.13	1	19.13	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+L	Inner_Full	18.6	1	19.6	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+L	Outer_Full	18.66	1	19.66	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+M	Inner_1RB_Left	22.81	1	23.81	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+M	Inner_1RB_Right	22.62	1	23.62	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+M	Inner_Full	22.82	1	23.82	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+M	Outer_Full	22.81	1	23.81	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+M	Inner_1RB_Left	22.91	1	23.91	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+M	Inner_1RB_Right	22.64	1	23.64	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+M	Inner_Full	22.79	1	23.79	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	22.24	1	23.24	33	PASS

DC_7A-n2A	15	5+10	DFT-16QAM	M+M	Inner_1RB_Left	22.37	1	23.37	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+M	Inner_1RB_Right	22.16	1	23.16	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+M	Inner_Full	22.32	1	23.32	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	19.74	1	20.74	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+M	Inner_1RB_Left	20.98	1	21.98	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+M	Inner_1RB_Right	20.86	1	21.86	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+M	Inner_Full	20.8	1	21.8	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+M	Outer_Full	19.31	1	20.31	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+M	Inner_1RB_Left	18.39	1	19.39	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+M	Inner_1RB_Right	18.16	1	19.16	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+M	Inner_Full	18.73	1	19.73	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+M	Outer_Full	18.75	1	19.75	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+H	Inner_1RB_Left	22.93	1	23.93	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+H	Inner_1RB_Right	23.03	1	24.03	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+H	Inner_Full	23	1	24	33	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+H	Outer_Full	23.05	1	24.05	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+H	Inner_1RB_Left	22.89	1	23.89	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+H	Inner_1RB_Right	23.05	1	24.05	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+H	Inner_Full	22.95	1	23.95	33	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	22.24	1	23.24	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+H	Inner_1RB_Left	22.44	1	23.44	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+H	Inner_1RB_Right	22.51	1	23.51	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+H	Inner_Full	22.56	1	23.56	33	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	19.95	1	20.95	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+H	Inner_1RB_Left	20.8	1	21.8	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+H	Inner_1RB_Right	21.23	1	22.23	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+H	Inner_Full	21.02	1	22.02	33	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+H	Outer_Full	19.49	1	20.49	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+H	Inner_1RB_Left	18.42	1	19.42	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+H	Inner_1RB_Right	18.47	1	19.47	33	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+H	Inner_Full	18.97	1	19.97	33	PASS

DC_7A-n2A	15	5+10	DFT-256QAM	M+H	Outer_Full	18.92	1	19.92	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+L	Inner_1RB_Left	22.73	1	23.73	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+L	Inner_1RB_Right	22.82	1	23.82	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+L	Inner_Full	22.73	1	23.73	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+L	Outer_Full	22.77	1	23.77	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+L	Inner_1RB_Left	22.72	1	23.72	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+L	Inner_1RB_Right	22.81	1	23.81	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+L	Inner_Full	22.71	1	23.71	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	22.25	1	23.25	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+L	Inner_1RB_Left	22.14	1	23.14	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+L	Inner_1RB_Right	22.29	1	23.29	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+L	Inner_Full	22.13	1	23.13	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	19.75	1	20.75	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+L	Inner_1RB_Left	20.97	1	21.97	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+L	Inner_1RB_Right	21.02	1	22.02	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+L	Inner_Full	20.65	1	21.65	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+L	Outer_Full	19.16	1	20.16	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+L	Inner_1RB_Left	18.24	1	19.24	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+L	Inner_1RB_Right	18.22	1	19.22	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+L	Inner_Full	18.6	1	19.6	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+L	Outer_Full	18.65	1	19.65	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+M	Inner_1RB_Left	22.81	1	23.81	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+M	Inner_1RB_Right	22.61	1	23.61	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+M	Inner_Full	22.85	1	23.85	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+M	Outer_Full	22.72	1	23.72	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+M	Inner_1RB_Left	22.9	1	23.9	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+M	Inner_1RB_Right	22.62	1	23.62	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+M	Inner_Full	22.8	1	23.8	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	22.51	1	23.51	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+M	Inner_1RB_Left	22.31	1	23.31	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+M	Inner_1RB_Right	22.06	1	23.06	33	PASS

DC_7A-n2A	15	5+15	DFT-16QAM	M+M	Inner_Full	22.2	1	23.2	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	19.78	1	20.78	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+M	Inner_1RB_Left	21.01	1	22.01	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+M	Inner_1RB_Right	20.76	1	21.76	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+M	Inner_Full	20.75	1	21.75	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+M	Outer_Full	19.2	1	20.2	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+M	Inner_1RB_Left	18.37	1	19.37	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+M	Inner_1RB_Right	18.59	1	19.59	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+M	Inner_Full	18.67	1	19.67	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+M	Outer_Full	18.64	1	19.64	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+H	Inner_1RB_Left	22.96	1	23.96	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+H	Inner_1RB_Right	23.29	1	24.29	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+H	Inner_Full	23.14	1	24.14	33	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+H	Outer_Full	23.13	1	24.13	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+H	Inner_1RB_Left	23.03	1	24.03	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+H	Inner_1RB_Right	23.28	1	24.28	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+H	Inner_Full	23.13	1	24.13	33	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	22.15	1	23.15	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+H	Inner_1RB_Left	22.51	1	23.51	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+H	Inner_1RB_Right	22.71	1	23.71	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+H	Inner_Full	22.62	1	23.62	33	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	20.19	1	21.19	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+H	Inner_1RB_Left	21.2	1	22.2	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+H	Inner_1RB_Right	21.48	1	22.48	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+H	Inner_Full	21.22	1	22.22	33	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+H	Outer_Full	19.65	1	20.65	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+H	Inner_1RB_Left	18.46	1	19.46	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+H	Inner_1RB_Right	18.72	1	19.72	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+H	Inner_Full	19.05	1	20.05	33	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+H	Outer_Full	19.11	1	20.11	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+L	Inner_1RB_Left	22.6	1	23.6	33	PASS

DC_7A-n2A	15	5+20	DFT-BPSK	M+L	Inner_1RB_Right	22.81	1	23.81	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+L	Inner_Full	22.72	1	23.72	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+L	Outer_Full	22.74	1	23.74	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+L	Inner_1RB_Left	22.69	1	23.69	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+L	Inner_1RB_Right	22.88	1	23.88	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+L	Inner_Full	22.7	1	23.7	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	22.28	1	23.28	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+L	Inner_1RB_Left	22.13	1	23.13	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+L	Inner_1RB_Right	22.31	1	23.31	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+L	Inner_Full	22.2	1	23.2	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	19.81	1	20.81	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+L	Inner_1RB_Left	20.38	1	21.38	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+L	Inner_1RB_Right	21.07	1	22.07	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+L	Inner_Full	20.68	1	21.68	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+L	Outer_Full	19.23	1	20.23	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+L	Inner_1RB_Left	18.07	1	19.07	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+L	Inner_1RB_Right	18.29	1	19.29	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+L	Inner_Full	18.68	1	19.68	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+L	Outer_Full	18.79	1	19.79	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+M	Inner_1RB_Left	22.88	1	23.88	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+M	Inner_1RB_Right	22.63	1	23.63	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+M	Inner_Full	22.71	1	23.71	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+M	Outer_Full	22.76	1	23.76	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+M	Inner_1RB_Left	22.89	1	23.89	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+M	Inner_1RB_Right	22.6	1	23.6	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+M	Inner_Full	22.77	1	23.77	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	22.69	1	23.69	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+M	Inner_1RB_Left	22.47	1	23.47	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+M	Inner_1RB_Right	22.01	1	23.01	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+M	Inner_Full	22.26	1	23.26	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	19.69	1	20.69	33	PASS

DC_7A-n2A	15	5+20	DFT-64QAM	M+M	Inner_1RB_Left	21.07	1	22.07	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+M	Inner_1RB_Right	20.76	1	21.76	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+M	Inner_Full	20.71	1	21.71	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+M	Outer_Full	19.26	1	20.26	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+M	Inner_1RB_Left	18.3	1	19.3	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+M	Inner_1RB_Right	18.04	1	19.04	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+M	Inner_Full	18.63	1	19.63	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+M	Outer_Full	18.75	1	19.75	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+H	Inner_1RB_Left	22.82	1	23.82	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+H	Inner_1RB_Right	23.19	1	24.19	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+H	Inner_Full	23.1	1	24.1	33	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+H	Outer_Full	23.11	1	24.11	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+H	Inner_1RB_Left	22.91	1	23.91	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+H	Inner_1RB_Right	23.18	1	24.18	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+H	Inner_Full	23.08	1	24.08	33	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	22.12	1	23.12	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+H	Inner_1RB_Left	22.4	1	23.4	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+H	Inner_1RB_Right	22.63	1	23.63	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+H	Inner_Full	22.55	1	23.55	33	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	19.99	1	20.99	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+H	Inner_1RB_Left	21.11	1	22.11	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+H	Inner_1RB_Right	21.28	1	22.28	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+H	Inner_Full	20.76	1	21.76	33	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+H	Outer_Full	19.56	1	20.56	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+H	Inner_1RB_Left	18.38	1	19.38	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+H	Inner_1RB_Right	18.68	1	19.68	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+H	Inner_Full	19.07	1	20.07	33	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+H	Outer_Full	19.14	1	20.14	33	PASS

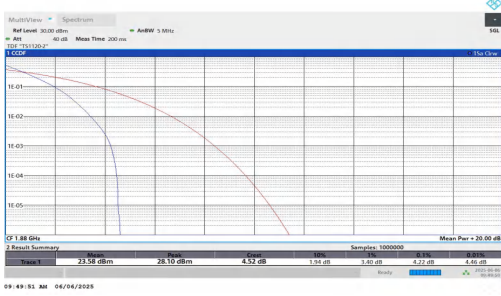
Peak-to-Average Ratio for ENDC

Peak-to-Average Ratio(CCDF)

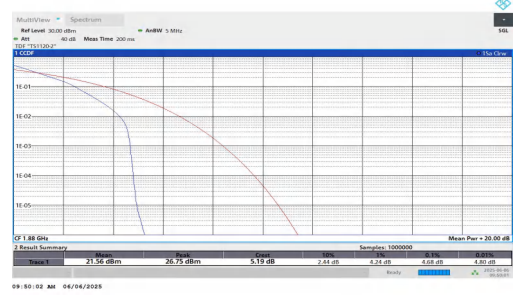
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result(dB)	Limit(dB)	Verdict
DC_7A-n2A	15	5+5	DFT-BPSK	M+M	Outer_Full	4.22	≤13	PASS
DC_7A-n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	4.68	≤13	PASS
DC_7A-n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	5.76	≤13	PASS
DC_7A-n2A	15	5+5	DFT-64QAM	M+M	Outer_Full	6.08	≤13	PASS
DC_7A-n2A	15	5+5	DFT-256QAM	M+M	Outer_Full	6.64	≤13	PASS
DC_7A-n2A	15	5+10	DFT-BPSK	M+M	Outer_Full	4.26	≤13	PASS
DC_7A-n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	4.84	≤13	PASS
DC_7A-n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	5.82	≤13	PASS
DC_7A-n2A	15	5+10	DFT-64QAM	M+M	Outer_Full	6.22	≤13	PASS
DC_7A-n2A	15	5+10	DFT-256QAM	M+M	Outer_Full	6.6	≤13	PASS
DC_7A-n2A	15	5+15	DFT-BPSK	M+M	Outer_Full	4.14	≤13	PASS
DC_7A-n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	5.14	≤13	PASS
DC_7A-n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	6.04	≤13	PASS
DC_7A-n2A	15	5+15	DFT-64QAM	M+M	Outer_Full	6.44	≤13	PASS
DC_7A-n2A	15	5+15	DFT-256QAM	M+M	Outer_Full	6.64	≤13	PASS
DC_7A-n2A	15	5+20	DFT-BPSK	M+M	Outer_Full	4.06	≤13	PASS
DC_7A-n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	5.08	≤13	PASS
DC_7A-n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	6.1	≤13	PASS
DC_7A-n2A	15	5+20	DFT-64QAM	M+M	Outer_Full	6.28	≤13	PASS
DC_7A-n2A	15	5+20	DFT-256QAM	M+M	Outer_Full	6.62	≤13	PASS

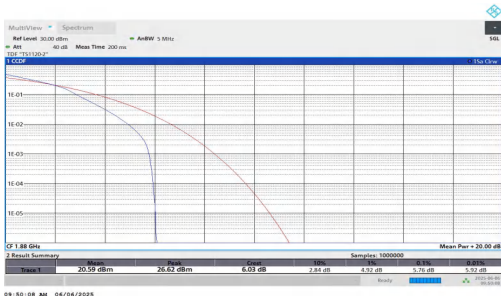
Test Graphs



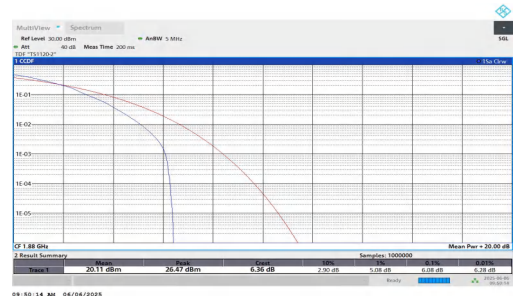
NTVN-DC_7A-n2A-PC3-15-5+5-M+M-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



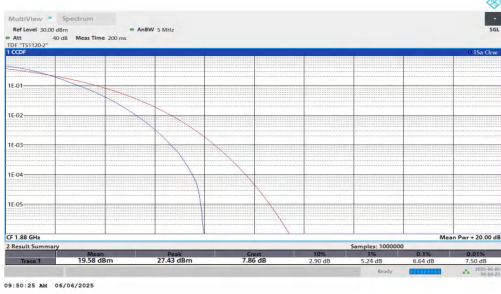
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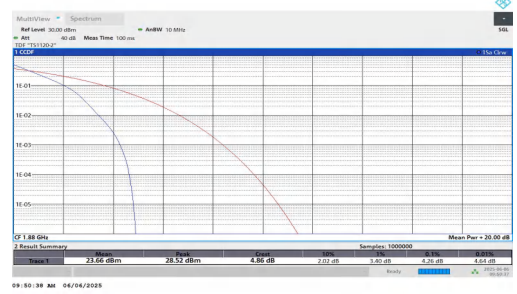
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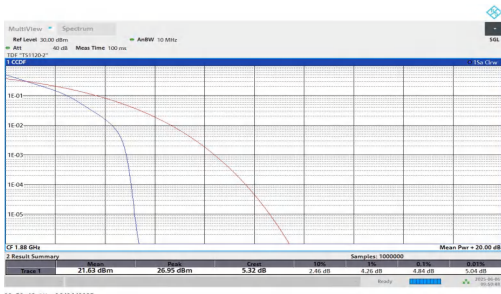
NTVN-DC_7A-n2A-PC3-15-5+5-M+M-QPSK-IDLE-DFT-64QAM-Outer_Full-PASS



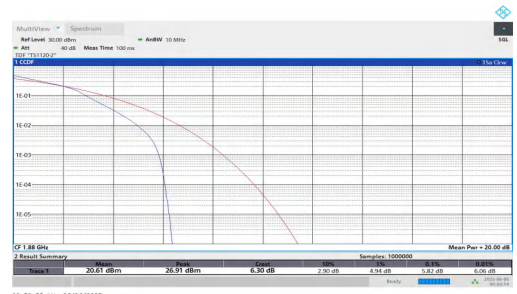
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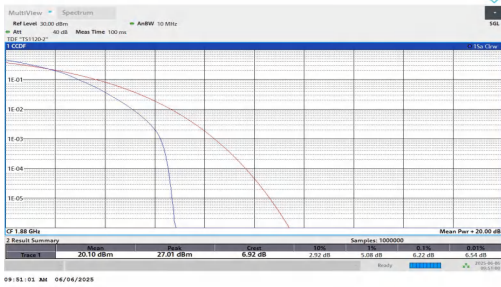
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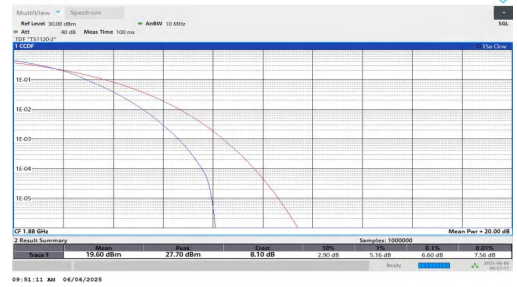
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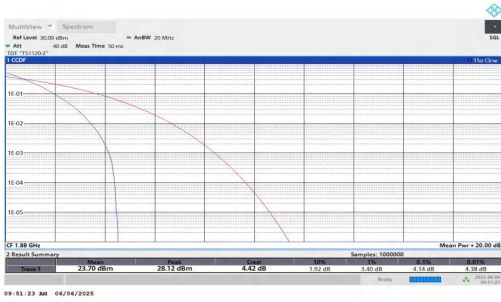
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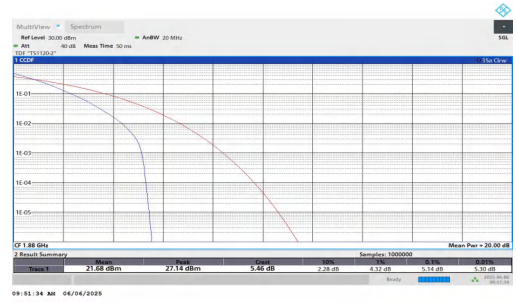
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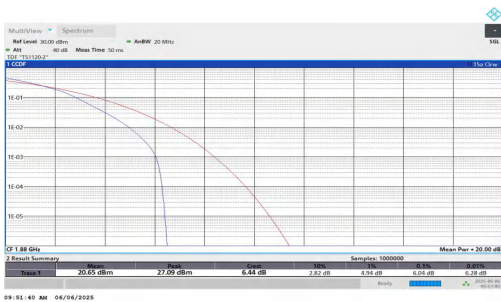
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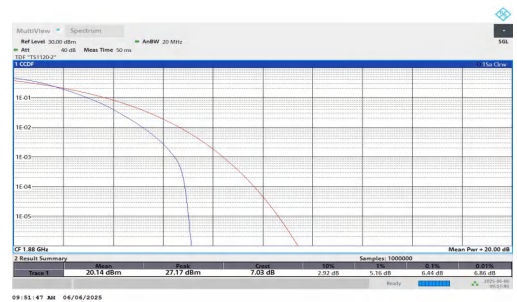
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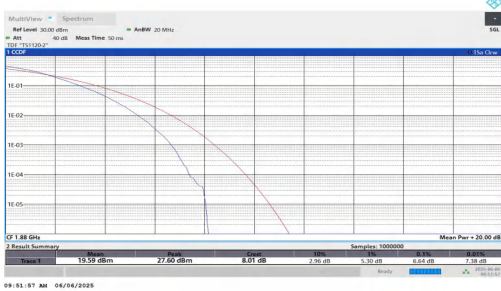
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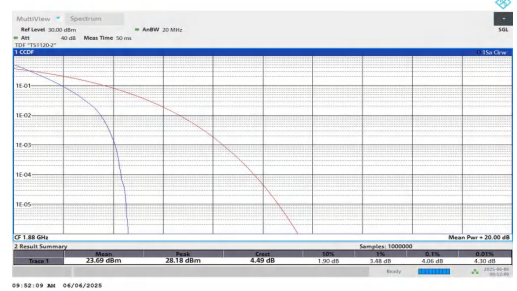
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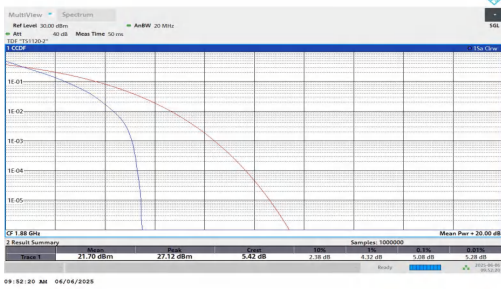
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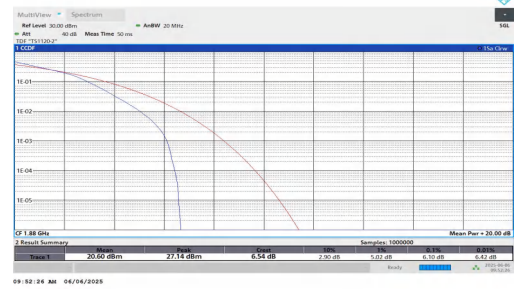
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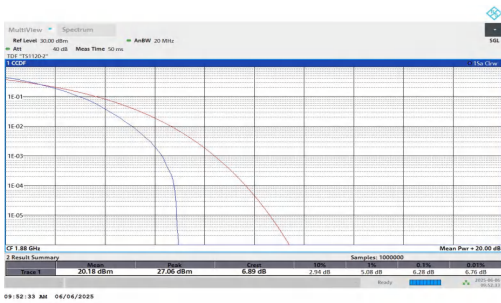
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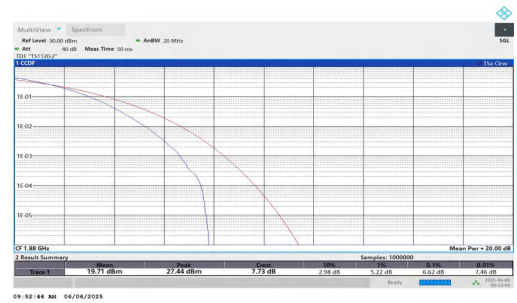
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NTNV-DC_7A-n2A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-16QAM-Outer_Full-PASS



NTNV-DC_7A-n2A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-64QAM-Outer_Full-PASS



NTNV-DC_7A-n2A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-256QAM-Outer_Full-PASS

26dB Bandwidth and Occupied Bandwidth for ENDC

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_7A-n2A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4838	4.944	PASS
DC_7A-n2A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4915	4.994	PASS
DC_7A-n2A	15	5+5	CP-64QAM	M+M	Outer_Full	4.4809	4.960	PASS
DC_7A-n2A	15	5+5	CP-256QAM	M+M	Outer_Full	4.4860	4.942	PASS
DC_7A-n2A	15	5+10	CP-QPSK	M+M	Outer_Full	9.3070	10.08	PASS
DC_7A-n2A	15	5+10	CP-16QAM	M+M	Outer_Full	9.3144	10.08	PASS
DC_7A-n2A	15	5+10	CP-64QAM	M+M	Outer_Full	9.2924	10.01	PASS
DC_7A-n2A	15	5+10	CP-256QAM	M+M	Outer_Full	9.3085	10.02	PASS
DC_7A-n2A	15	5+15	CP-QPSK	M+M	Outer_Full	14.119	15.04	PASS
DC_7A-n2A	15	5+15	CP-16QAM	M+M	Outer_Full	14.106	15.02	PASS
DC_7A-n2A	15	5+15	CP-64QAM	M+M	Outer_Full	14.119	21.39	PASS
DC_7A-n2A	15	5+15	CP-256QAM	M+M	Outer_Full	14.134	15.02	PASS
DC_7A-n2A	15	5+20	CP-QPSK	M+M	Outer_Full	18.923	22.00	PASS
DC_7A-n2A	15	5+20	CP-16QAM	M+M	Outer_Full	18.937	28.57	PASS
DC_7A-n2A	15	5+20	CP-64QAM	M+M	Outer_Full	18.957	19.95	PASS
DC_7A-n2A	15	5+20	CP-256QAM	M+M	Outer_Full	18.944	19.96	PASS

Test Graphs



NTNV-DC_7A-n2A-PC3-15-5+5-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-Ant1-PASS



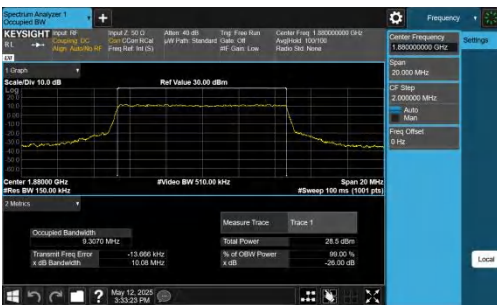
NTNV-DC_7A-n2A-PC3-15-5+5-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+5-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+5-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+10-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+10-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+10-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+10-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+15-M+M-QPSK-IDLE-CP-QPSK-Out er_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+15-M+M-QPSK-IDLE-CP-16QAM-Out er_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+15-M+M-QPSK-IDLE-CP-64QAM-Out er_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+15-M+M-QPSK-IDLE-CP-256QAM-Out er_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+20-M+M-QPSK-IDLE-CP-QPSK-Out er_Full-Ant1-PASS



NTNV-DC_7A-n2A-PC3-15-5+20-M+M-QPSK-IDLE-CP-16QAM-Out er_Full-Ant1-PASS



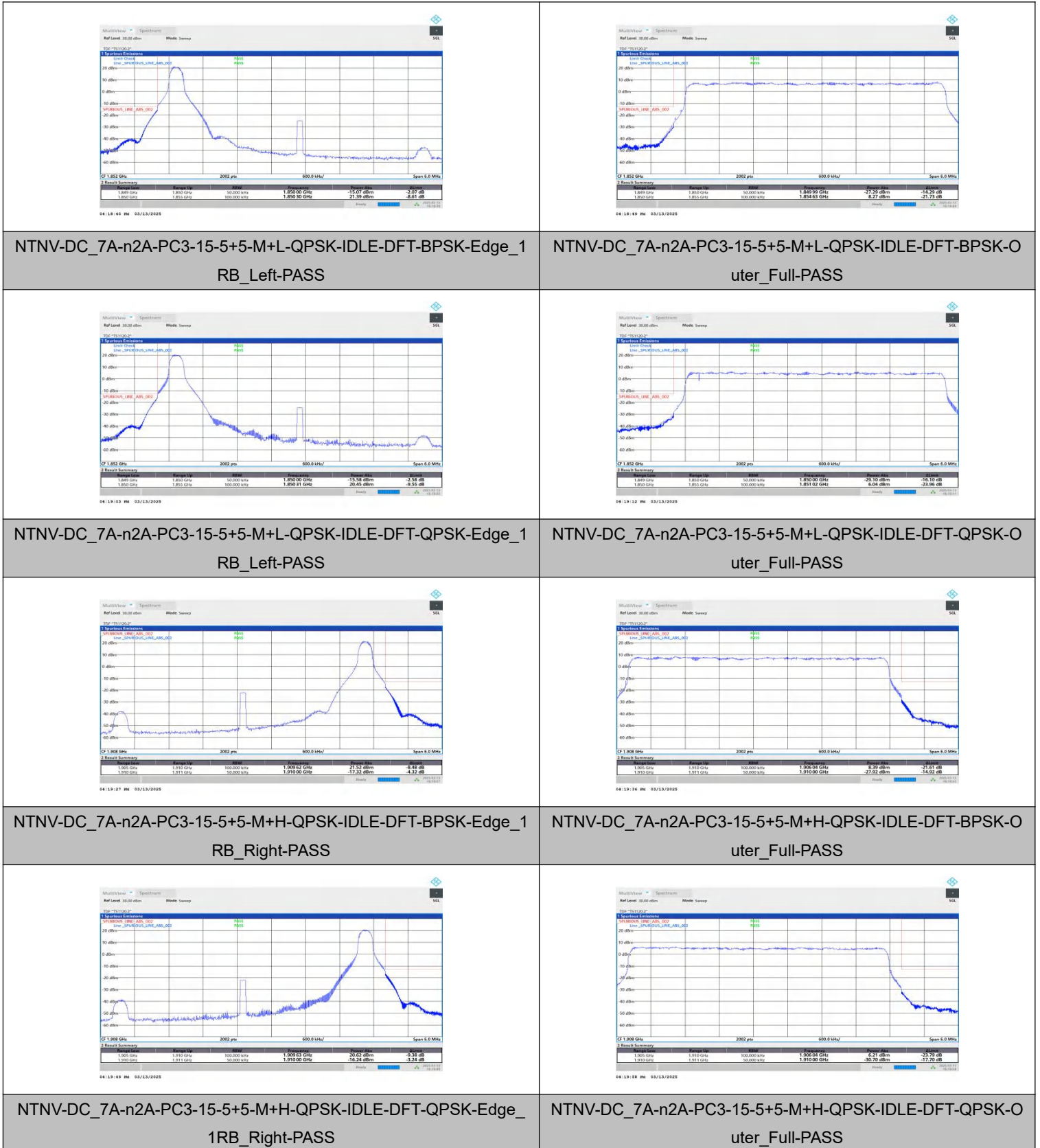
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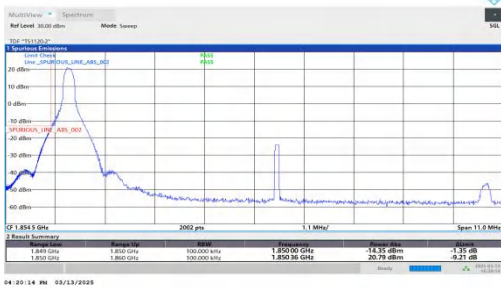


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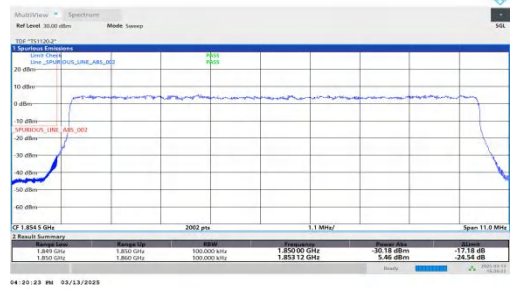
Band Edge for ENDC

Test Graphs

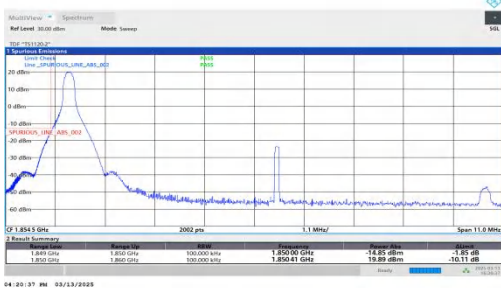




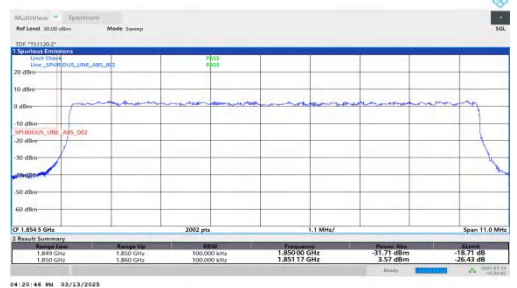
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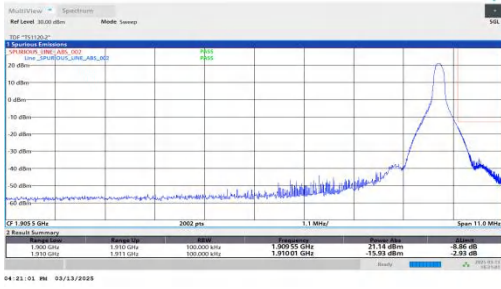
NTNV-DC_7A-n2A-PC3-15-5+10-M+L-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



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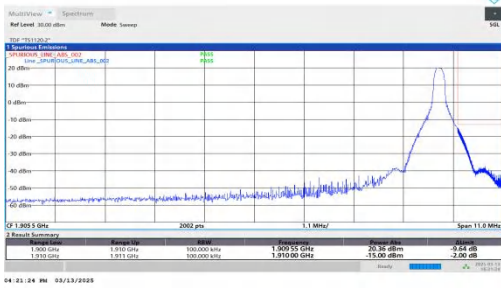
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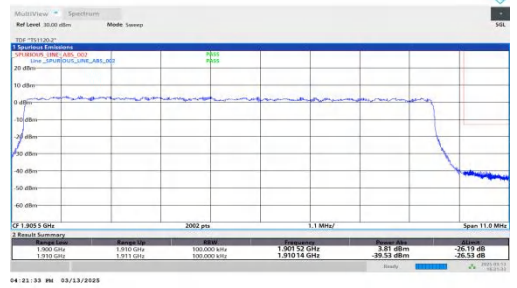
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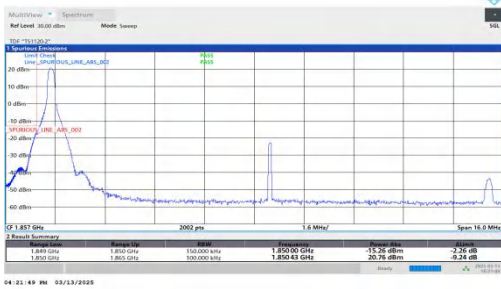
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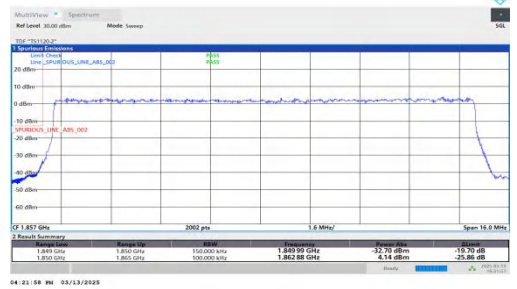
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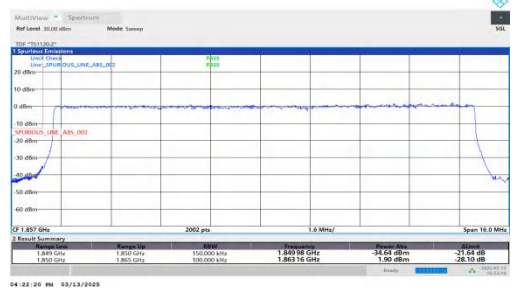
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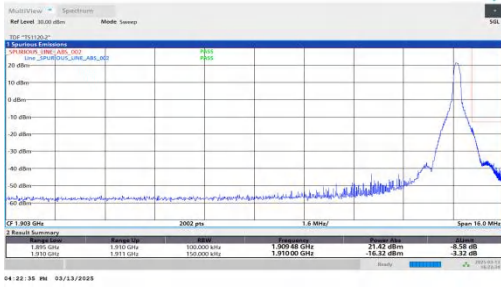
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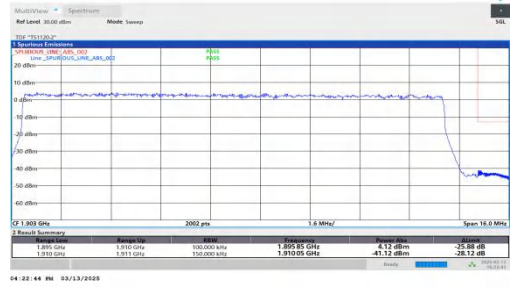
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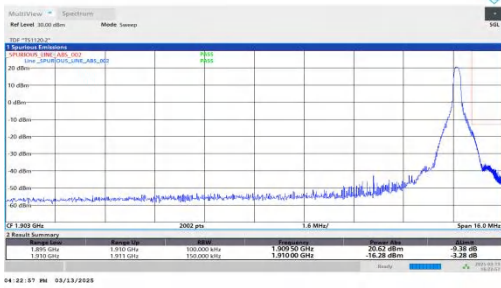
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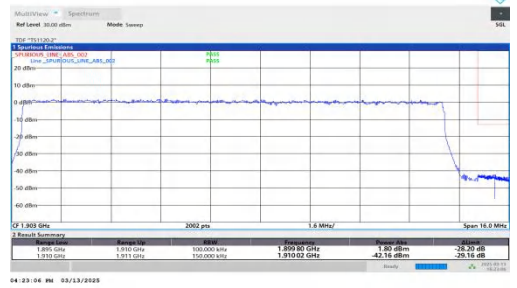
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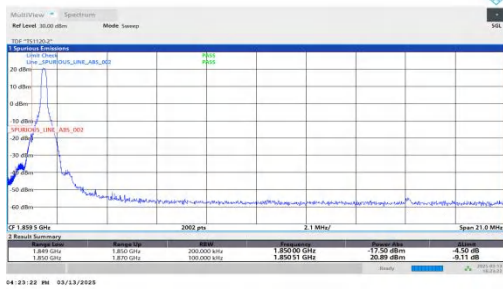
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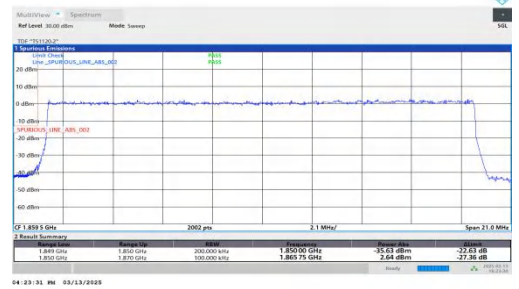
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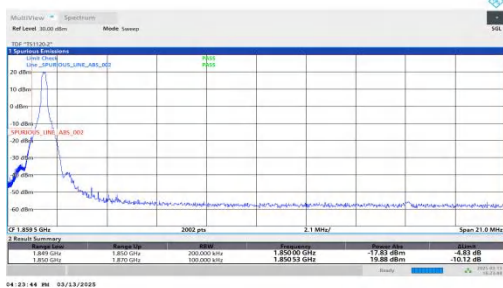
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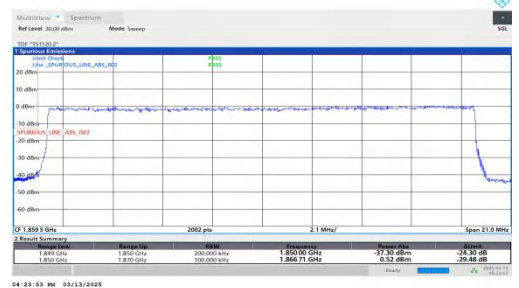
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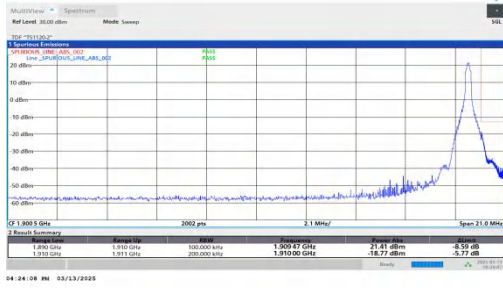
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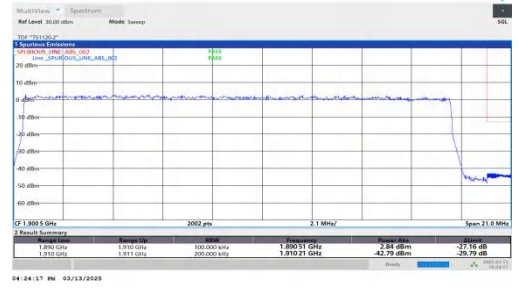
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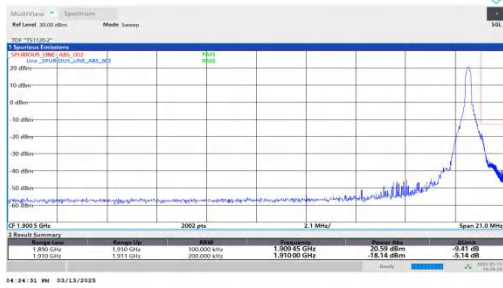
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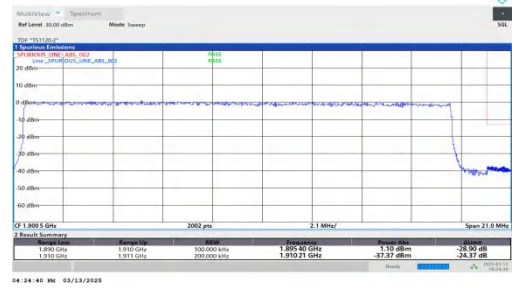
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NTNV-DC_7A-n2A-PC3-15-5+20-M+H-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



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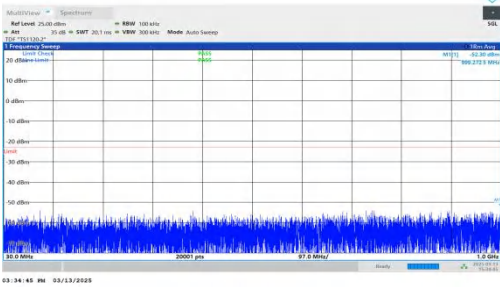


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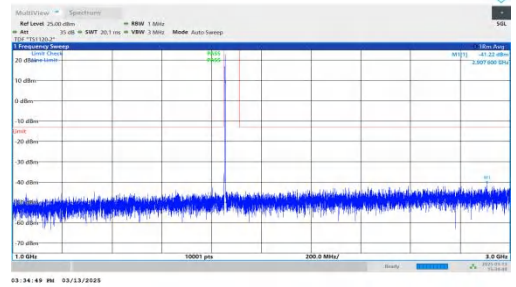
Conducted Spurious Emission for ENDC

Test Graphs

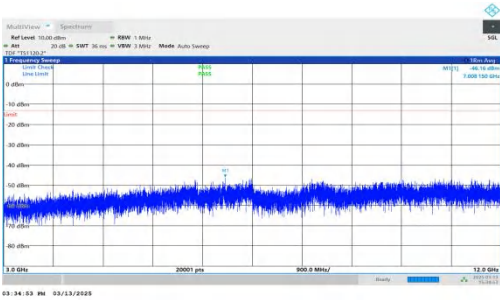
<p>NTNV-DC_7A-n2A-PC3-15-5+5-M+L-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-0.009-0.15-PASS</p>	<p>NTNV-DC_7A-n2A-PC3-15-5+5-M+L-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-0.15-30-PASS</p>
<p>NTNV-DC_7A-n2A-PC3-15-5+5-M+L-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-30-1000-PASS</p>	<p>NTNV-DC_7A-n2A-PC3-15-5+5-M+L-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-1000-3000-PASS</p>
<p>NTNV-DC_7A-n2A-PC3-15-5+5-M+L-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-3000-12000-PASS</p>	<p>NTNV-DC_7A-n2A-PC3-15-5+5-M+L-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-12000-20000-PASS</p>
<p>NTNV-DC_7A-n2A-PC3-15-5+5-M+L-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-0.009-0.15-PASS</p>	<p>NTNV-DC_7A-n2A-PC3-15-5+5-M+L-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-0.15-30-PASS</p>



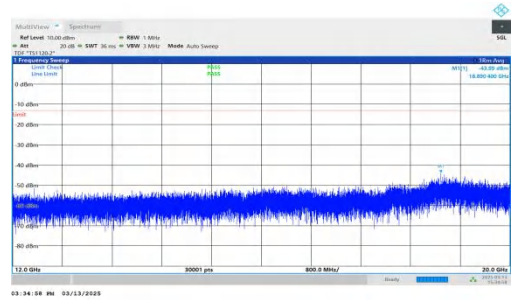
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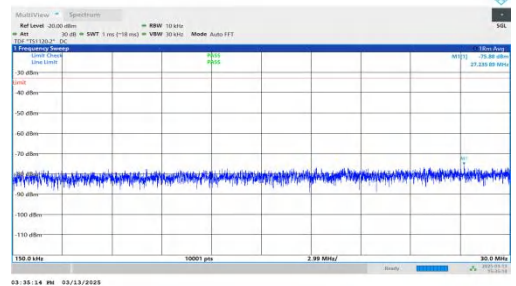
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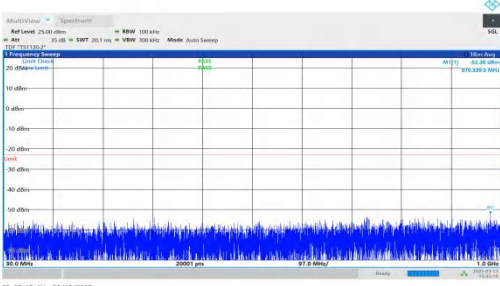
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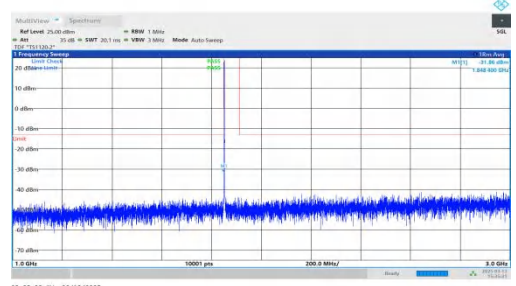
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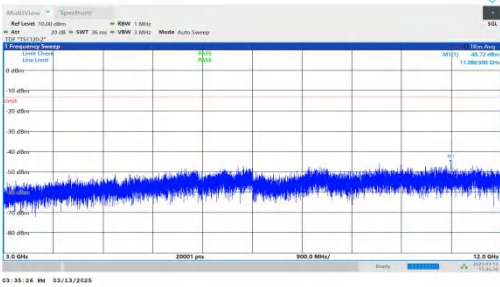
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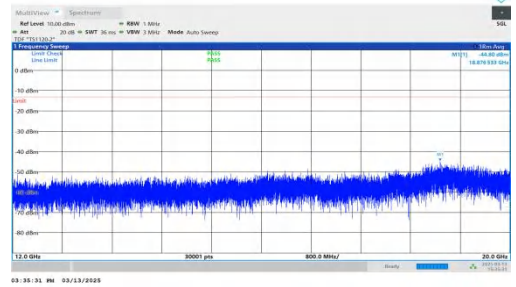
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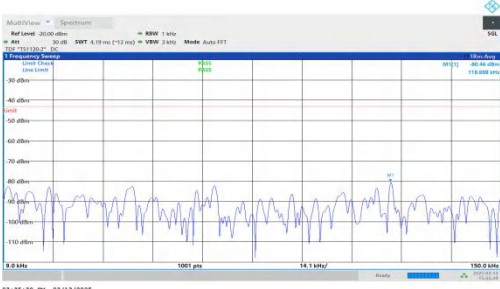
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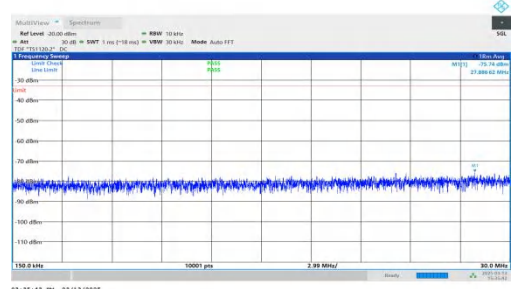
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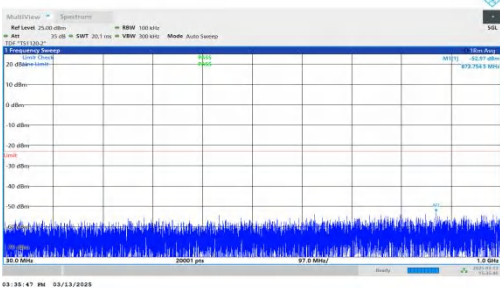
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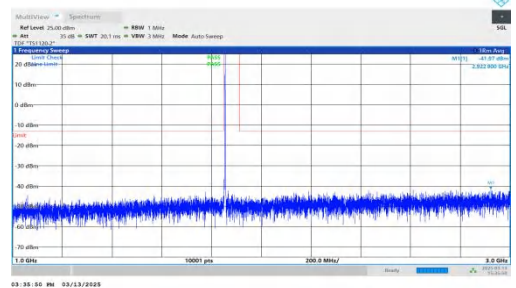
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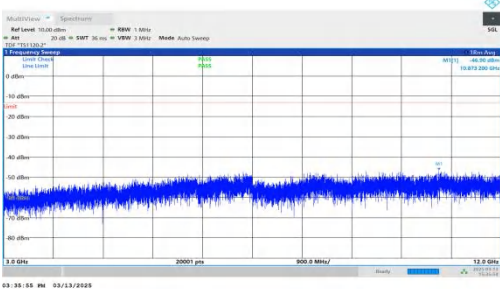
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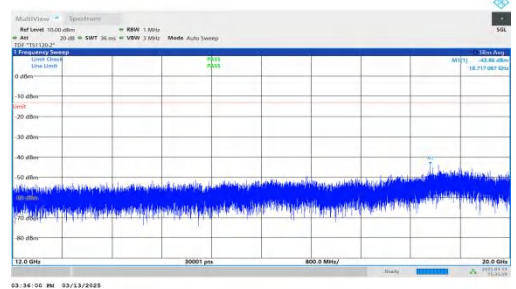
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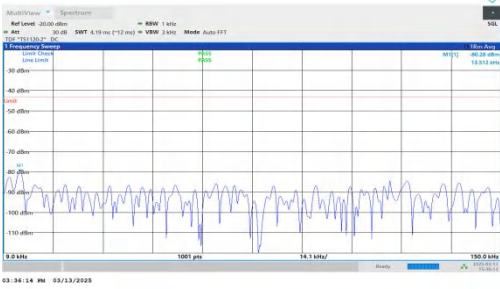
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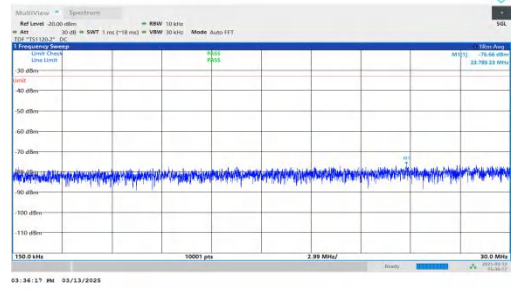
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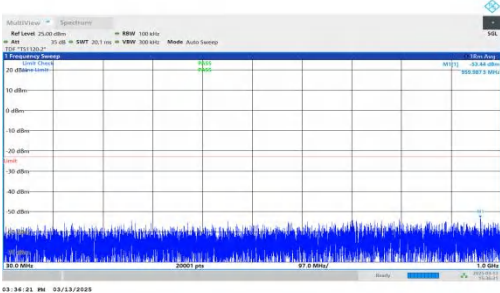
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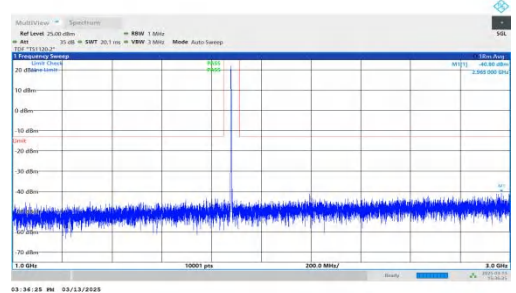
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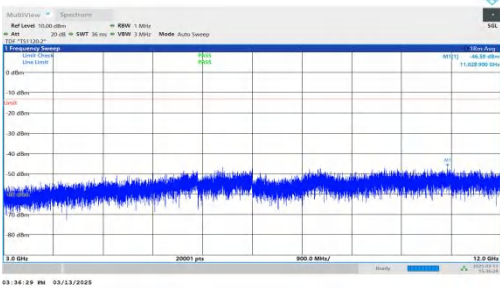
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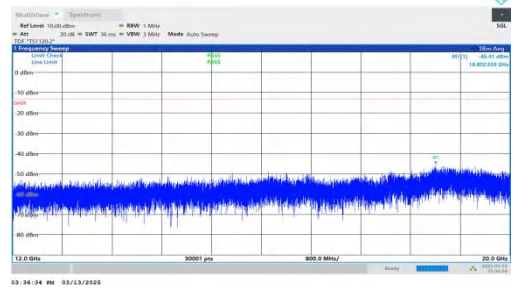
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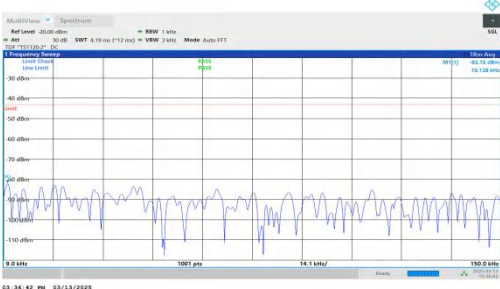
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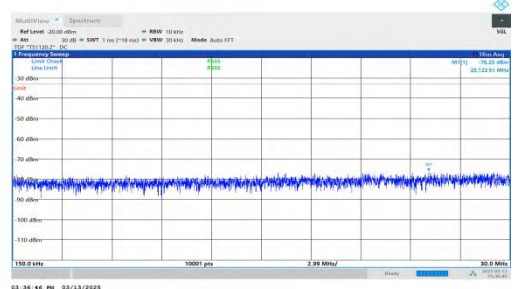
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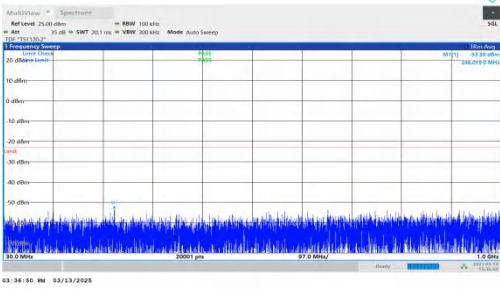
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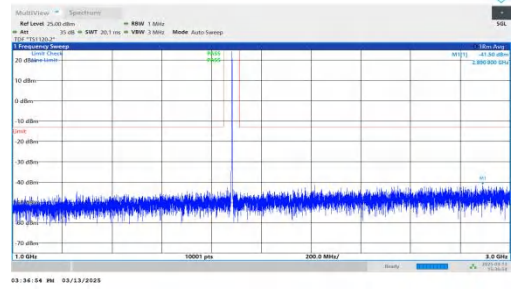
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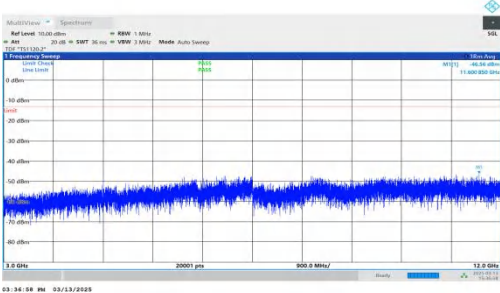
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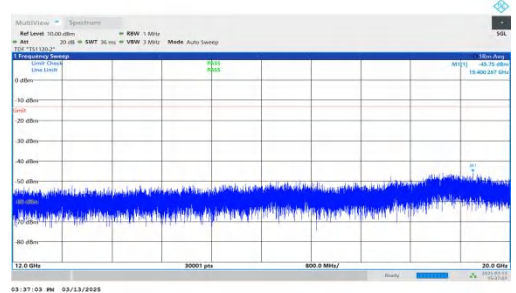
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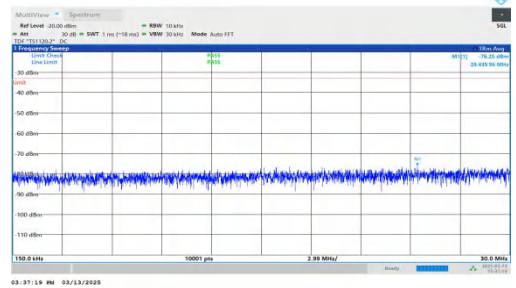
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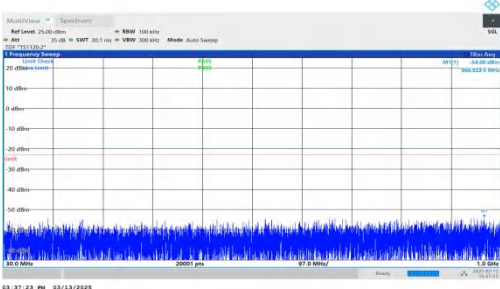
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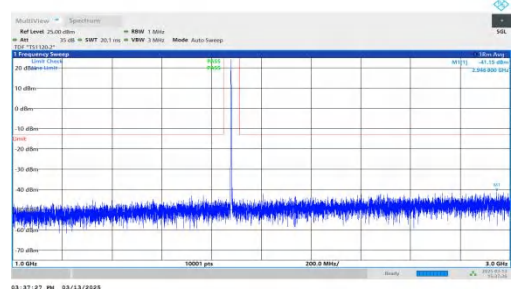
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NTNV-DC_7A-n2A-PC3-15-5+5-M+M-QPSK-IDLE-DFT-QPSK-Inner_1RB_Left-0.15-30-PASS



NTNV-DC_7A-n2A-PC3-15-5+5-M+M-QPSK-IDLE-DFT-QPSK-Inner_1RB_Left-30-1000-PASS



NTNV-DC_7A-n2A-PC3-15-5+5-M+M-QPSK-IDLE-DFT-QPSK-Inner_1RB_Left-1000-3000-PASS