

Appendix for 2A_n41A

Product Name: 5G CPE

Model No: C150

Appendix A: Average Power Output for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	25.83	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Inner_Full	26.03	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	25.37	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	25.21	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	25.89	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Inner_Full	25.87	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	25.13	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	24.91	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	24.97	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Inner_Full	24.91	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	24.70	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	24.26	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	24.96	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Inner_Full	24.97	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	24.48	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	23.95	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	23.79	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Inner_Full	23.91	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	23.66	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	23.33	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	24.09	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Inner_Full	23.92	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	23.63	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	23.54	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	25.91	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Inner_Full	25.71	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	24.91	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	25.39	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	25.86	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Inner_Full	25.74	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	24.85	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	25.24	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	25.26	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Inner_Full	25.08	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	25.63	PC3	PASS

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DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	24.19	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	25.17	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Inner_Full	25.14	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	25.18	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	24.14	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	24.45	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Inner_Full	24.15	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	24.05	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	23.97	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	24.25	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Inner_Full	24.05	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	23.40	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	23.92	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	25.60	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Inner_Full	25.28	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	25.12	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	24.82	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	25.62	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Inner_Full	25.32	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	25.11	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	24.81	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	24.89	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Inner_Full	24.78	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	25.14	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	23.67	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	24.75	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Inner_Full	24.69	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	24.82	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	23.43	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	24.08	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Inner_Full	23.70	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	23.36	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	23.62	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	24.11	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Inner_Full	23.85	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	23.72	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	23.15	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	25.46	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Inner_Full	25.50	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	24.62	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	23.99	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	26.06	PC3	PASS

DC_2A_n41A	30	5+100	CP-16QAM	M+L	Inner_Full	25.54	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	24.62	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	23.99	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	24.98	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Inner_Full	24.77	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	24.84	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	23.51	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Outer_Full	24.83	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Inner_Full	24.88	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	24.86	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	23.20	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	23.99	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Inner_Full	23.88	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	24.04	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	23.13	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Outer_Full	23.99	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Inner_Full	23.90	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Edge_1RB_Left	24.31	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Edge_1RB_Right	23.48	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

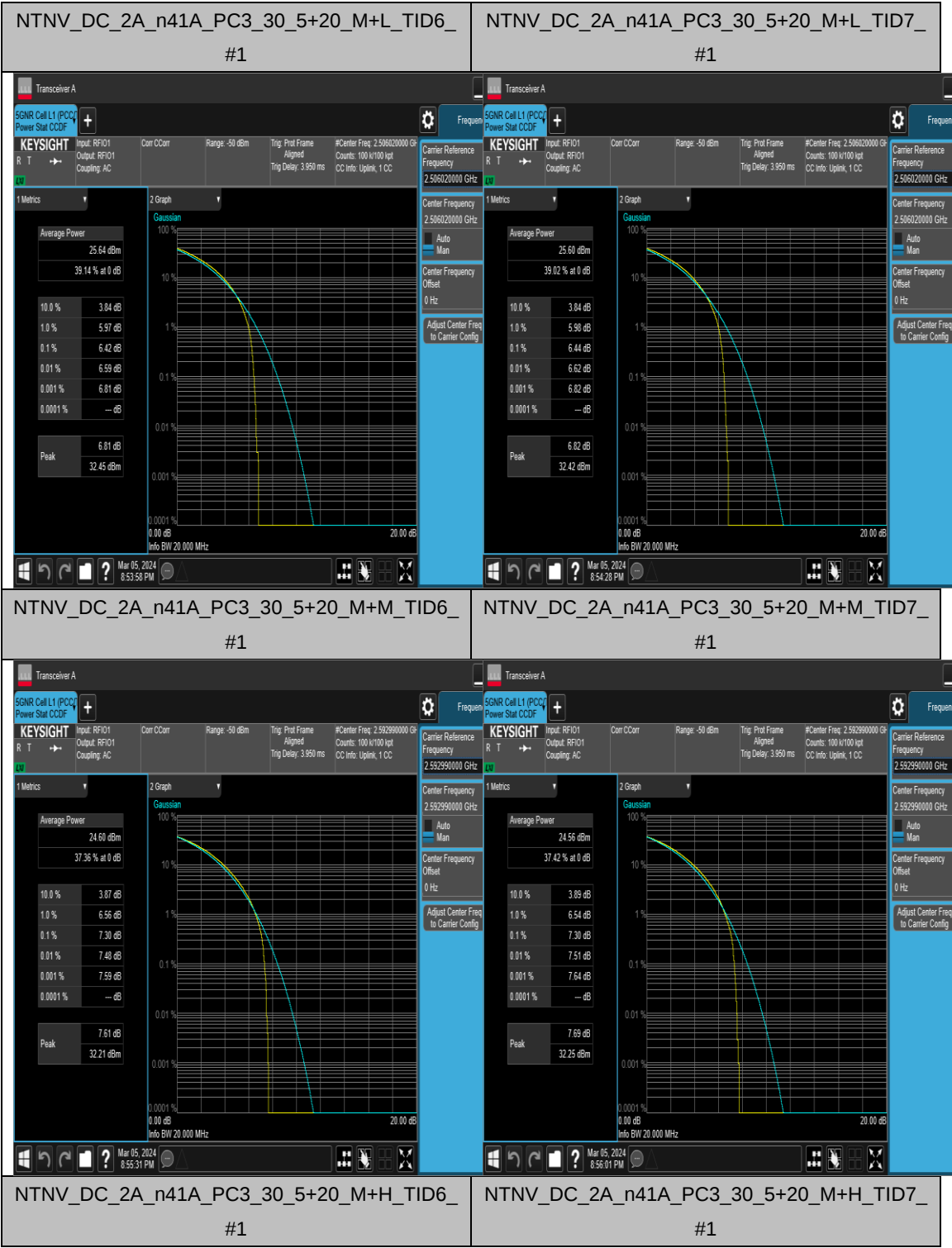
Peak-to-Average Ratio(CCDF)

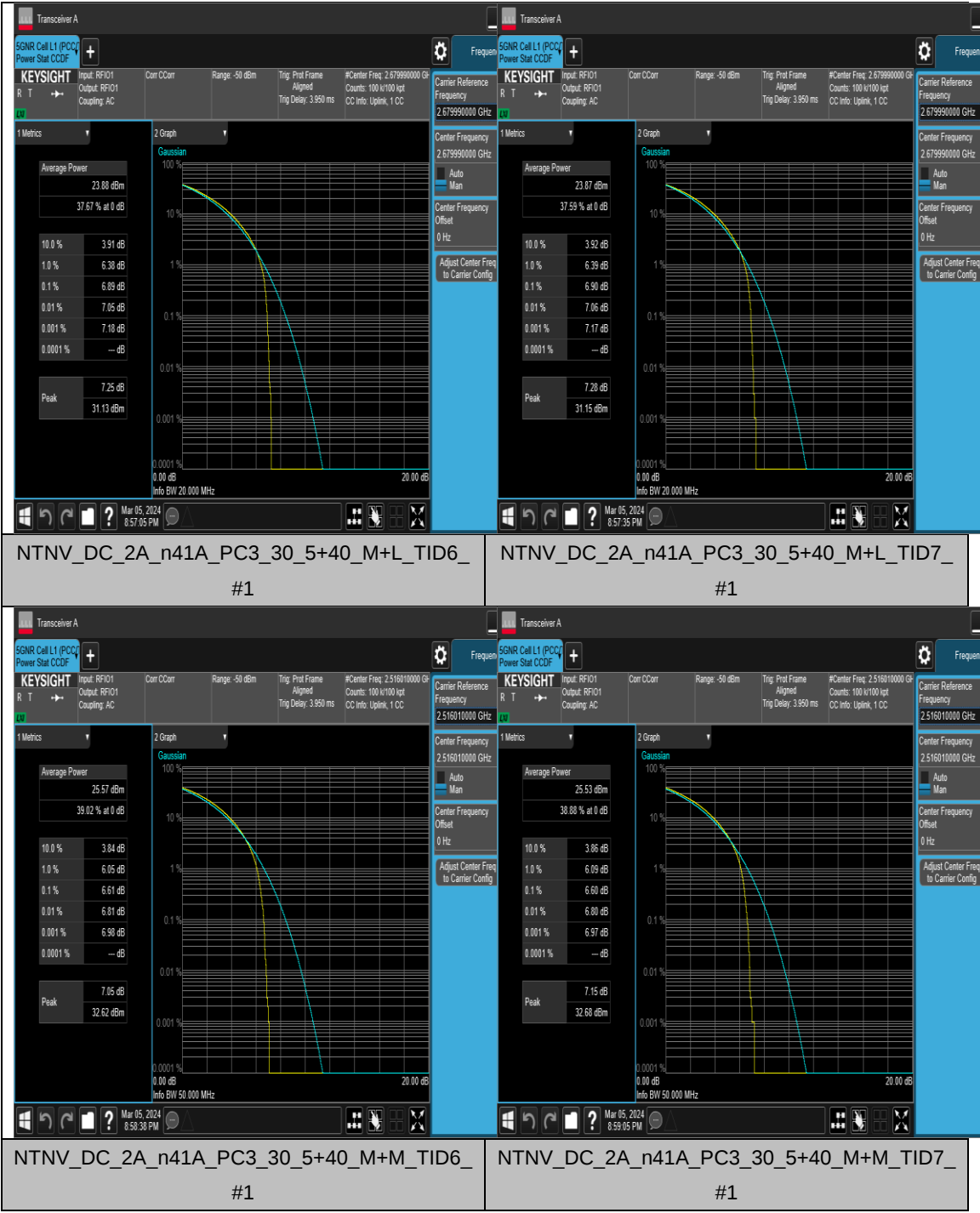
Test Result

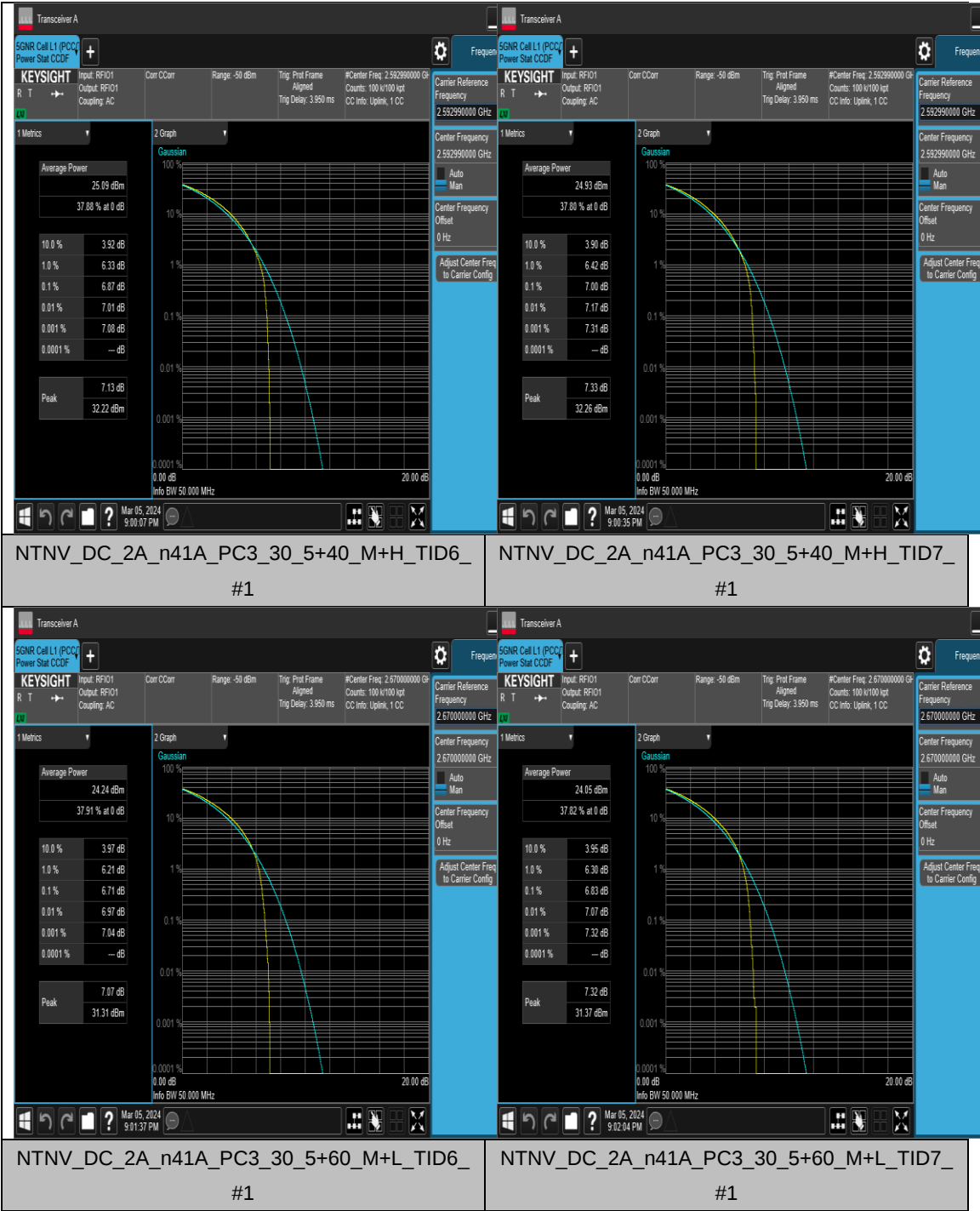
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n4 1A	30	5+20	CP-QPSK	M+L	Outer_Full	6.42	≤13	PASS
DC_2A_n4 1A	30	5+20	CP-16QAM	M+L	Outer_Full	6.44	≤13	PASS
DC_2A_n4 1A	30	5+20	CP-QPSK	M+M	Outer_Full	7.30	≤13	PASS
DC_2A_n4 1A	30	5+20	CP-16QAM	M+M	Outer_Full	7.30	≤13	PASS
DC_2A_n4 1A	30	5+20	CP-QPSK	M+H	Outer_Full	6.89	≤13	PASS
DC_2A_n4 1A	30	5+20	CP-16QAM	M+H	Outer_Full	6.90	≤13	PASS
DC_2A_n4 1A	30	5+40	CP-QPSK	M+L	Outer_Full	6.61	≤13	PASS
DC_2A_n4 1A	30	5+40	CP-16QAM	M+L	Outer_Full	6.60	≤13	PASS
DC_2A_n4 1A	30	5+40	CP-QPSK	M+M	Outer_Full	6.87	≤13	PASS
DC_2A_n4 1A	30	5+40	CP-16QAM	M+M	Outer_Full	7.00	≤13	PASS
DC_2A_n4 1A	30	5+40	CP-QPSK	M+H	Outer_Full	6.71	≤13	PASS
DC_2A_n4 1A	30	5+40	CP-16QAM	M+H	Outer_Full	6.83	≤13	PASS
DC_2A_n4 1A	30	5+60	CP-QPSK	M+L	Outer_Full	6.88	≤13	PASS
DC_2A_n4 1A	30	5+60	CP-16QAM	M+L	Outer_Full	6.90	≤13	PASS
DC_2A_n4 1A	30	5+60	CP-QPSK	M+M	Outer_Full	7.19	≤13	PASS
DC_2A_n4 1A	30	5+60	CP-16QAM	M+M	Outer_Full	7.11	≤13	PASS
DC_2A_n4 1A	30	5+60	CP-QPSK	M+H	Outer_Full	7.10	≤13	PASS

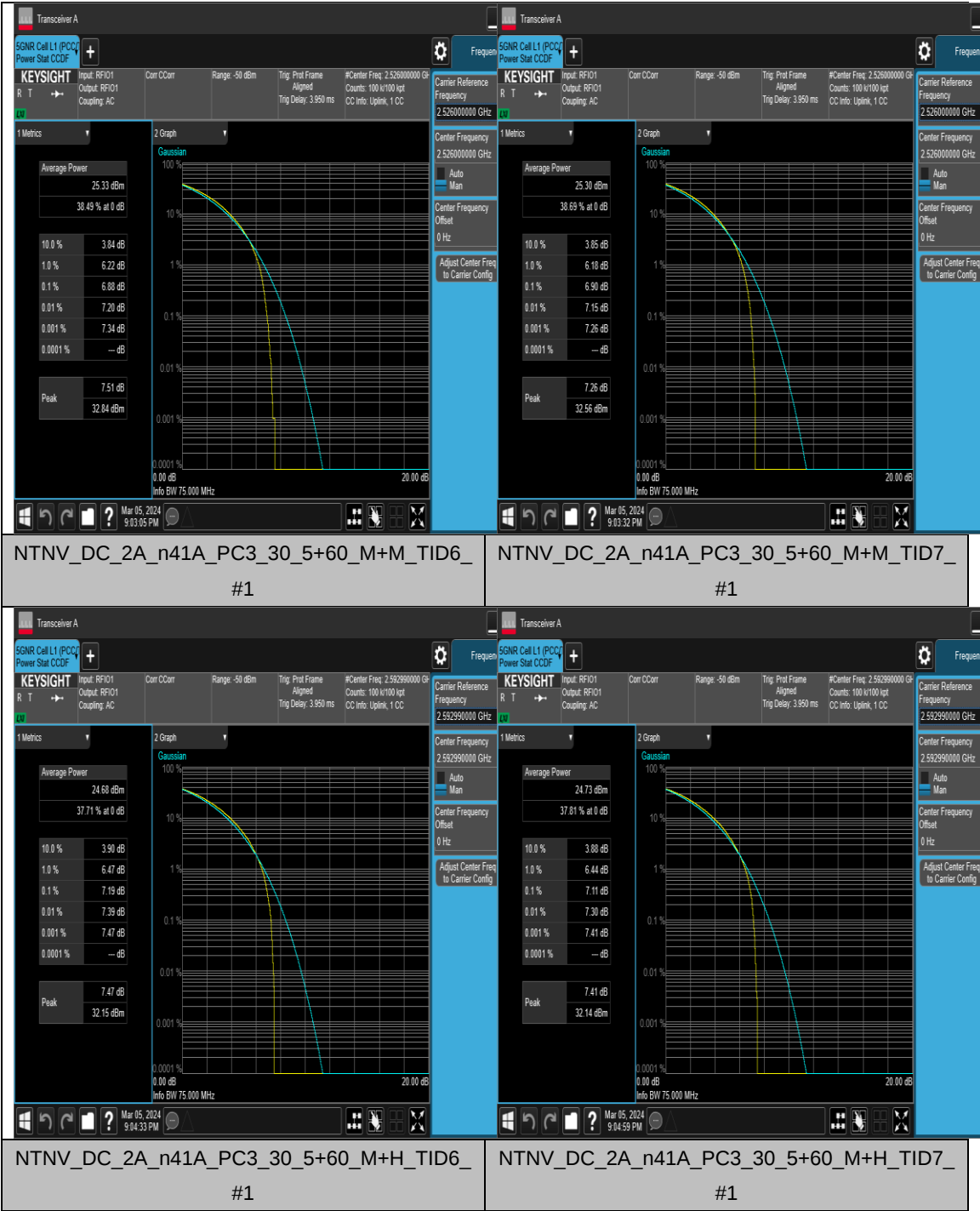
DC_2A_n4 1A	30	5+60	CP-16QAM	M+H	Outer_Full	6.97	≤13	PASS
DC_2A_n4 1A	30	5+100	CP-QPSK	M+L	Outer_Full	7.12	≤13	PASS
DC_2A_n4 1A	30	5+100	CP-16QAM	M+L	Outer_Full	7.13	≤13	PASS
DC_2A_n4 1A	30	5+100	CP-QPSK	M+M	Outer_Full	7.21	≤13	PASS
DC_2A_n4 1A	30	5+100	CP-16QAM	M+M	Outer_Full	7.21	≤13	PASS
DC_2A_n4 1A	30	5+100	CP-QPSK	M+H	Outer_Full	7.35	≤13	PASS
DC_2A_n4 1A	30	5+100	CP-16QAM	M+H	Outer_Full	7.49	≤13	PASS

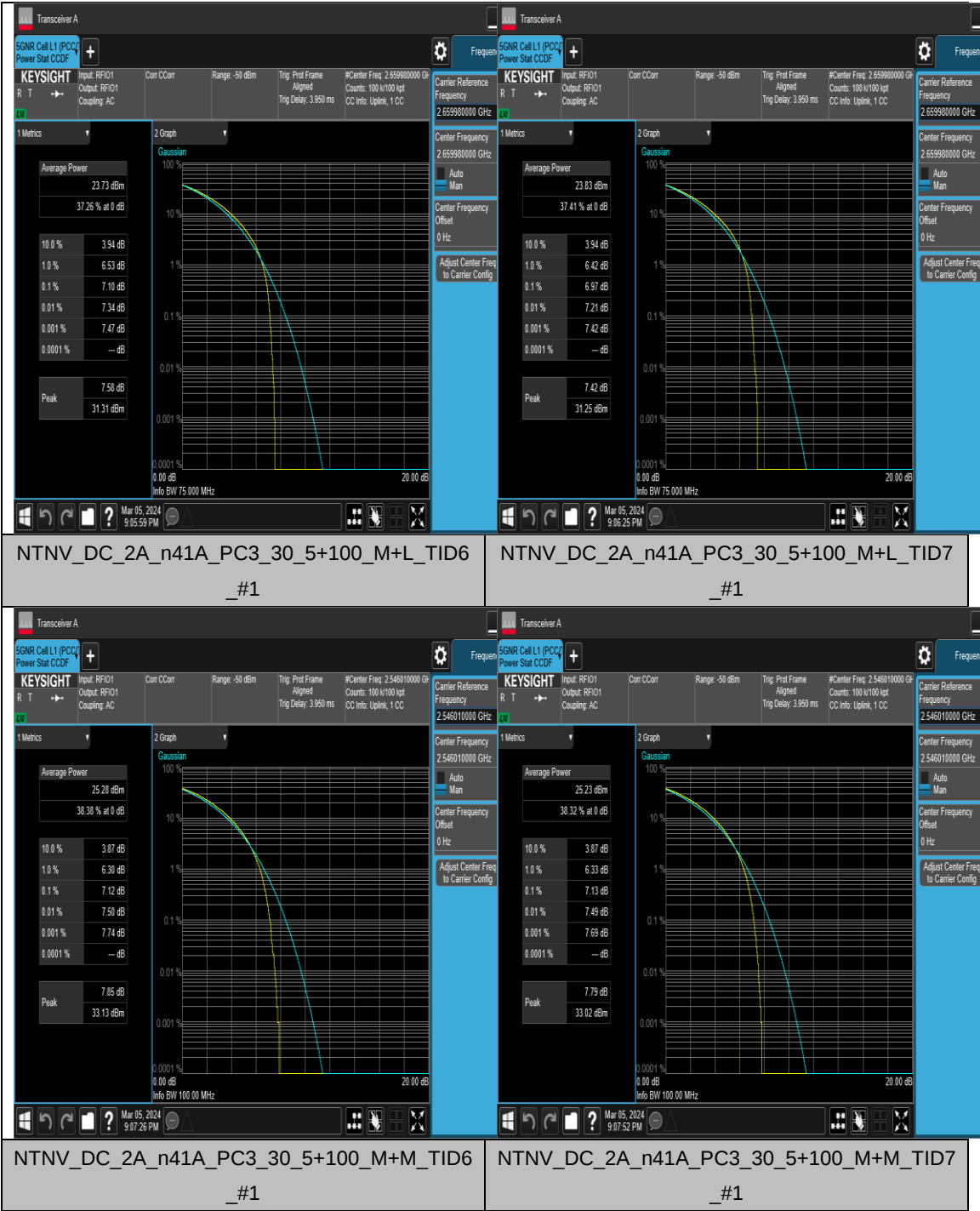
Test Graphs

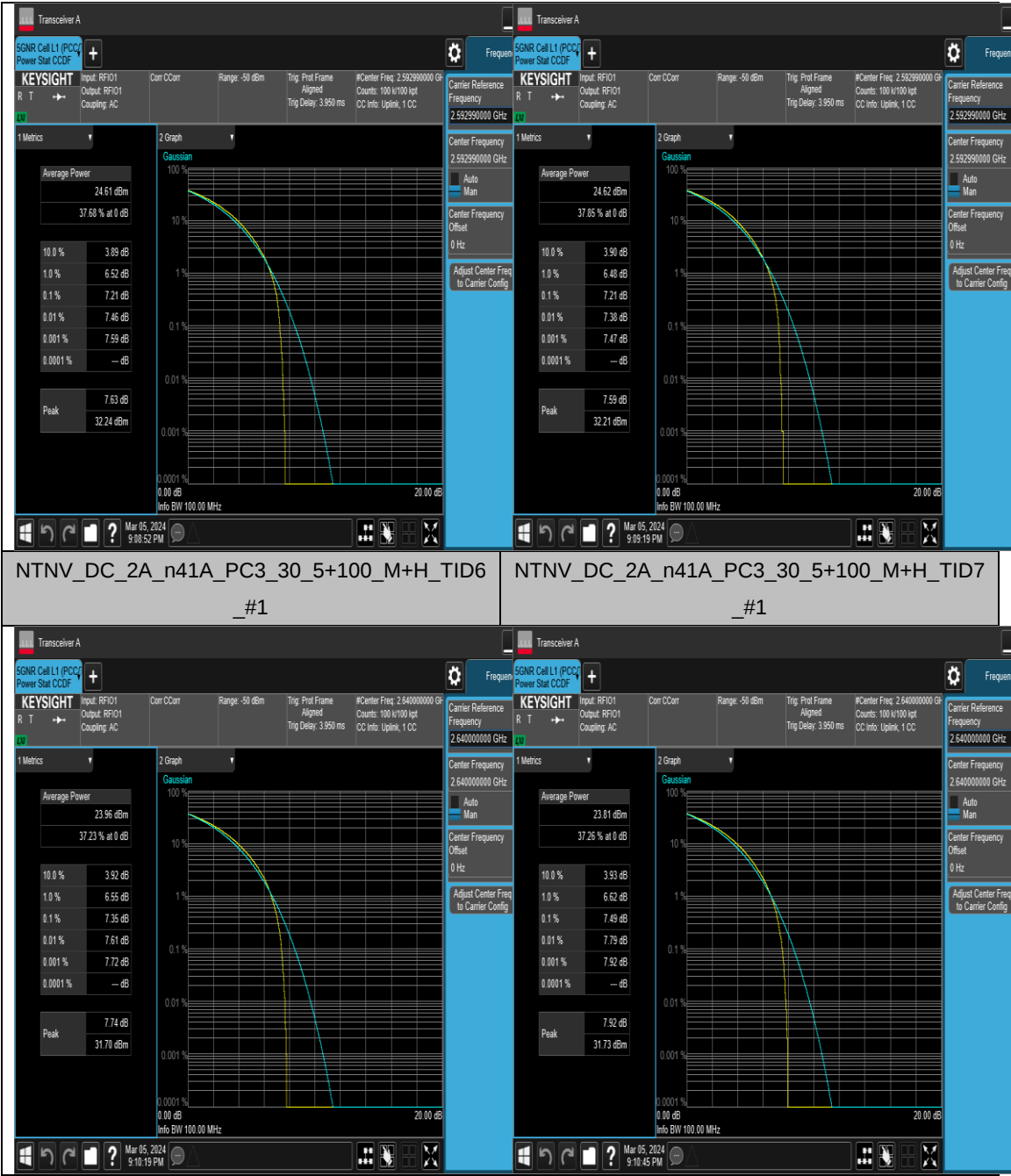












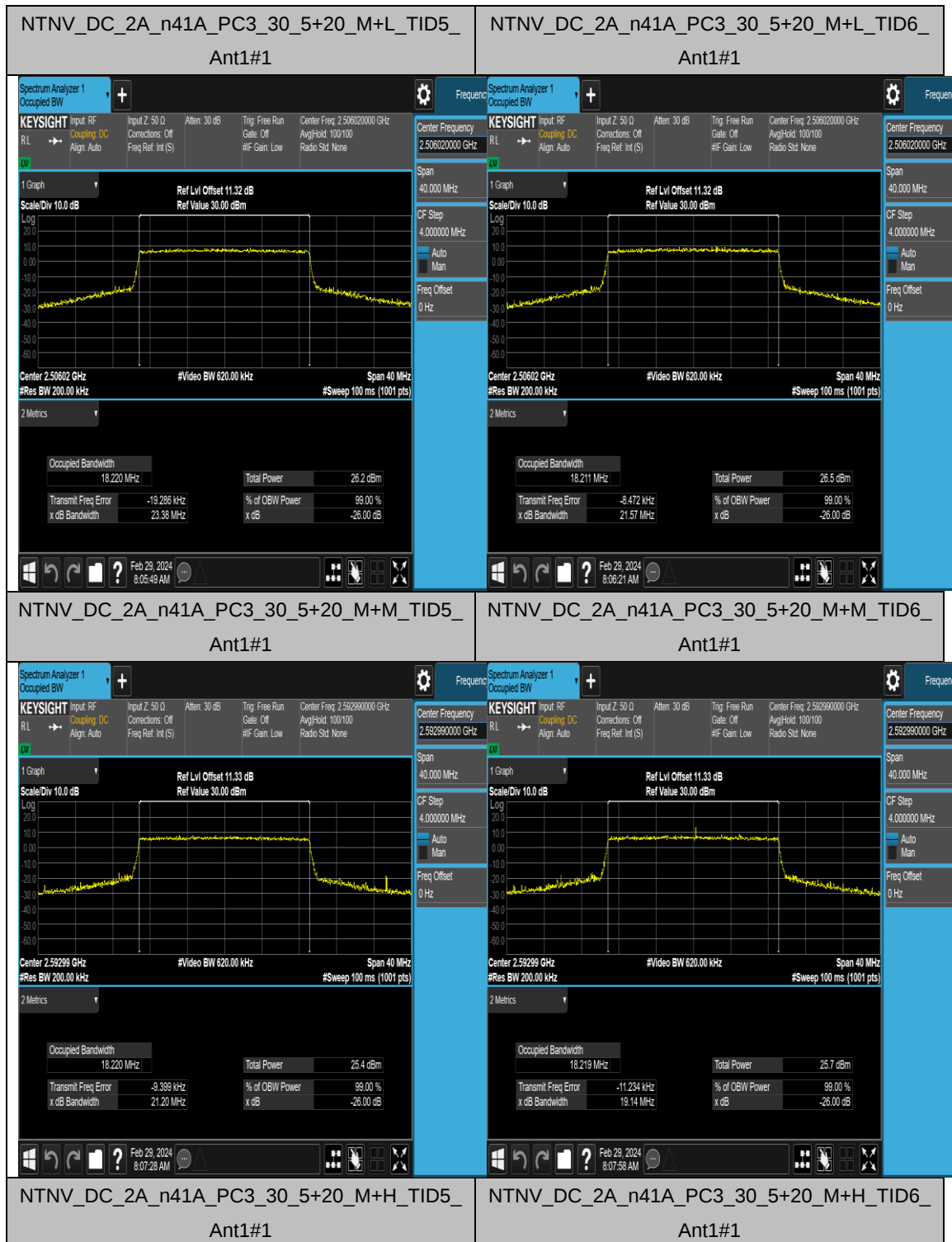
Appendix C: 26dB Bandwidth and Occupied Bandwidth for

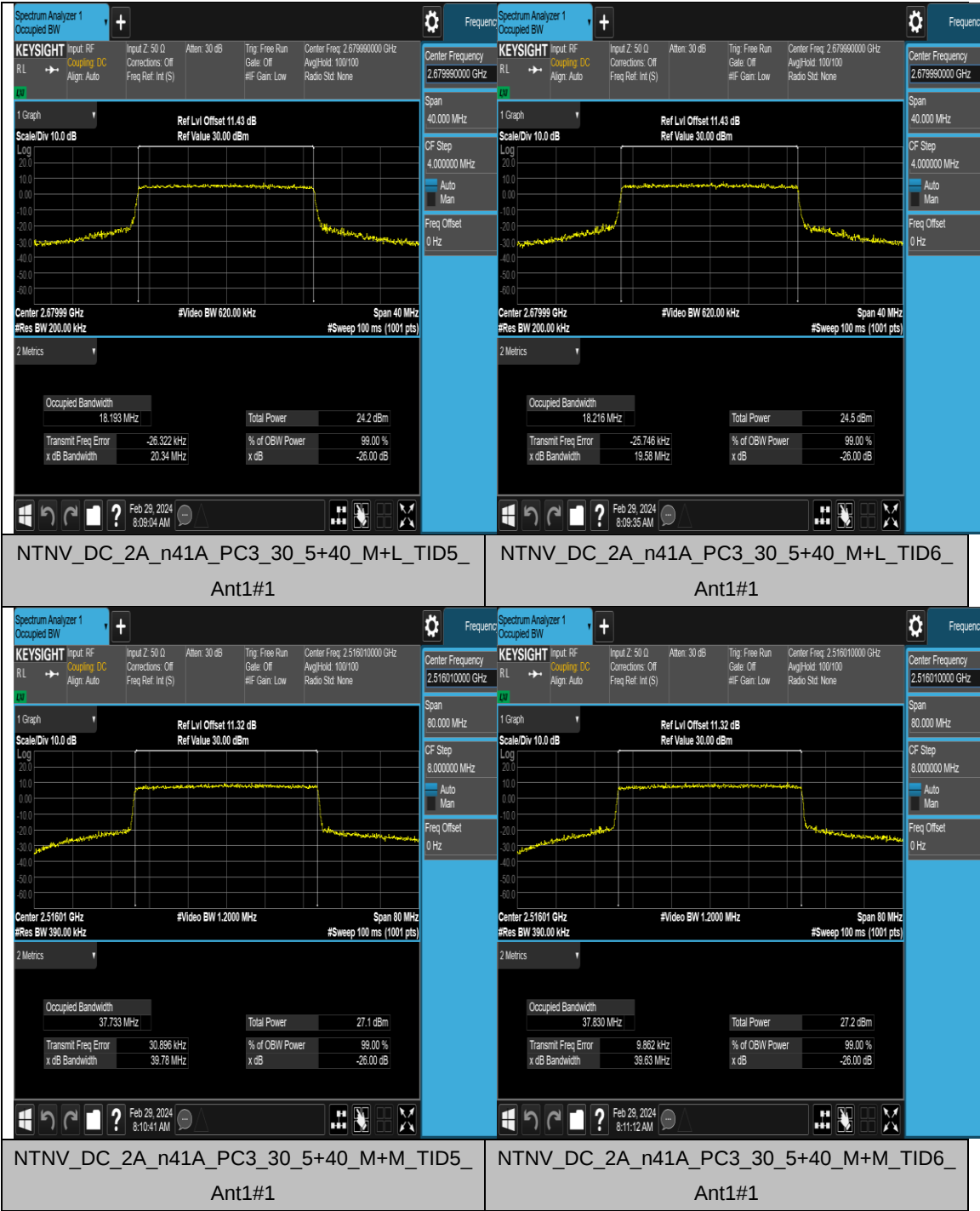
NSA

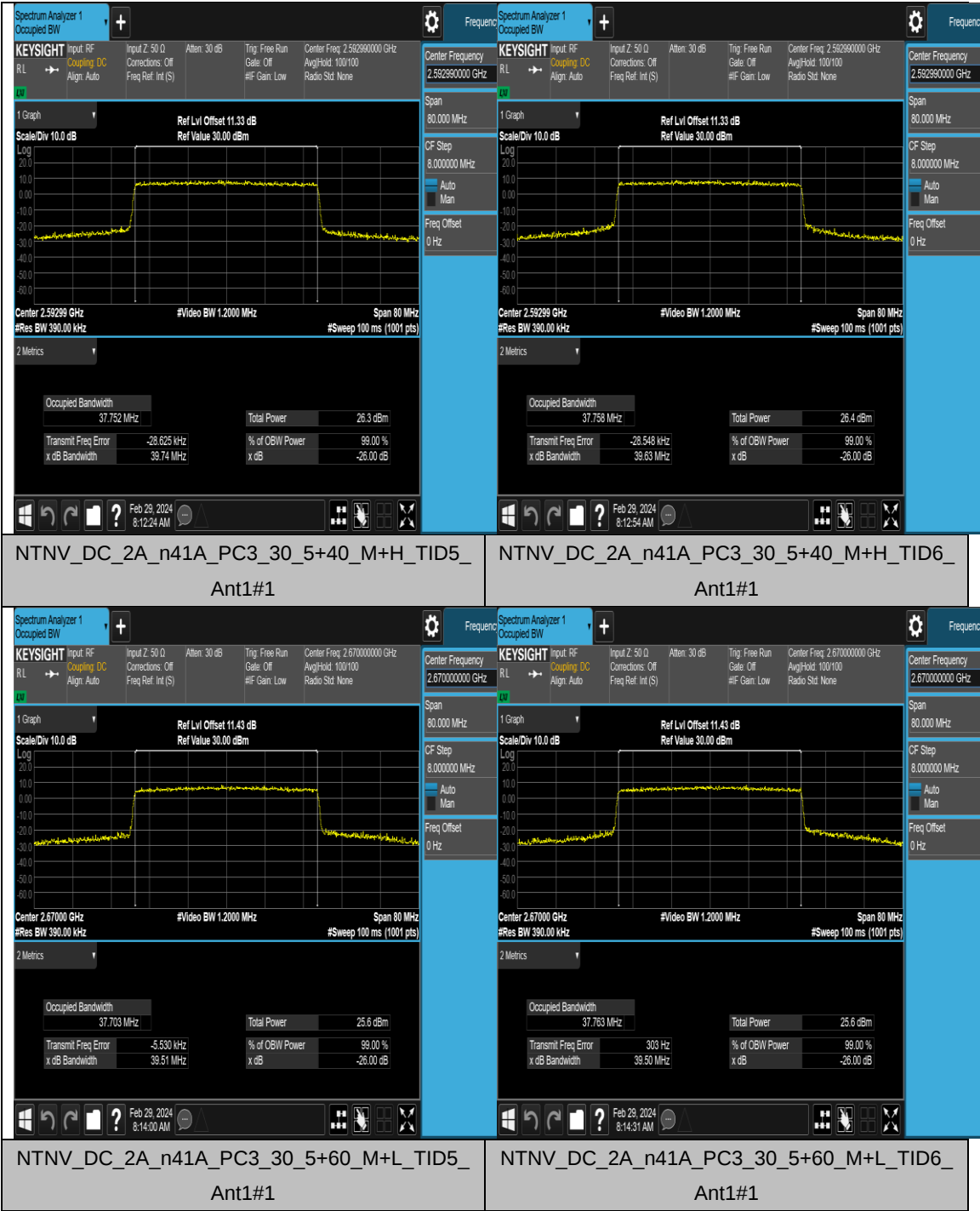
Test Result

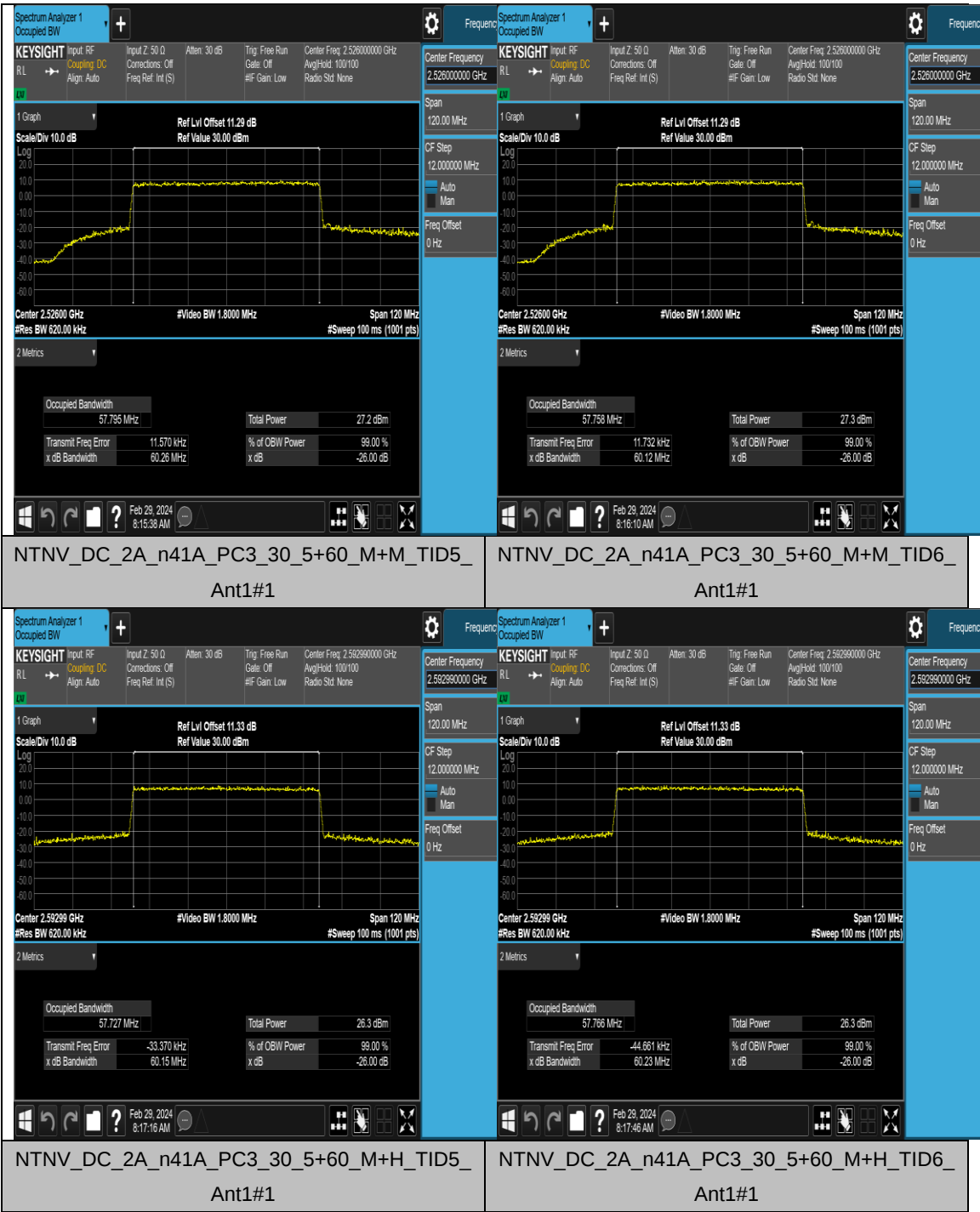
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	18.220	23.38	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	18.211	21.57	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	18.220	21.20	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	18.219	19.14	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	18.193	20.34	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	18.216	19.58	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	37.733	39.78	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	37.830	39.63	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	37.752	39.74	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	37.758	39.63	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	37.703	39.51	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	37.763	39.50	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	57.795	60.26	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	57.758	60.12	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	57.727	60.15	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	57.766	60.23	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	57.662	60.13	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	57.784	60.22	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	97.178	100.9	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	97.248	100.7	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	97.286	100.7	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Outer_Full	97.183	100.7	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	97.293	100.8	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Outer_Full	97.151	100.6	PASS

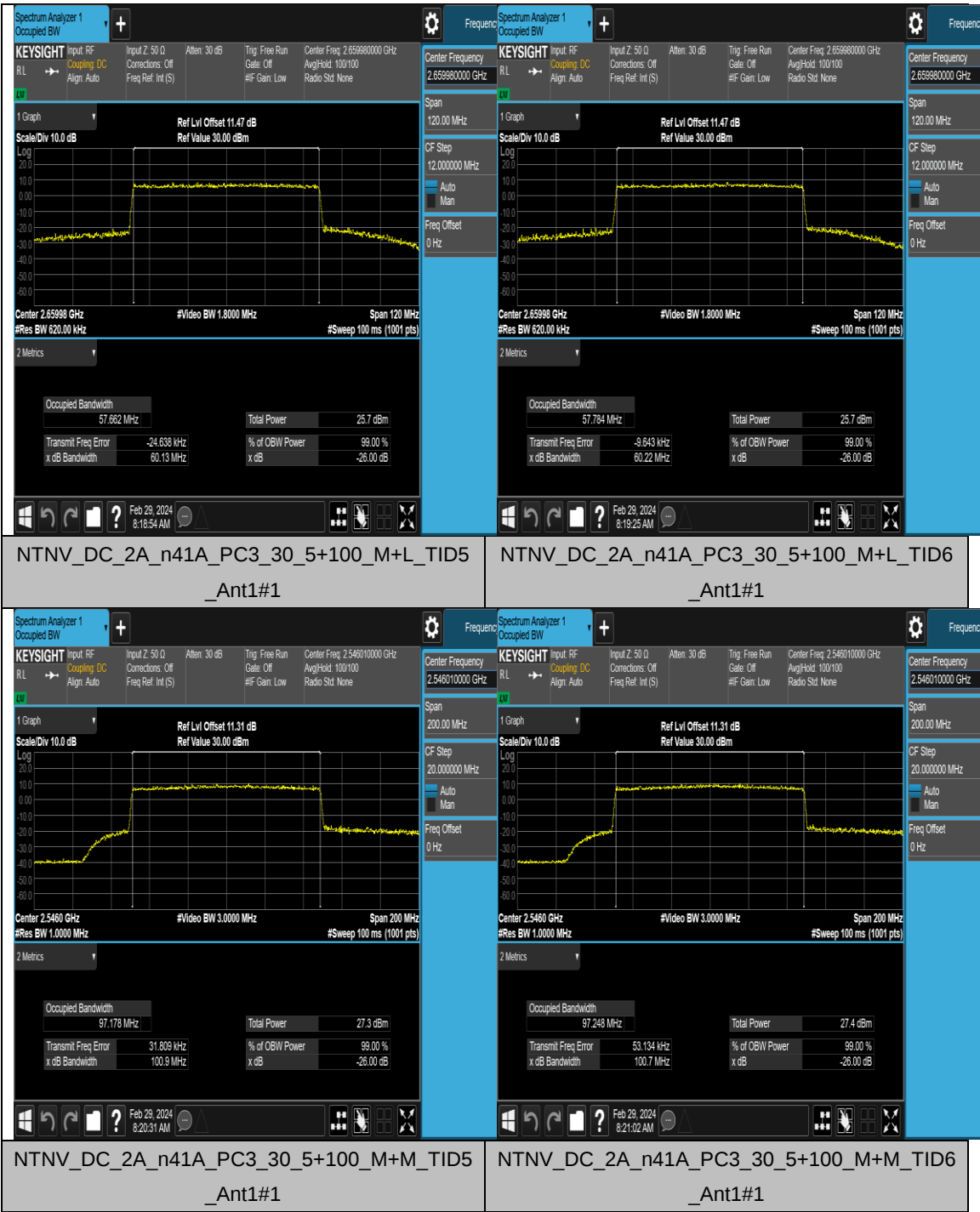
Test Graphs













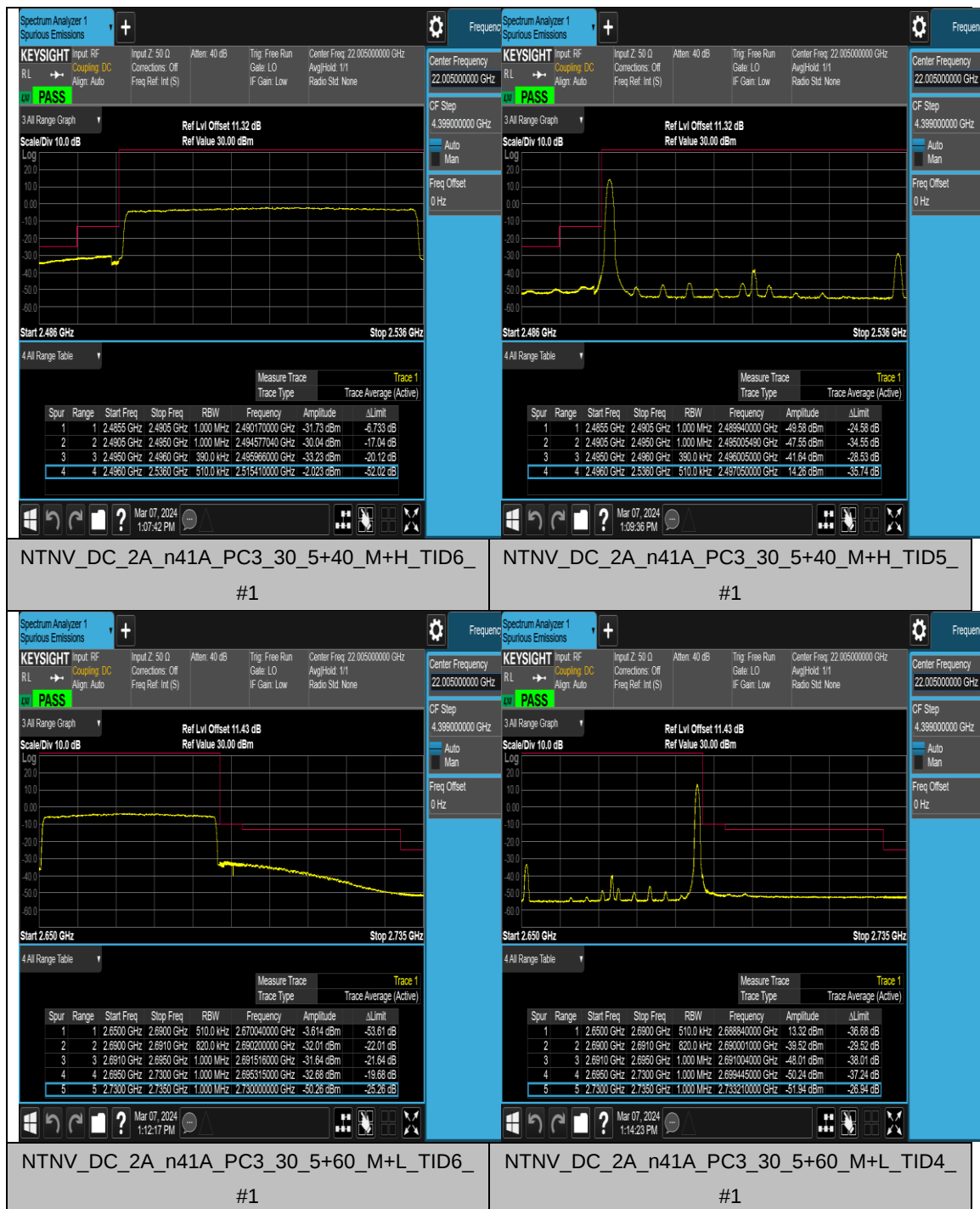
Appendix D: Band Edge for NSA

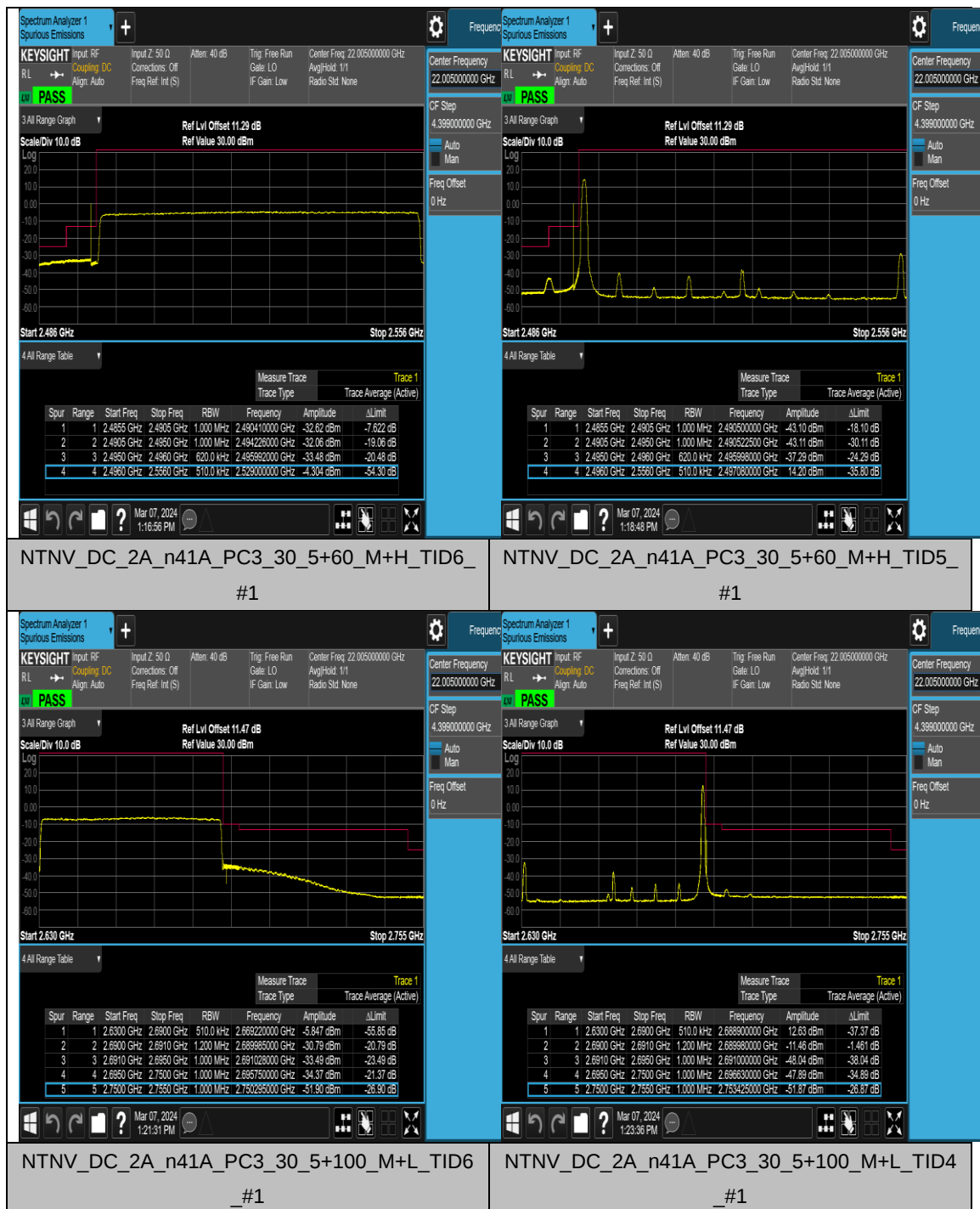
Test Result

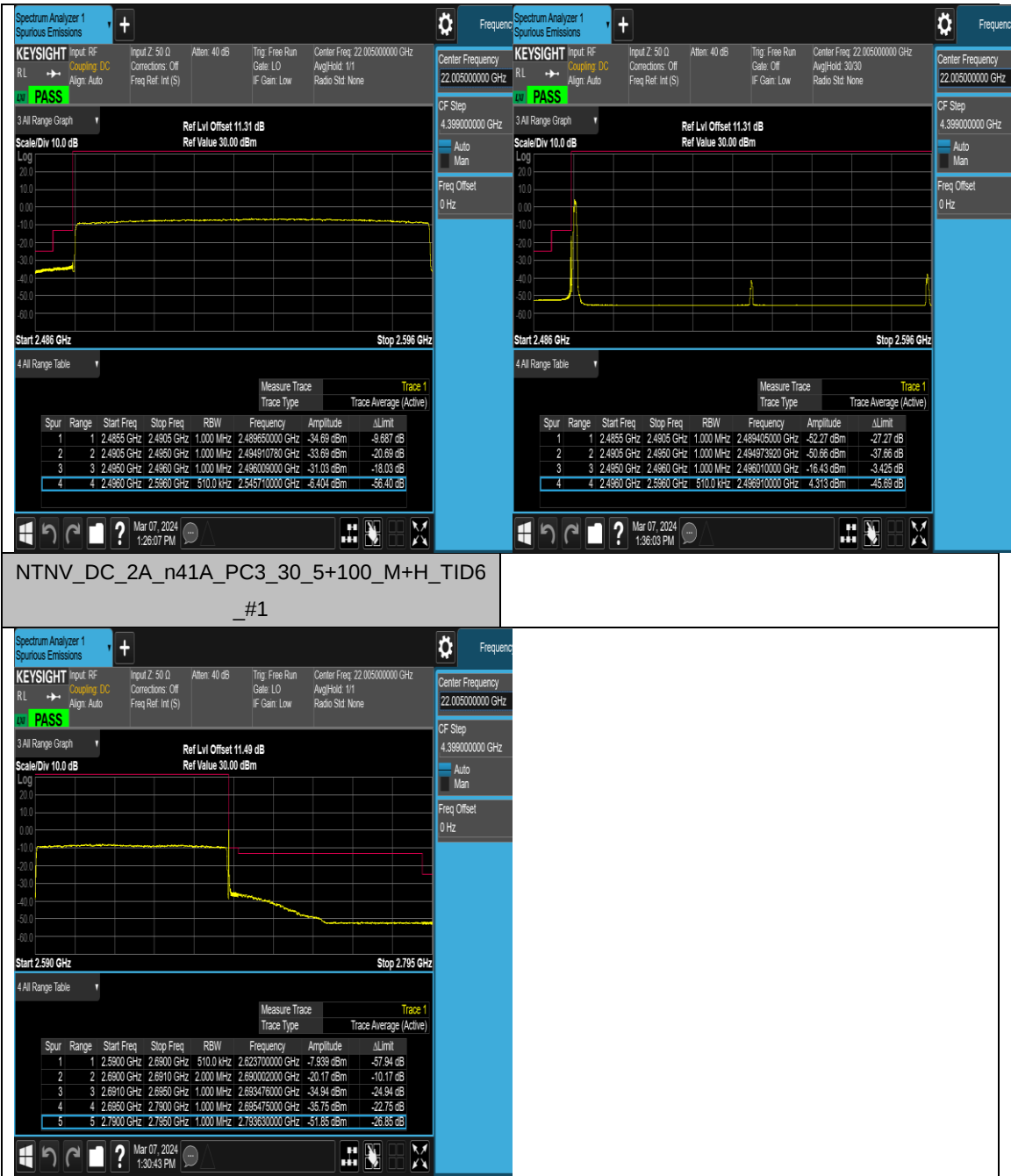
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS

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DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS

DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS

DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS

DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS

DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Outer_Full	18000	26500	-25	PASS

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DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Outer_Full	30	1000	-35	PASS

DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS

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DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS