

Appendix for 2A_n5A

Product Name: 5G CPE

Model No: C150

Appendix A: Average Power Output for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	22.04	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Inner_Full	23.72	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	22.32	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	22.06	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	22.02	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Inner_Full	23.13	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	21.97	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	21.56	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	21.63	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Inner_Full	23.08	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	21.84	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	21.63	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	21.66	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Inner_Full	22.69	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	21.62	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	21.58	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	21.64	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Inner_Full	23.13	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	21.96	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	21.61	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	21.66	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Inner_Full	22.59	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	21.99	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	21.76	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	22.04	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Inner_Full	23.49	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	22.04	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	21.93	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	22.02	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Inner_Full	22.98	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	22.25	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	21.82	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	21.78	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Inner_Full	23.16	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	21.78	PC3	PASS

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DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	21.46	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	21.84	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Inner_Full	22.68	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	21.41	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	21.80	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	21.60	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Inner_Full	23.11	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	21.65	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	21.61	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	21.58	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Inner_Full	22.59	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	21.80	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	21.75	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	21.96	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Inner_Full	23.43	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	22.04	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	22.02	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	21.99	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Inner_Full	23.03	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	22.21	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	21.81	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	21.97	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Inner_Full	23.33	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	21.84	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	23.88	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	21.81	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Inner_Full	22.81	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	22.07	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	21.90	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	21.79	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Inner_Full	23.42	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	22.01	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	21.79	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	21.86	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Inner_Full	22.72	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	21.77	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	21.25	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	21.90	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Inner_Full	23.90	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	22.11	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	21.80	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	21.86	PC3	PASS

DC_2A_n5A	15	5+20	CP-16QAM	M+L	Inner_Full	22.83	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	21.90	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	21.60	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	21.85	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Inner_Full	23.23	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	22.17	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	21.75	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	21.89	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Inner_Full	22.94	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	21.96	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	21.57	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	21.79	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Inner_Full	23.19	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	21.90	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	21.65	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	21.76	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Inner_Full	22.65	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	22.00	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	21.84	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

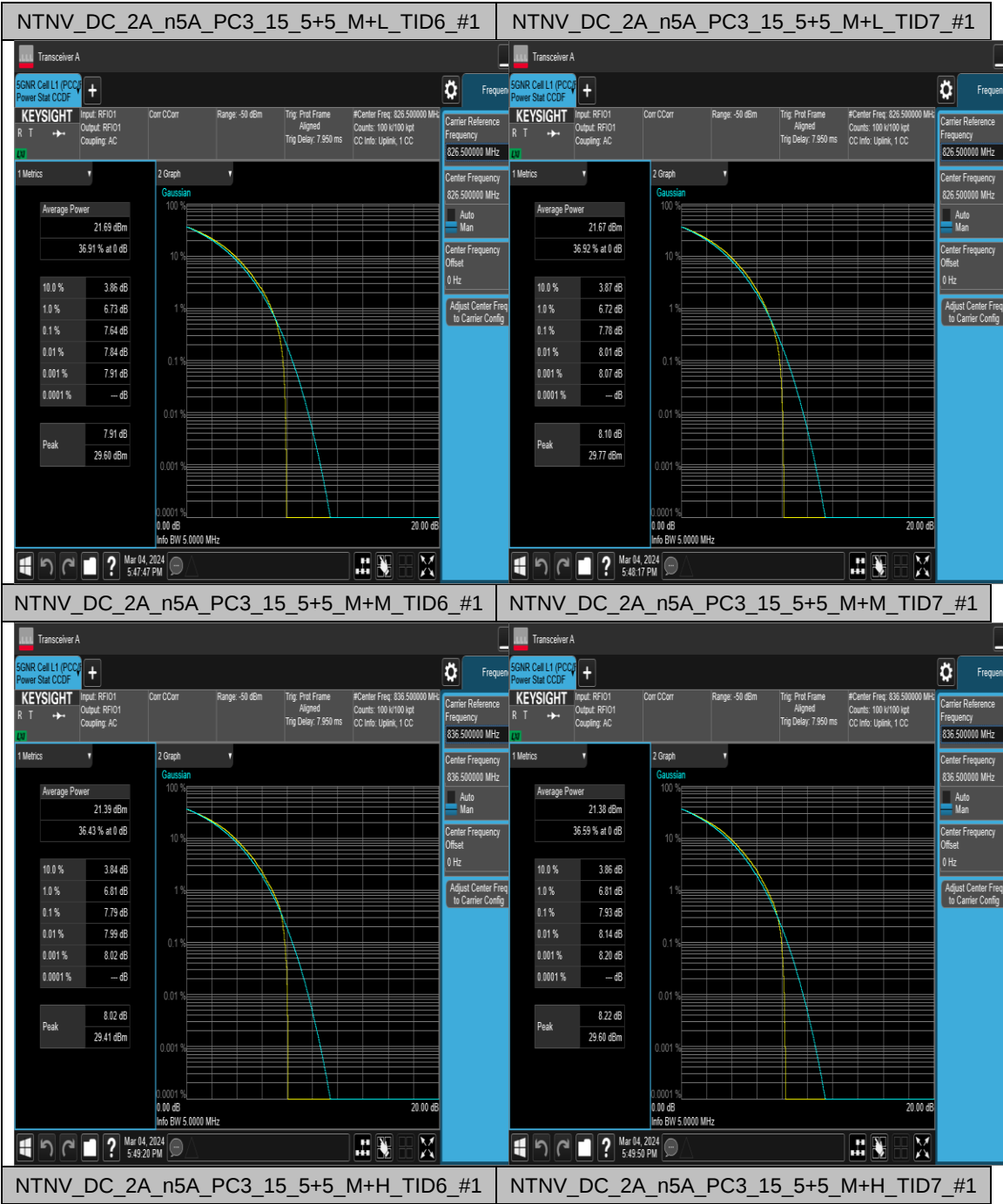
Peak-to-Average Ratio(CCDF)

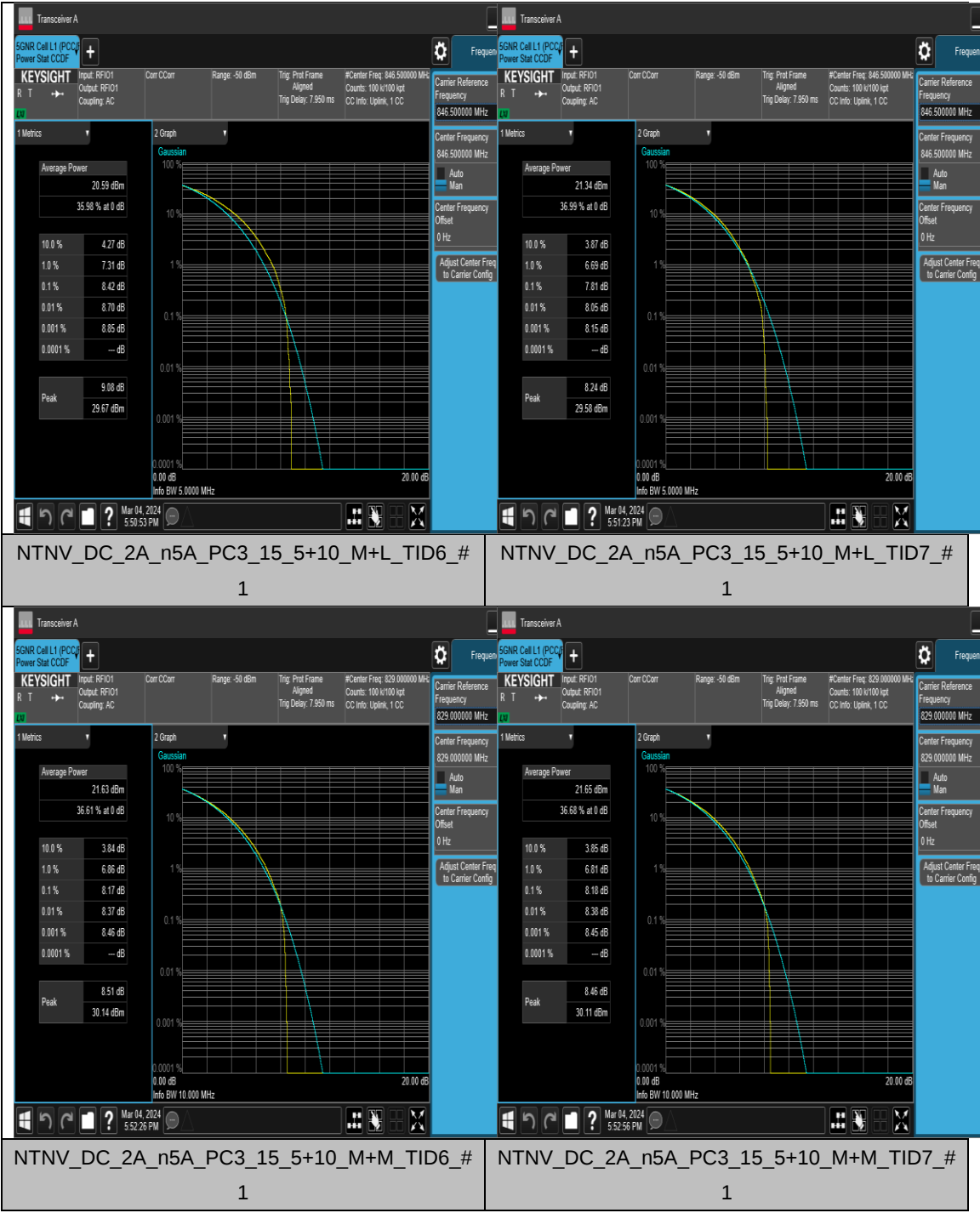
Test Result

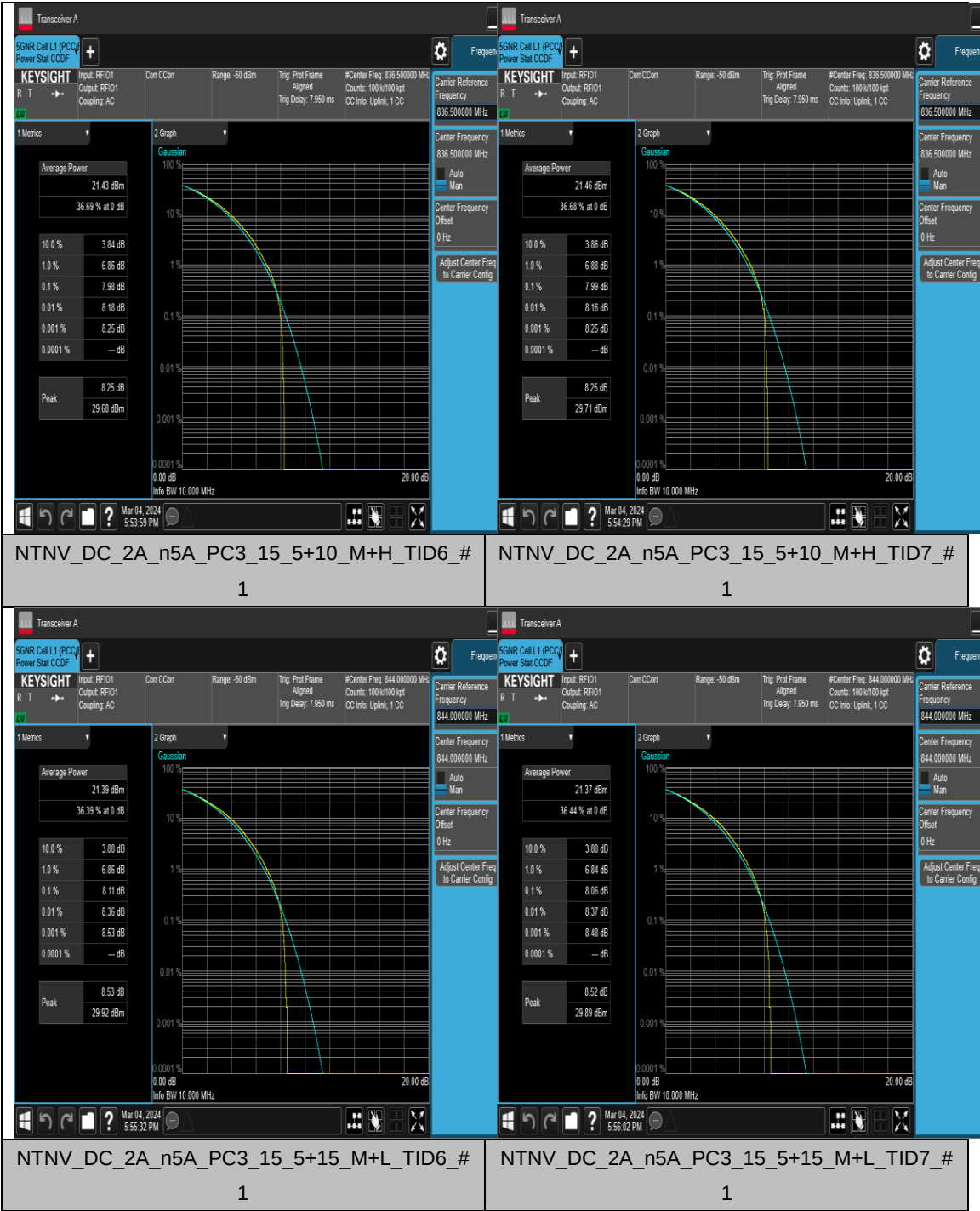
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n5 A	15	5+5	CP-QPSK	M+L	Outer_Full	7.64	≤13	PASS
DC_2A_n5 A	15	5+5	CP-16QAM	M+L	Outer_Full	7.78	≤13	PASS
DC_2A_n5 A	15	5+5	CP-QPSK	M+M	Outer_Full	7.79	≤13	PASS
DC_2A_n5 A	15	5+5	CP-16QAM	M+M	Outer_Full	7.93	≤13	PASS
DC_2A_n5 A	15	5+5	CP-QPSK	M+H	Outer_Full	8.42	≤13	PASS
DC_2A_n5 A	15	5+5	CP-16QAM	M+H	Outer_Full	7.81	≤13	PASS
DC_2A_n5 A	15	5+10	CP-QPSK	M+L	Outer_Full	8.17	≤13	PASS
DC_2A_n5 A	15	5+10	CP-16QAM	M+L	Outer_Full	8.18	≤13	PASS
DC_2A_n5 A	15	5+10	CP-QPSK	M+M	Outer_Full	7.98	≤13	PASS
DC_2A_n5 A	15	5+10	CP-16QAM	M+M	Outer_Full	7.99	≤13	PASS
DC_2A_n5 A	15	5+10	CP-QPSK	M+H	Outer_Full	8.11	≤13	PASS
DC_2A_n5 A	15	5+10	CP-16QAM	M+H	Outer_Full	8.06	≤13	PASS
DC_2A_n5 A	15	5+15	CP-QPSK	M+L	Outer_Full	7.66	≤13	PASS
DC_2A_n5 A	15	5+15	CP-16QAM	M+L	Outer_Full	7.65	≤13	PASS
DC_2A_n5 A	15	5+15	CP-QPSK	M+M	Outer_Full	7.82	≤13	PASS
DC_2A_n5 A	15	5+15	CP-16QAM	M+M	Outer_Full	8.56	≤13	PASS
DC_2A_n5 A	15	5+15	CP-QPSK	M+H	Outer_Full	7.96	≤13	PASS

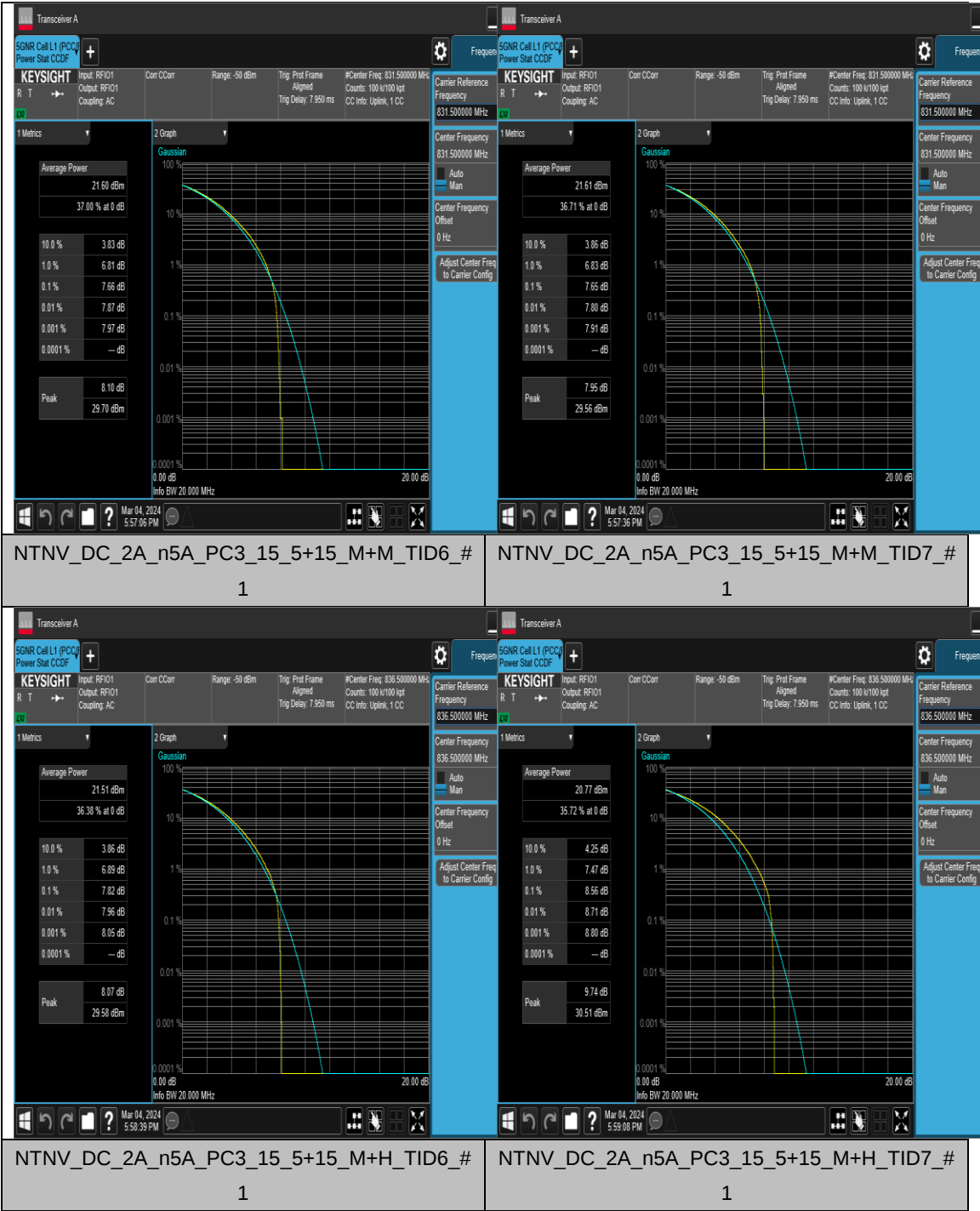
DC_2A_n5 A	15	5+15	CP-16QAM	M+H	Outer_Full	7.96	≤13	PASS
DC_2A_n5 A	15	5+20	CP-QPSK	M+L	Outer_Full	7.57	≤13	PASS
DC_2A_n5 A	15	5+20	CP-16QAM	M+L	Outer_Full	7.55	≤13	PASS
DC_2A_n5 A	15	5+20	CP-QPSK	M+M	Outer_Full	7.61	≤13	PASS
DC_2A_n5 A	15	5+20	CP-16QAM	M+M	Outer_Full	7.61	≤13	PASS
DC_2A_n5 A	15	5+20	CP-QPSK	M+H	Outer_Full	7.87	≤13	PASS
DC_2A_n5 A	15	5+20	CP-16QAM	M+H	Outer_Full	7.84	≤13	PASS

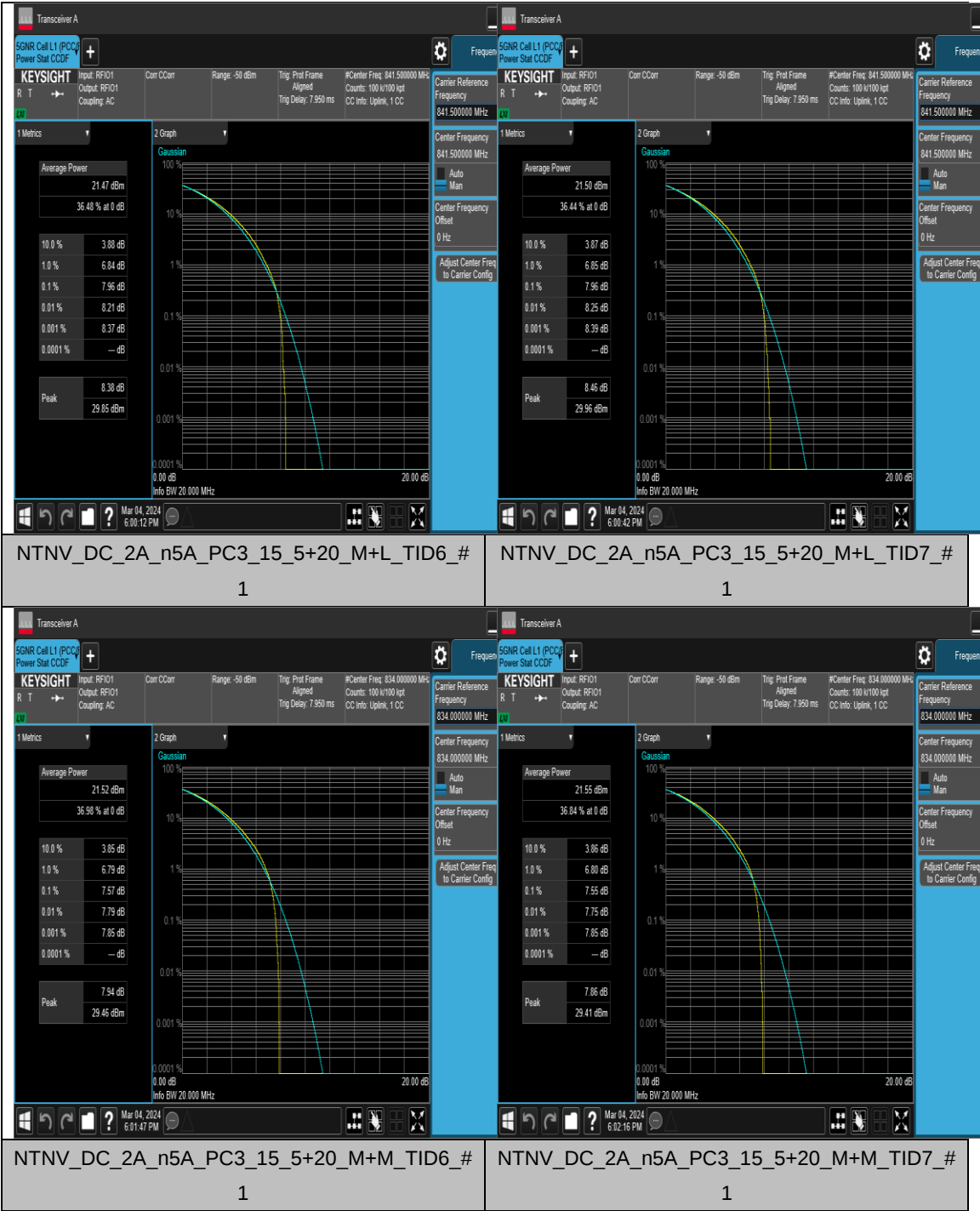
Test Graphs

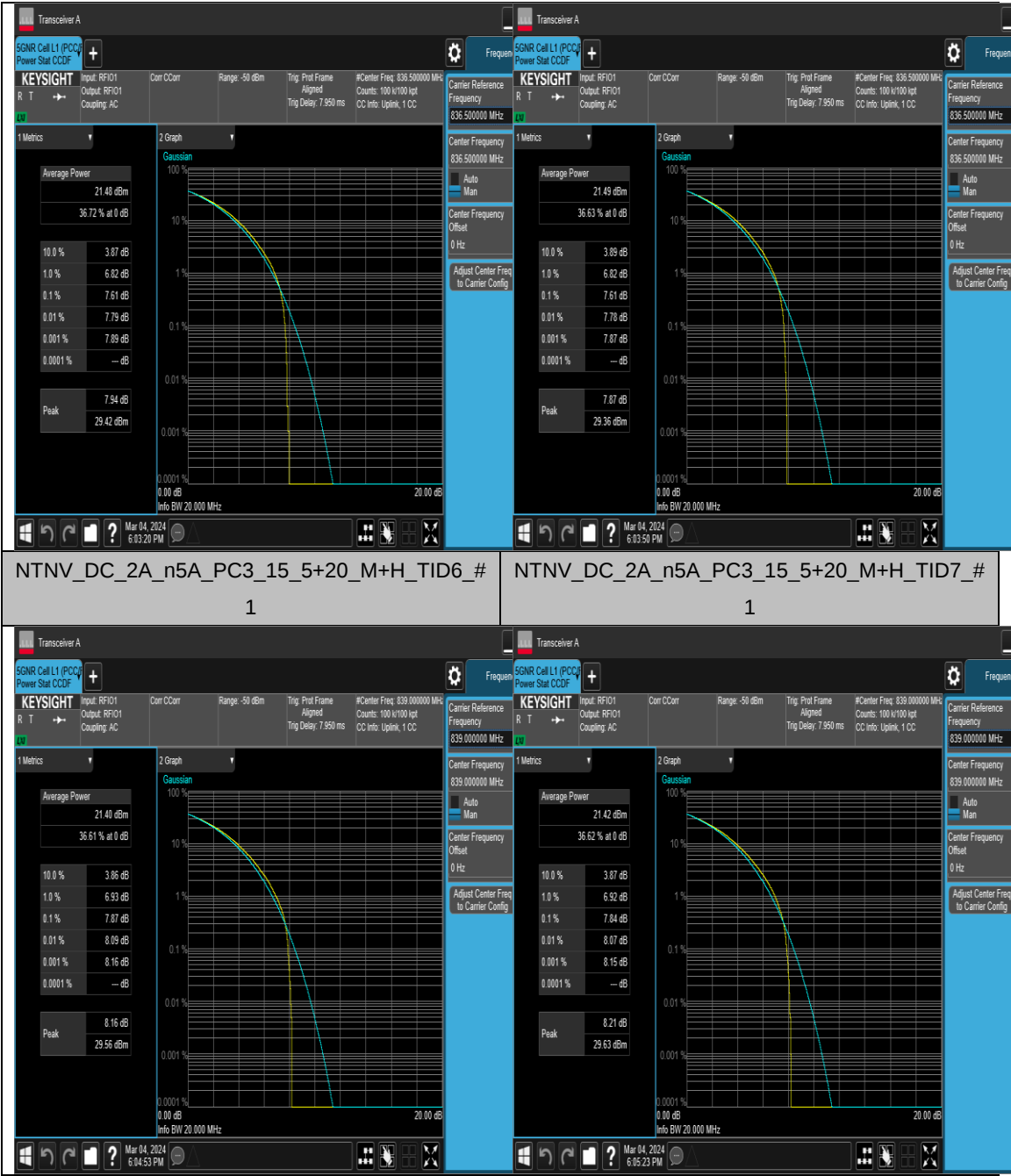












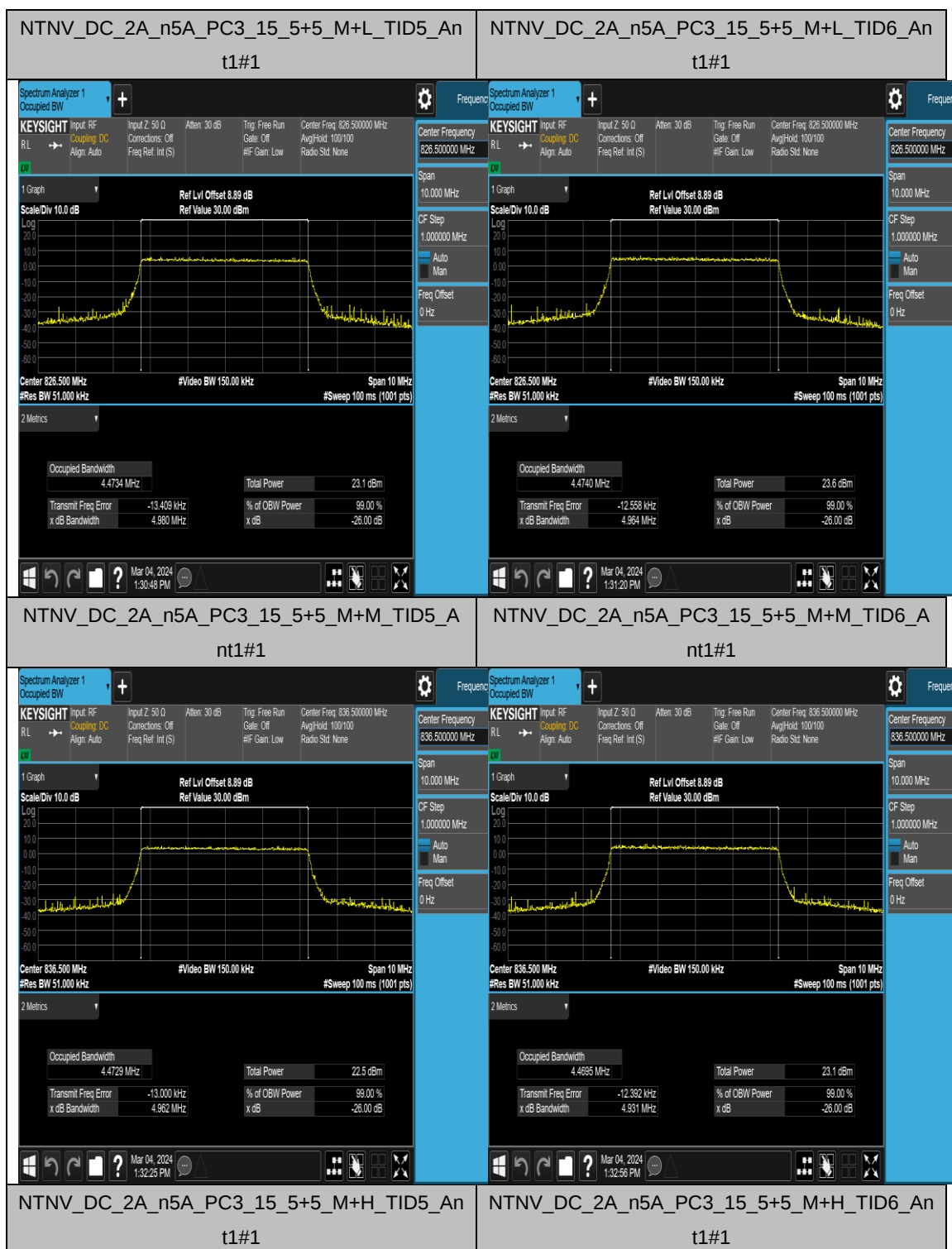
Appendix C: 26dB Bandwidth and Occupied Bandwidth for

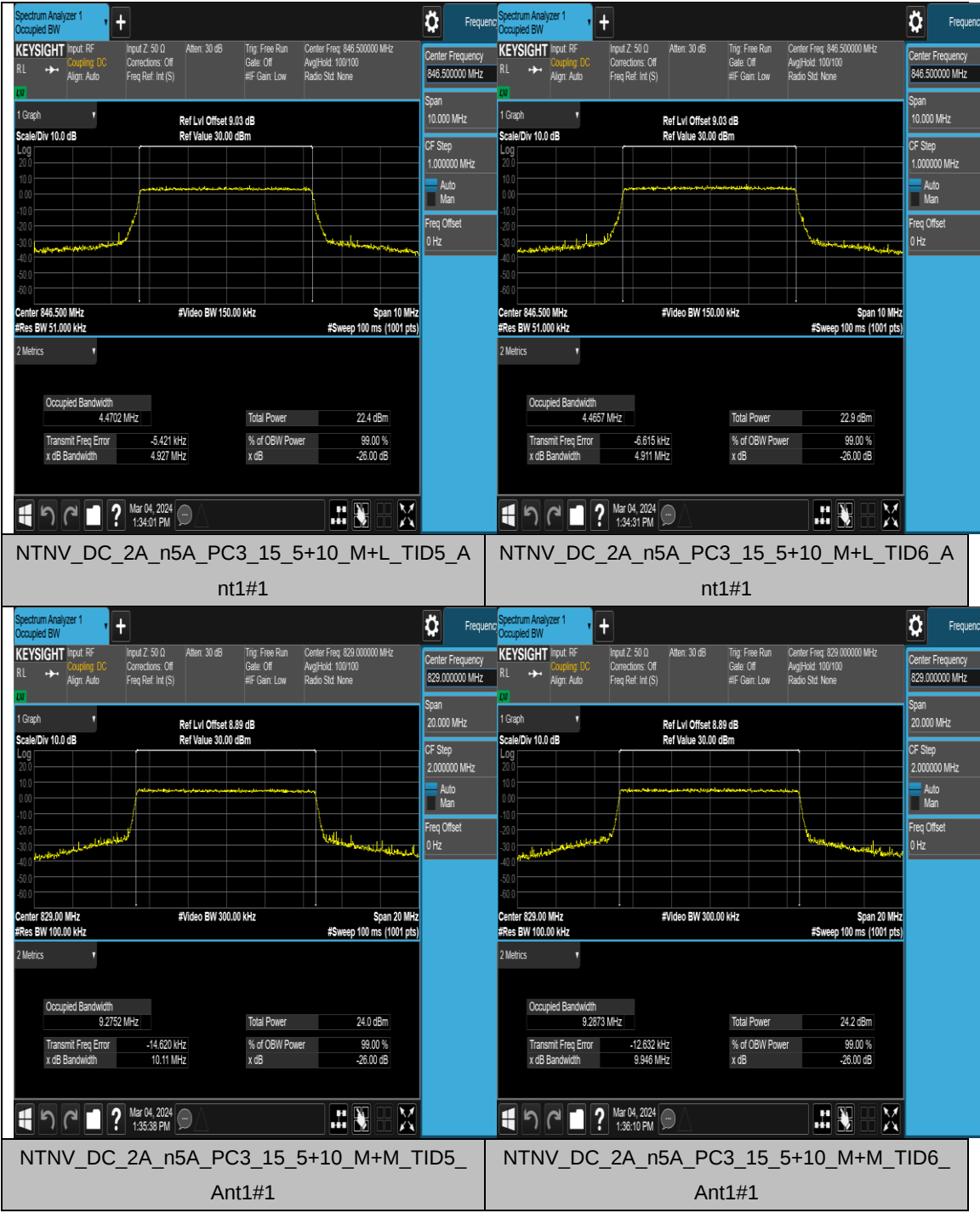
NSA

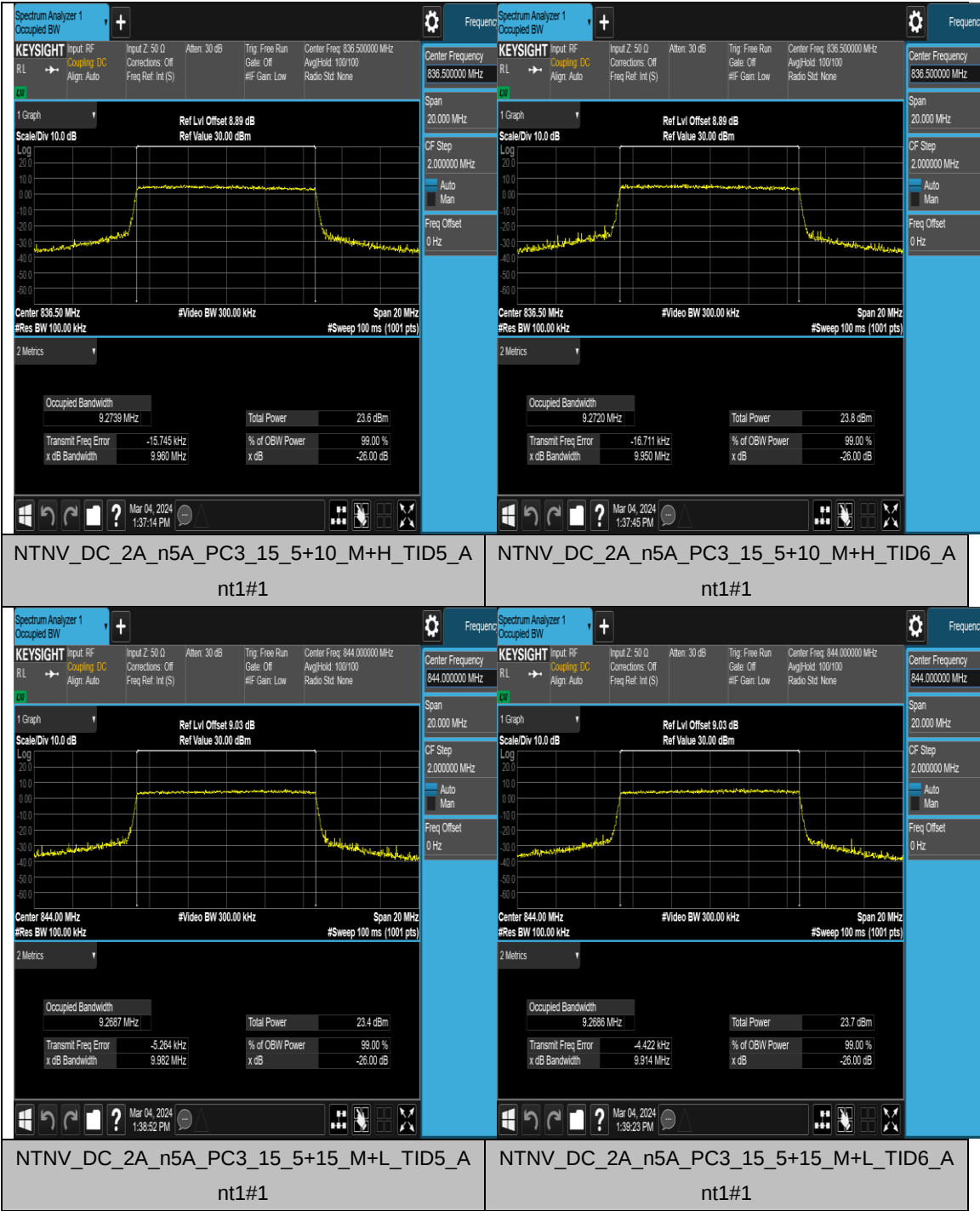
Test Result

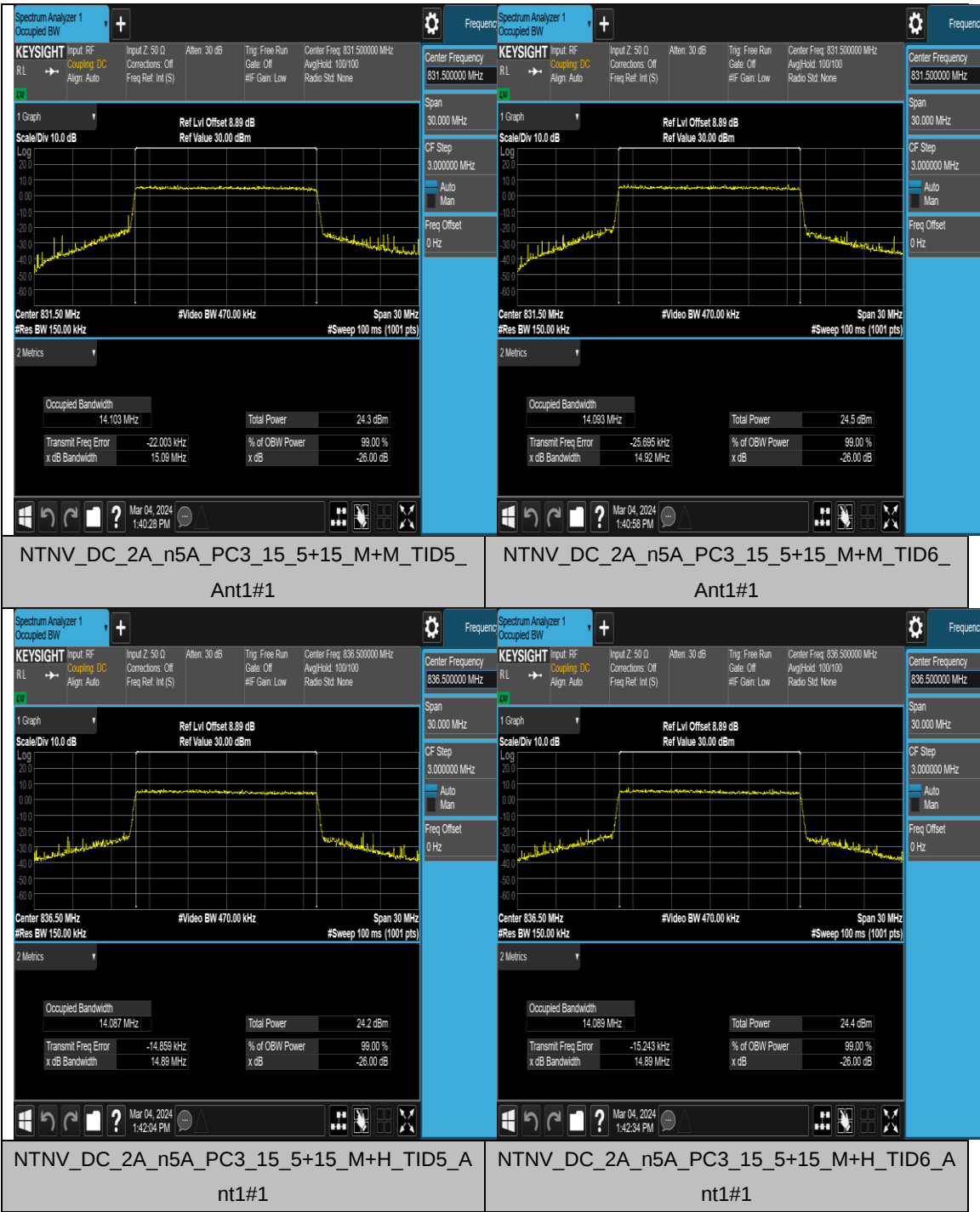
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4734	4.980	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4740	4.964	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4729	4.962	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4695	4.931	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4702	4.927	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4657	4.911	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2752	10.11	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2873	9.946	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2739	9.960	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2720	9.950	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2687	9.982	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2686	9.914	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	14.103	15.09	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	14.093	14.92	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	14.087	14.89	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	14.089	14.89	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	14.103	14.93	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	14.115	14.94	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	18.891	19.94	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	18.894	19.85	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	18.887	19.90	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	18.908	19.86	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	18.881	19.84	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	18.904	19.82	PASS

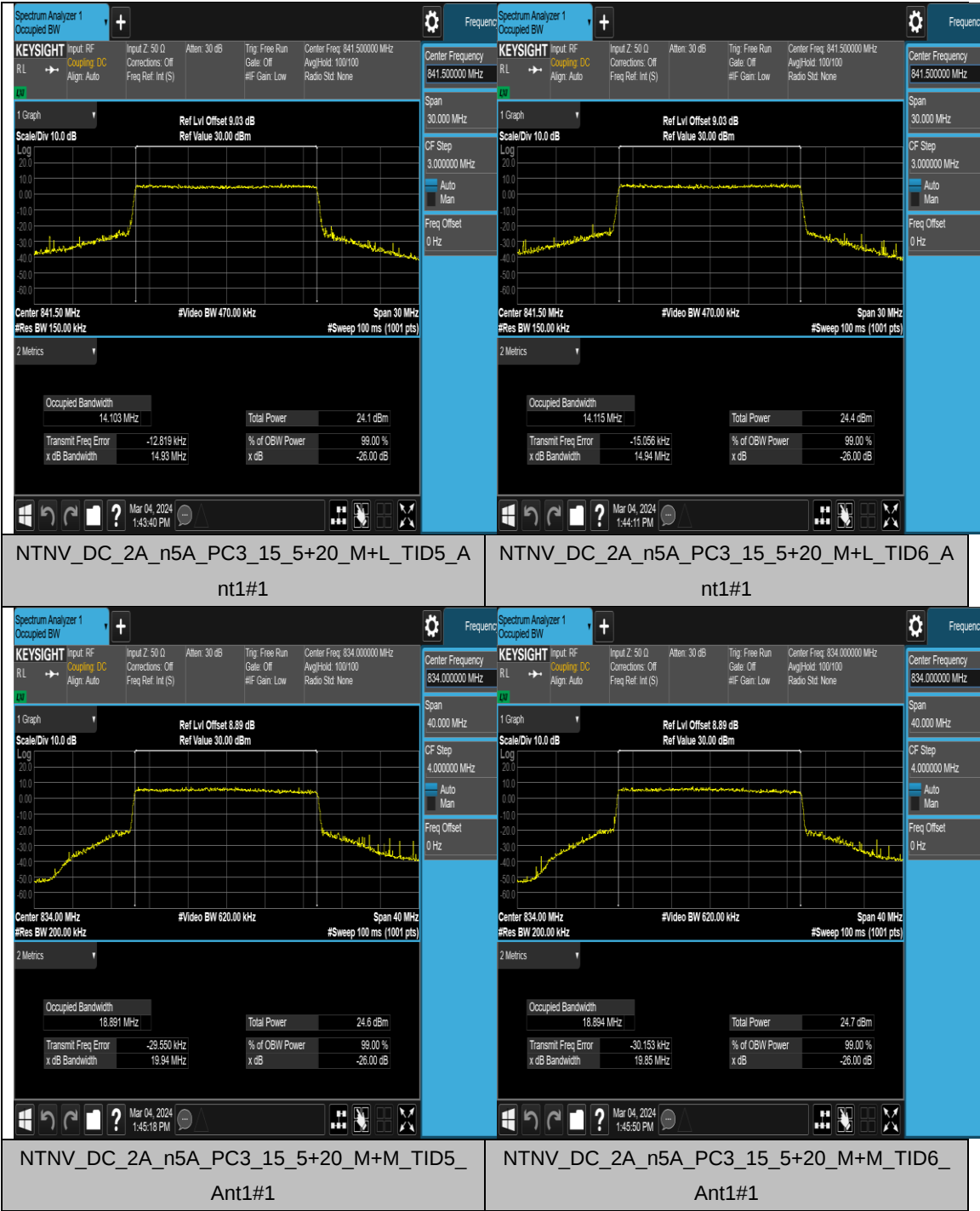
Test Graphs

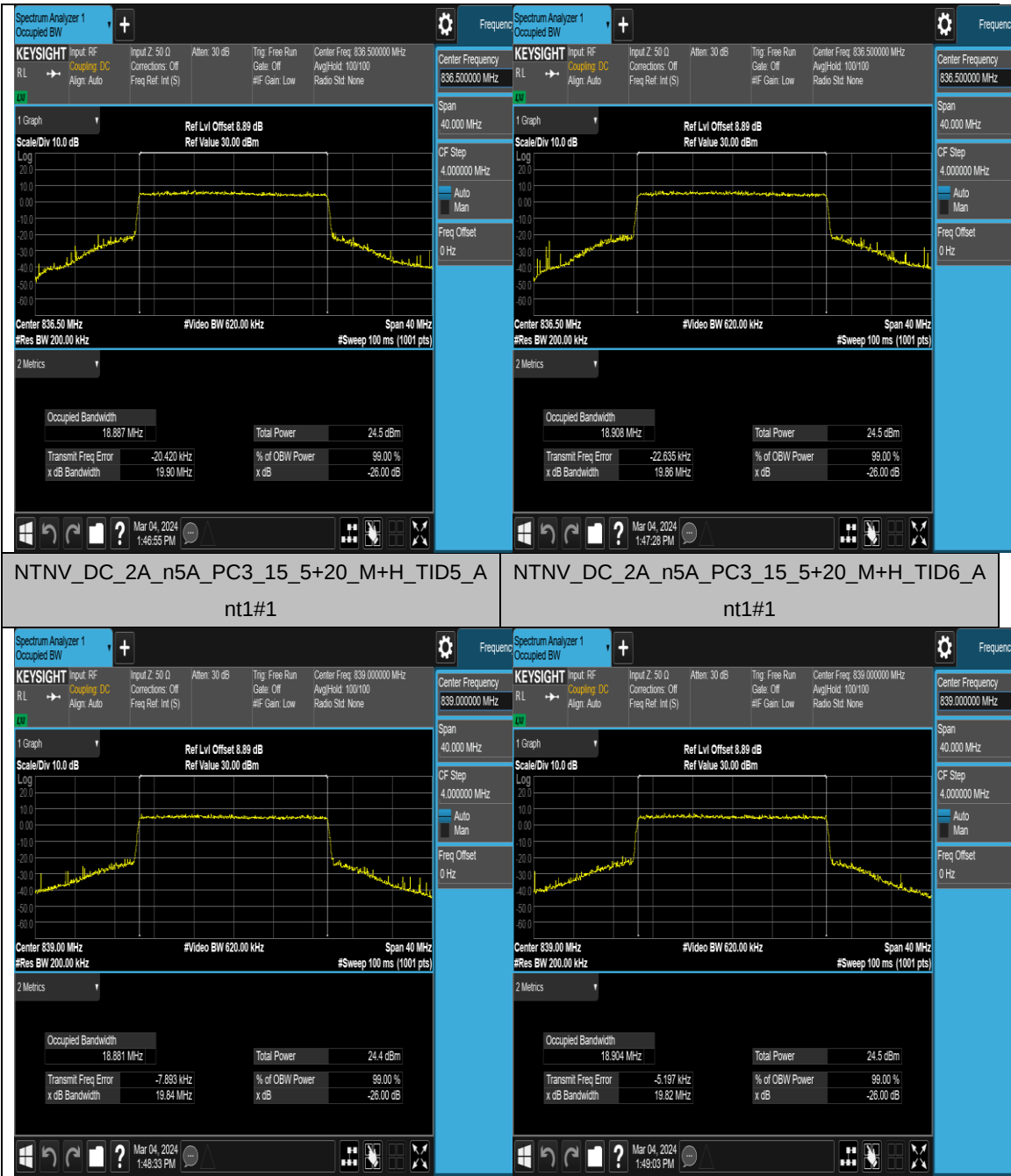










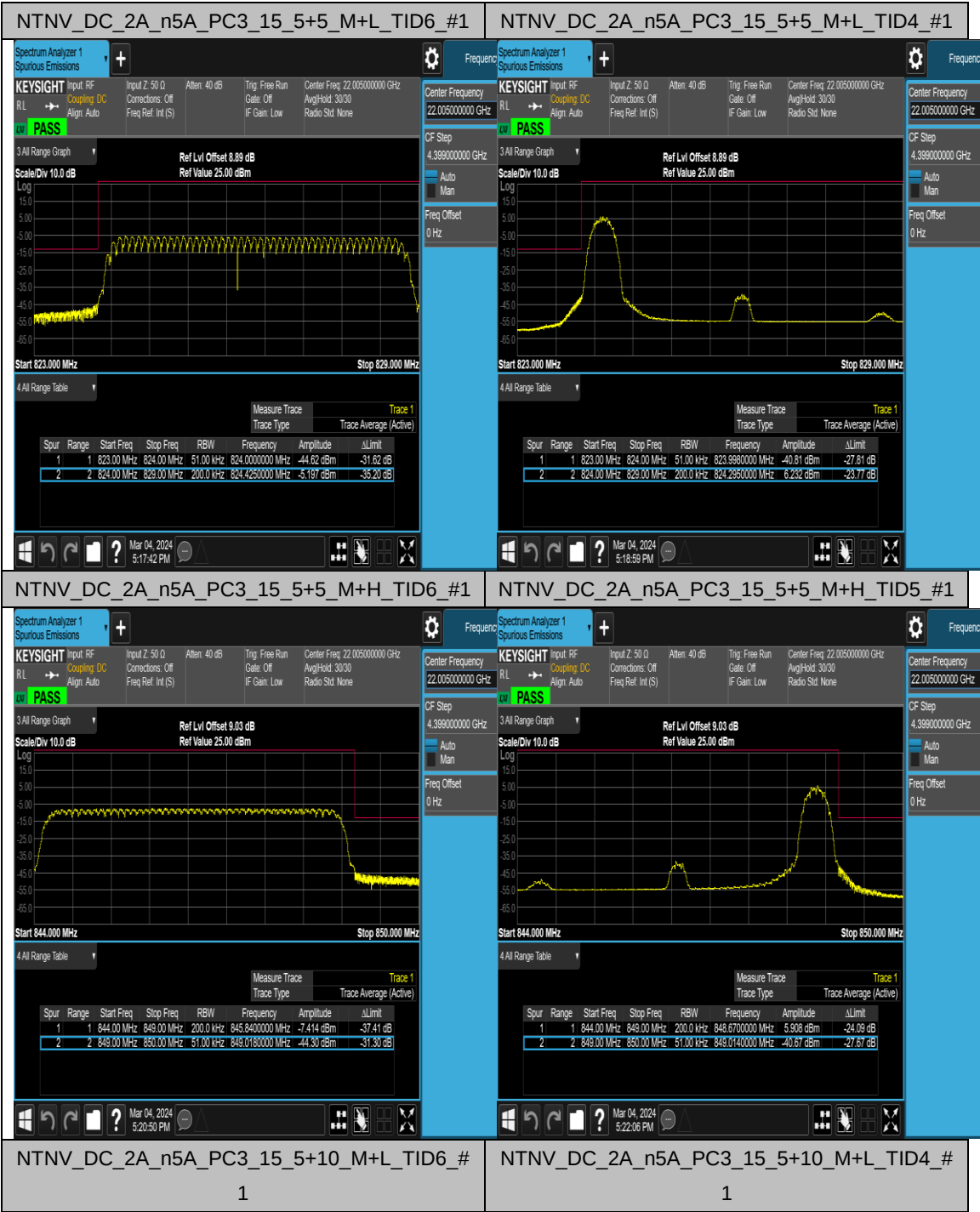


Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs



NTNV_DC_2A_n5A_PC3_15_5+5_M+H_TID6_#1

Spectrum Analyzer 1
Spurious Emissions

KEYSIGHT
Input: RF
Coupling: DC
RL: →
Align: Auto

Input Z: 50 Ω
Corrections: Off
Freq Ref: Int (S)

Atten: 40 dB
Ting: Free Run
Gate: Off
IF Gain: Low

Center Freq: 22.005000000 GHz
Avg/Hold: 30/30
Radio Std: None

3 All Range Graph
Scale/Div 10.0 dB
Log
Ref Lvl Offset 9.03 dB
Ref Value 25.00 dBm
Start 844.000 MHz
Stop 850.000 MHz

4 All Range Table
Measure Trace
Trace Type
Trace Average (Active)

Spur	Range	Start Freq	Stop Freq	RBW	Frequency	Amplitude	ΔLimit
1	1	844.00 MHz	849.00 MHz	200.0 kHz	845.8400000 MHz	-7.414 dBm	-37.41 dB
2	2	849.00 MHz	850.00 MHz	51.00 kHz	849.0180000 MHz	-44.30 dBm	-31.30 dB

NTNV_DC_2A_n5A_PC3_15_5+5_M+H_TID5_#1

Spectrum Analyzer 1
Spurious Emissions

KEYSIGHT
Input: RF
Coupling: DC
RL: →
Align: Auto

Input Z: 50 Ω
Corrections: Off
Freq Ref: Int (S)

Atten: 40 dB
Ting: Free Run
Gate: Off
IF Gain: Low

Center Freq: 22.005000000 GHz
Avg/Hold: 30/30
Radio Std: None

3 All Range Graph
Scale/Div 10.0 dB
Log
Ref Lvl Offset 9.03 dB
Ref Value 25.00 dBm
Start 844.000 MHz
Stop 850.000 MHz

4 All Range Table
Measure Trace
Trace Type
Trace Average (Active)

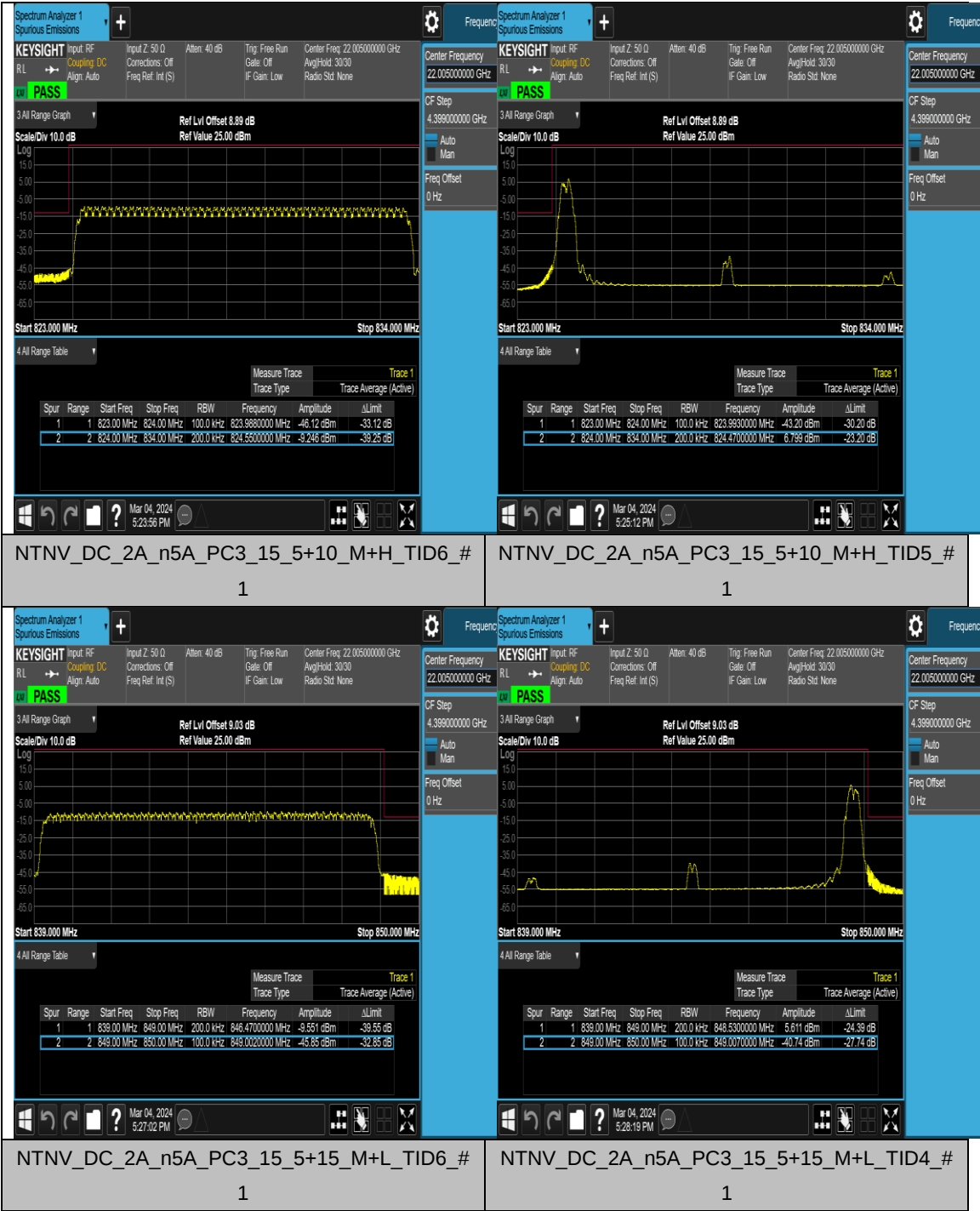
Spur	Range	Start Freq	Stop Freq	RBW	Frequency	Amplitude	ΔLimit
1	1	844.00 MHz	849.00 MHz	200.0 kHz	848.6700000 MHz	5.908 dBm	-24.08 dB
2	2	849.00 MHz	850.00 MHz	51.00 kHz	849.0140000 MHz	-40.67 dBm	-27.67 dB

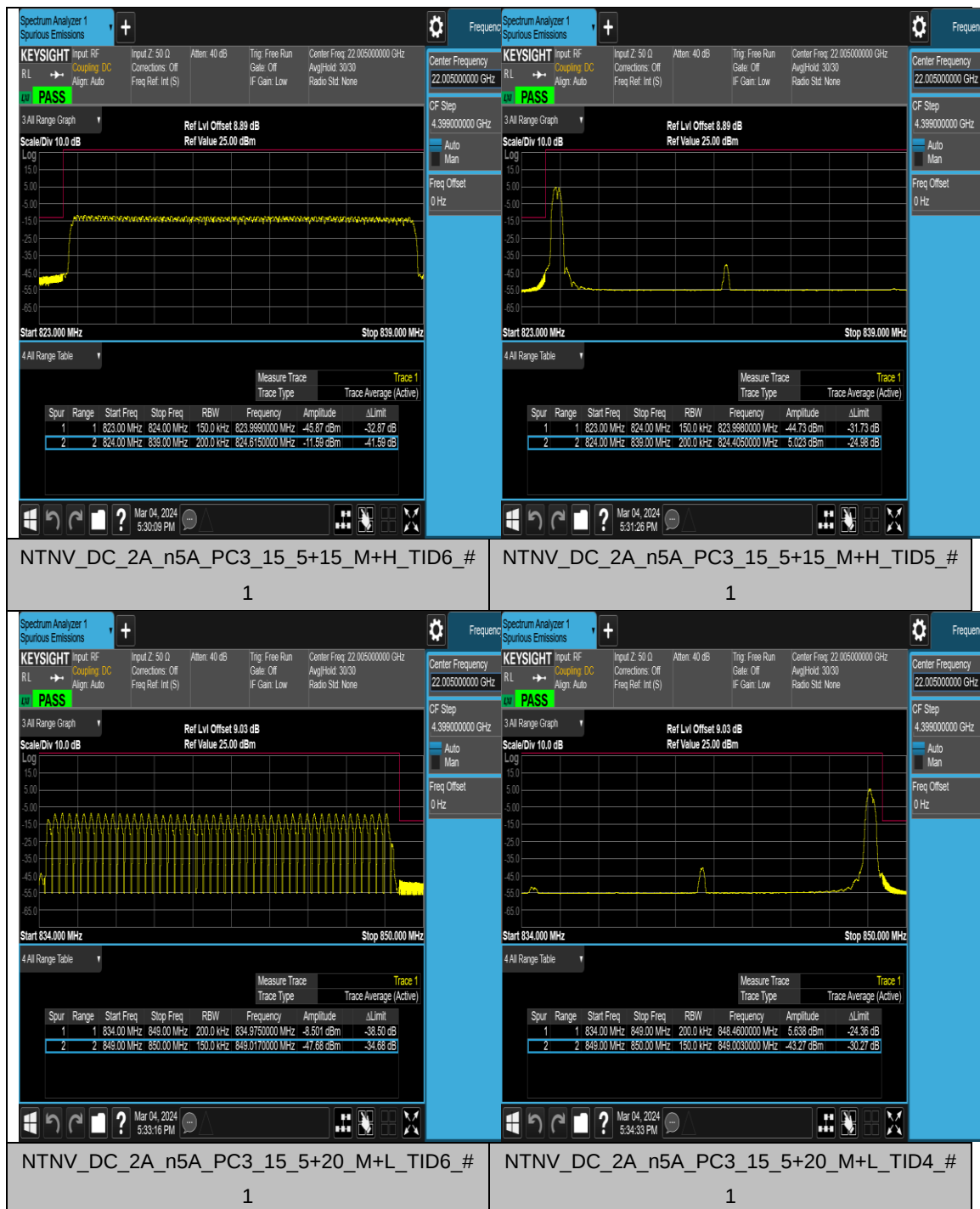
NTNV_DC_2A_n5A_PC3_15_5+10_M+L_TID6_#

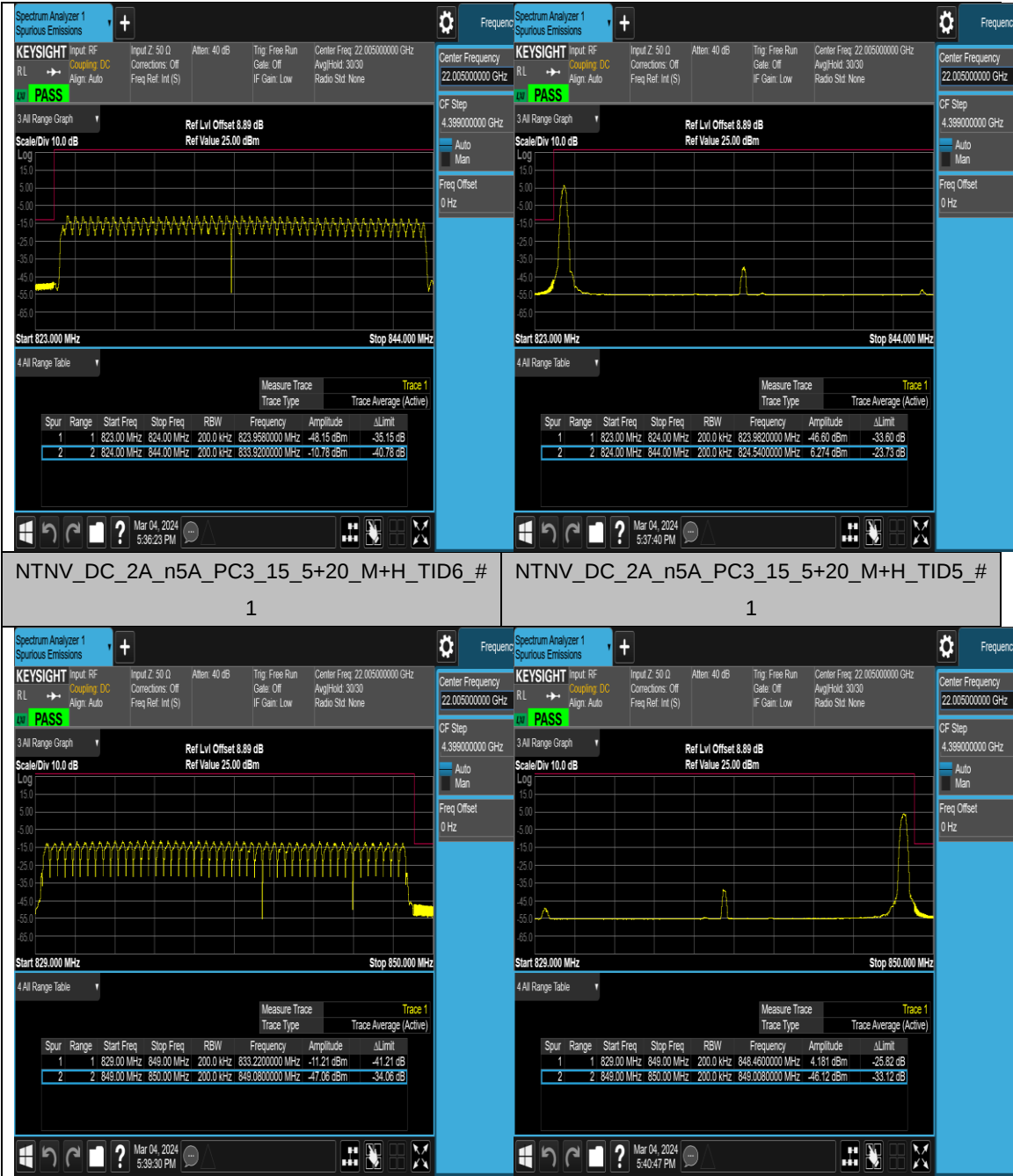
1

NTNV_DC_2A_n5A_PC3_15_5+10_M+L_TID4_#

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Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS

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DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS

DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS

DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS

DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS

DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS

DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS