

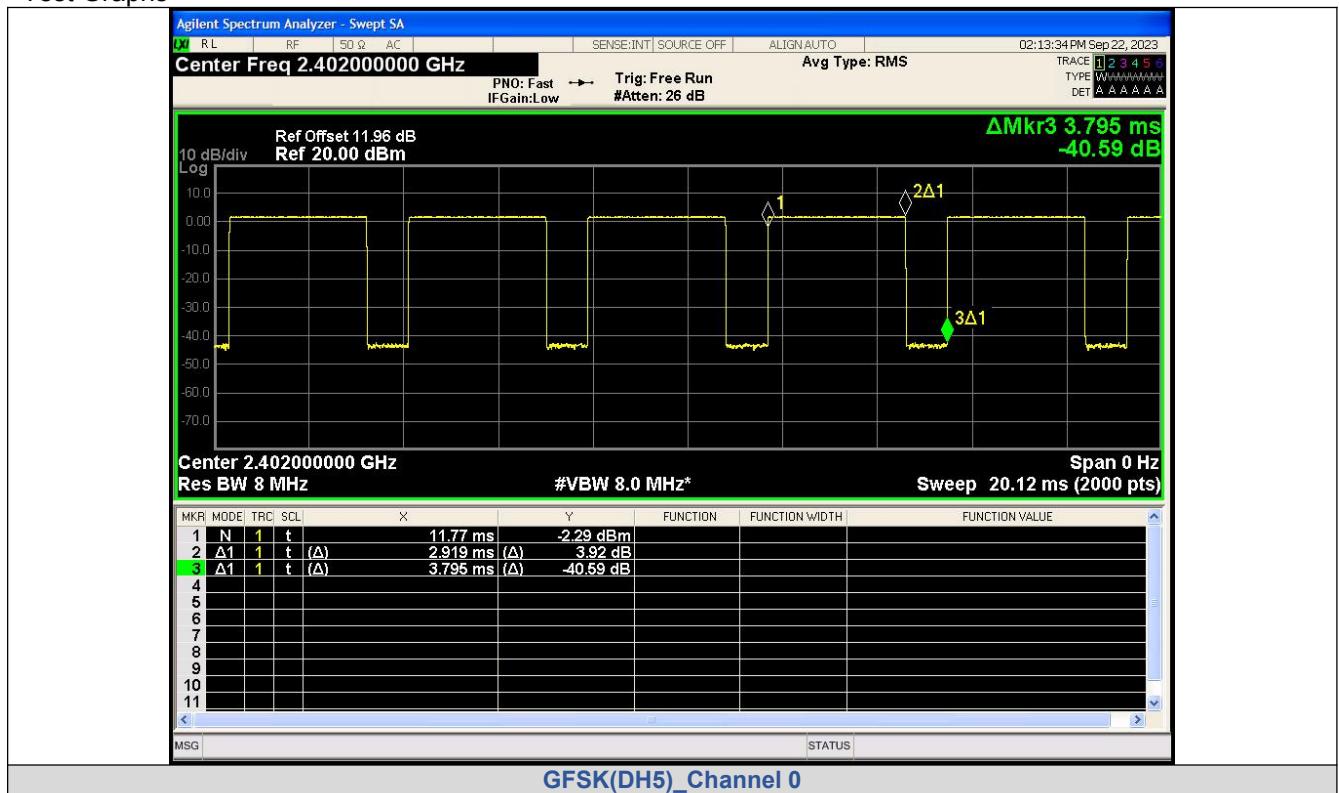


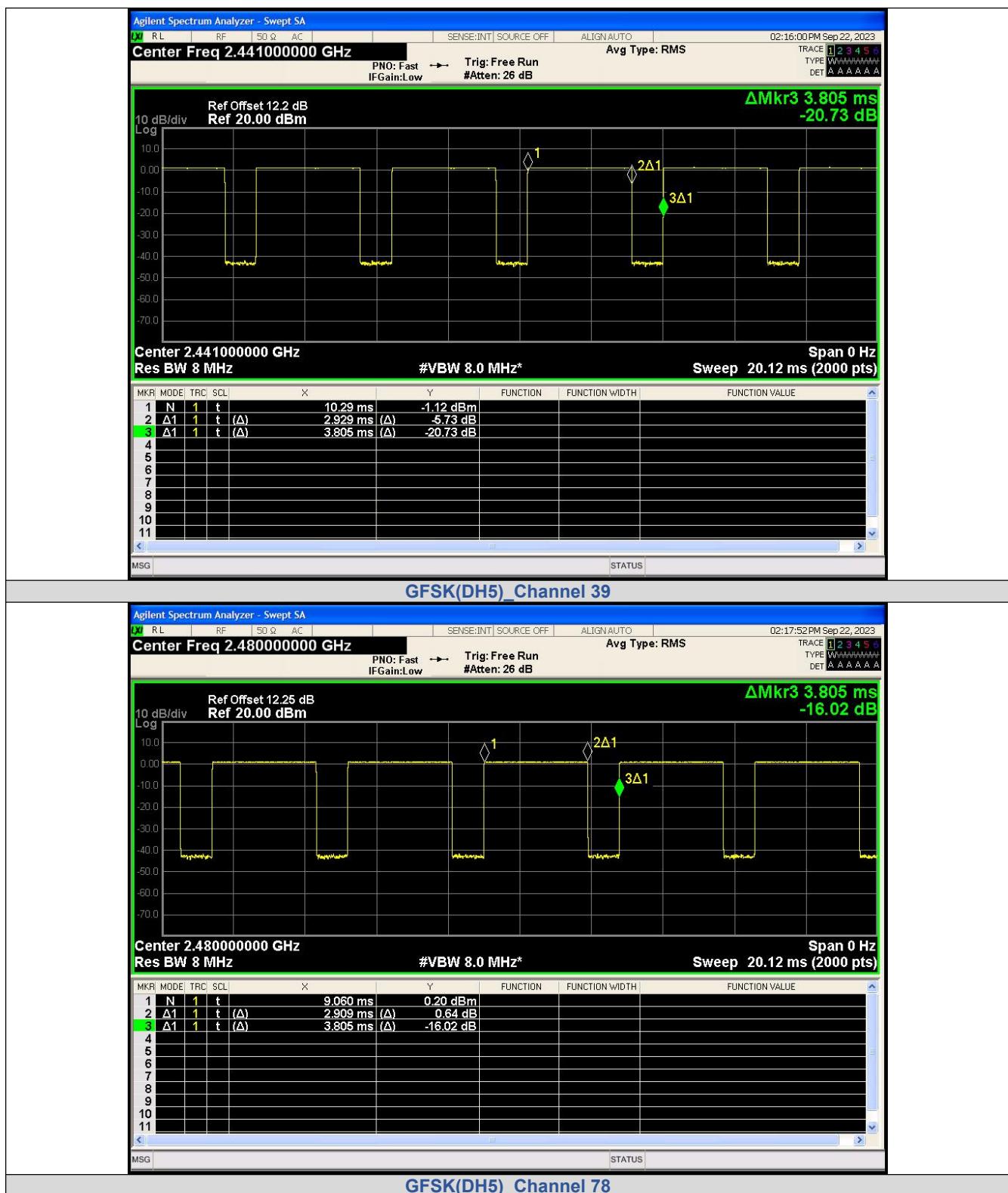
APPENDIX VI. Duty Cycle

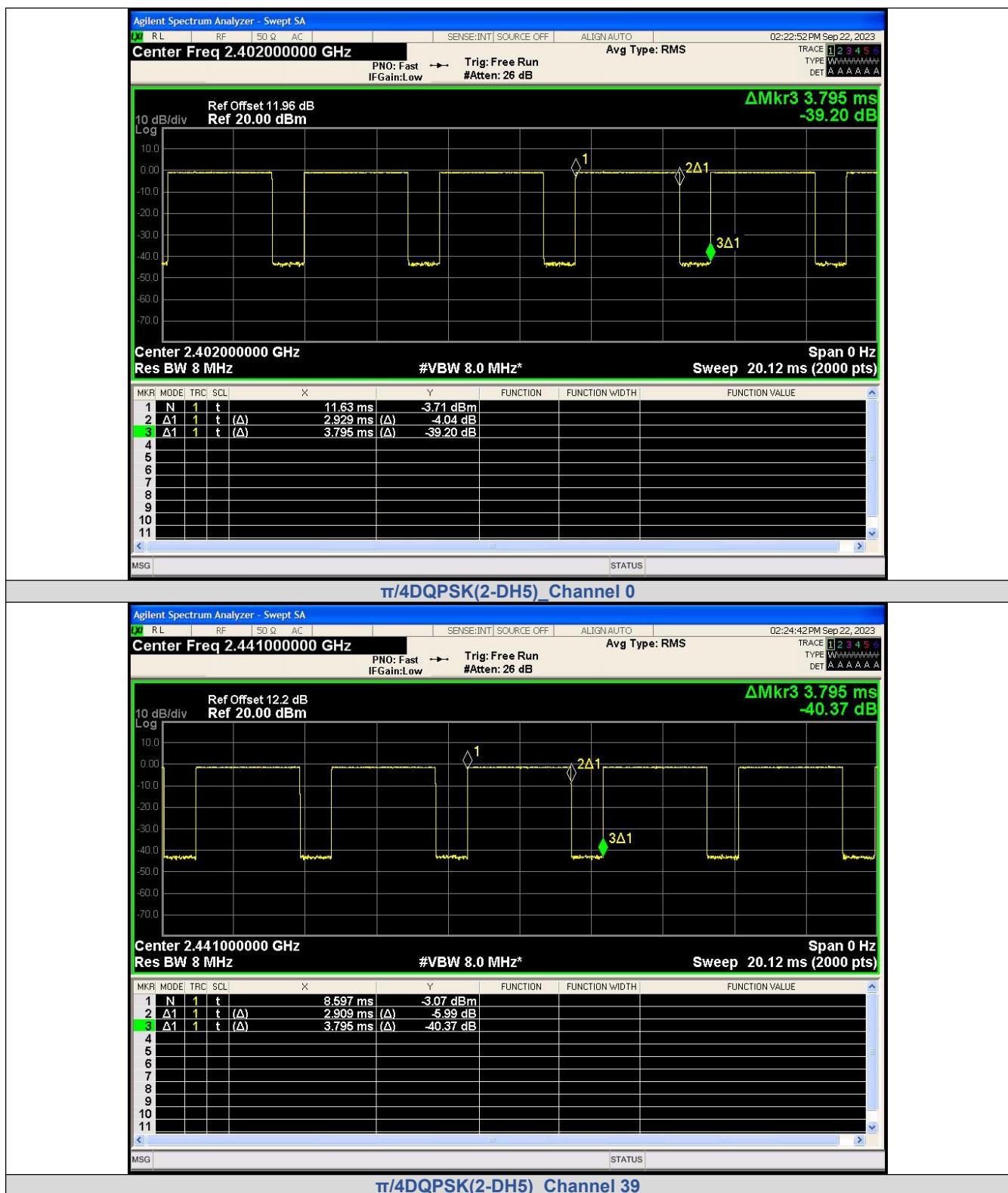
Test Result

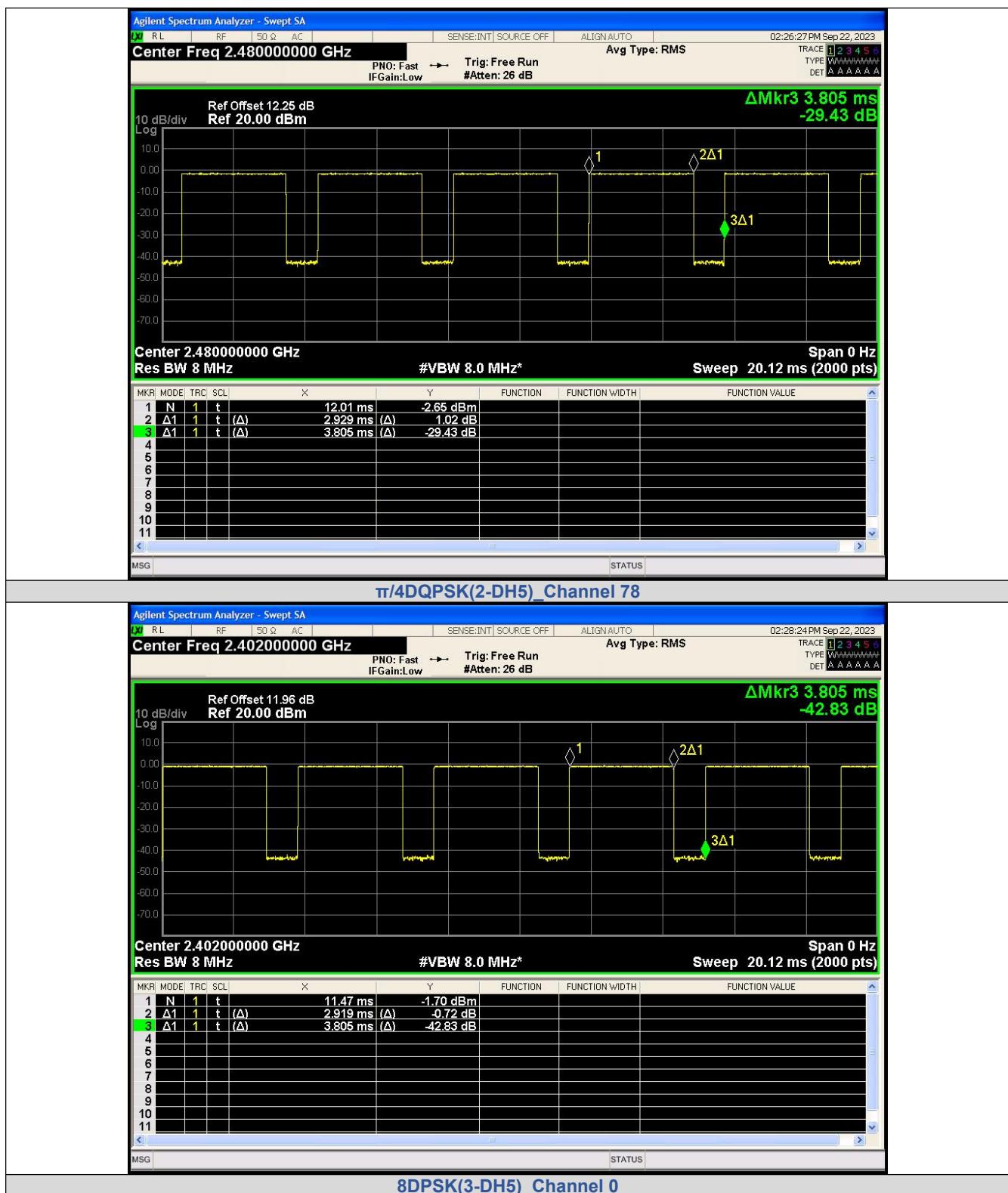
Modulation	packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)
GFSK	DH5	0	2.919	3.795	76.92	0.7692	1.1396
		39	2.929	3.805	76.98	0.7698	1.1362
		78	2.909	3.805	76.45	0.7646	1.1657
$\pi/4$ DQPSK	2-DH5	0	2.929	3.795	77.19	0.7719	1.1244
		39	2.909	3.795	76.66	0.7666	1.1543
		78	2.929	3.805	76.98	0.7698	1.1362
8DPSK	3-DH5	0	2.919	3.805	76.72	0.7672	1.1509
		39	2.909	3.795	76.66	0.7666	1.1543
		78	2.919	3.805	76.72	0.7672	1.1509

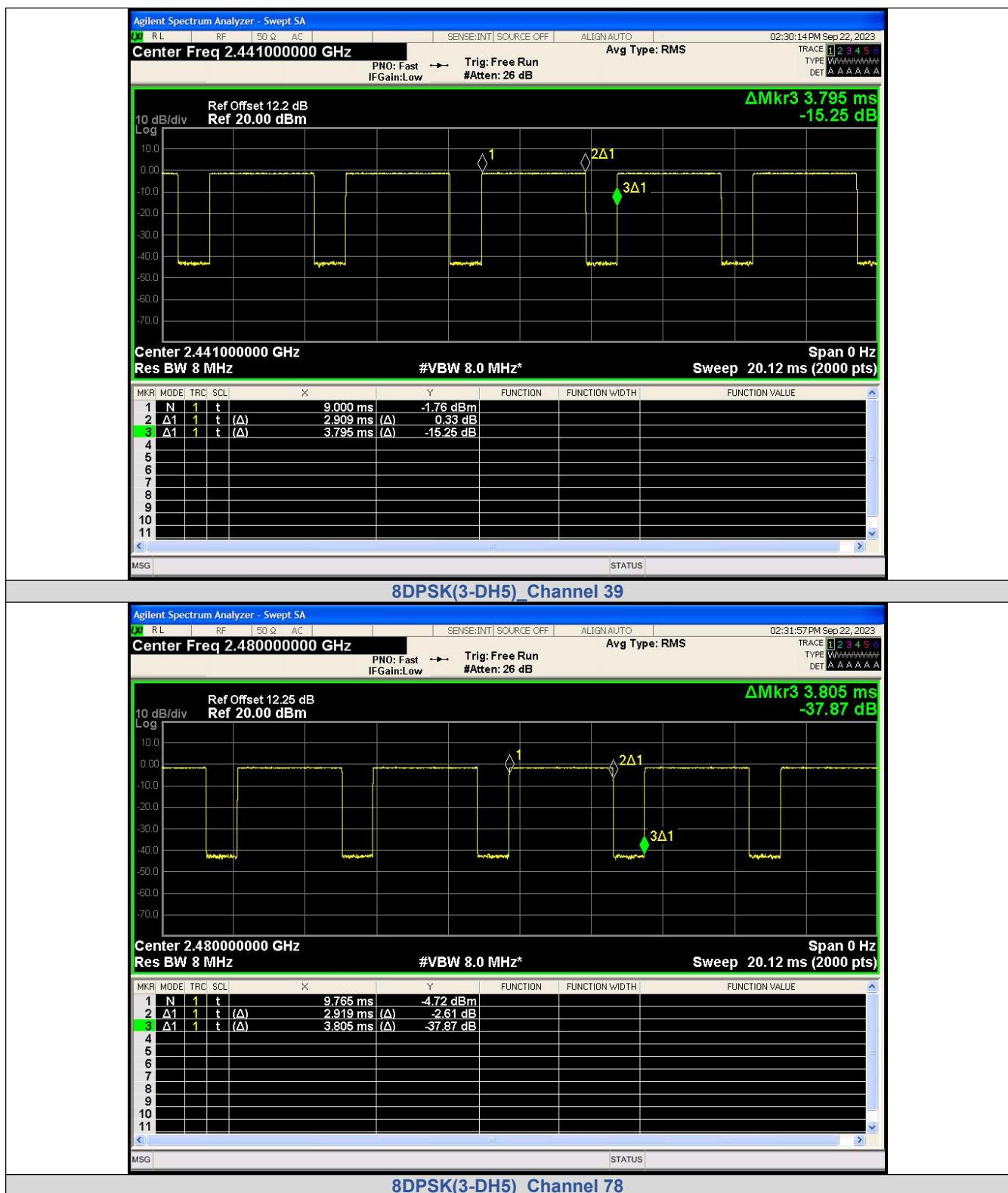
Test Graphs











APPENDIX VII. Dwell Time

Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	CH39 (2441MHz)	0.3912	309	120.88	< 400	PASS
	DH3		1.656	155	256.68		PASS
	DH5		2.912	104	302.85		PASS
π/4DQPSK	2-DH1		0.3696	310	114.58		PASS
	2-DH3		1.656	155	256.68		PASS
	2-DH5		2.912	104	302.85		PASS
8DPSK	3-DH1		0.3792	309	117.17		PASS
	3-DH3		1.656	155	256.68		PASS
	3-DH5		2.912	103	299.94		PASS

Test Graphs

