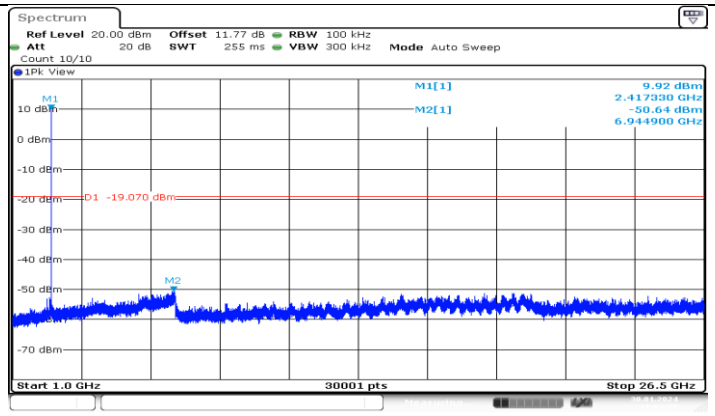
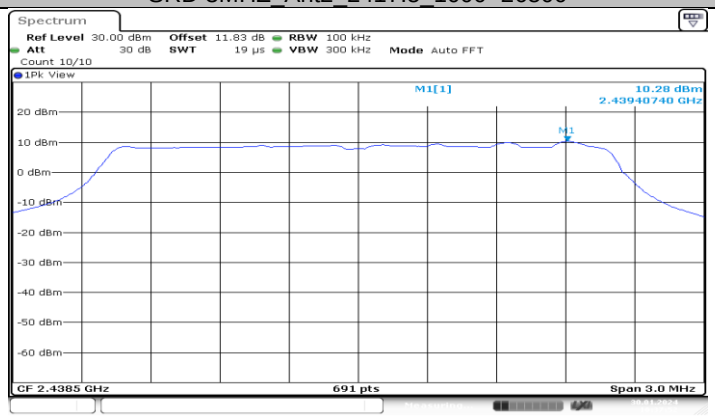
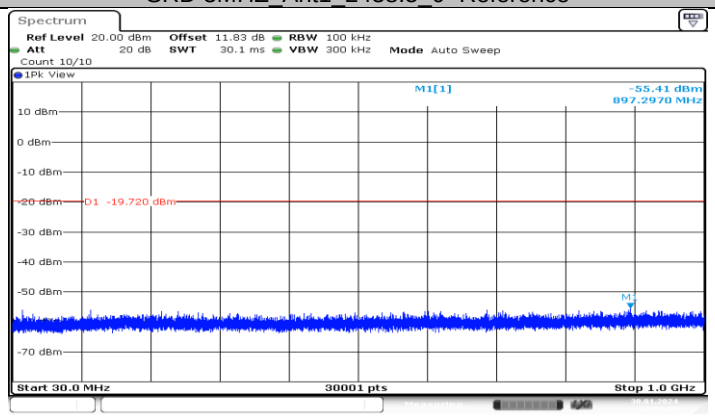


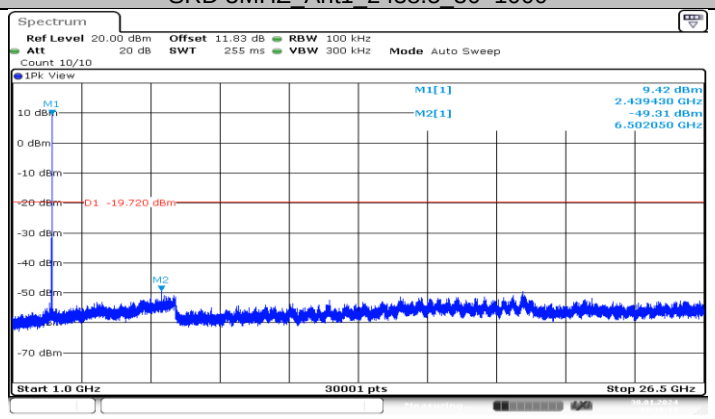
SRD 3MHZ_Ant2_2417.5_1000~26500



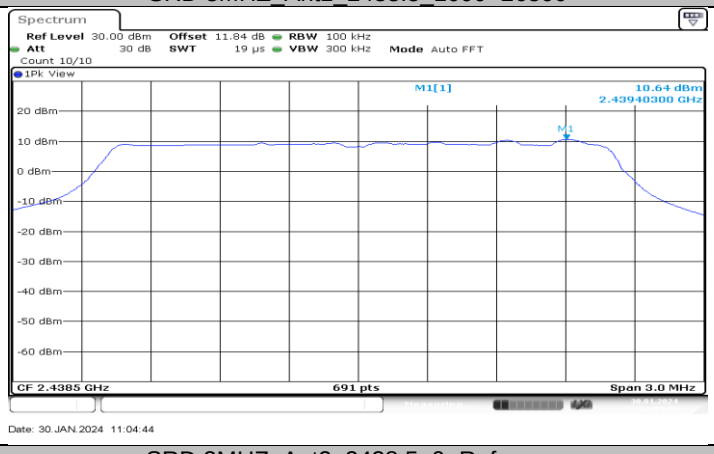
SRD 3MHZ_Ant1_2438.5_0~Reference



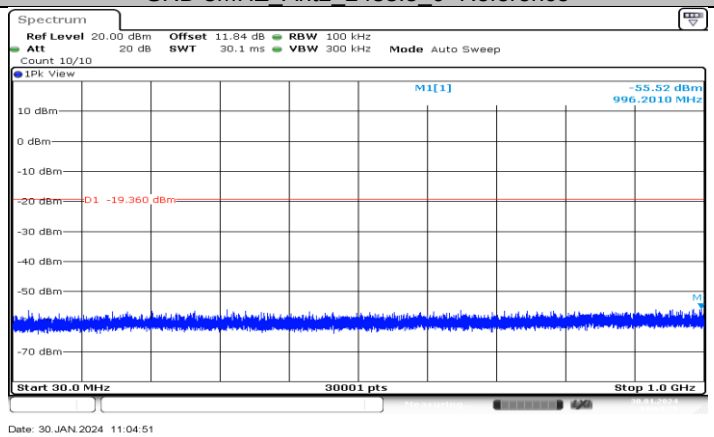
SRD 3MHZ_Ant1_2438.5_30~1000



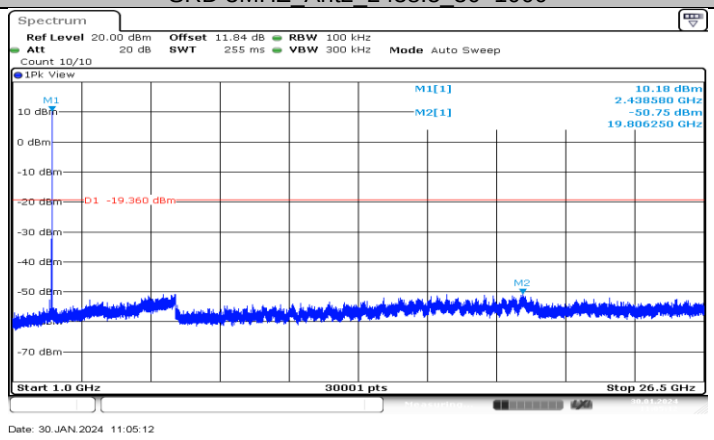
SRD 3MHZ_Ant1_2438.5_1000~26500



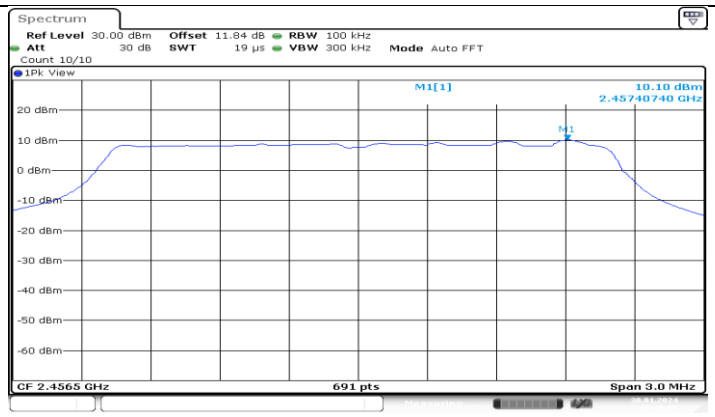
SRD 3MHZ_Ant2_2438.5_0~Reference



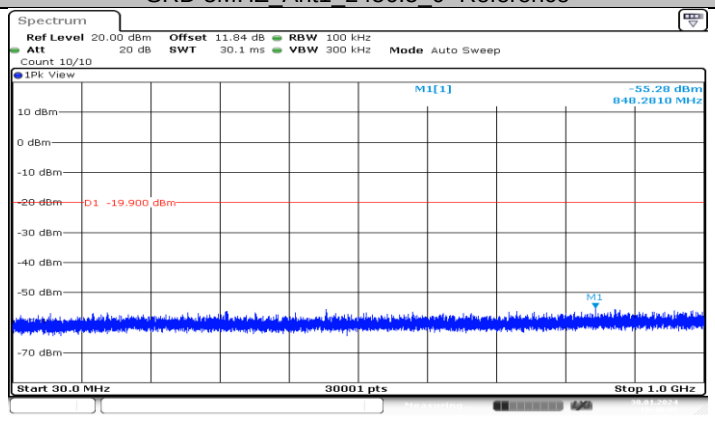
SRD 3MHZ_Ant2_2438.5_30~1000



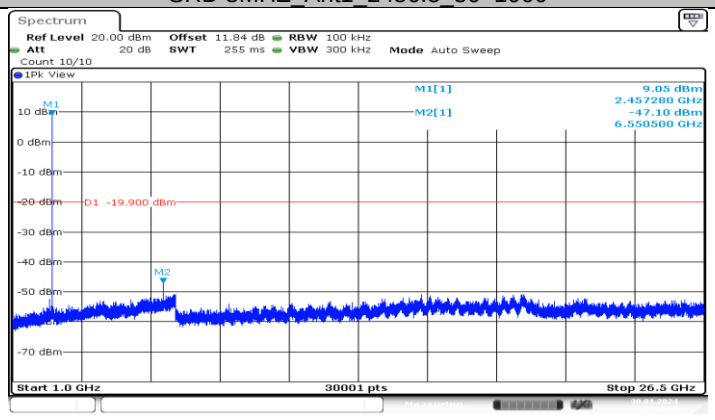
SRD 3MHZ_Ant2_2438.5_1000~26500



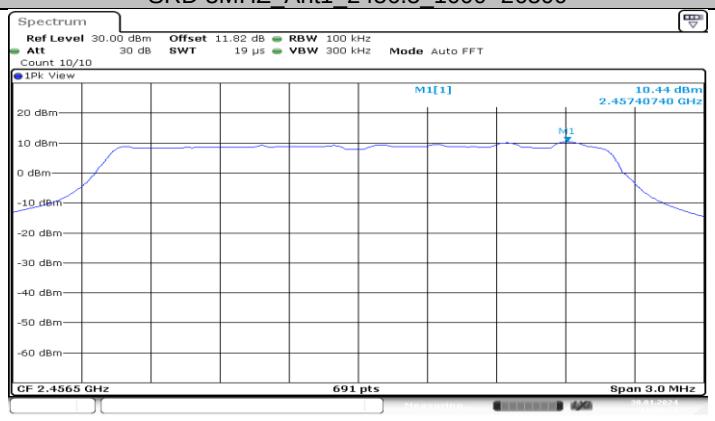
SRD 3MHZ_Ant1_2456.5_0~Reference



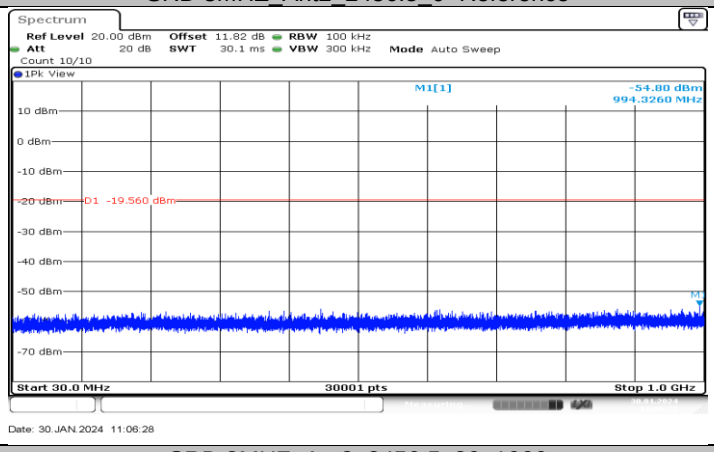
SRD 3MHZ_Ant1_2456.5_30~1000



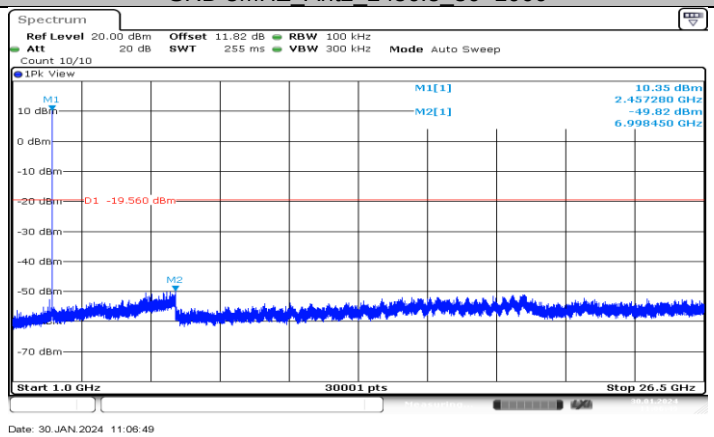
SRD 3MHZ_Ant1_2456.5_1000~26500



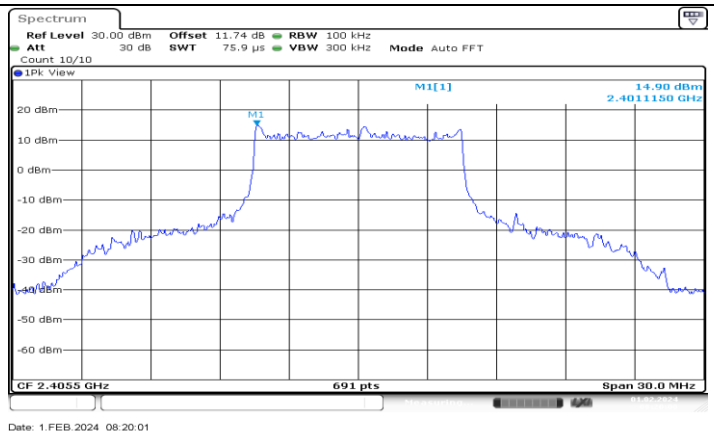
SRD 3MHZ_Ant2_2456.5_0~Reference



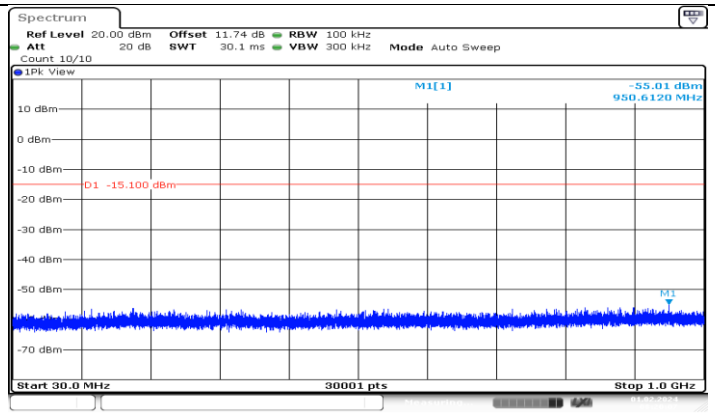
SRD 3MHZ_Ant2_2456.5_30~1000



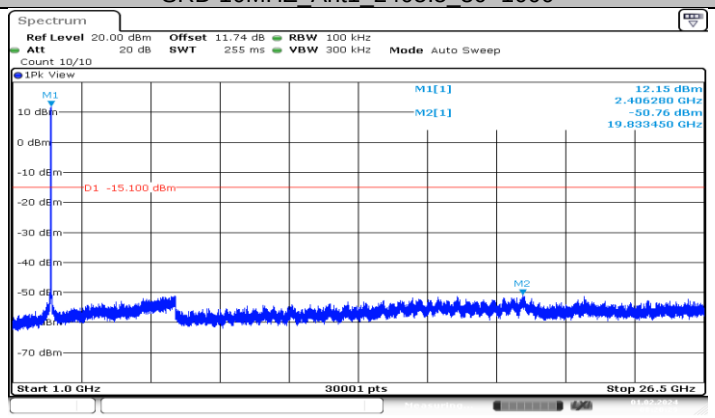
SRD 3MHZ_Ant2_2456.5_1000~26500



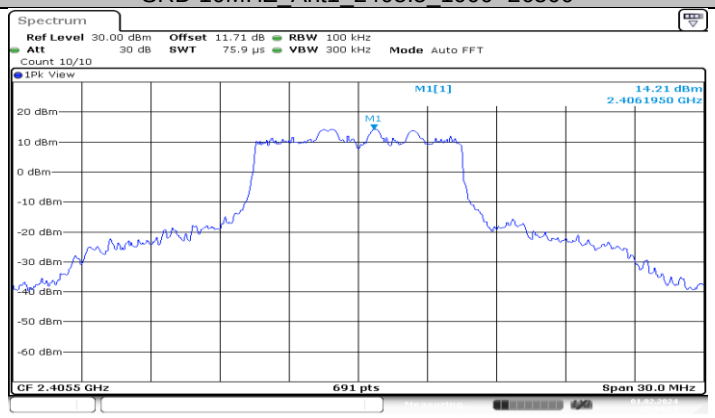
SRD 10MHZ_Ant1_2405.5_0~Reference



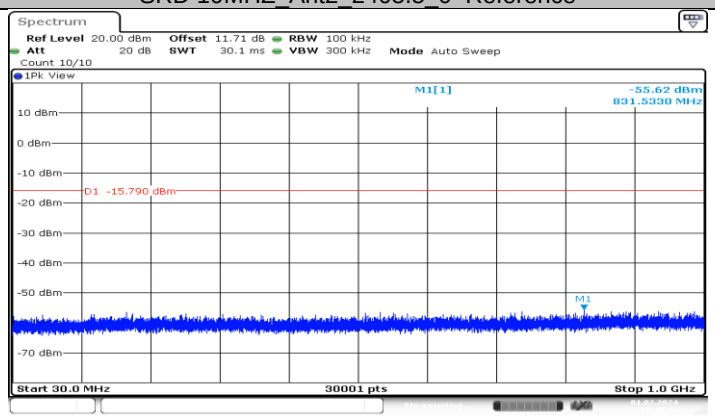
SRD 10MHZ_Ant1_2405.5_30~1000



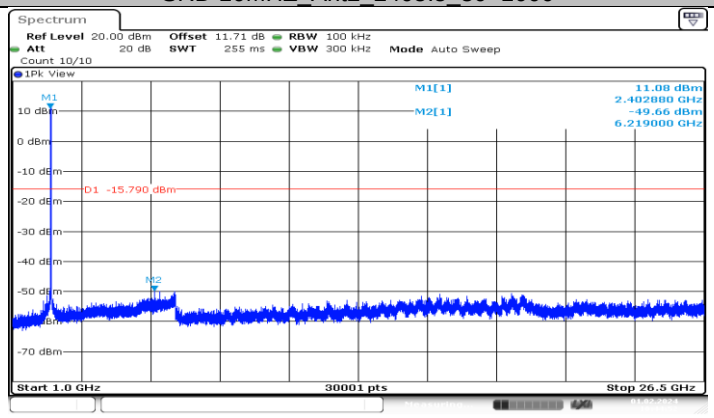
SRD 10MHZ_Ant1_2405.5_1000~26500



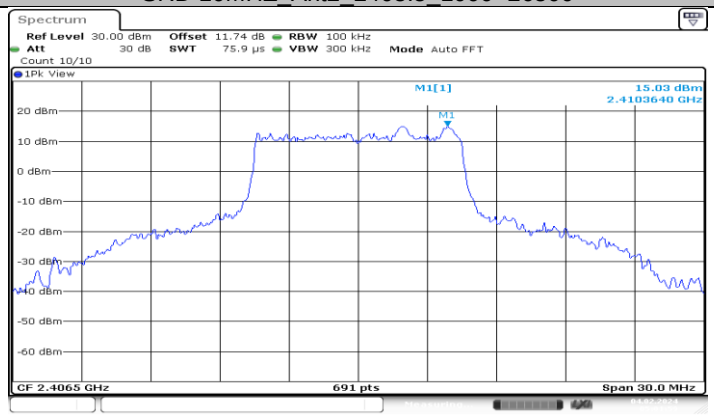
SRD 10MHZ_Ant2_2405.5_0~Reference



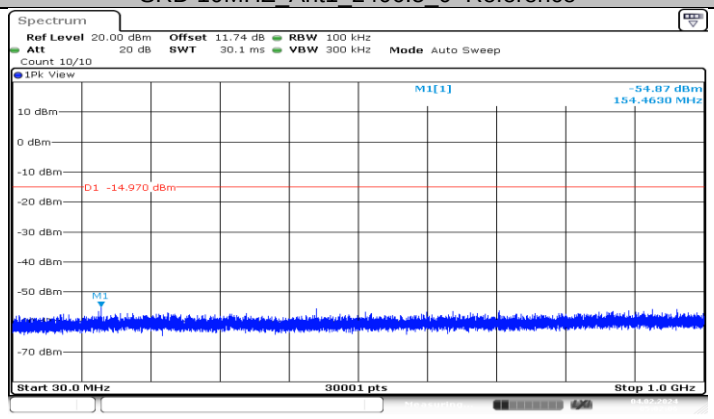
SRD 10MHZ_Ant2_2405.5_30~1000



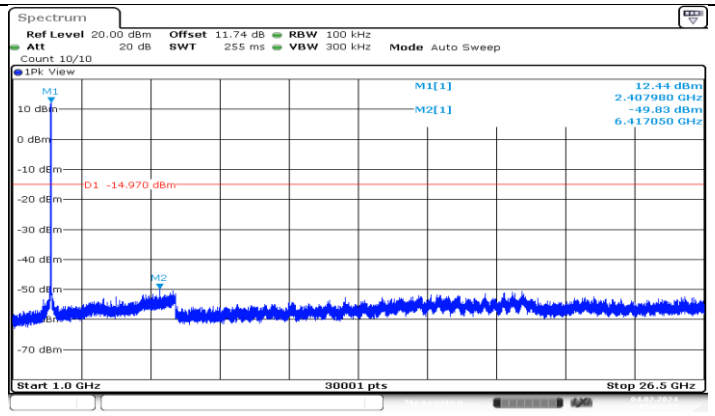
SRD 10MHZ_Ant2_2405.5_1000~26500



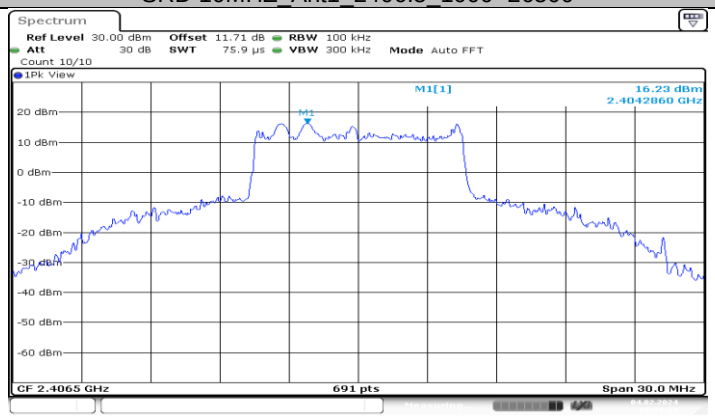
SRD 10MHZ_Ant1_2406.5_0~Reference



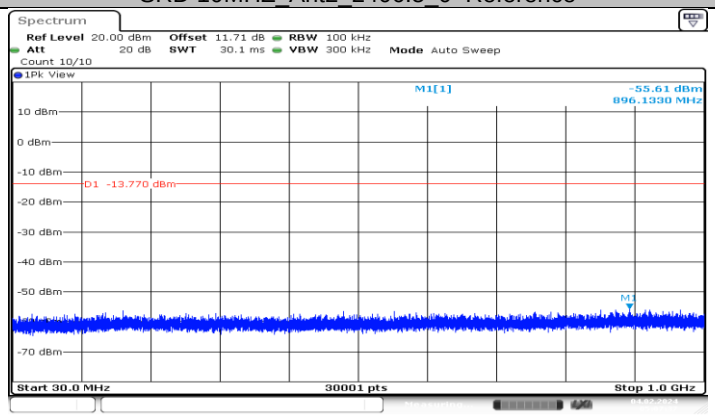
SRD 10MHZ_Ant1_2406.5_30~1000



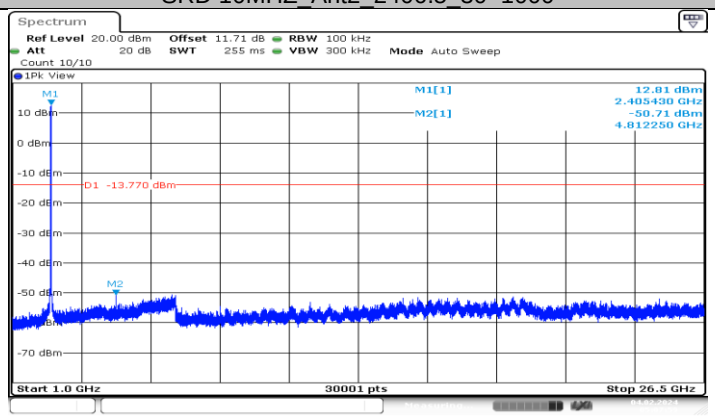
SRD 10MHZ_Ant1_2406.5_1000~26500



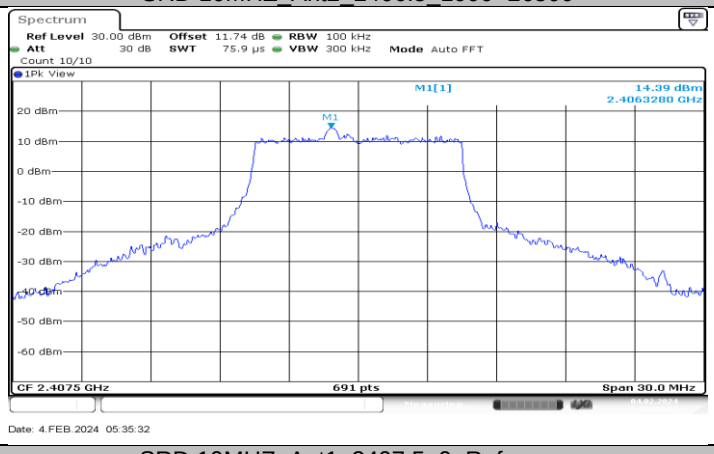
SRD 10MHZ_Ant2_2406.5_0~Reference



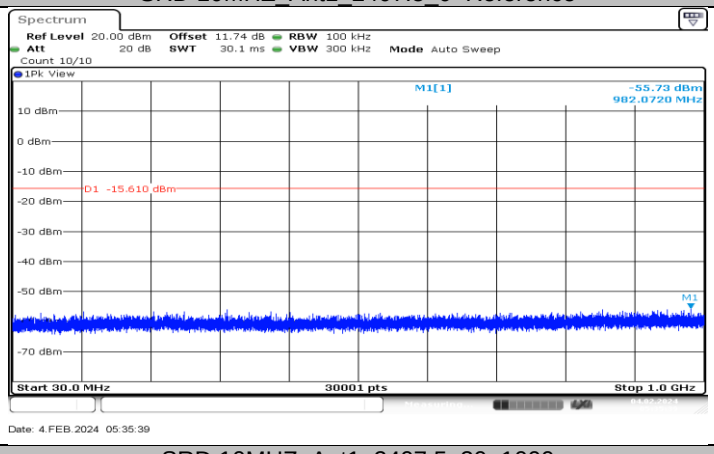
SRD 10MHZ_Ant2_2406.5_30~1000



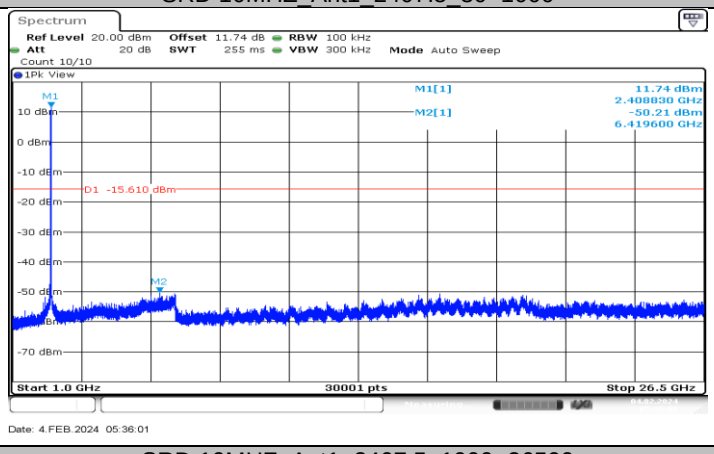
SRD 10MHZ_Ant2_2406.5_1000~26500



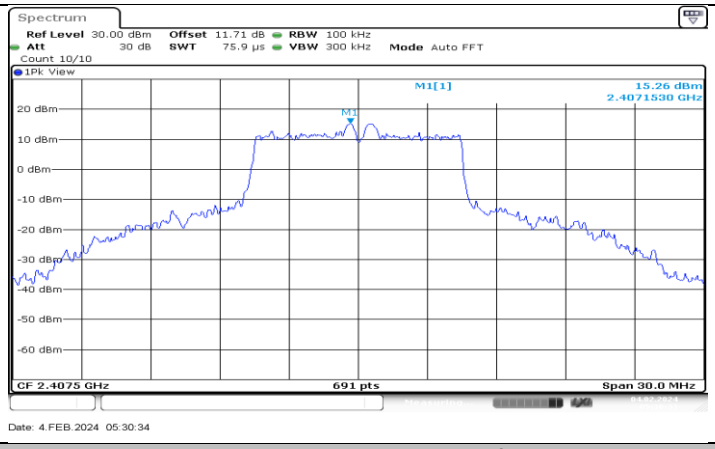
SRD 10MHZ_Ant1_2407.5_0~Reference



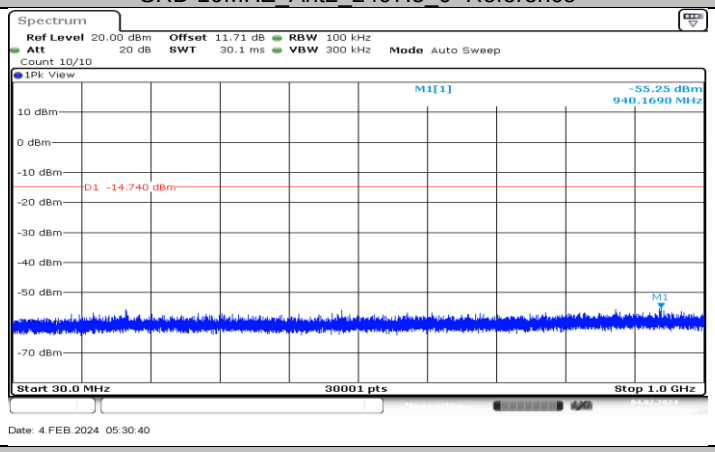
SRD 10MHZ_Ant1_2407.5_30~1000



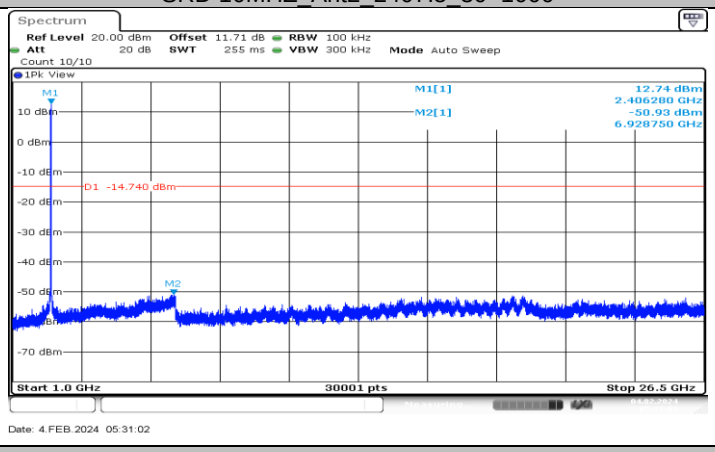
SRD 10MHZ_Ant1_2407.5_1000~26500



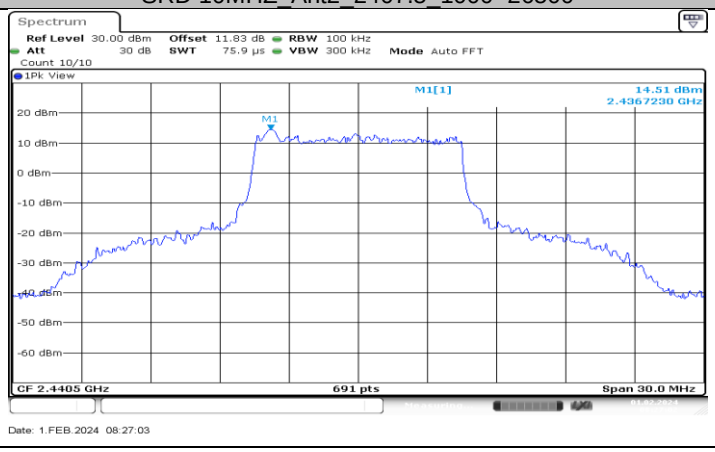
SRD 10MHZ_Ant2_2407.5_0~Reference



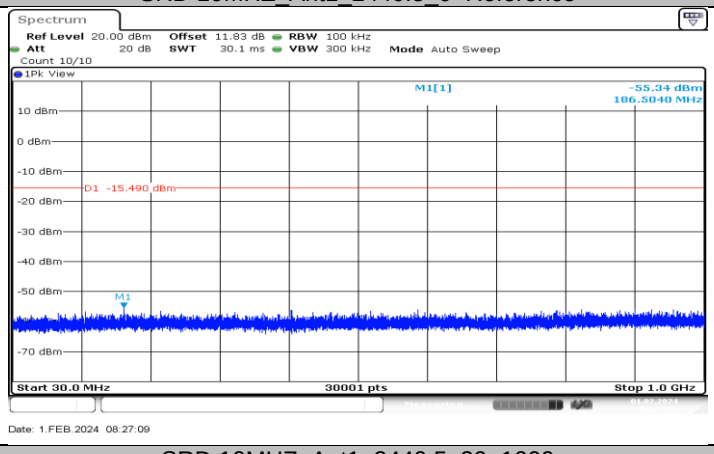
SRD 10MHZ_Ant2_2407.5_30~1000



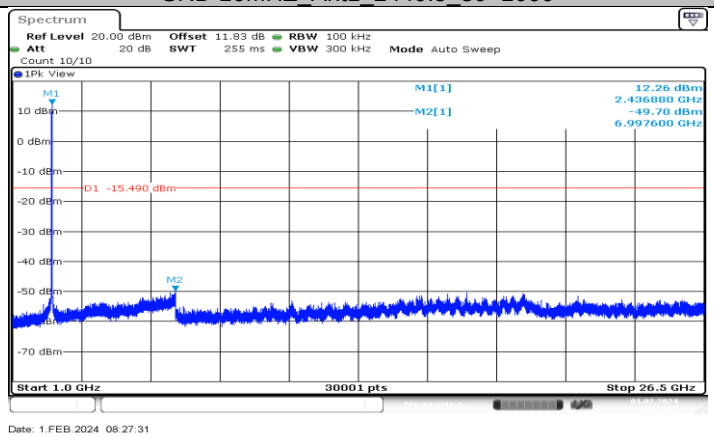
SRD 10MHZ_Ant2_2407.5_1000~26500



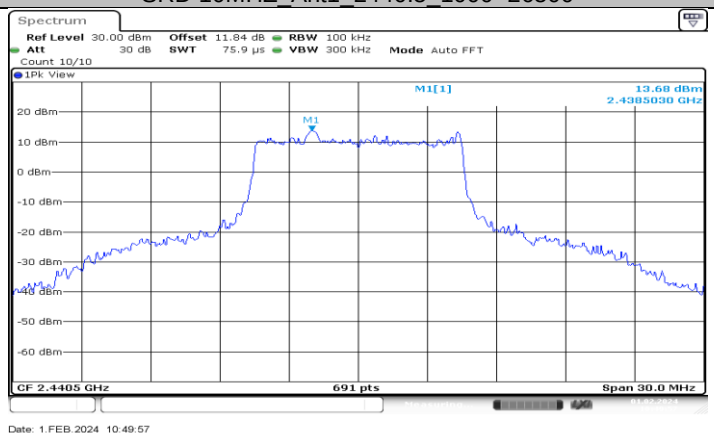
SRD 10MHZ_Ant1_2440.5_0~Reference



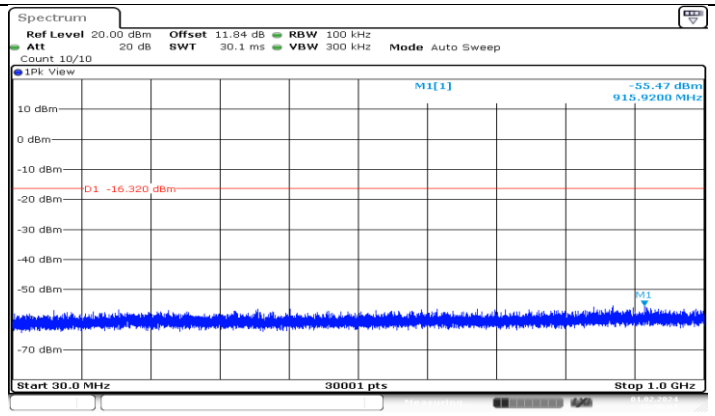
SRD 10MHZ_Ant1_2440.5_30~1000



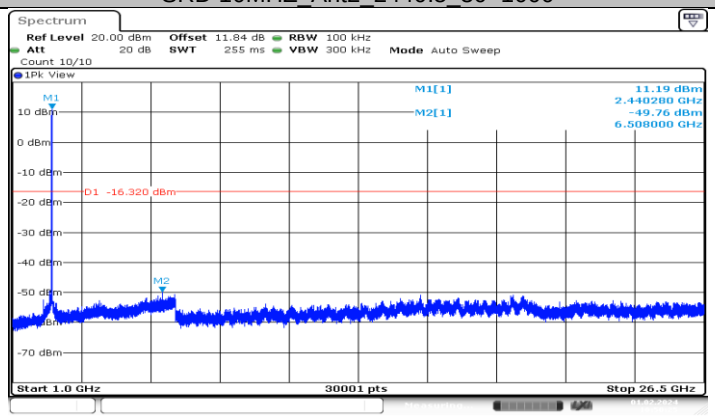
SRD 10MHZ_Ant1_2440.5_1000~26500



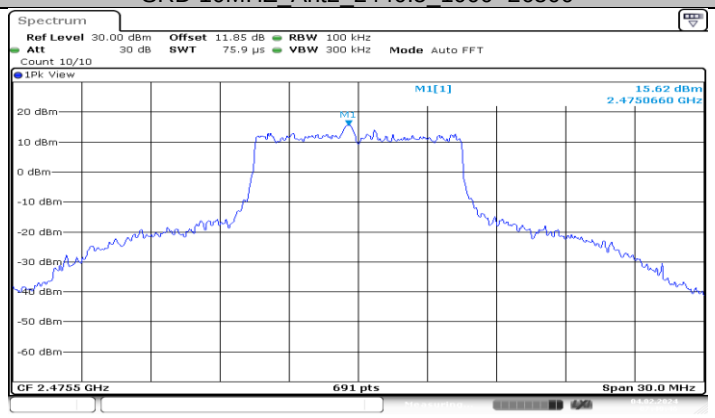
SRD 10MHZ_Ant2_2440.5_0~Reference



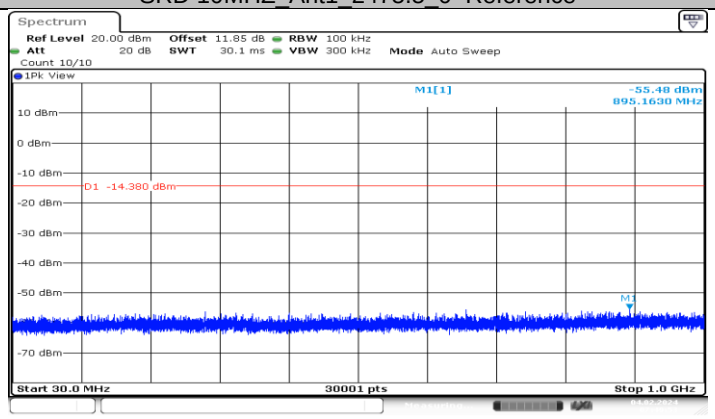
SRD 10MHZ_Ant2_2440.5_30~1000



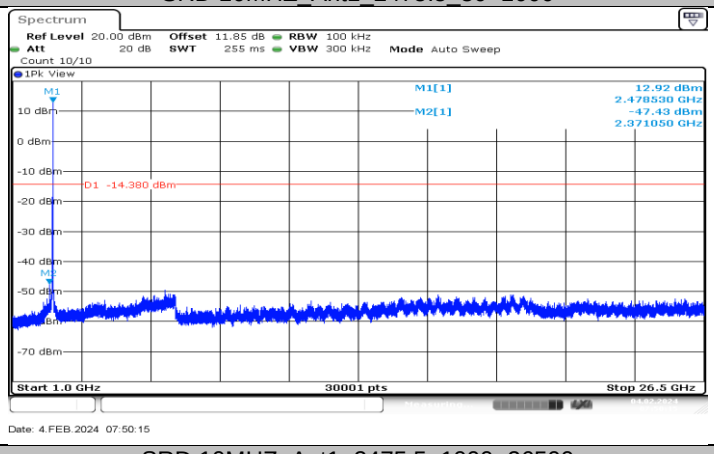
SRD 10MHZ_Ant2_2440.5_1000~26500



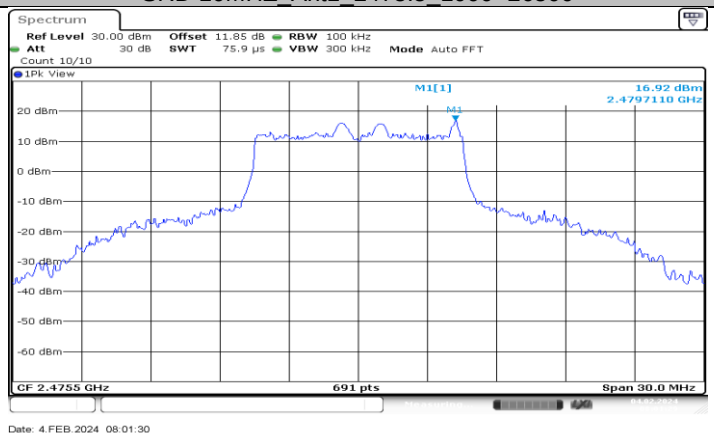
SRD 10MHZ_Ant1_2475.5_0~Reference



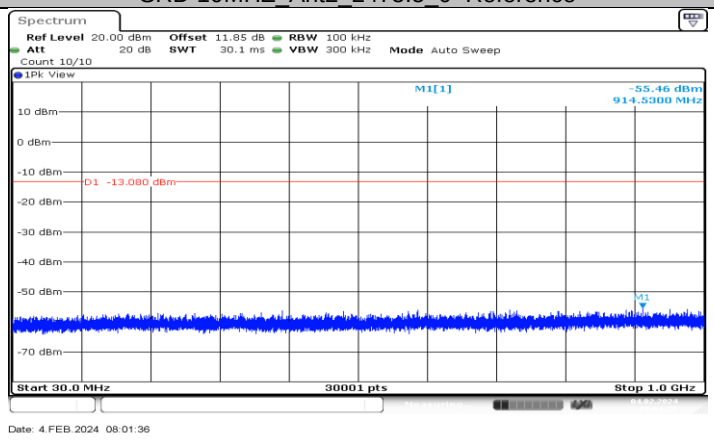
SRD 10MHZ_Ant1_2475.5_30~1000



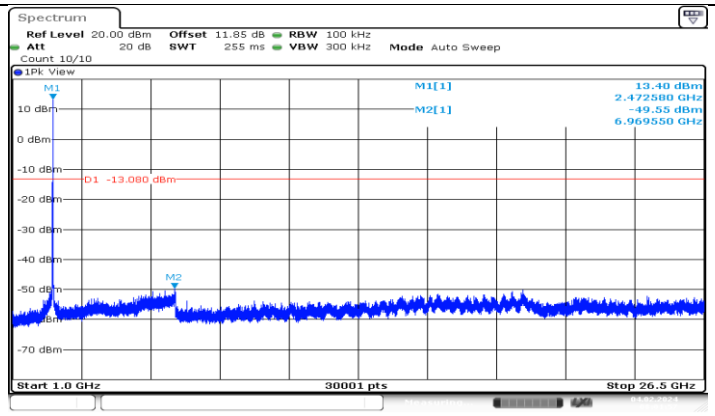
SRD 10MHZ_Ant1_2475.5_1000~26500



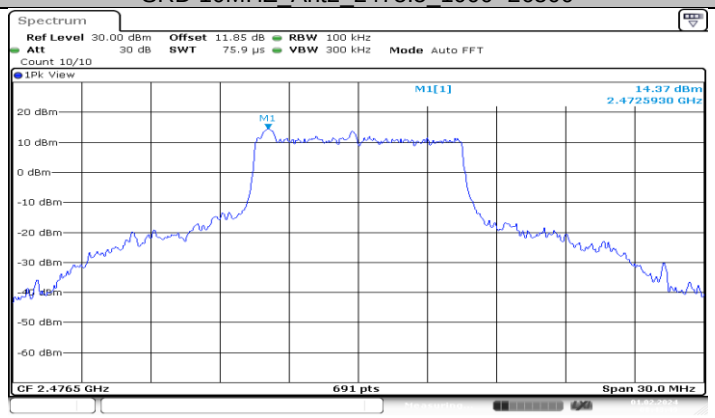
SRD 10MHZ_Ant2_2475.5_0~Reference



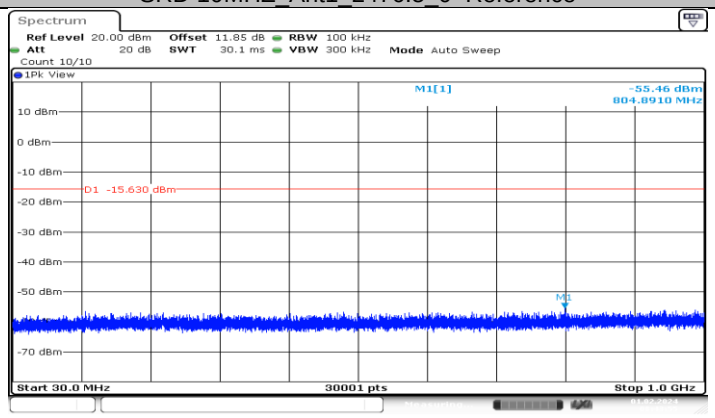
SRD 10MHZ_Ant2_2475.5_30~1000



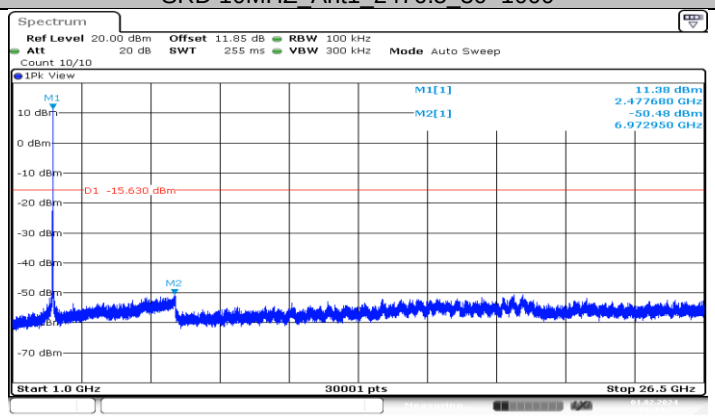
SRD 10MHZ_Ant2_2475.5_1000~26500



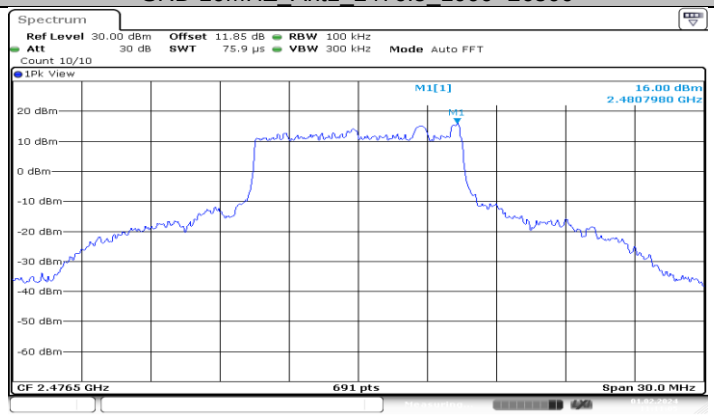
SRD 10MHZ_Ant1_2476.5_0~Reference



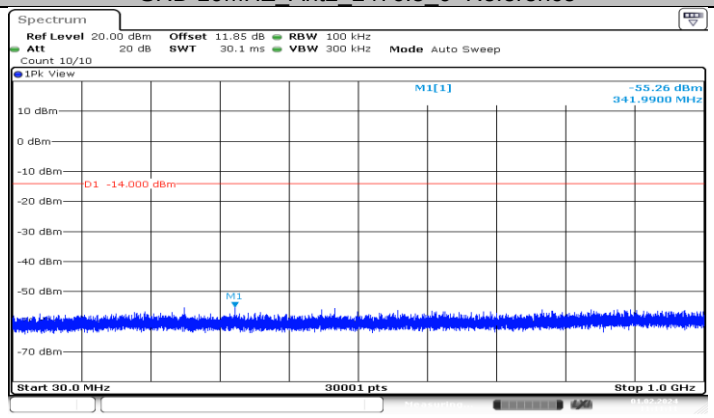
SRD 10MHZ_Ant1_2476.5_30~1000



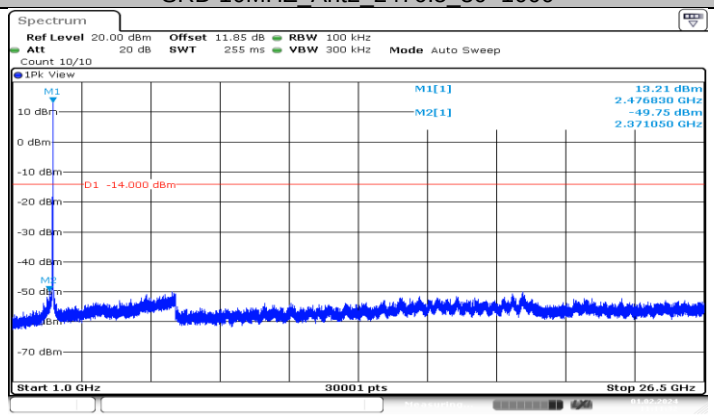
SRD 10MHZ_Ant1_2476.5_1000~26500



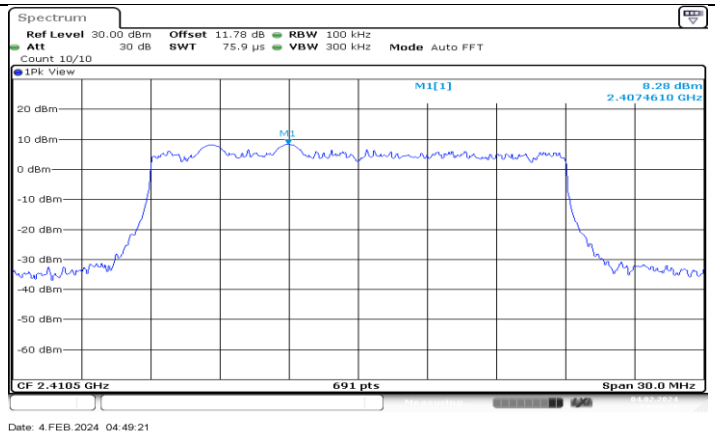
SRD 10MHZ_Ant2_2476.5_0~Reference



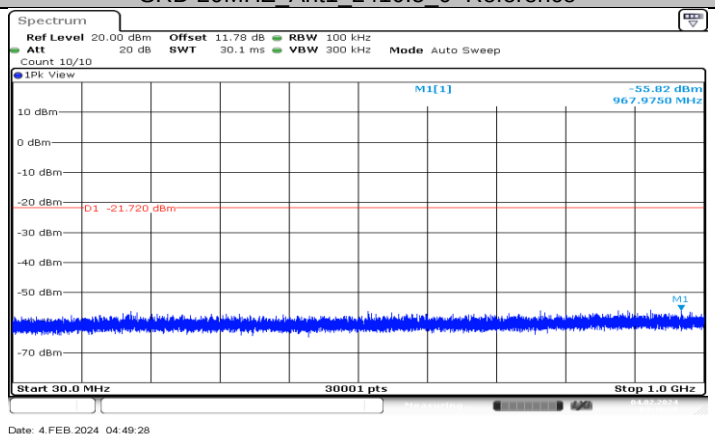
SRD 10MHZ_Ant2_2476.5_30~1000



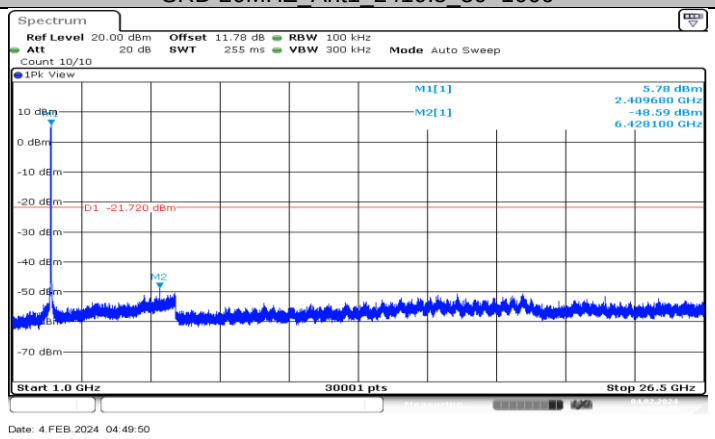
SRD 10MHZ_Ant2_2476.5_1000~26500



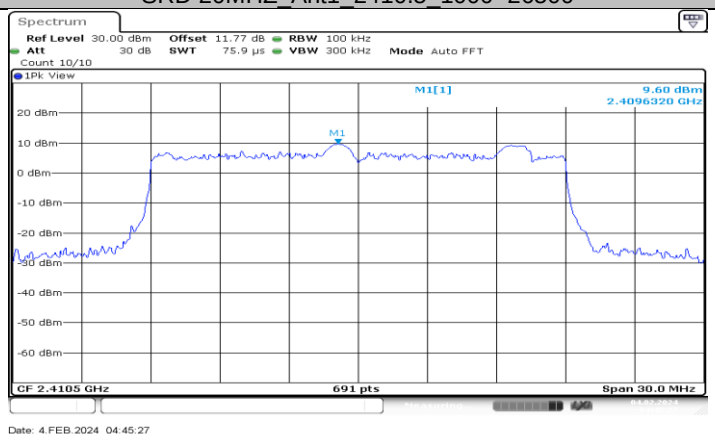
SRD 20MHZ_Ant1_2410.5_0~Reference



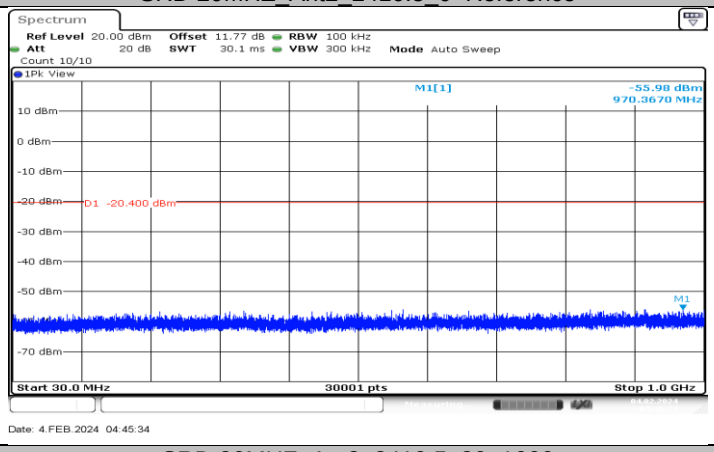
SRD 20MHZ_Ant1_2410.5_30~1000



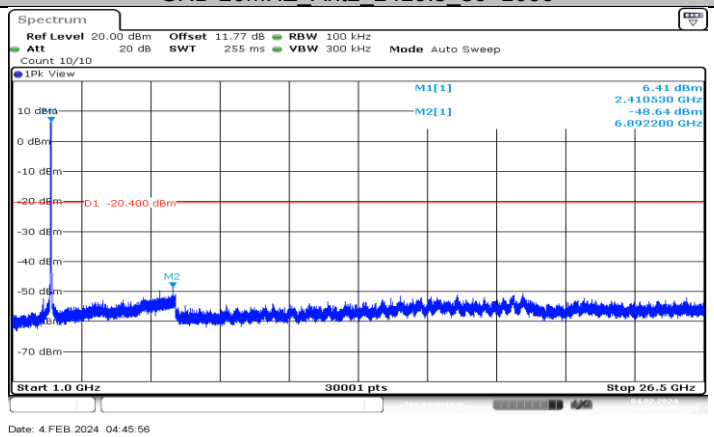
SRD 20MHZ_Ant1_2410.5_1000~26500



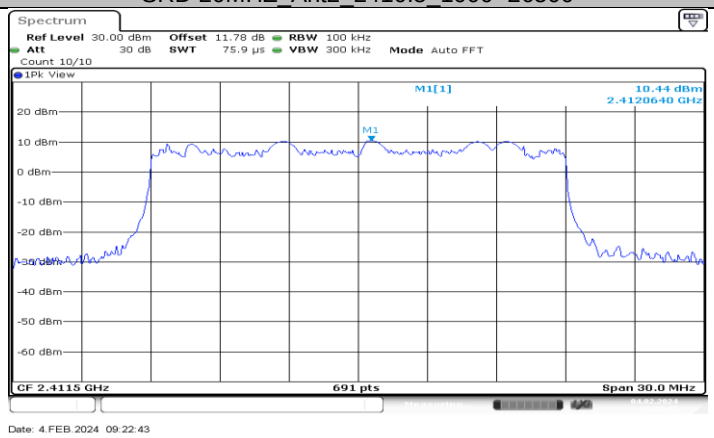
SRD 20MHZ_Ant2_2410.5_0~Reference



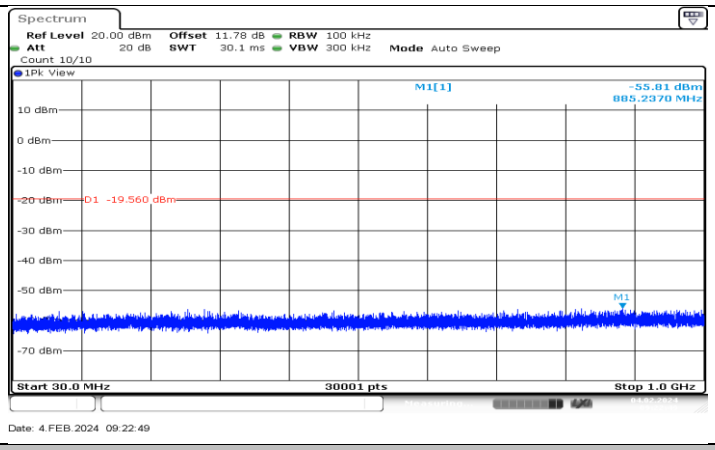
SRD 20MHZ_Ant2_2410.5_30~1000



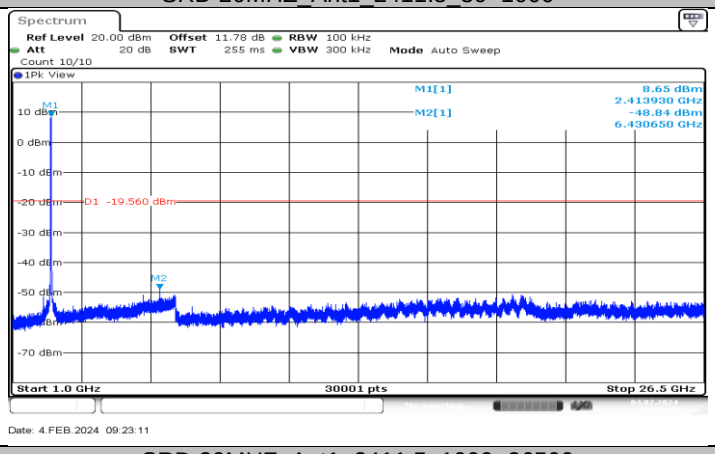
SRD 20MHZ_Ant2_2410.5_1000~26500



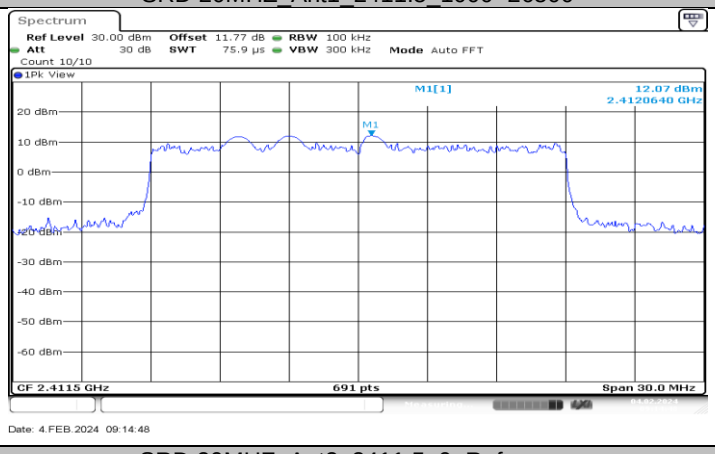
SRD 20MHZ_Ant1_2411.5_0~Reference



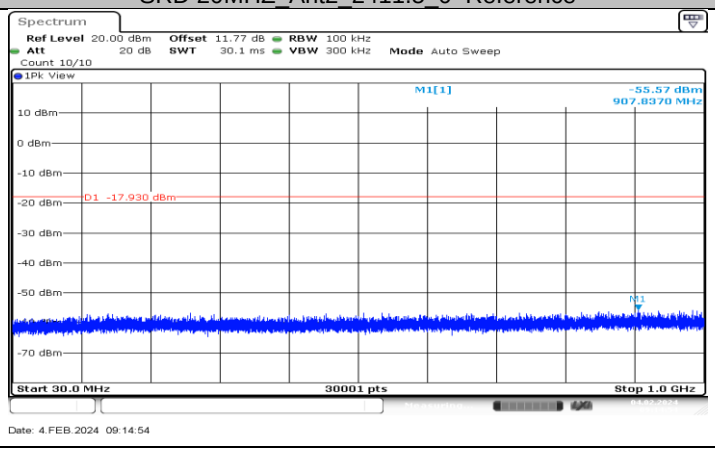
SRD 20MHZ_Ant1_2411.5_30~1000



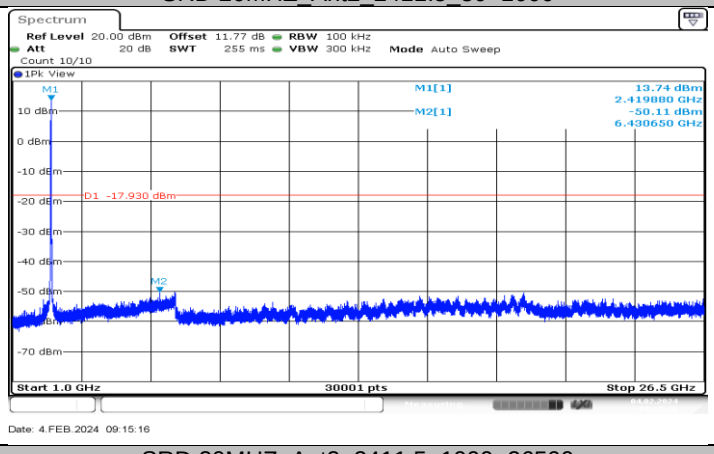
SRD 20MHZ_Ant1_2411.5_1000~26500



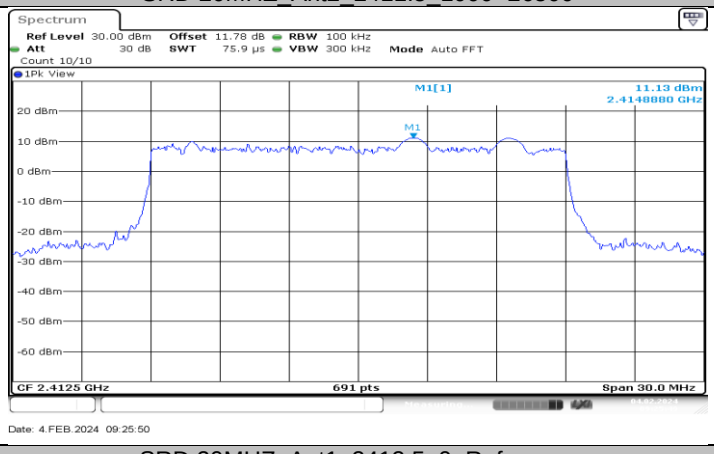
SRD 20MHZ_Ant2_2411.5_0~Reference



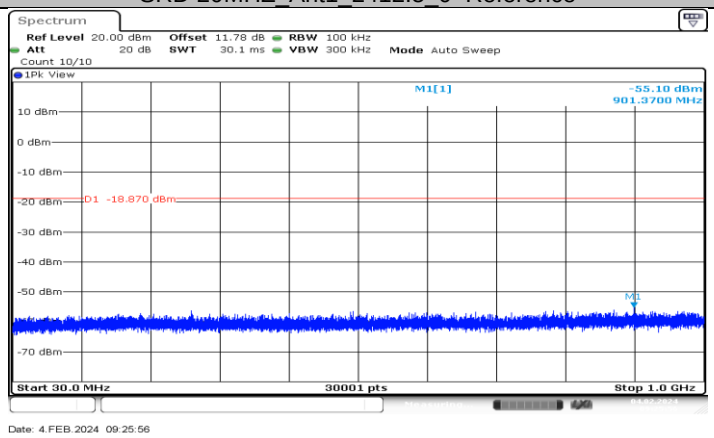
SRD 20MHZ_Ant2_2411.5_30~1000



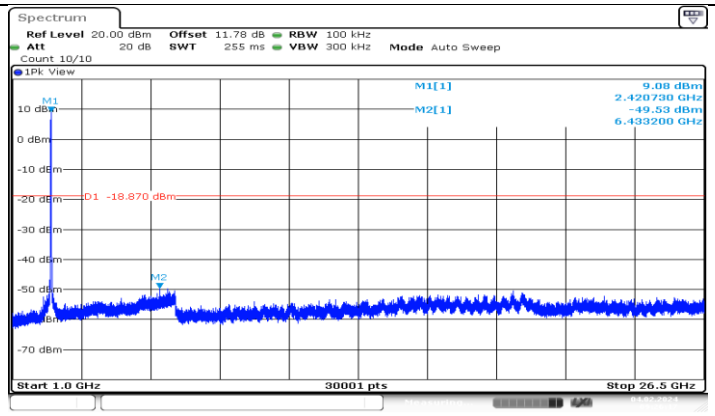
SRD 20MHZ_Ant2_2411.5_1000~26500



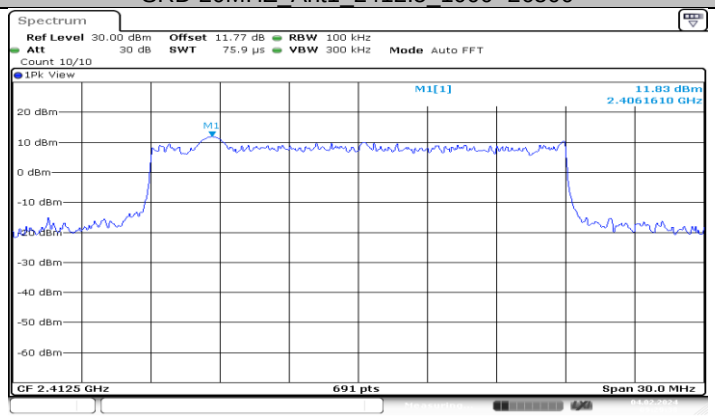
SRD 20MHZ_Ant1_2412.5_0~Reference



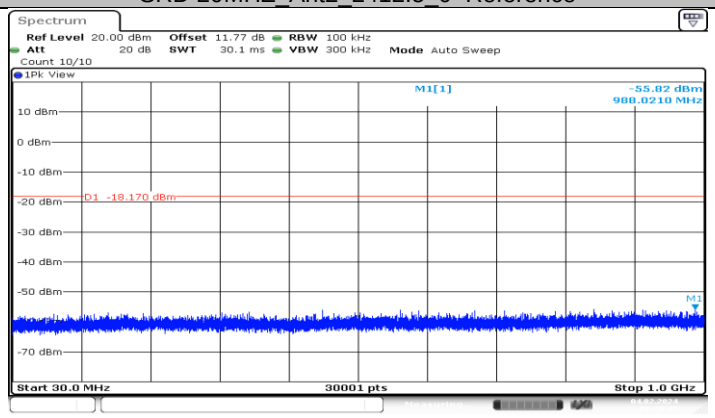
SRD 20MHZ_Ant1_2412.5_30~1000



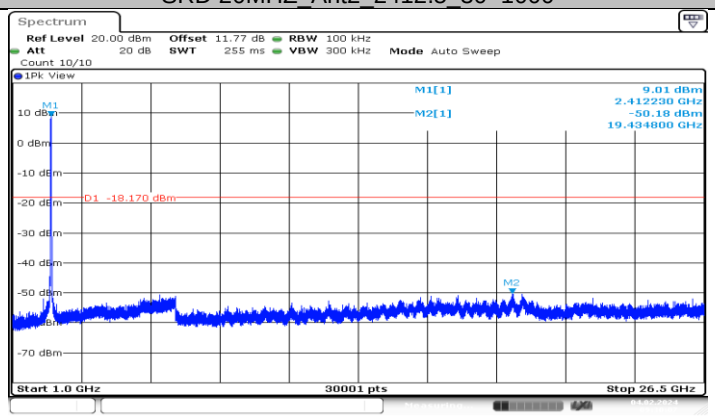
SRD 20MHZ_Ant1_2412.5_1000~26500



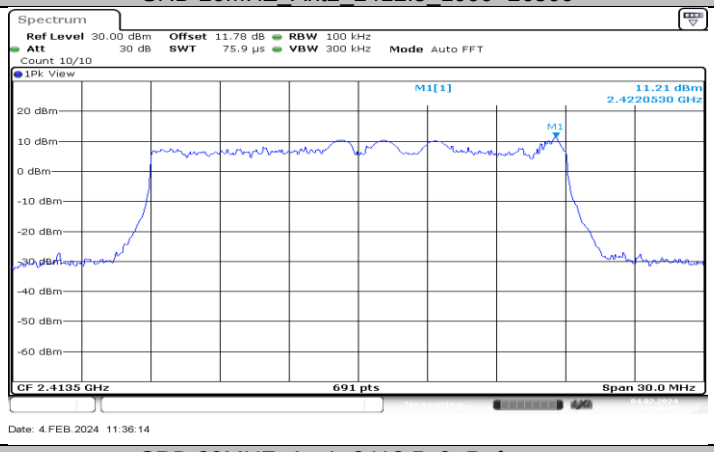
SRD 20MHZ_Ant2_2412.5_0~Reference



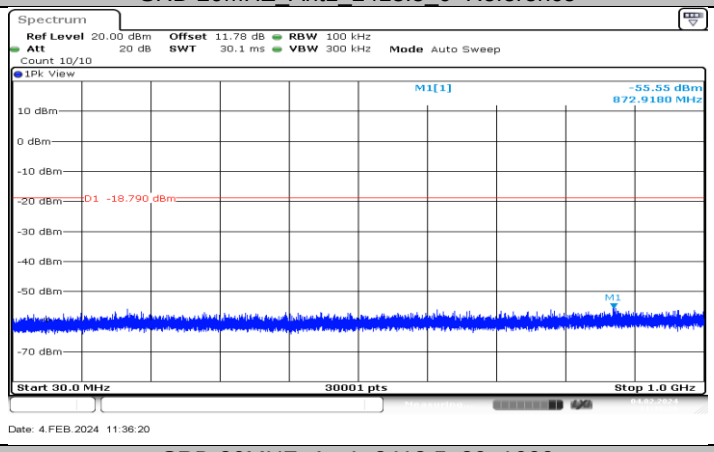
SRD 20MHZ_Ant2_2412.5_30~1000



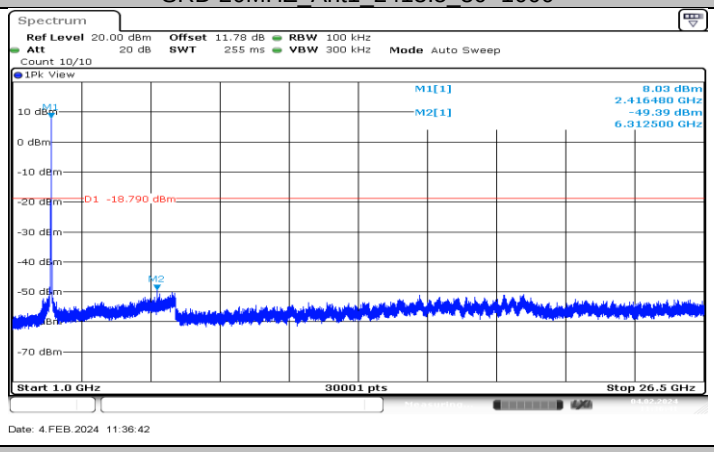
SRD 20MHZ_Ant2_2412.5_1000~26500



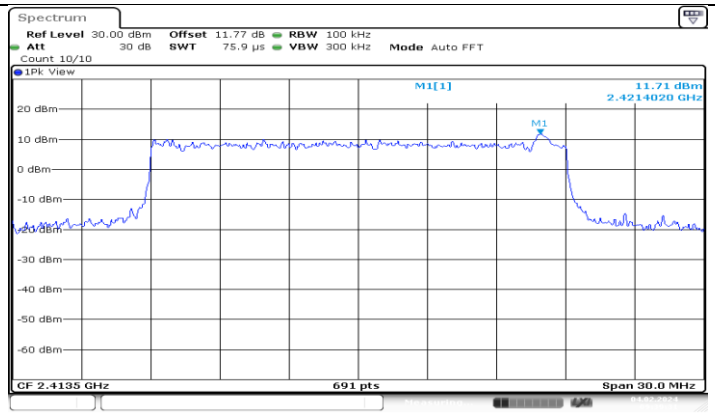
SRD 20MHZ_Ant1_2413.5_0~Reference



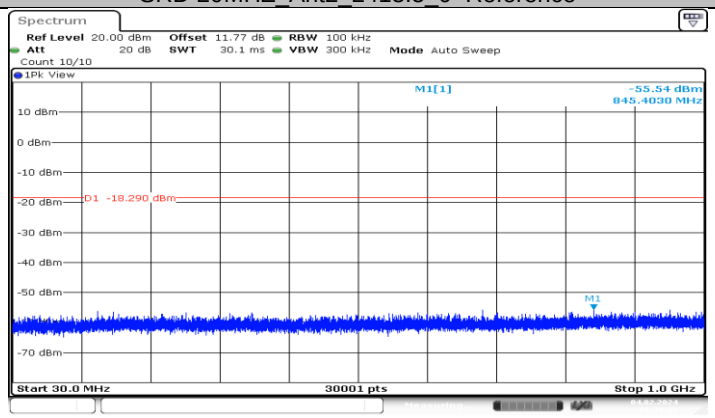
SRD 20MHZ_Ant1_2413.5_30~1000



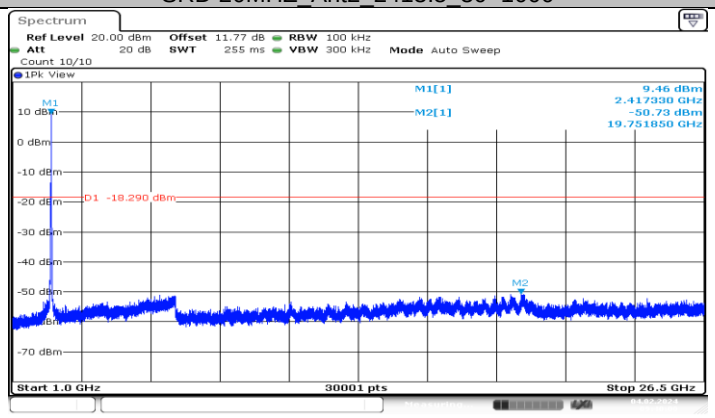
SRD 20MHZ_Ant1_2413.5_1000~26500



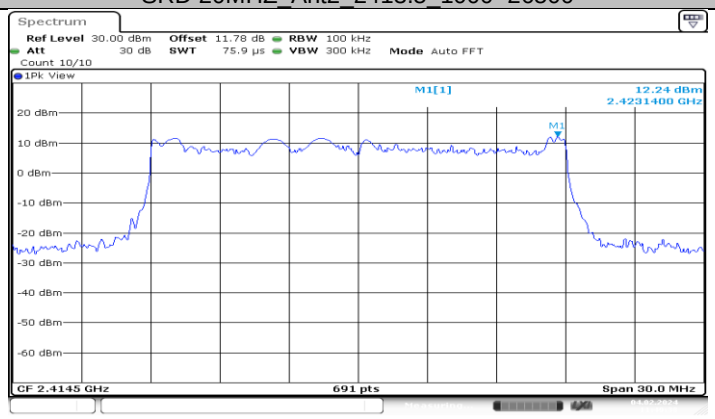
SRD 20MHZ_Ant2_2413.5_0~Reference



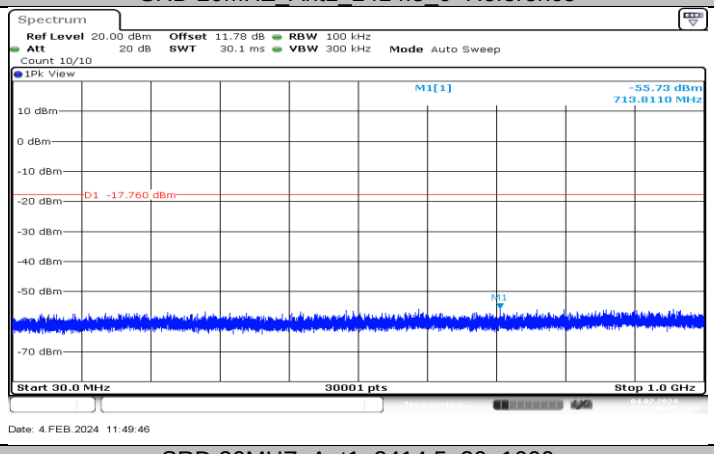
SRD 20MHZ_Ant2_2413.5_30~1000



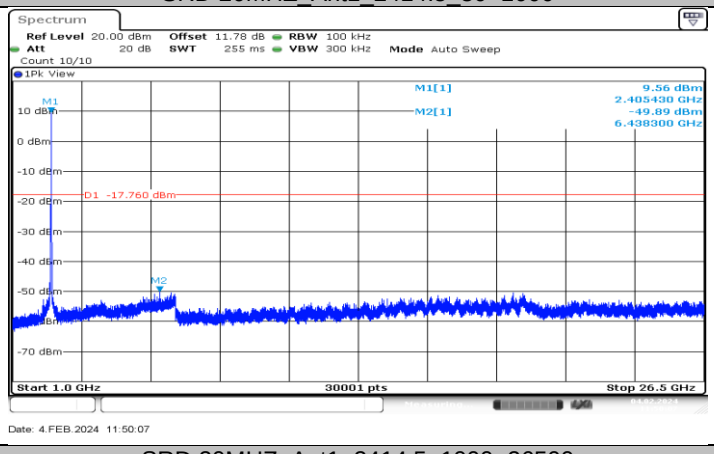
SRD 20MHZ_Ant2_2413.5_1000~26500



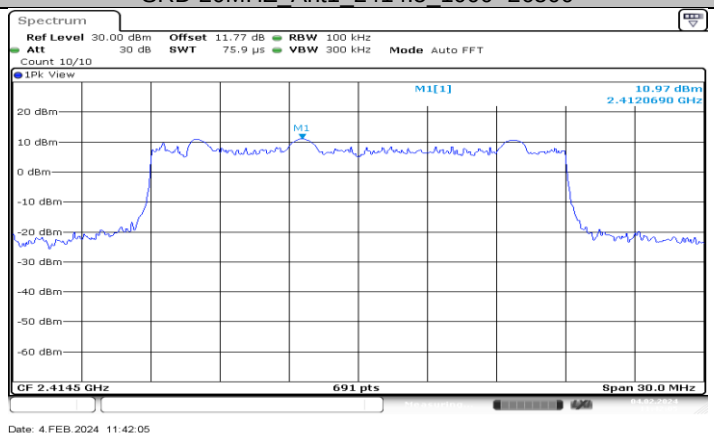
SRD 20MHZ_Ant1_2414.5_0~Reference



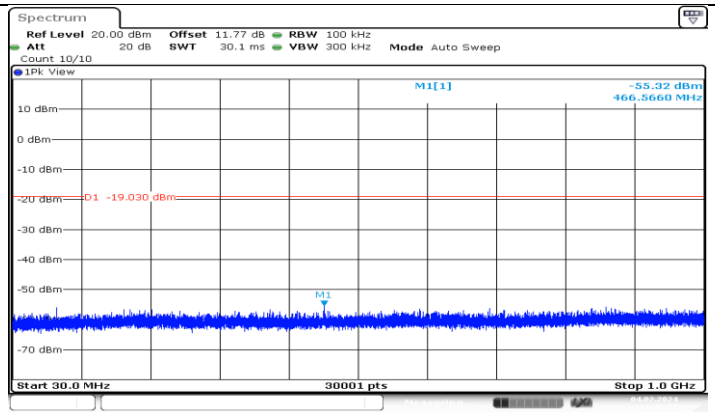
SRD 20MHZ_Ant1_2414.5_30~1000



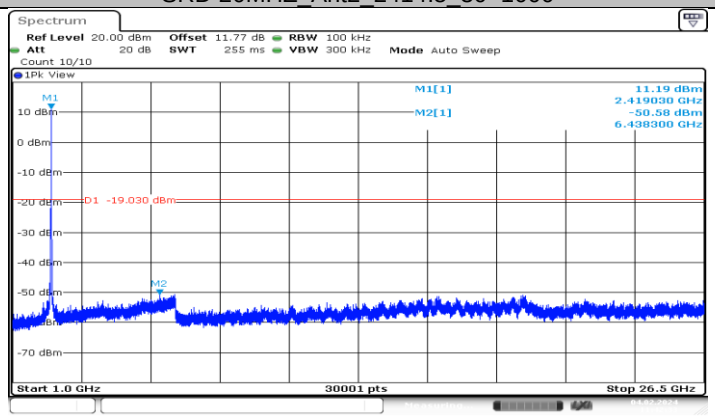
SRD 20MHZ_Ant1_2414.5_1000~26500



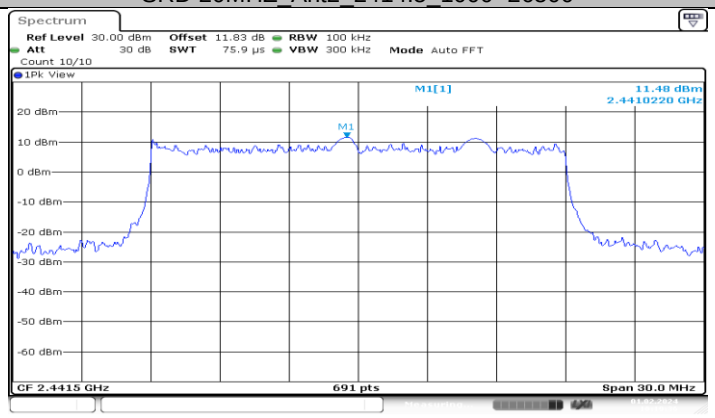
SRD 20MHZ_Ant2_2414.5_0~Reference



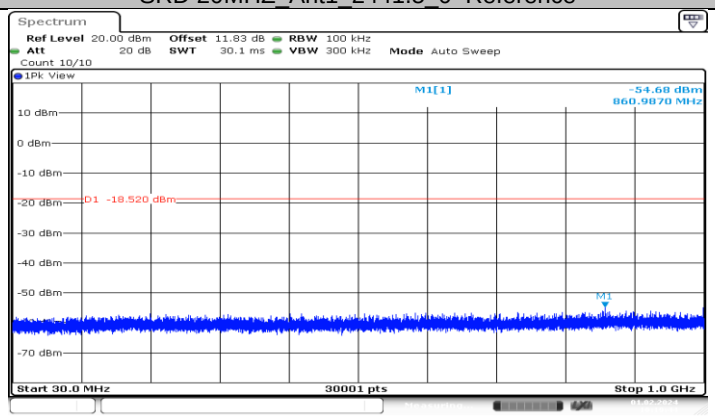
SRD 20MHZ_Ant2_2414.5_30~1000



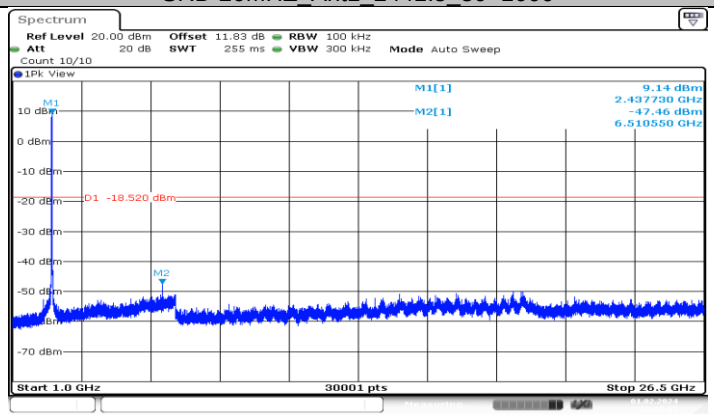
SRD 20MHZ_Ant2_2414.5_1000~26500



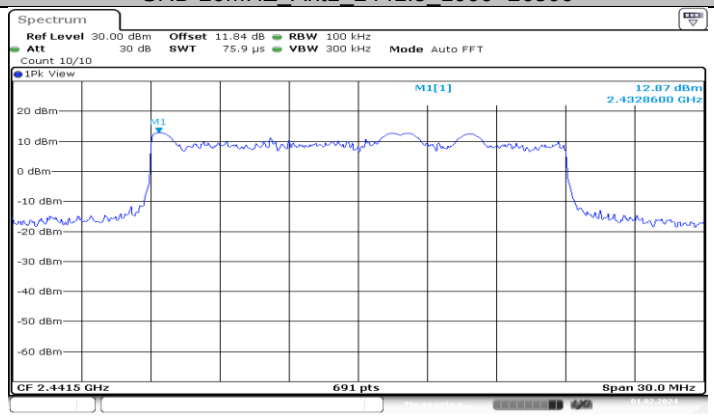
SRD 20MHZ_Ant1_2441.5_0~Reference



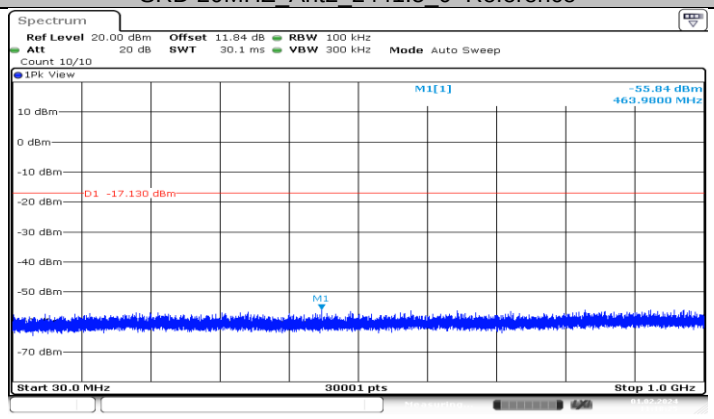
SRD 20MHZ_Ant1_2441.5_30~1000



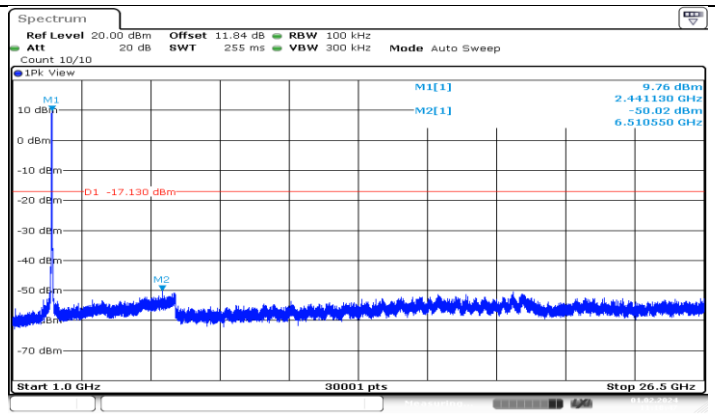
SRD 20MHZ_Ant1_2441.5_1000~26500



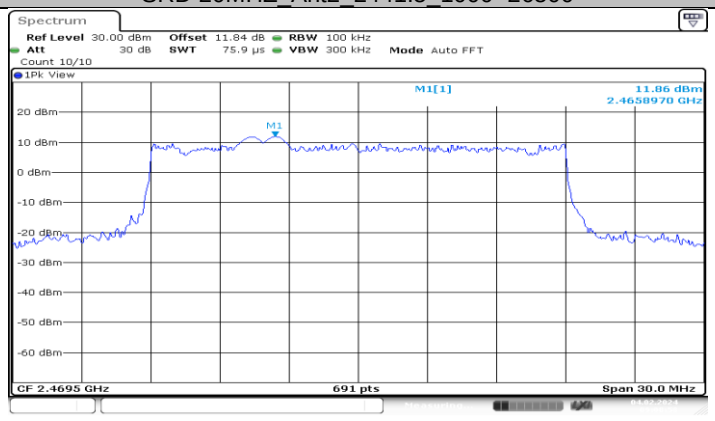
SRD 20MHZ_Ant2_2441.5_0~Reference



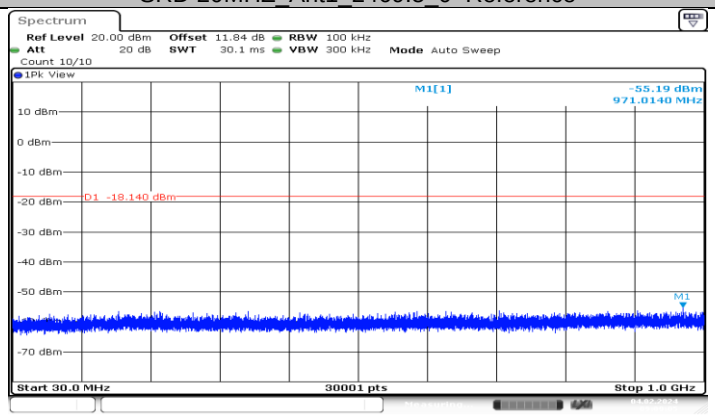
SRD 20MHZ_Ant2_2441.5_30~1000



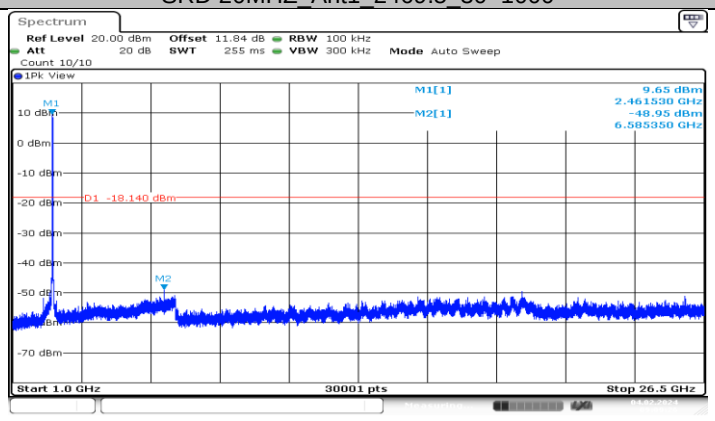
SRD 20MHZ_Ant2_2441.5_1000~26500



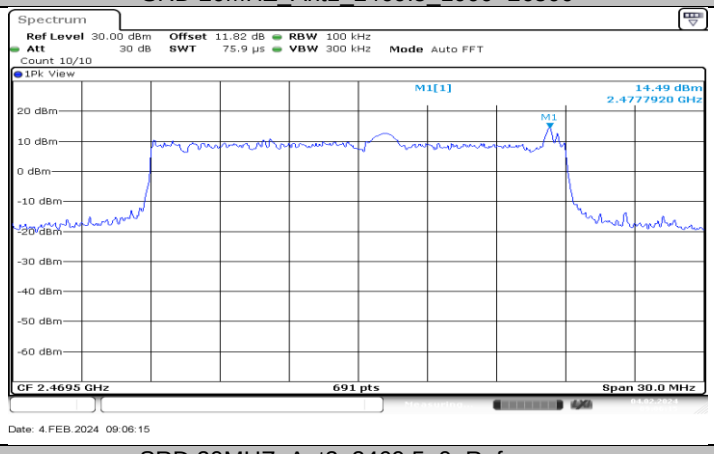
SRD 20MHZ_Ant1_2469.5_0~Reference



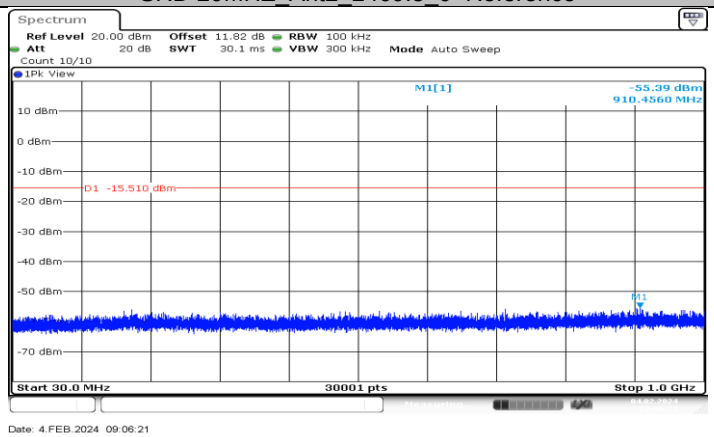
SRD 20MHZ_Ant1_2469.5_30~1000



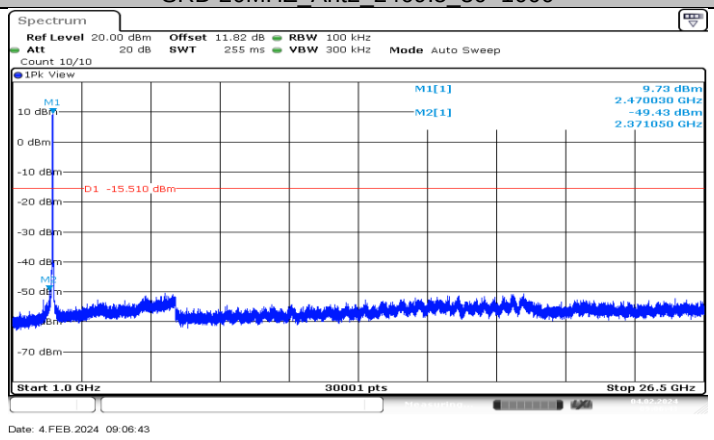
SRD 20MHZ_Ant1_2469.5_1000~26500



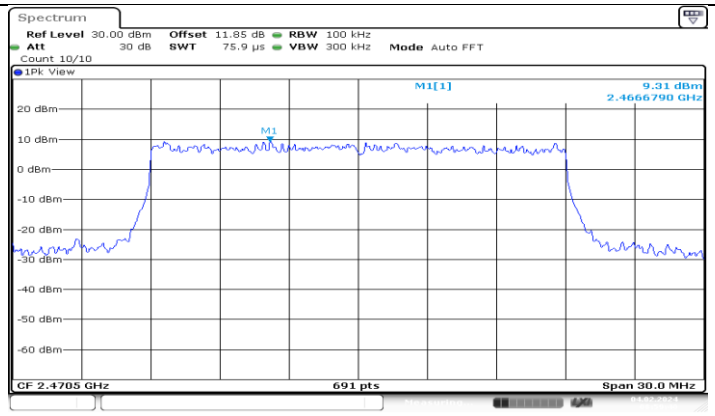
SRD 20MHZ_Ant2_2469.5_0~Reference



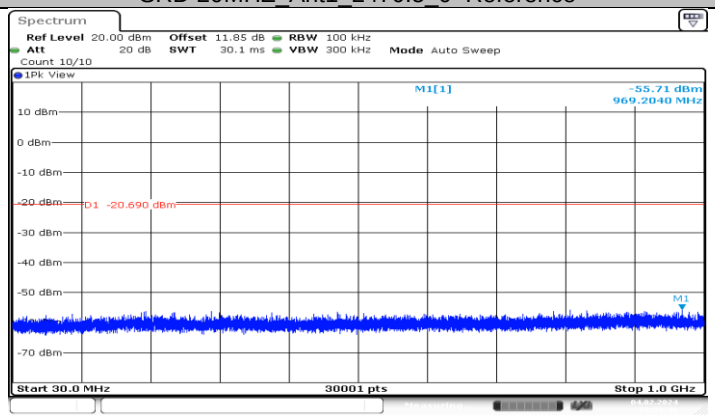
SRD 20MHZ_Ant2_2469.5_30~1000



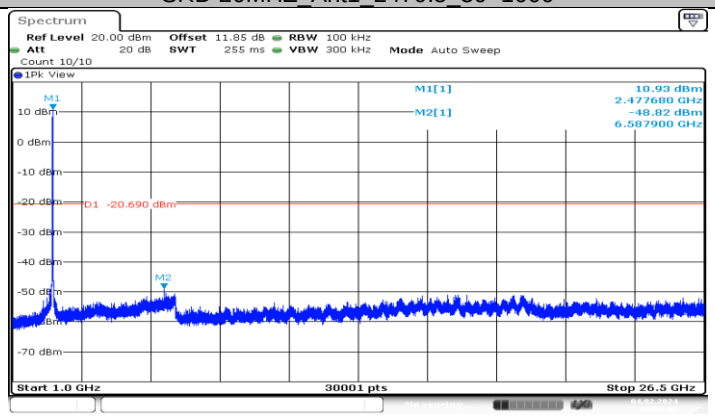
SRD 20MHZ_Ant2_2469.5_1000~26500



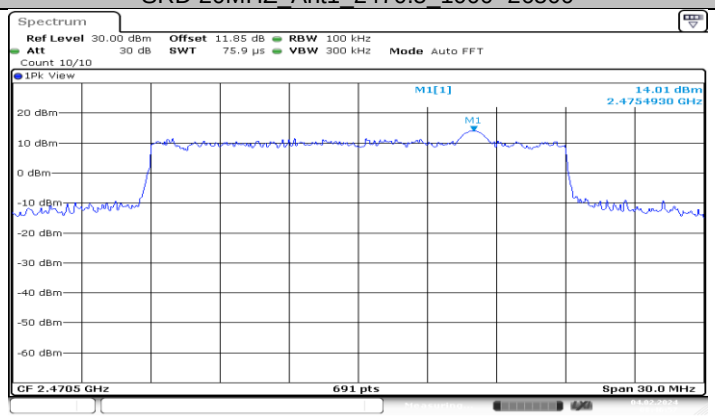
SRD 20MHZ_Ant1_2470.5_0~Reference



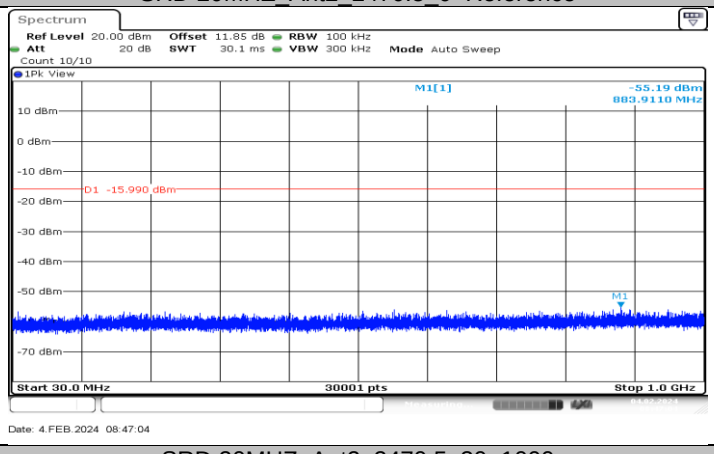
SRD 20MHZ_Ant1_2470.5_30~1000



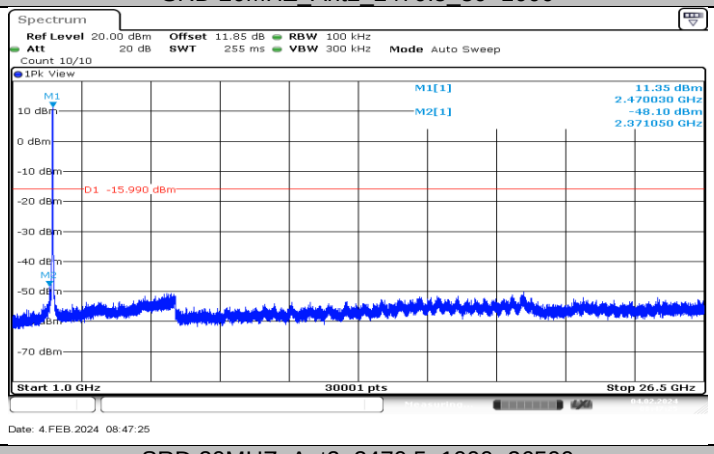
SRD 20MHZ_Ant1_2470.5_1000~26500



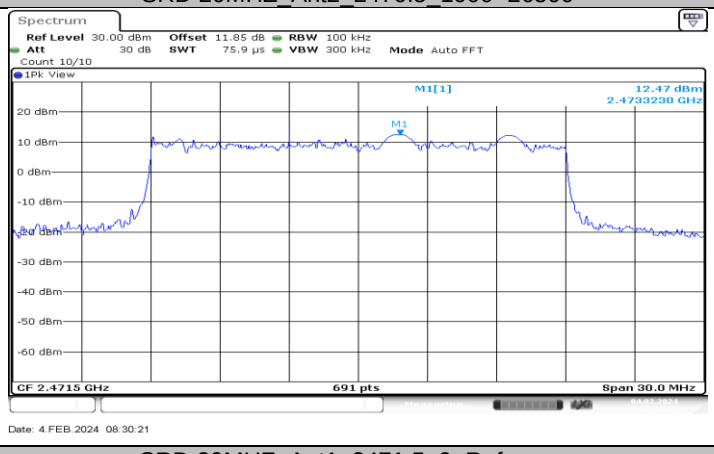
SRD 20MHZ_Ant2_2470.5_0~Reference



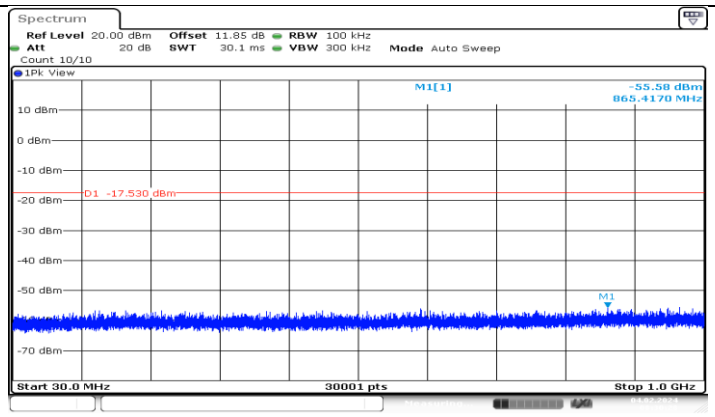
SRD 20MHZ_Ant2_2470.5_30~1000



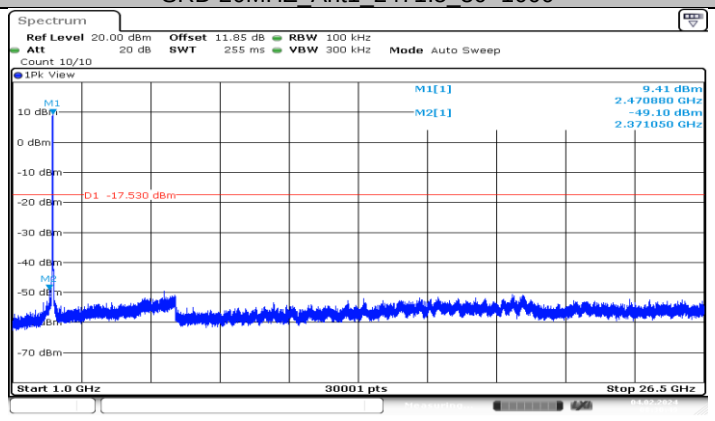
SRD 20MHZ_Ant2_2470.5_1000~26500



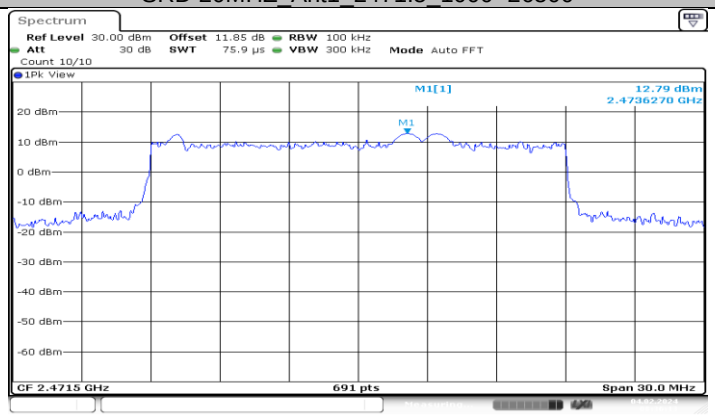
SRD 20MHZ_Ant1_2471.5_0~Reference



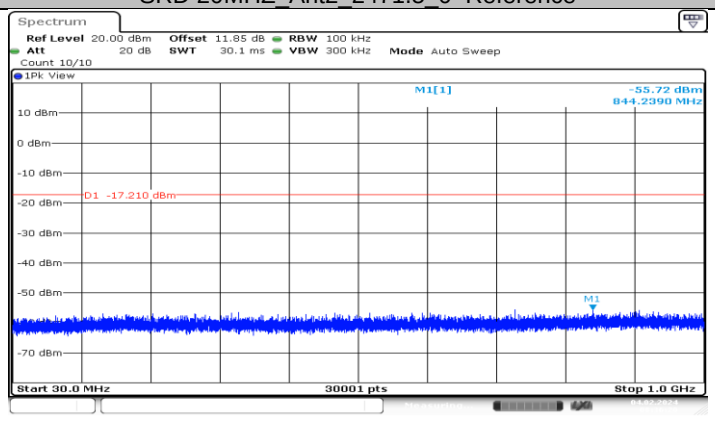
SRD 20MHZ_Ant1_2471.5_30~1000



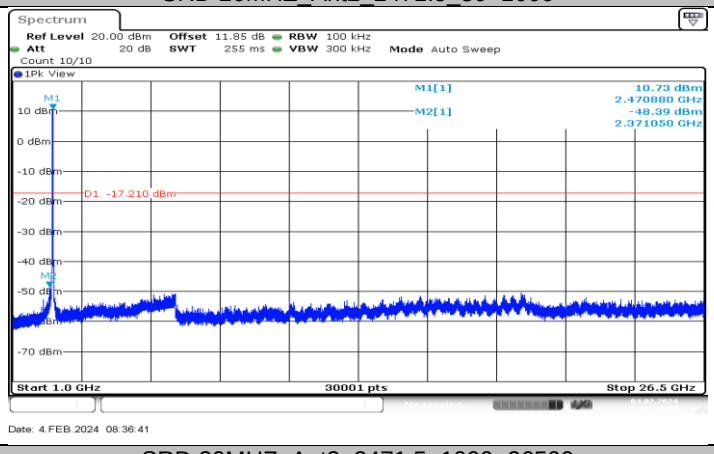
SRD 20MHZ_Ant1_2471.5_1000~26500



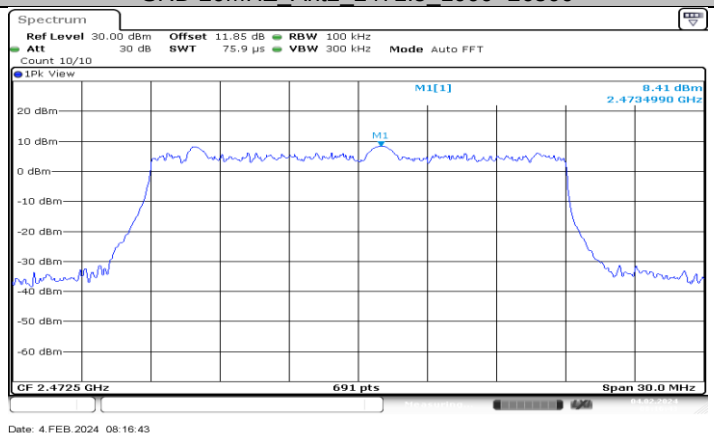
SRD 20MHZ_Ant2_2471.5_0~Reference



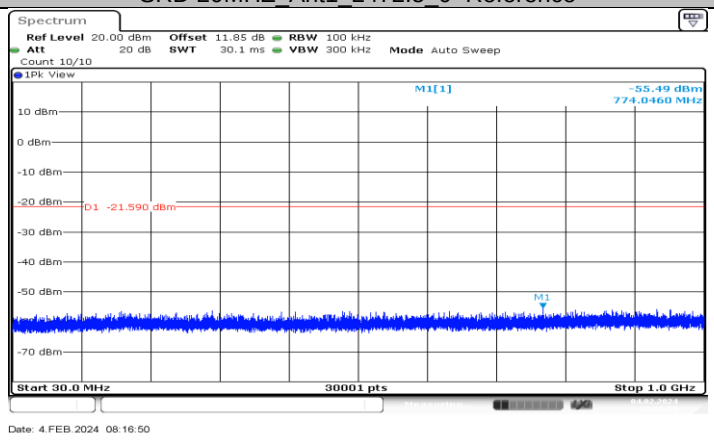
SRD 20MHZ_Ant2_2471.5_30~1000



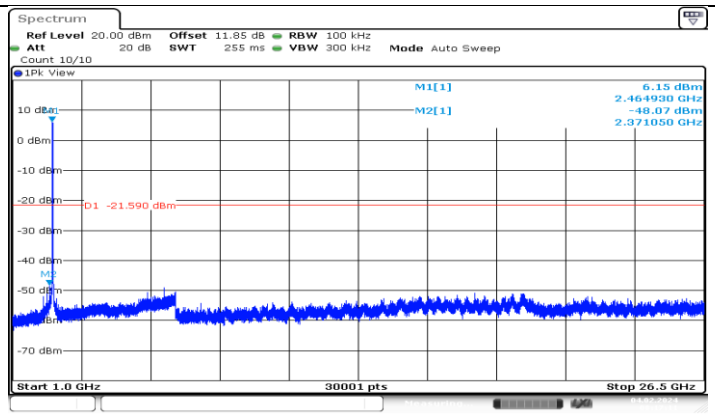
SRD 20MHZ_Ant2_2471.5_1000~26500



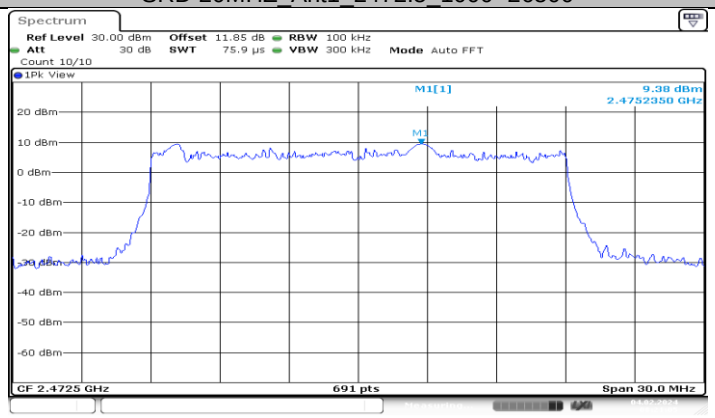
SRD 20MHZ_Ant1_2472.5_0~Reference



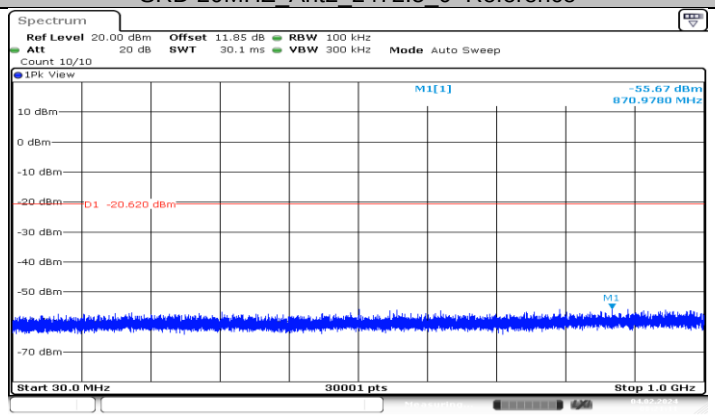
SRD 20MHZ_Ant1_2472.5_30~1000



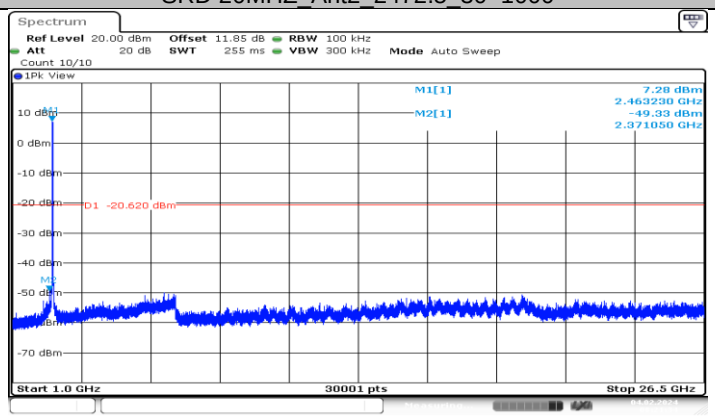
SRD 20MHZ_Ant1_2472.5_1000~26500



SRD 20MHZ_Ant2_2472.5_0~Reference



SRD 20MHZ_Ant2_2472.5_30~1000



SRD 20MHZ_Ant2_2472.5_1000~26500

11.7. APPENDIX G: DUTY CYCLE

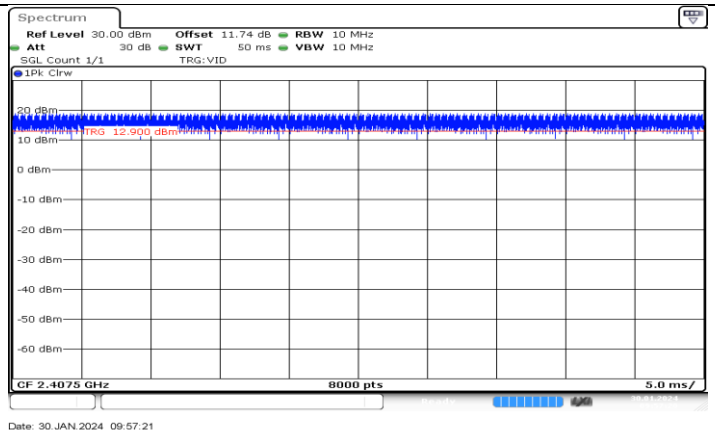
11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
SRD 1.4MHZ	50.00	50.00	1.0000	100.00	0.00	0.02	0.01
SRD 1.4MHZ CA	50.00	50.00	1.0000	100.00	0.00	0.02	0.01
SRD 3MHZ	50.00	50.00	1.0000	100.00	0.00	0.02	0.01

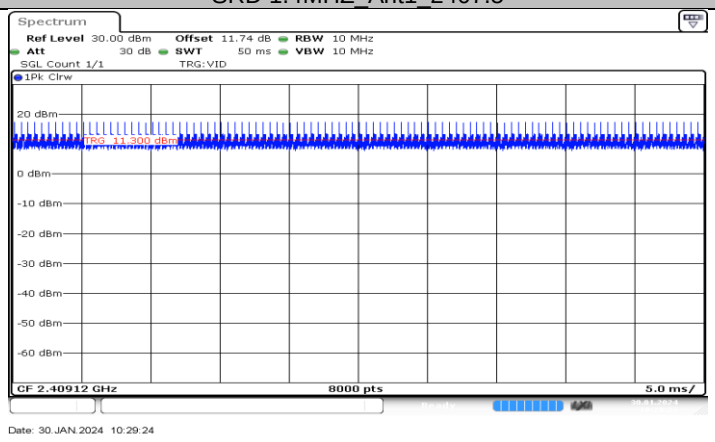
Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
SRD 10MHZ	105.00	105.00	1.0000	100.00	0.00	0.01	0.01
SRD 20MHZ	50.00	50.00	1.0000	100.00	0.00	0.02	0.01

Note:
Duty Cycle Correction Factor=10log (1/x).
Where: x is Duty Cycle (Linear)
Where: T is On Time
If that calculated VBW is not available on the analyzer then the next higher value should be used.

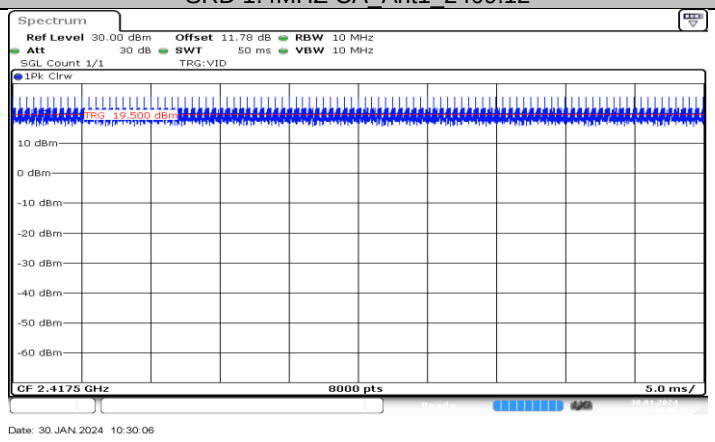
11.7.2. Test Graphs



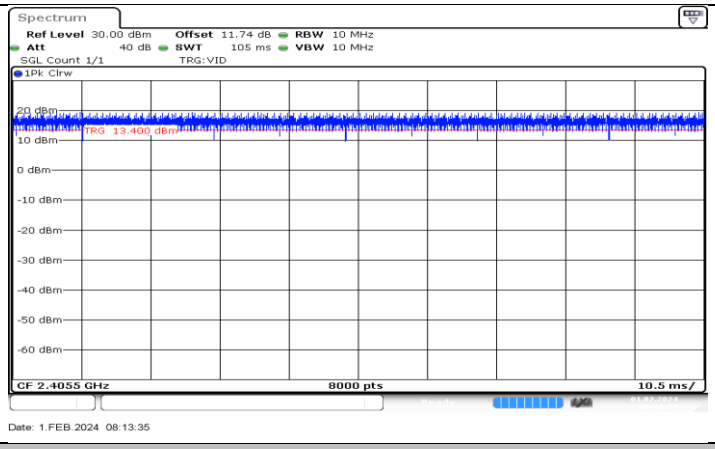
SRD 1.4MHZ_Ant1_2407.5



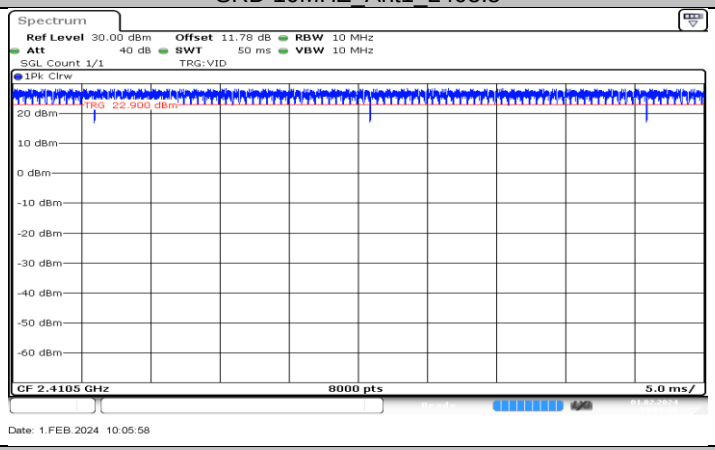
SRD 1.4MHZ CA_Ant1_2409.12



SRD 3MHZ_Ant1_2417.5



SRD 10MHZ_Ant1_2405.5



SRD 20MHZ_Ant1_2410.5

END OF REPORT