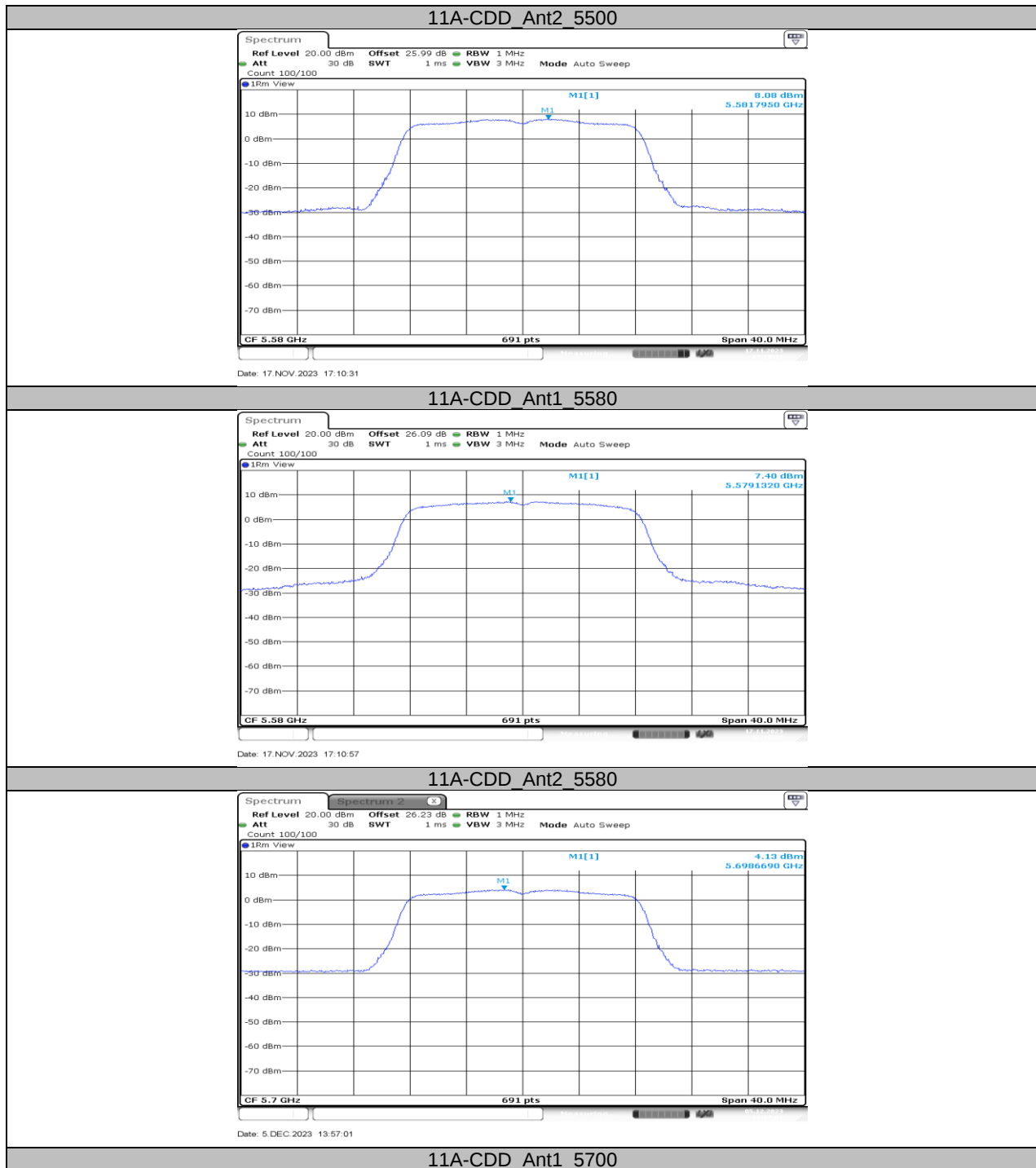
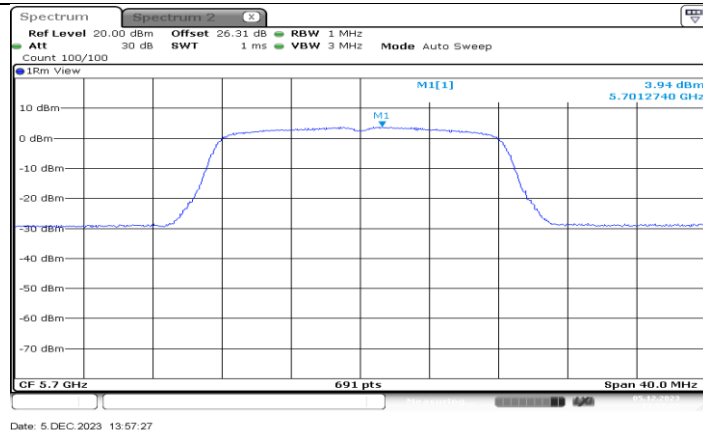
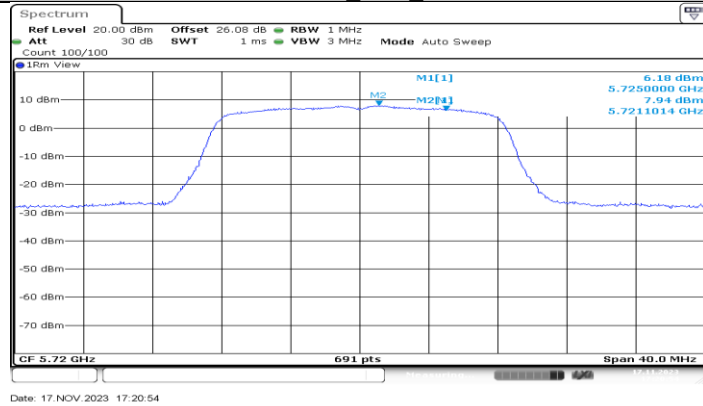


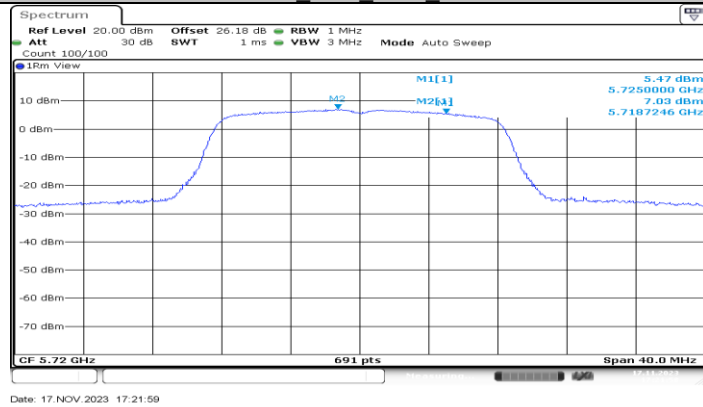




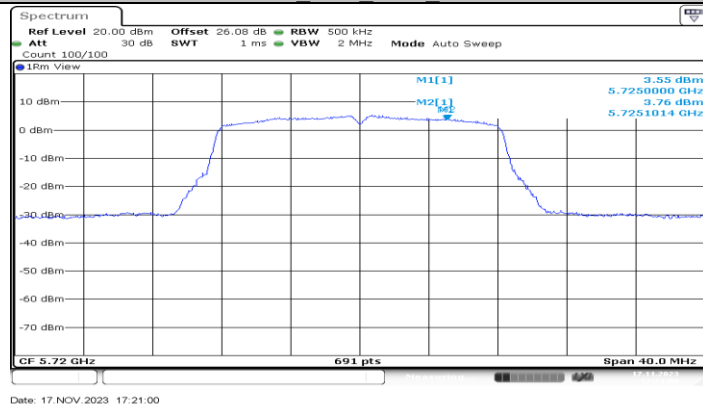
11A-CDD_Ant2_5700



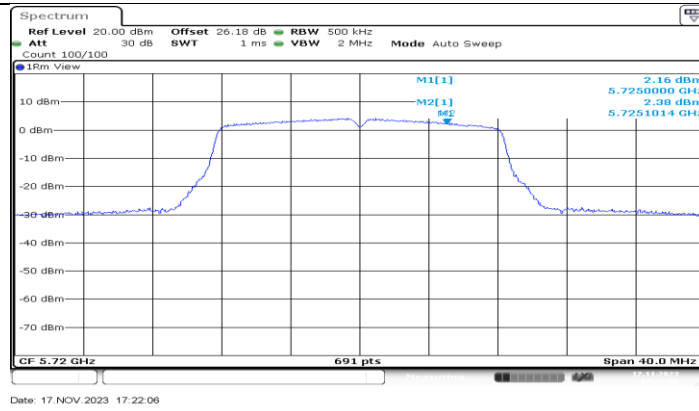
11A-CDD_Ant1_5720_UNII-2C



11A-CDD_Ant2_5720_UNII-2C



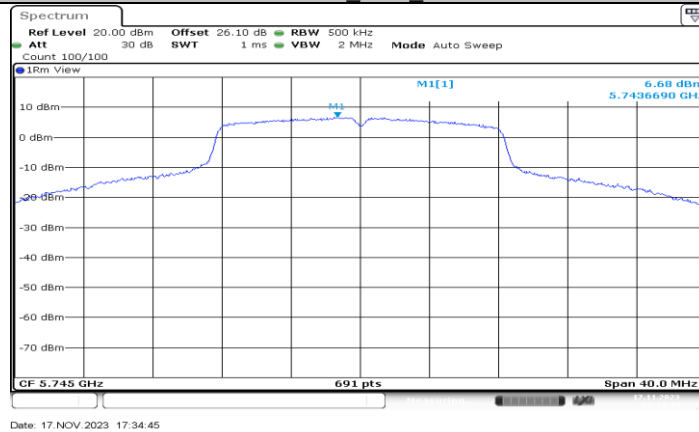
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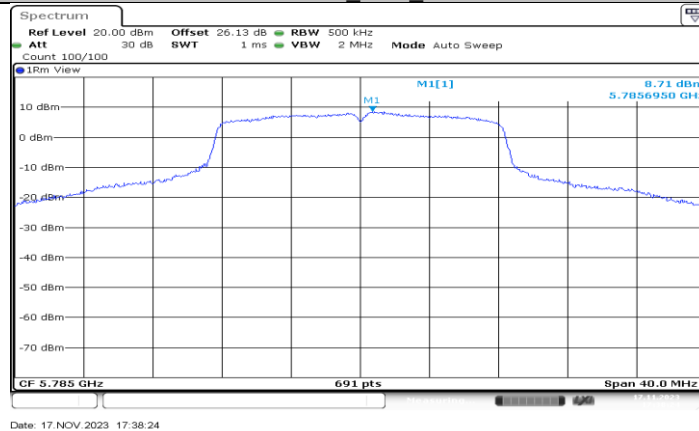
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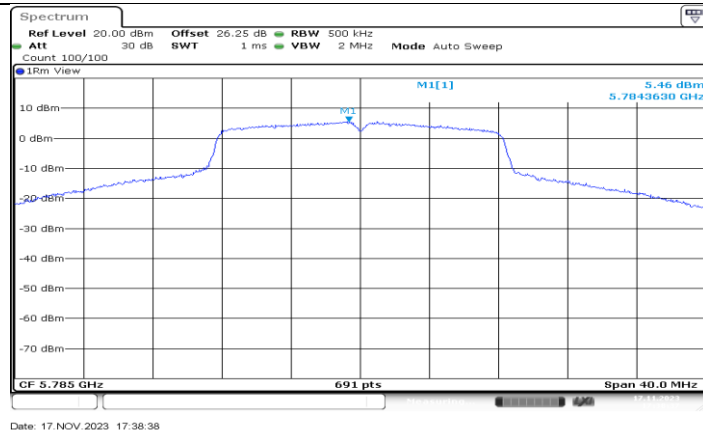
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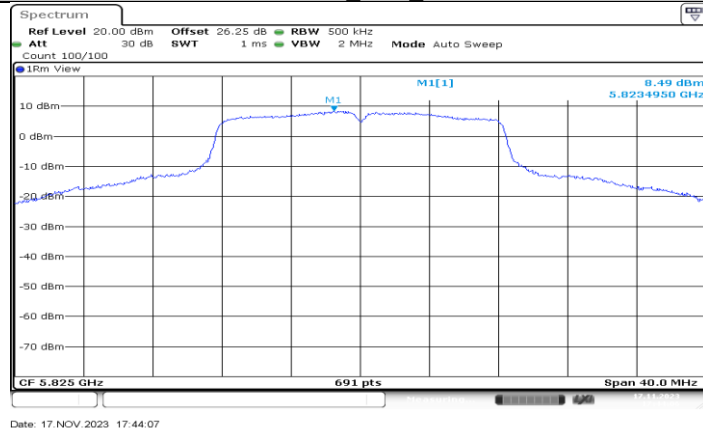
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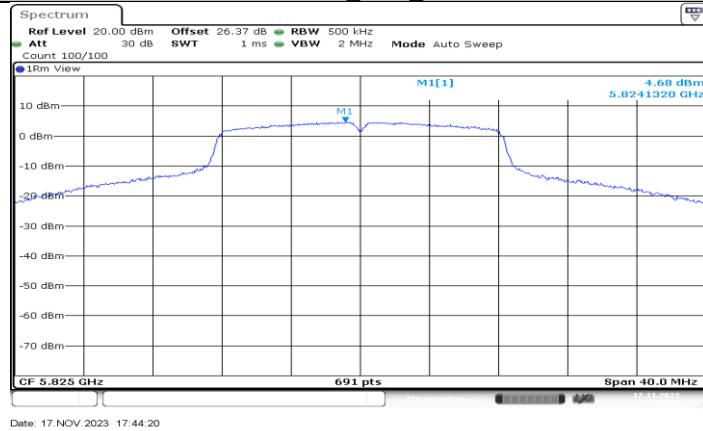
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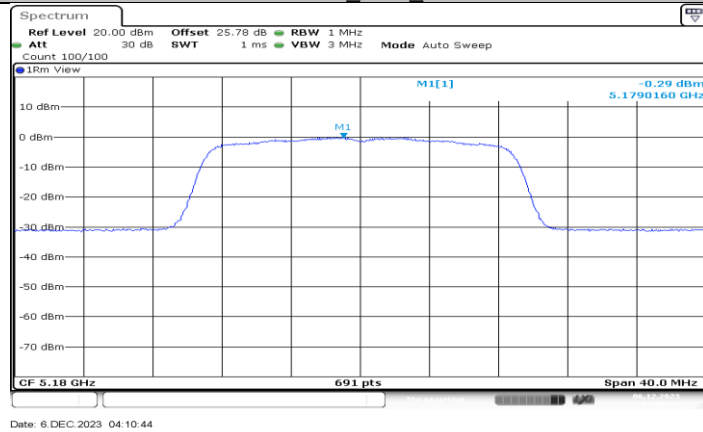
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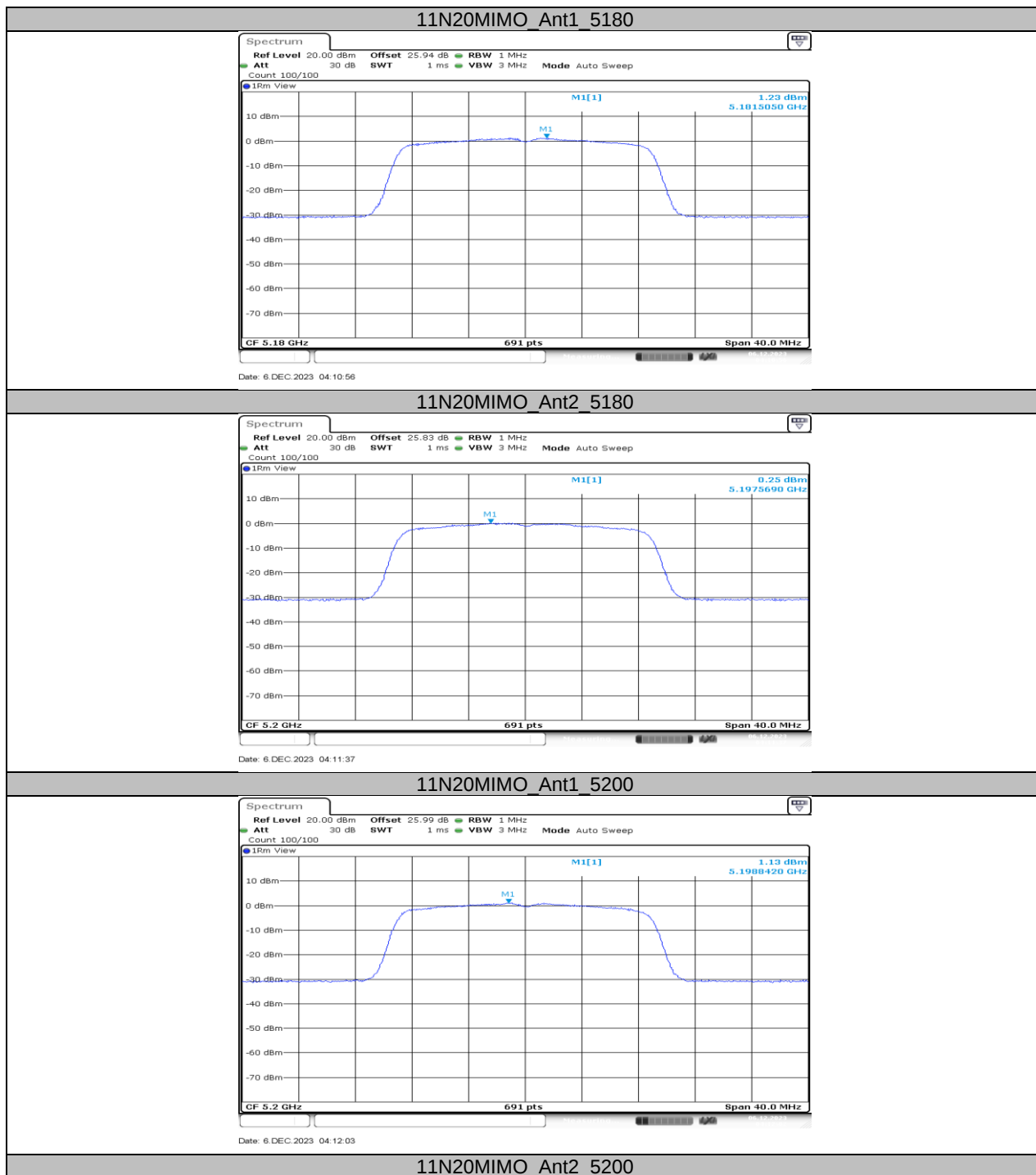


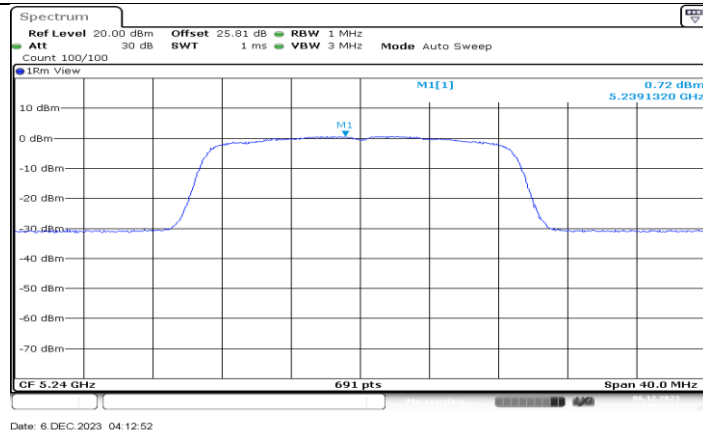
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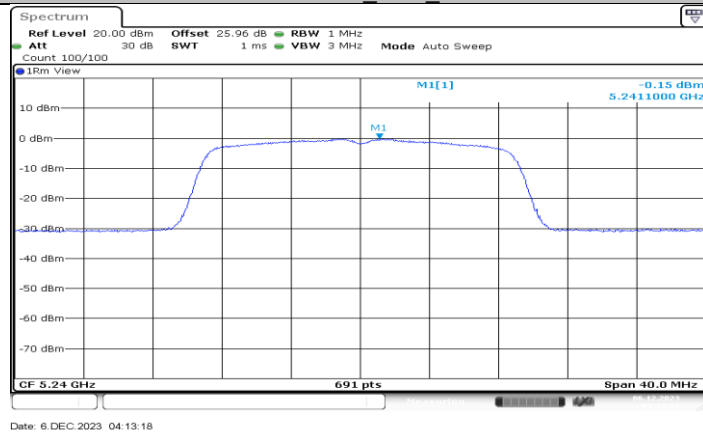
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11N20MIMO_Ant1_5240

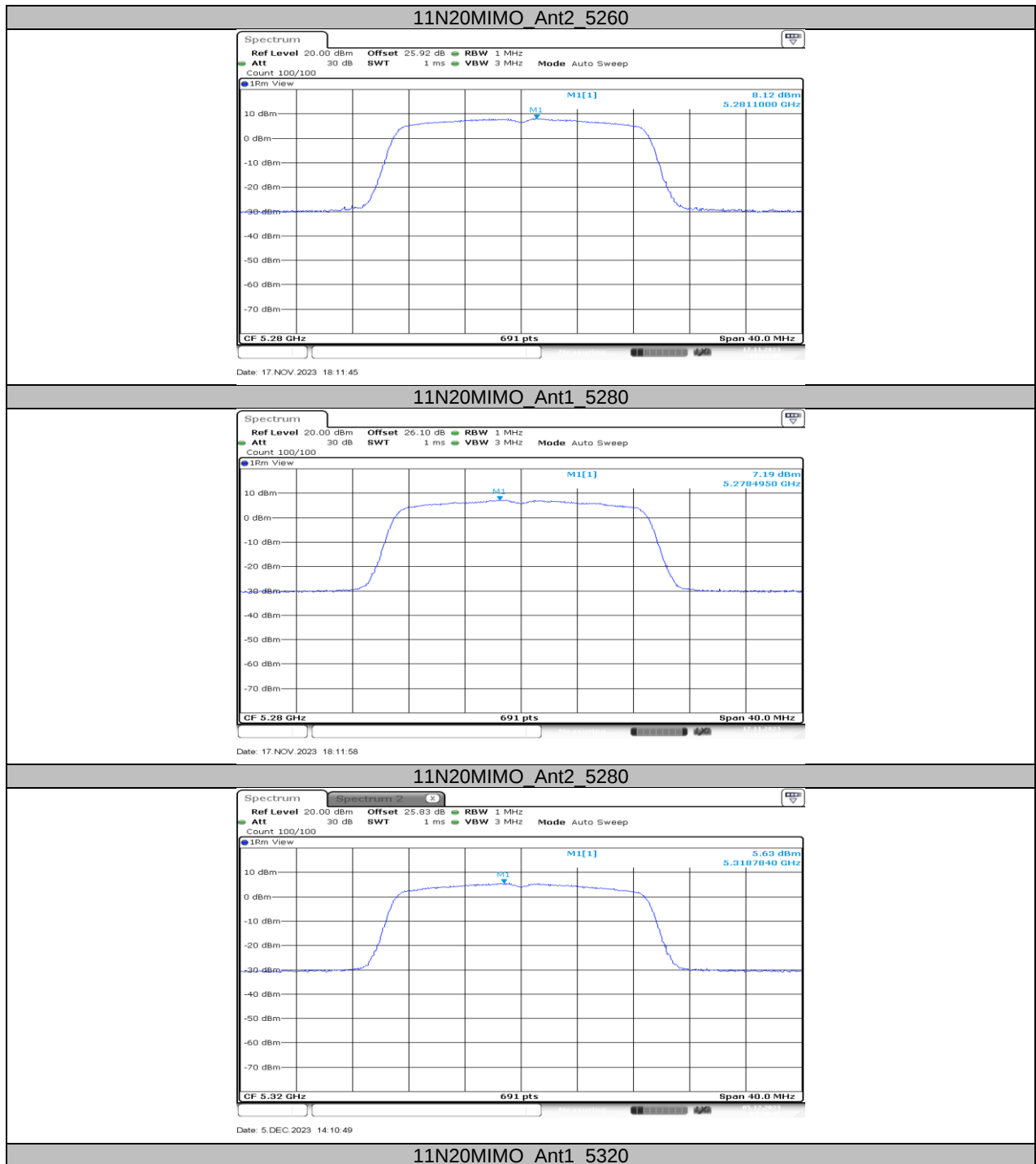


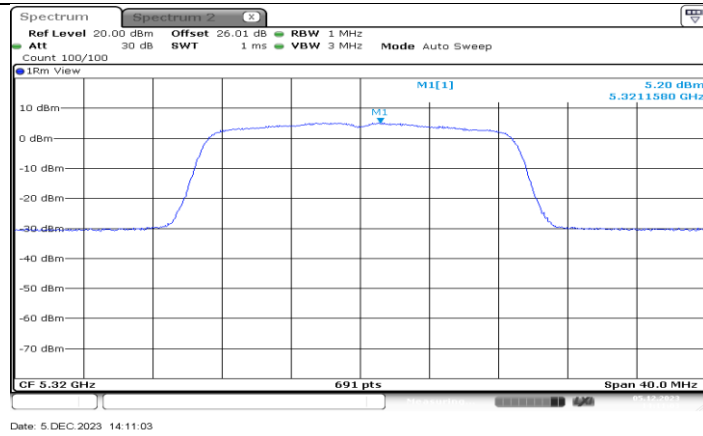
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11N20MIMO_Ant1_5260

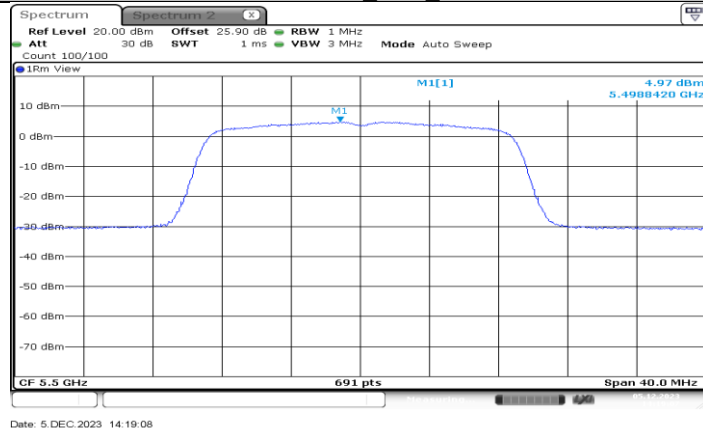






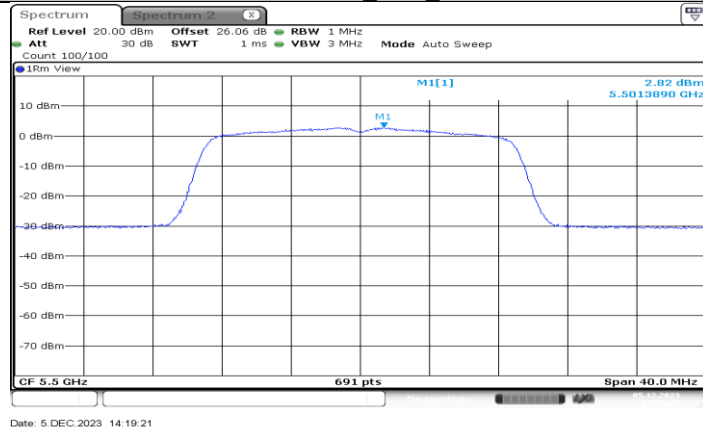
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11N20MIMO_Ant2_5320



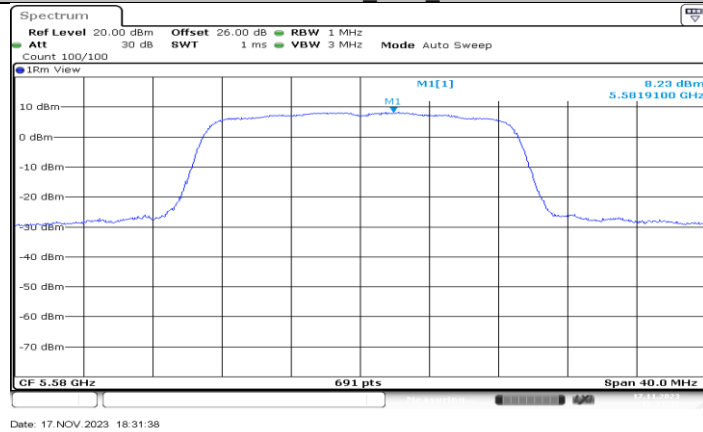
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11N20MIMO_Ant1_5500



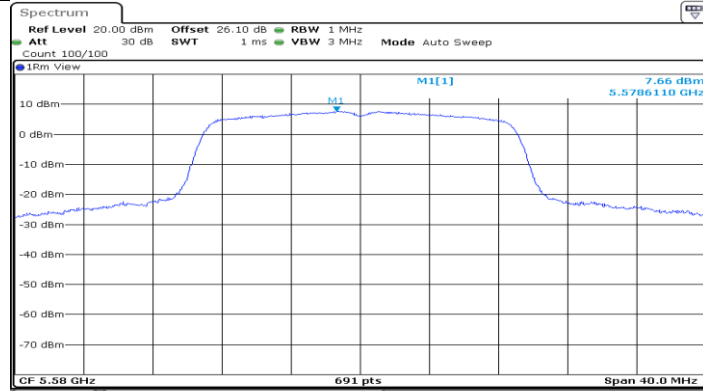
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11N20MIMO_Ant2_5500



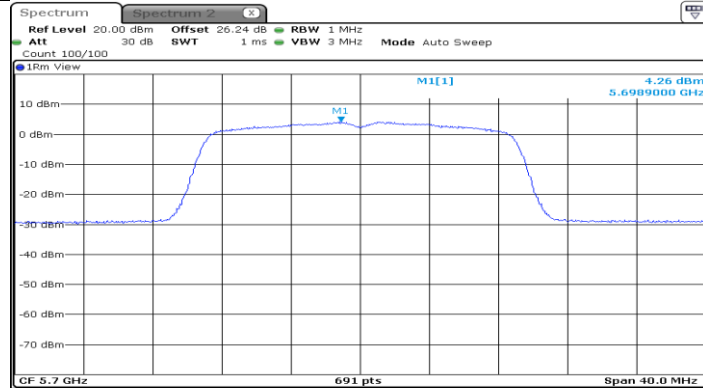
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11N20MIMO_Ant1_5580



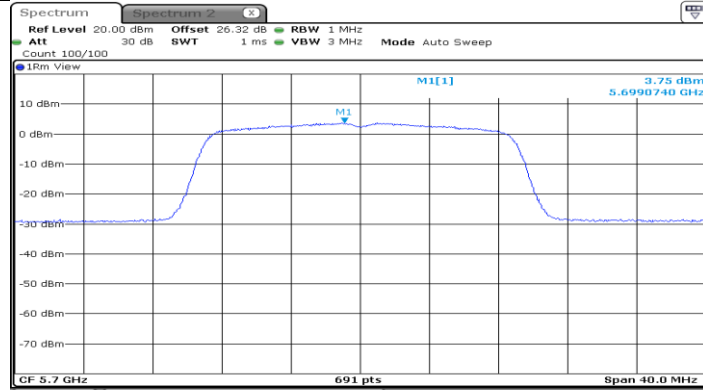
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11N20MIMO_Ant2_5580



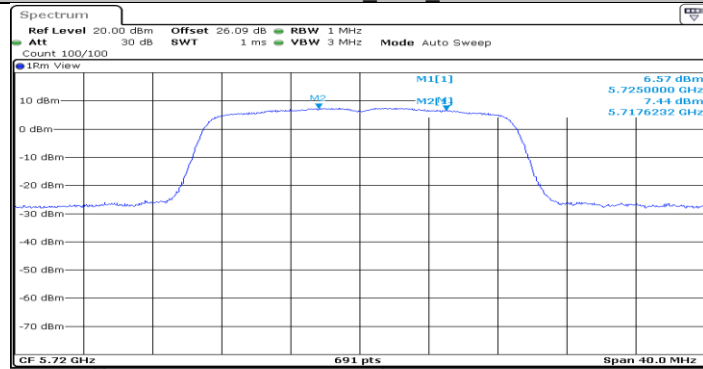
Date: 5 DEC 2023 14:28:31

11N20MIMO_Ant1_5700



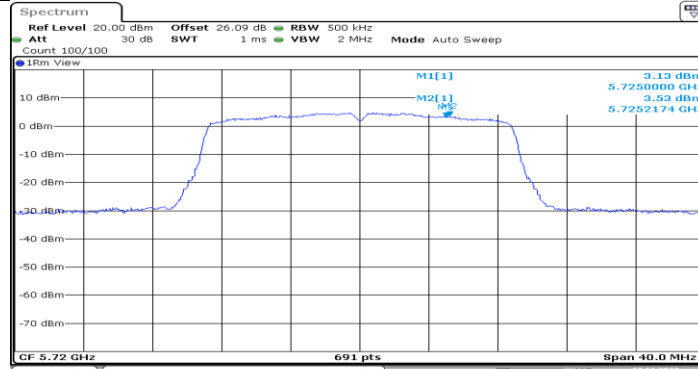
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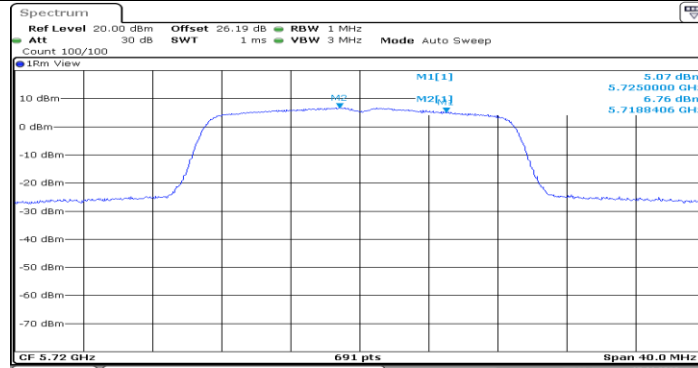
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11N20MIMO_Ant1_5720_UNII-2C



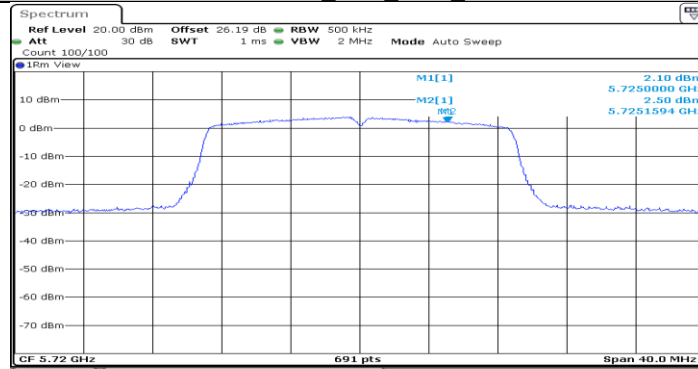
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11N20MIMO_Ant2_5720_UNII-2C



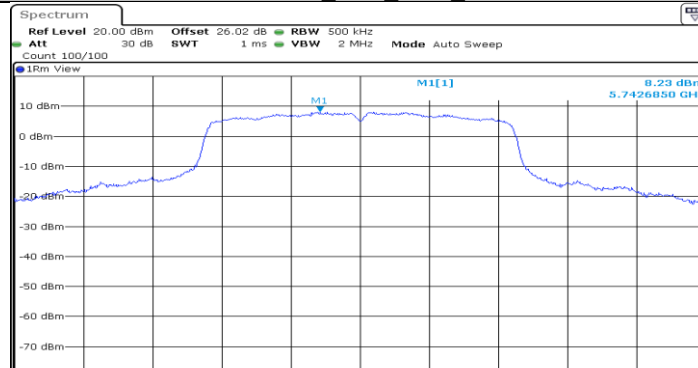
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11N20MIMO_Ant1_5720_UNII-3



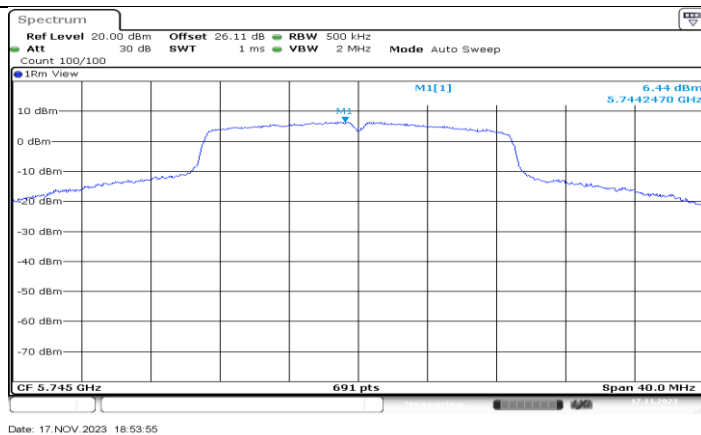
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11N20MIMO_Ant2_5720_UNII-3



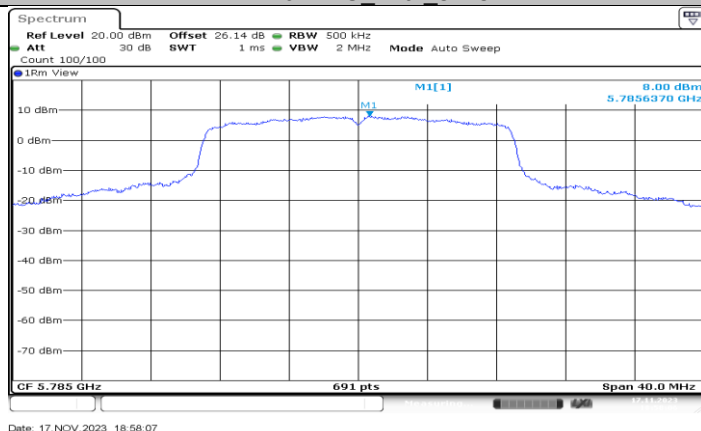
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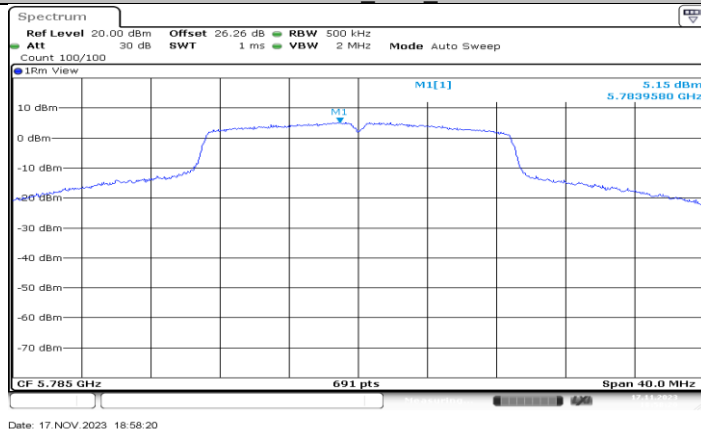
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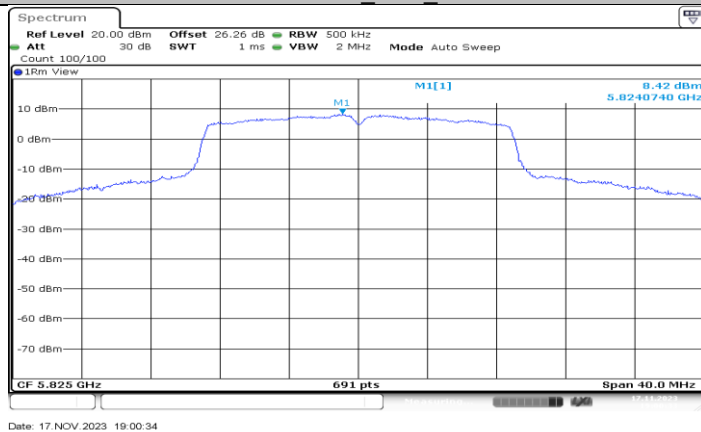
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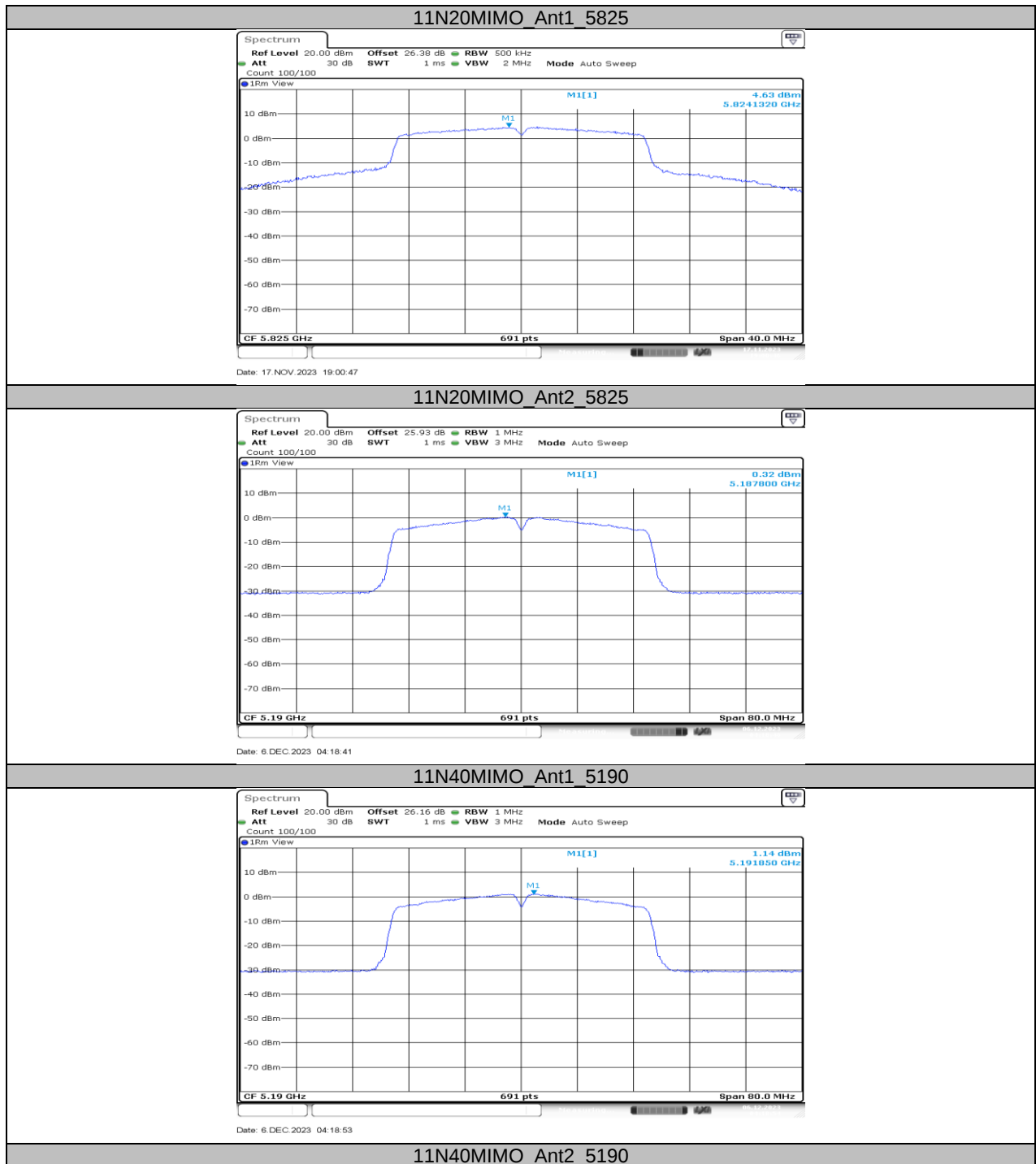


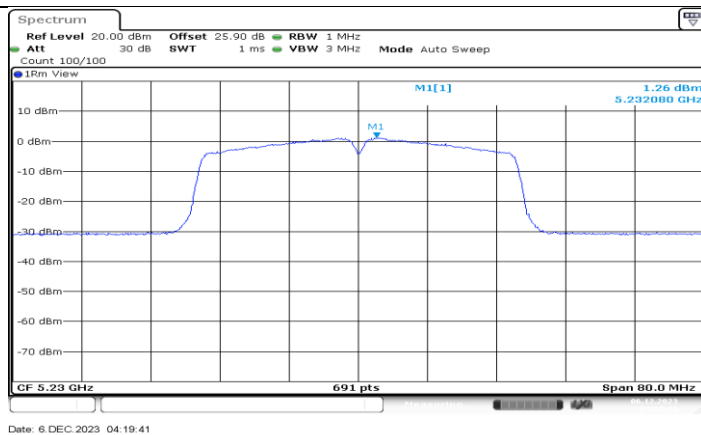
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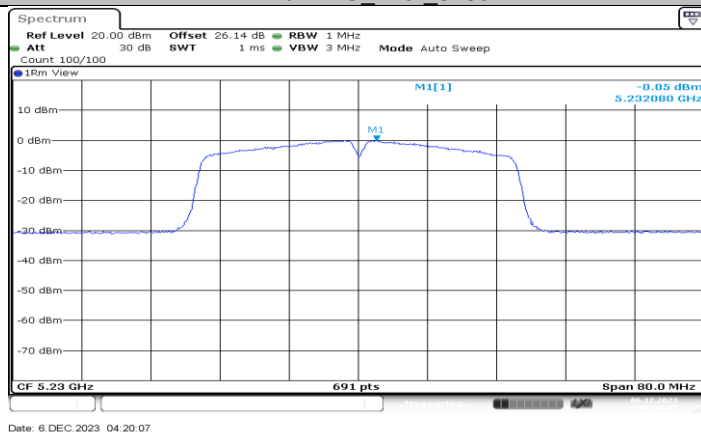


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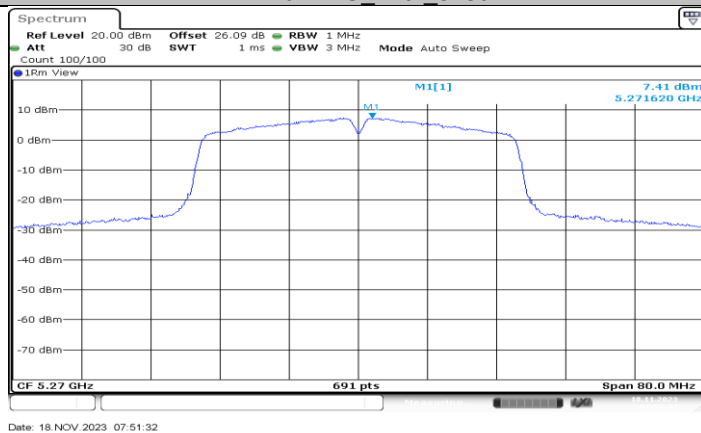




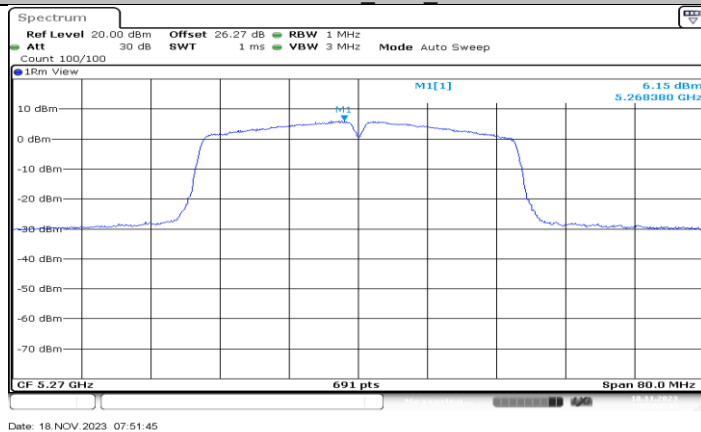
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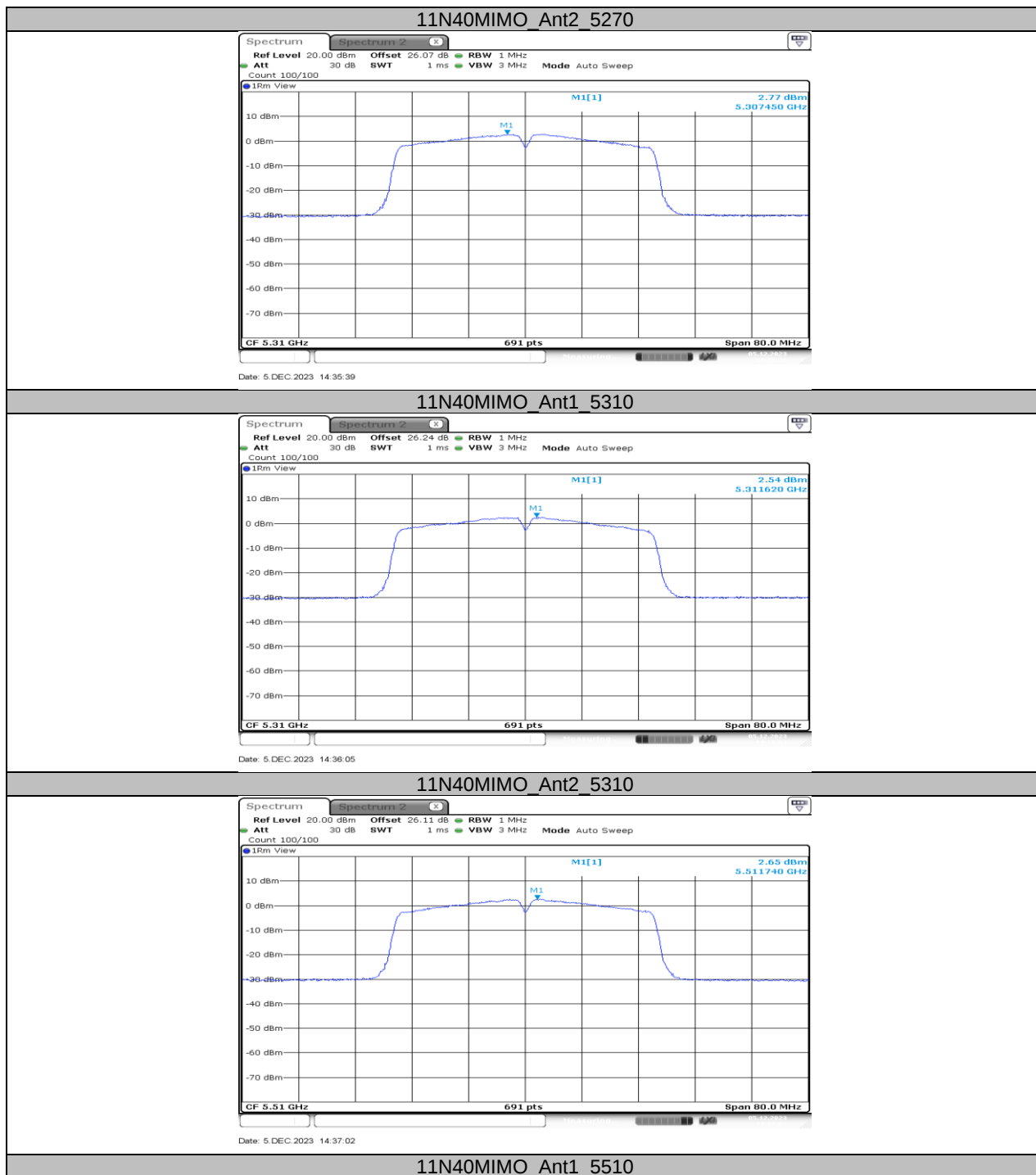


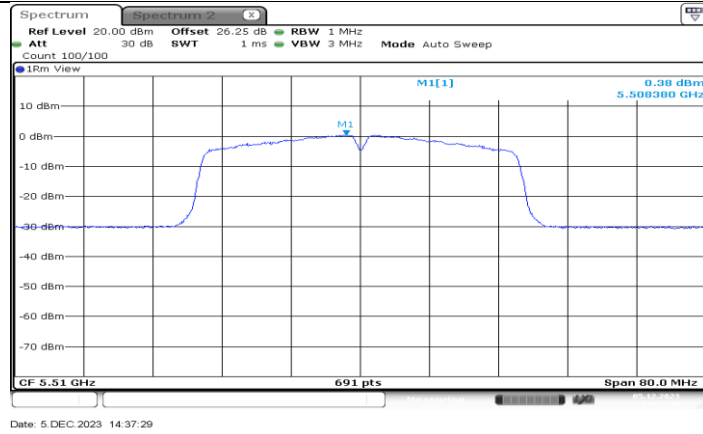
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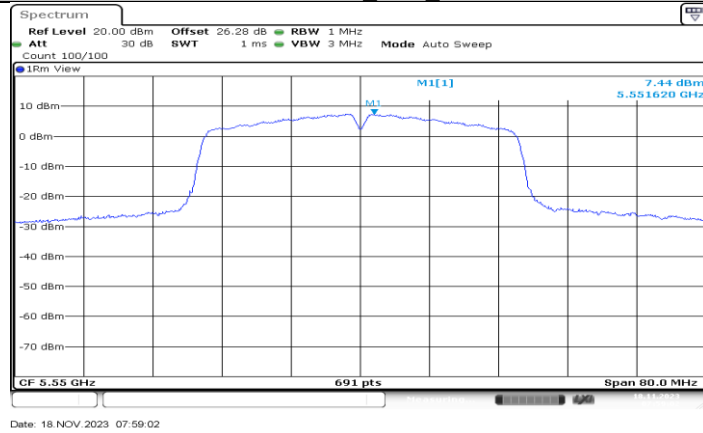
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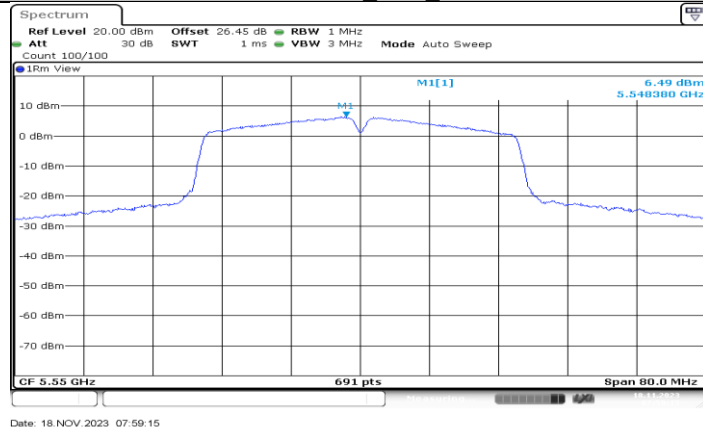




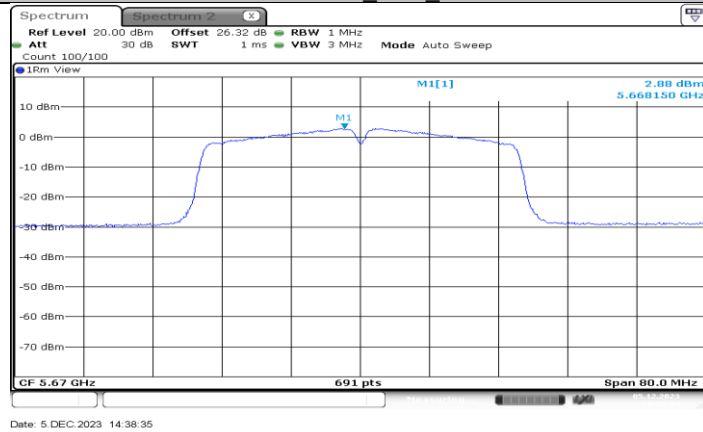
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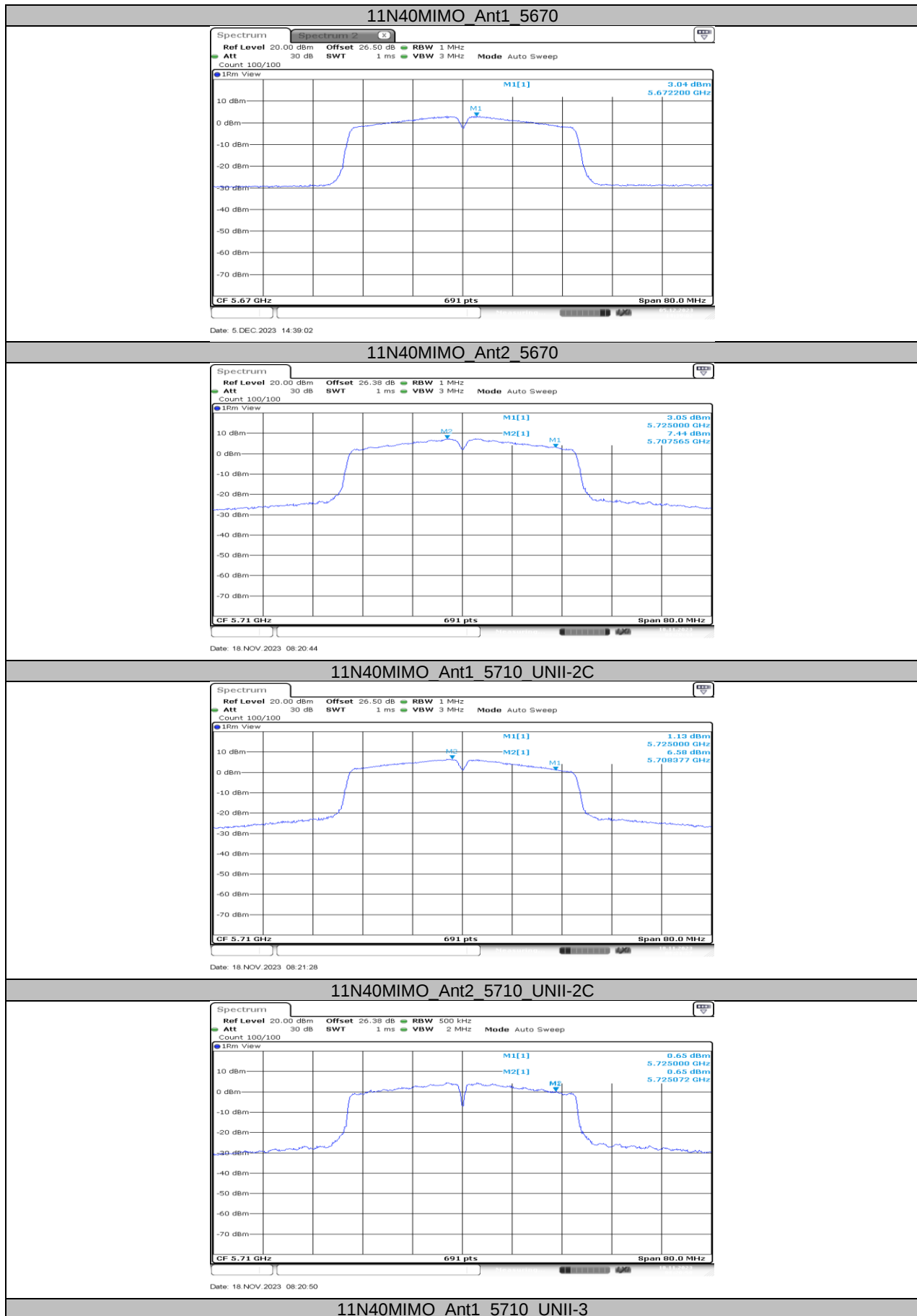


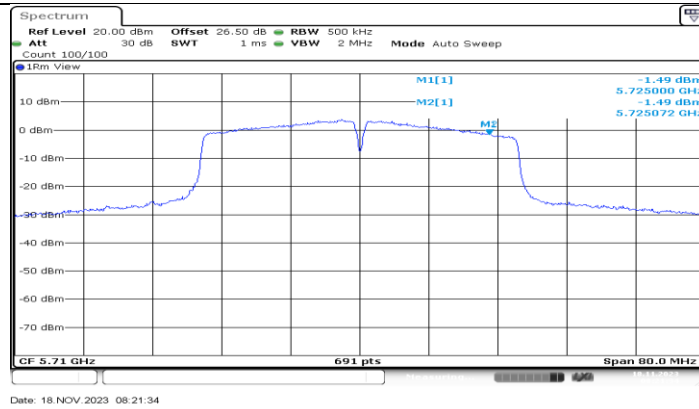
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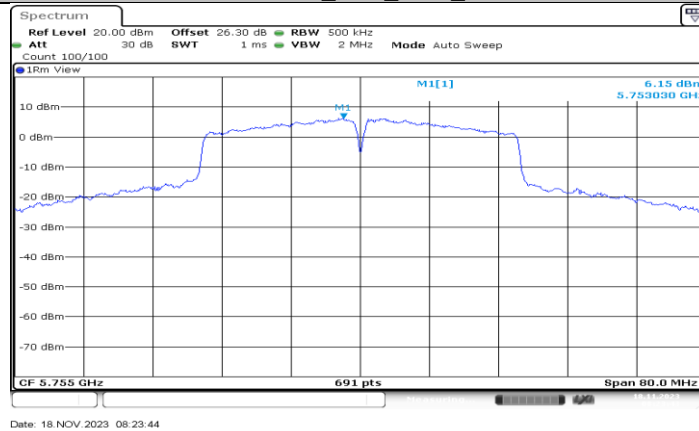
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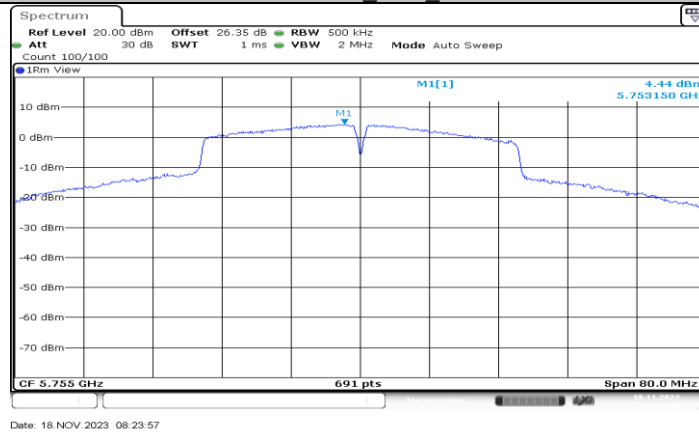




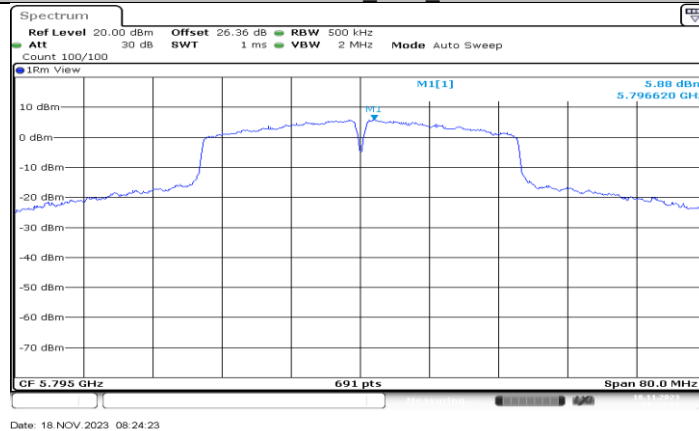
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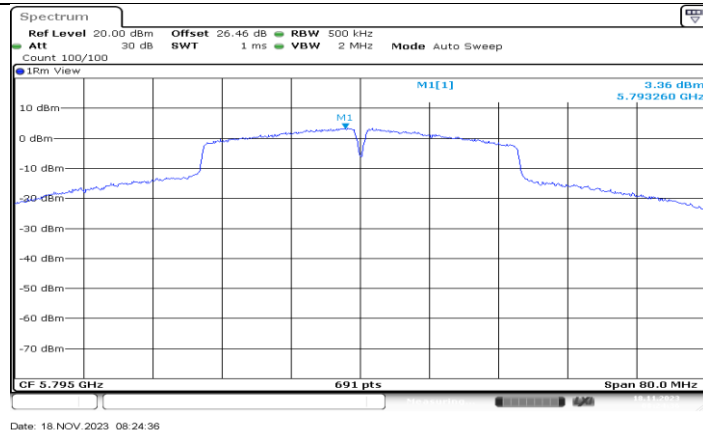
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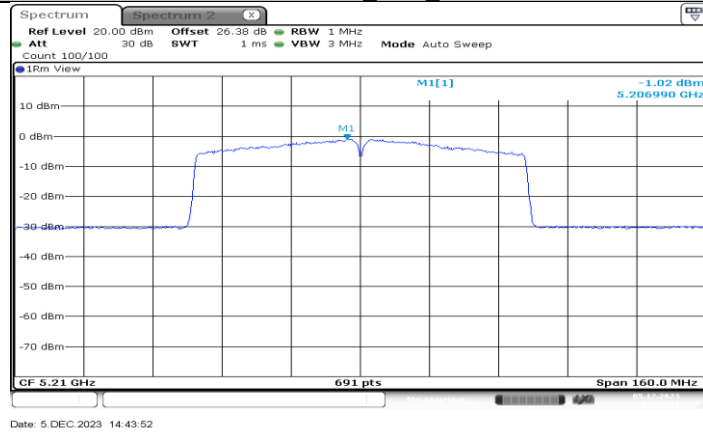
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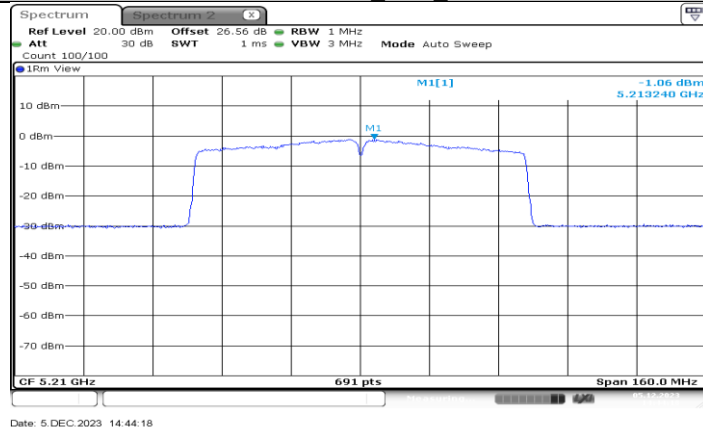
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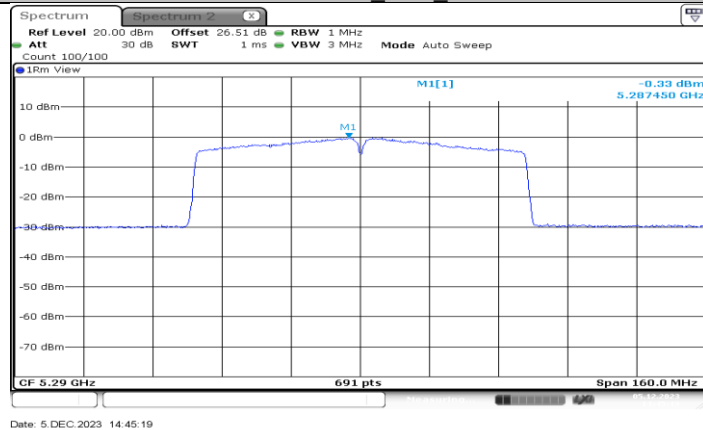
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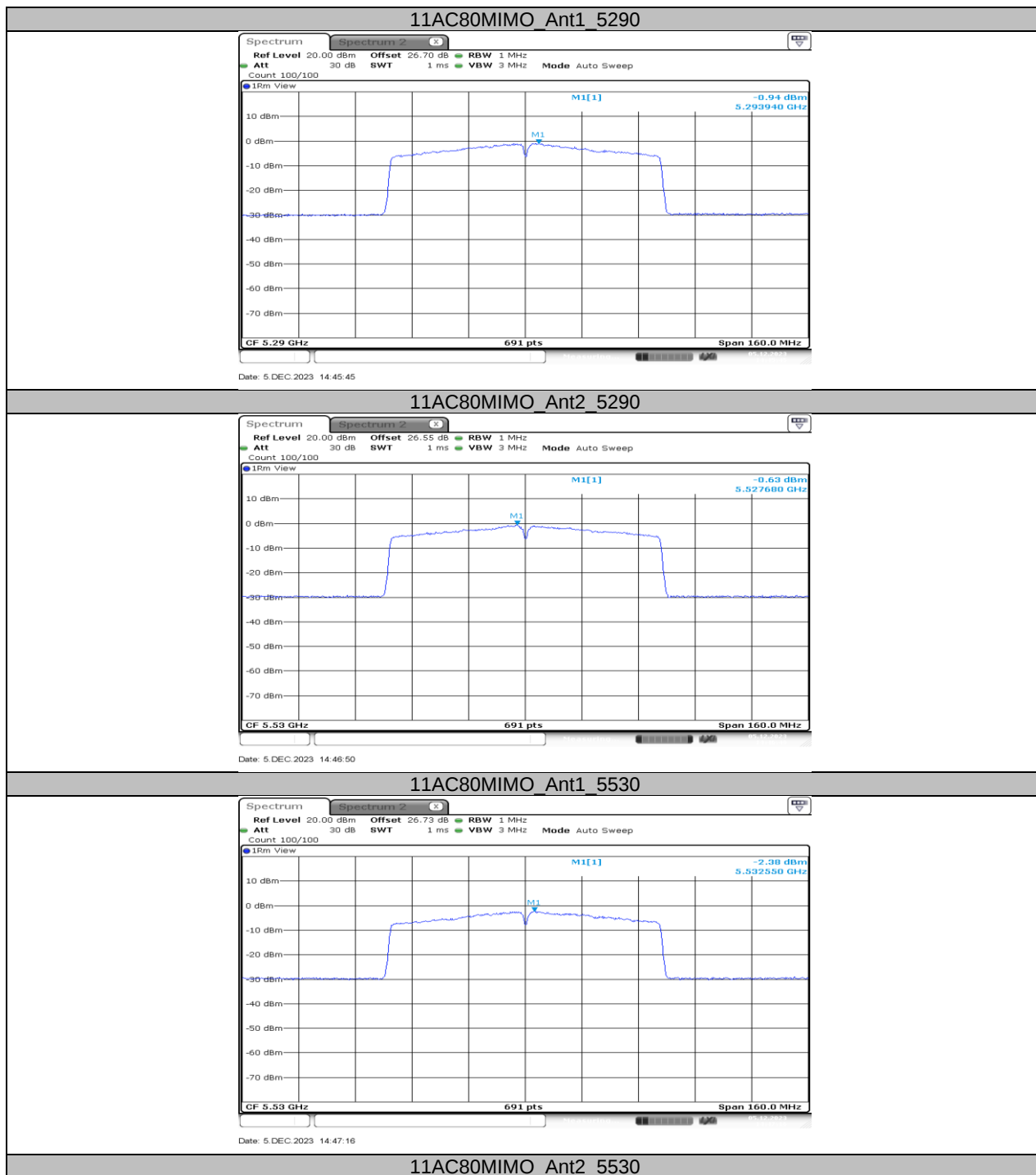


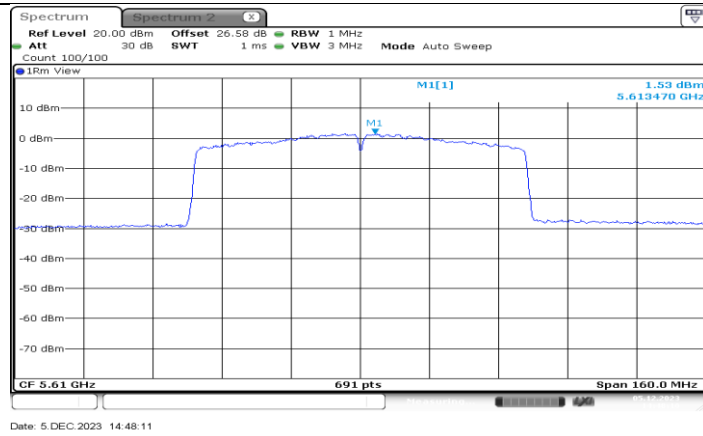
11AC80MIMO_Ant1_5210



11AC80MIMO_Ant2_5210

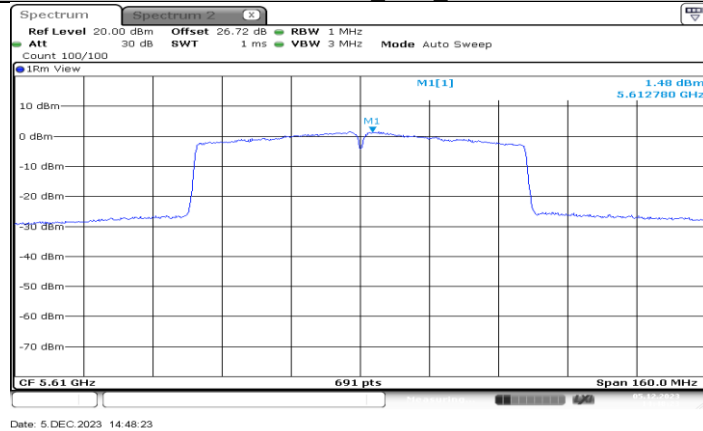






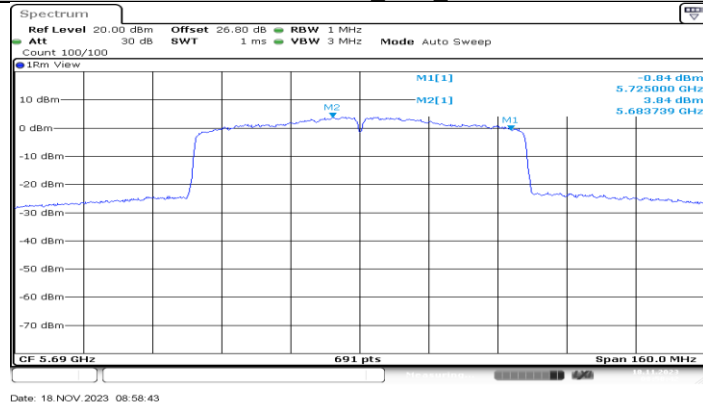
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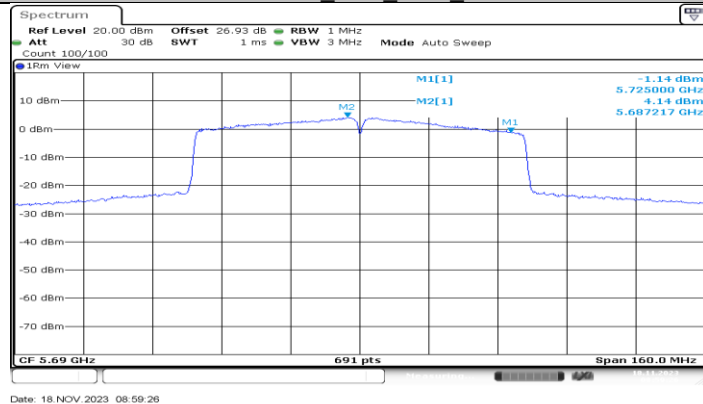
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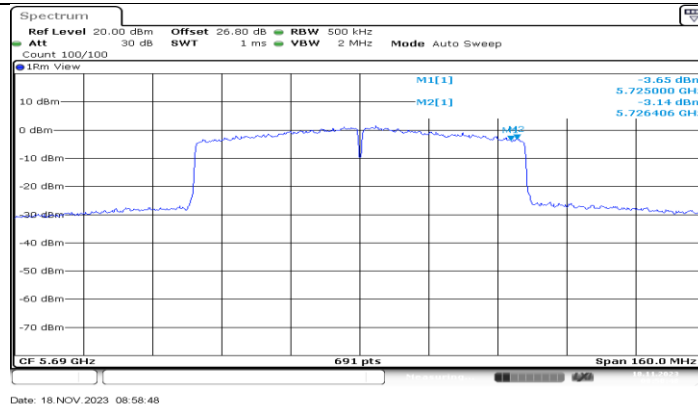
Date: 18 NOV 2023 08:58:43

11AC80MIMO_Ant1_5690_UNII-2C

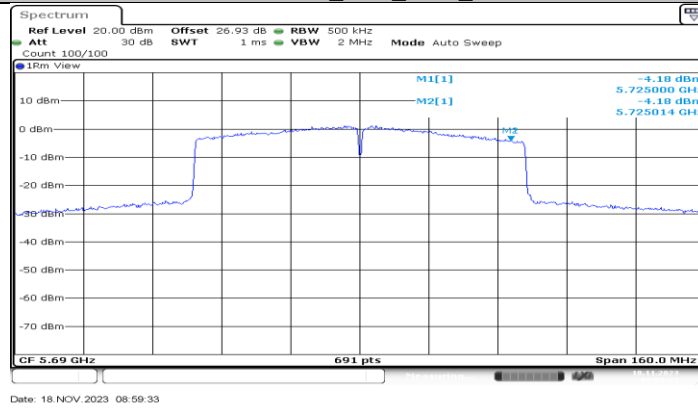


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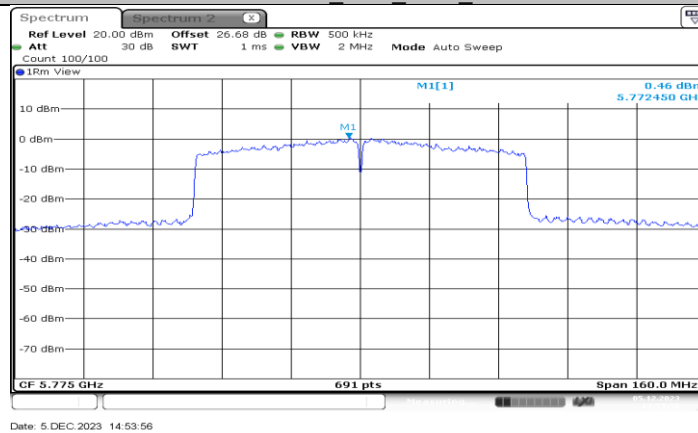
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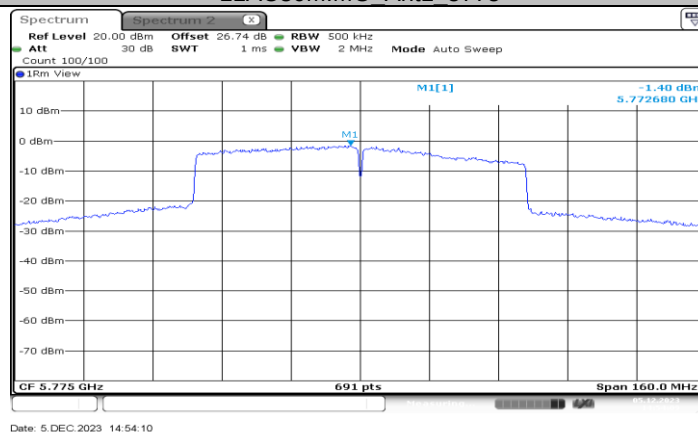
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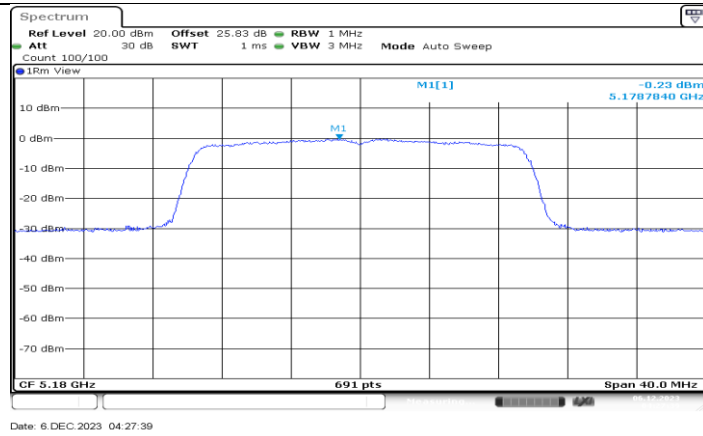
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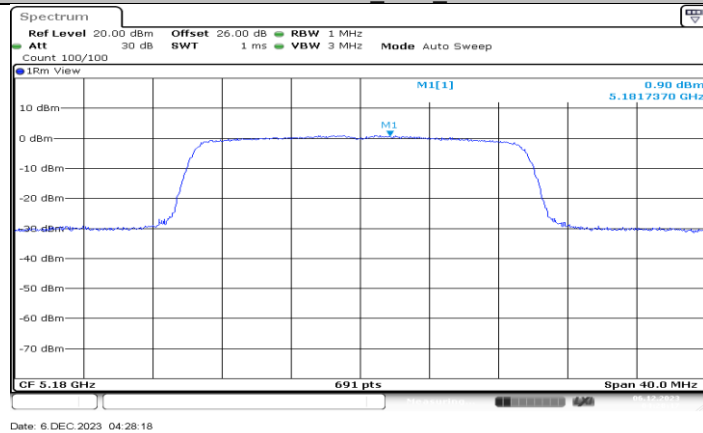
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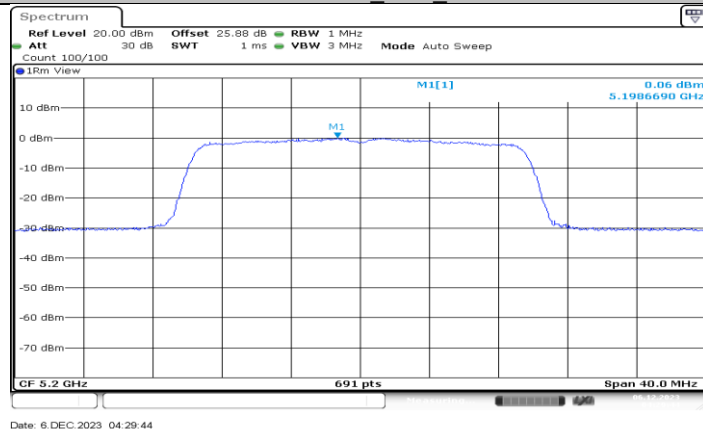
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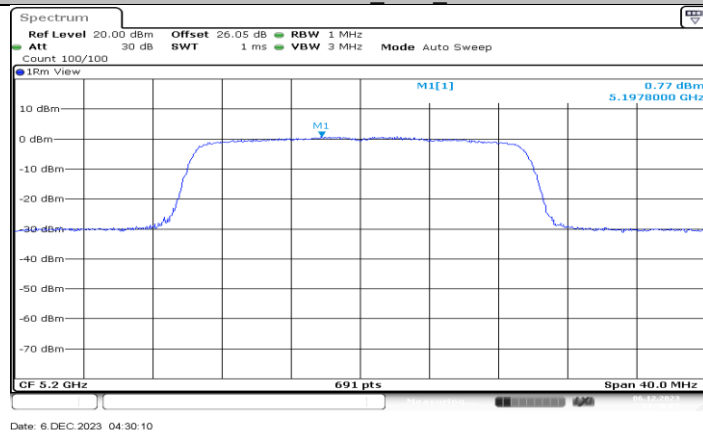
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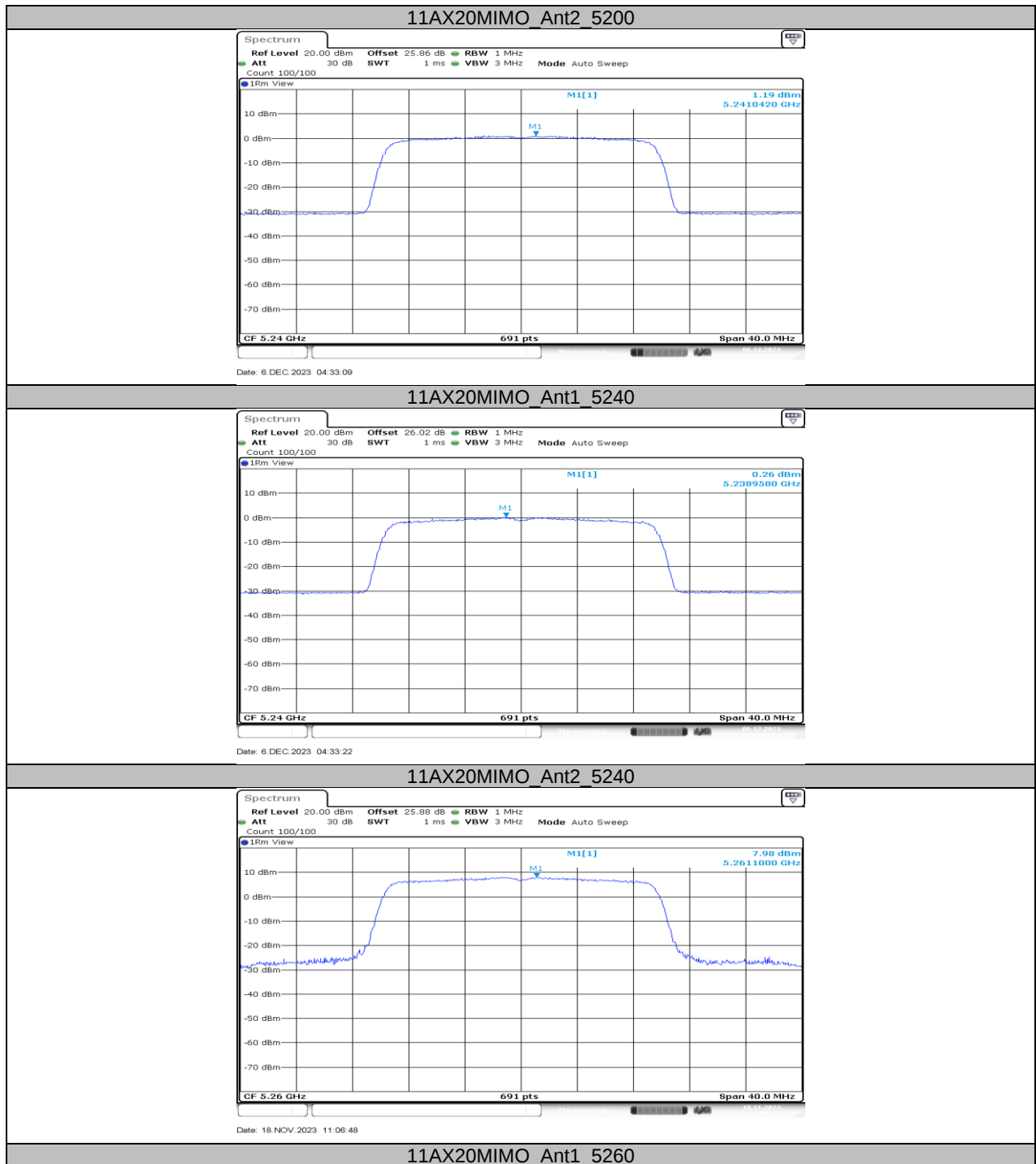


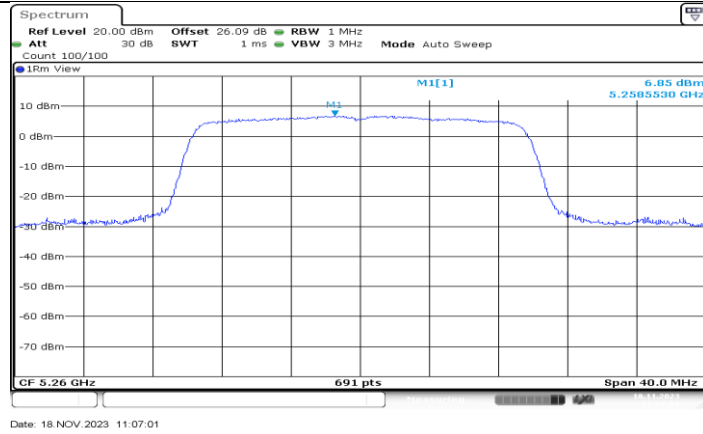
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11AX20MIMO_Ant1_5200

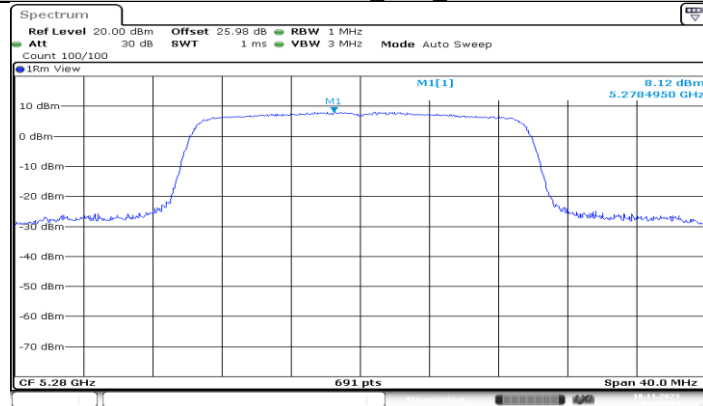






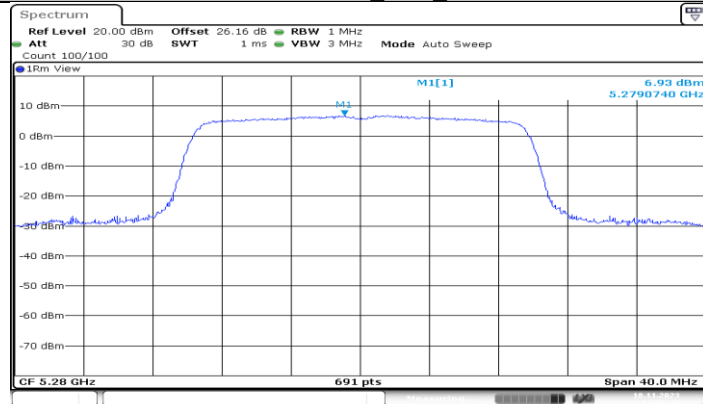
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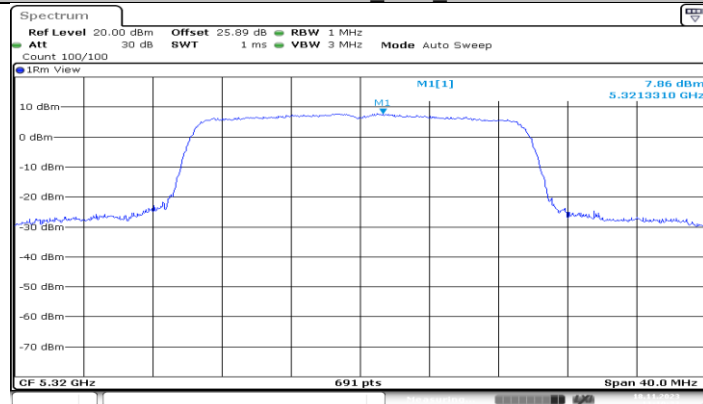
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11AX20MIMO_Ant1_5280

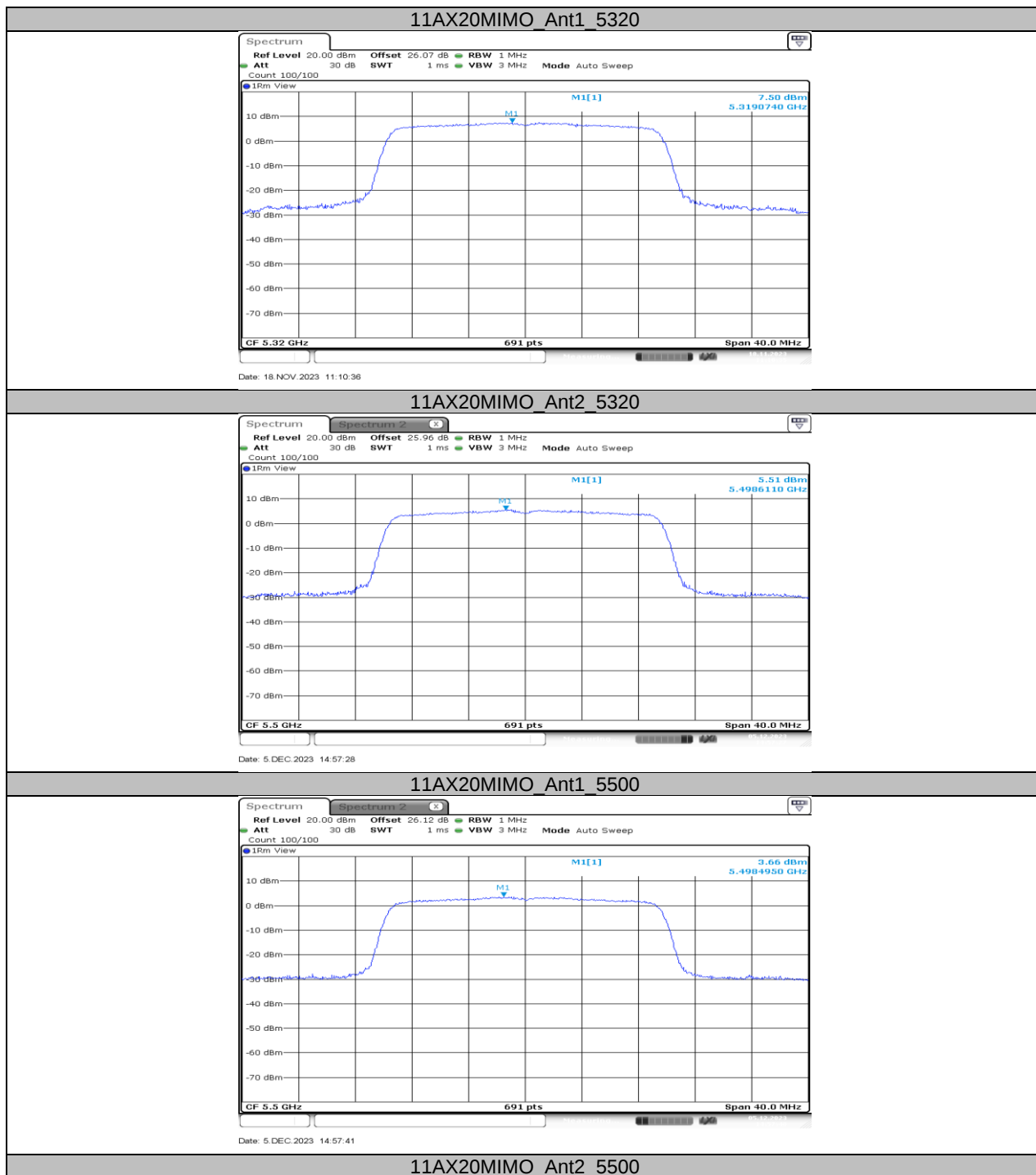


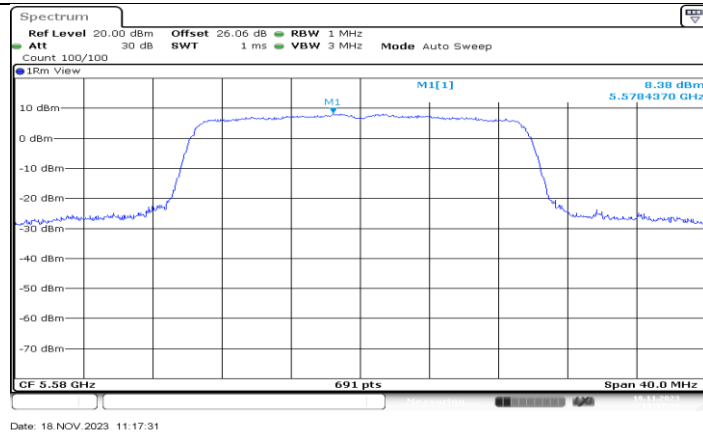
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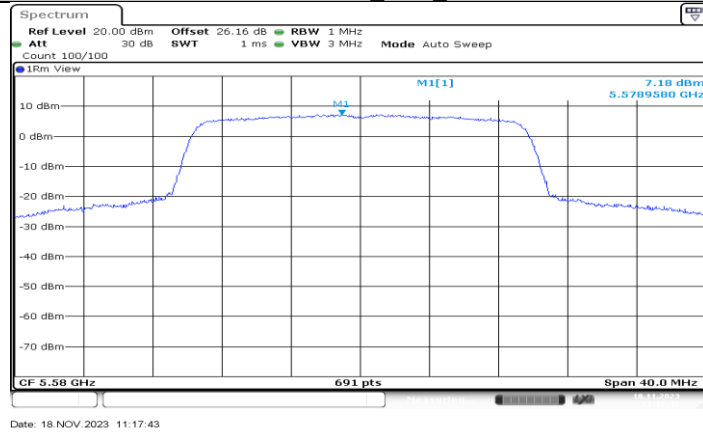
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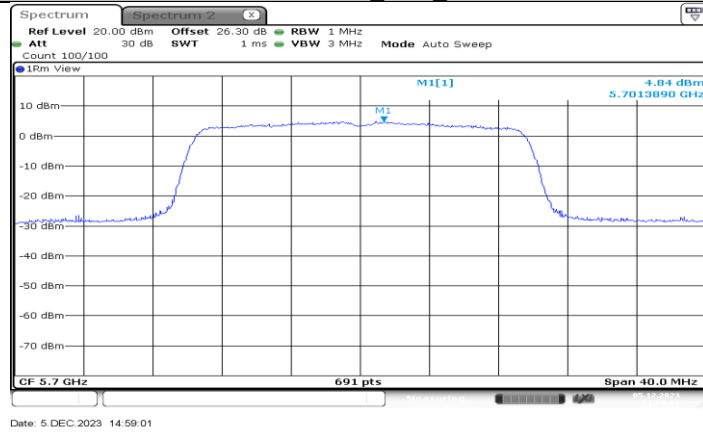
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11AX20MIMO_Ant1_5580



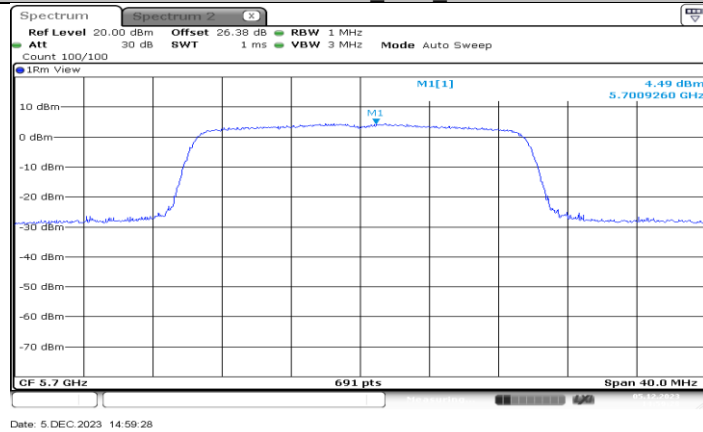
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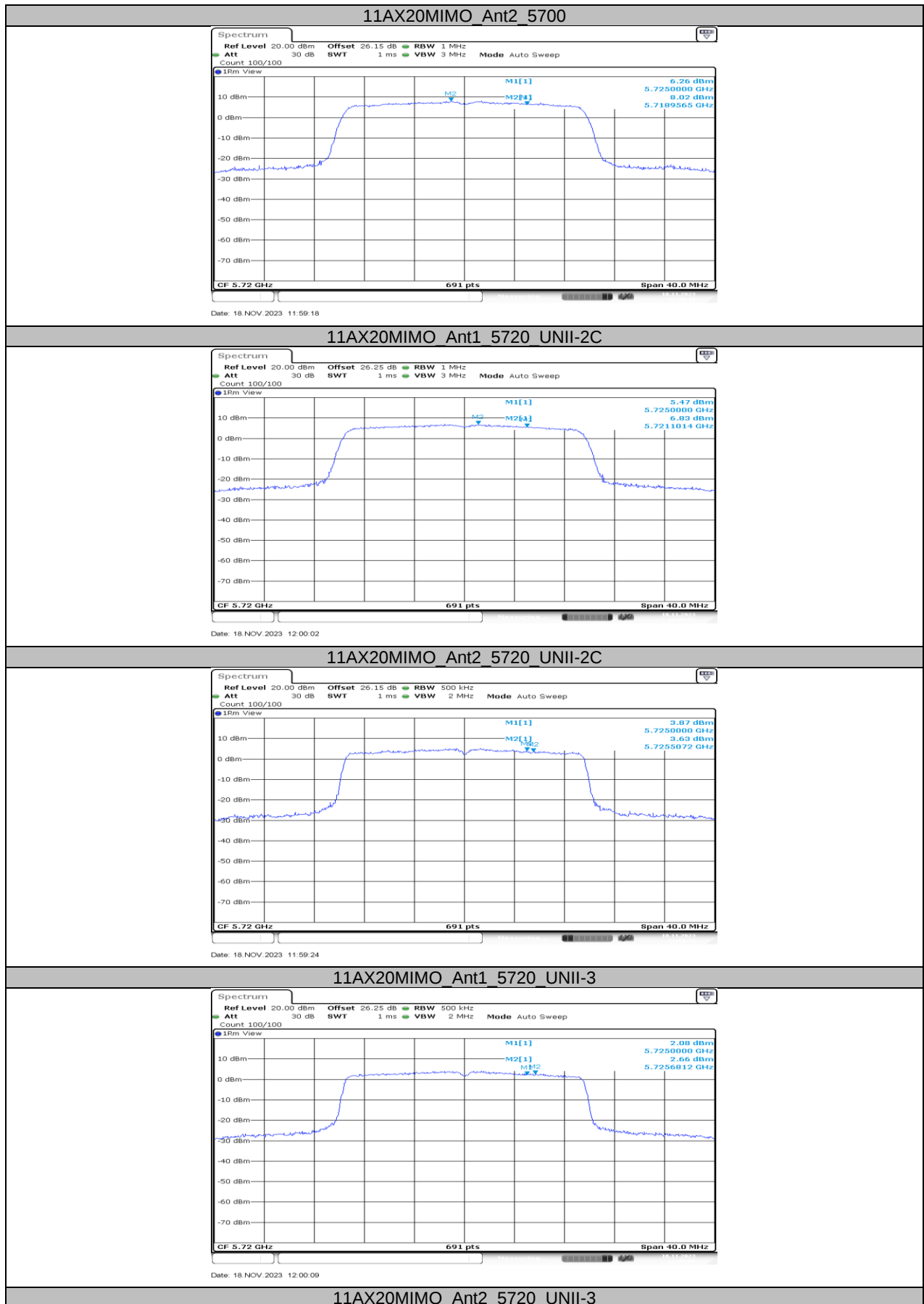


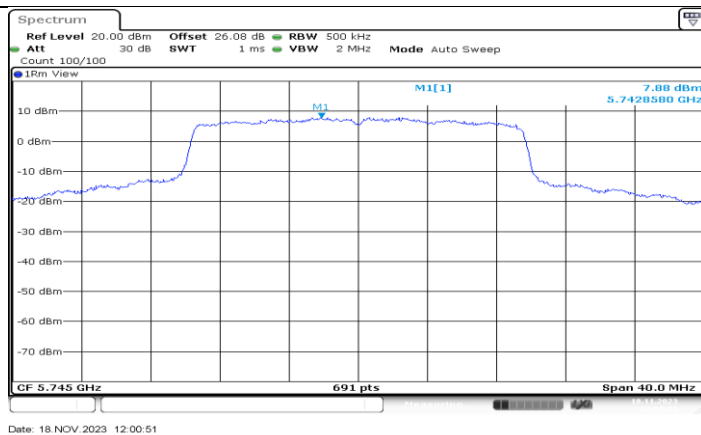
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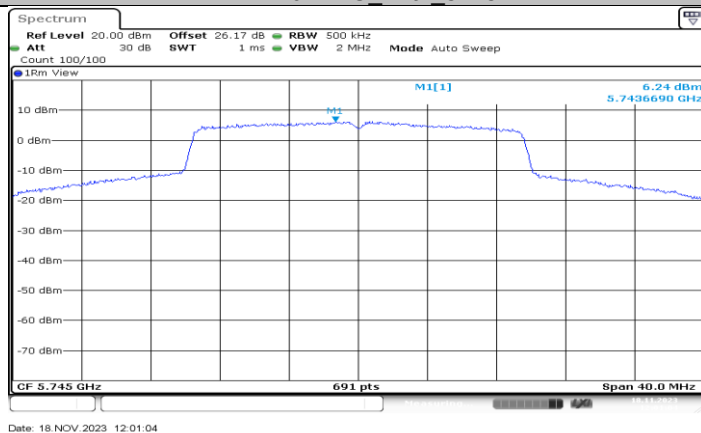


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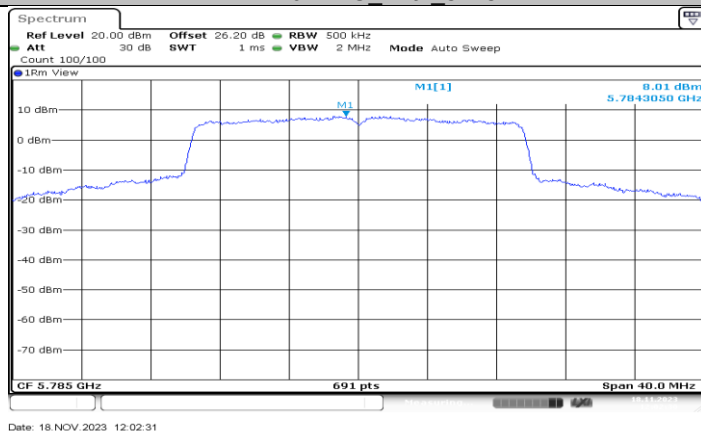




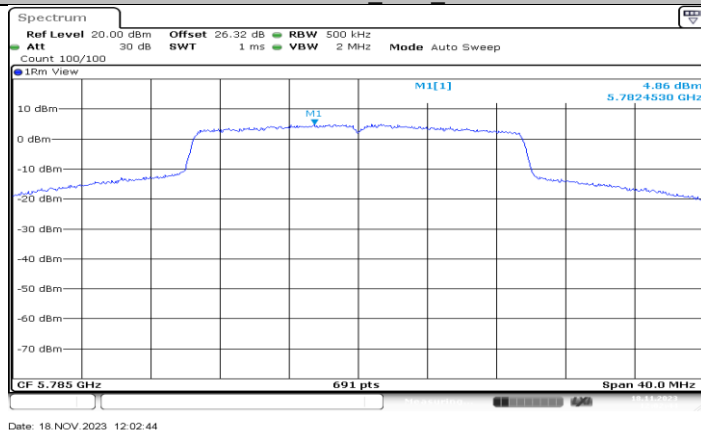
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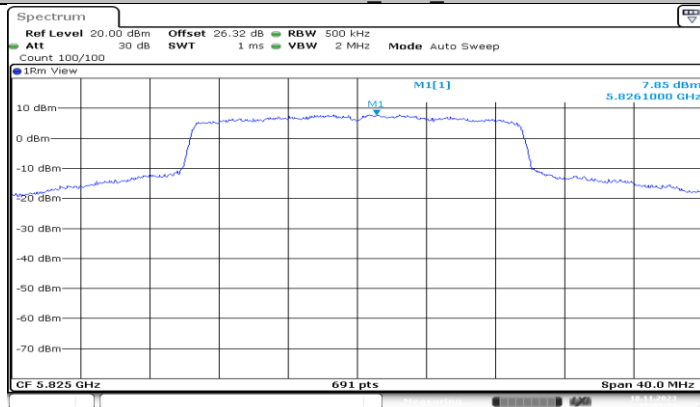
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11AX20MIMO_Ant1_5785

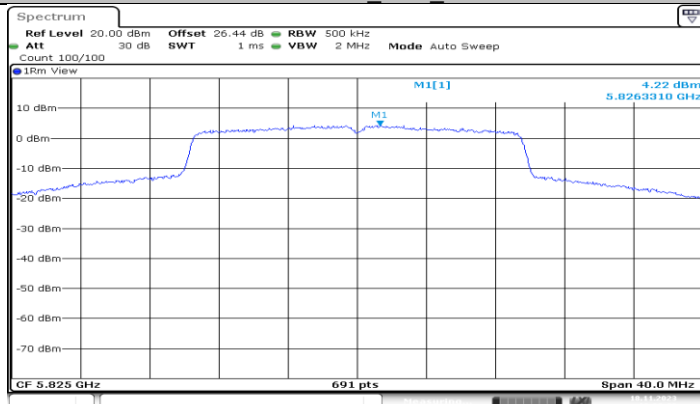


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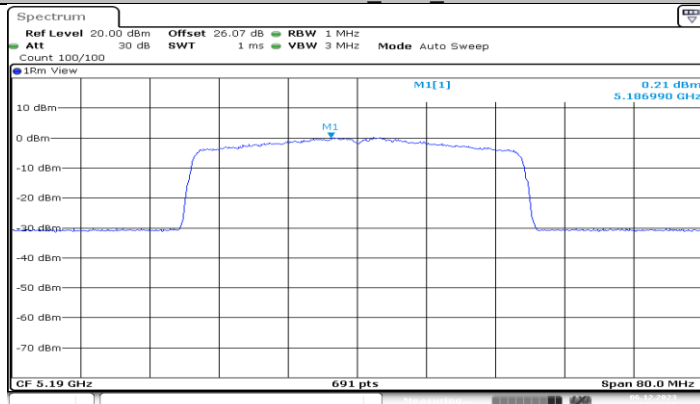
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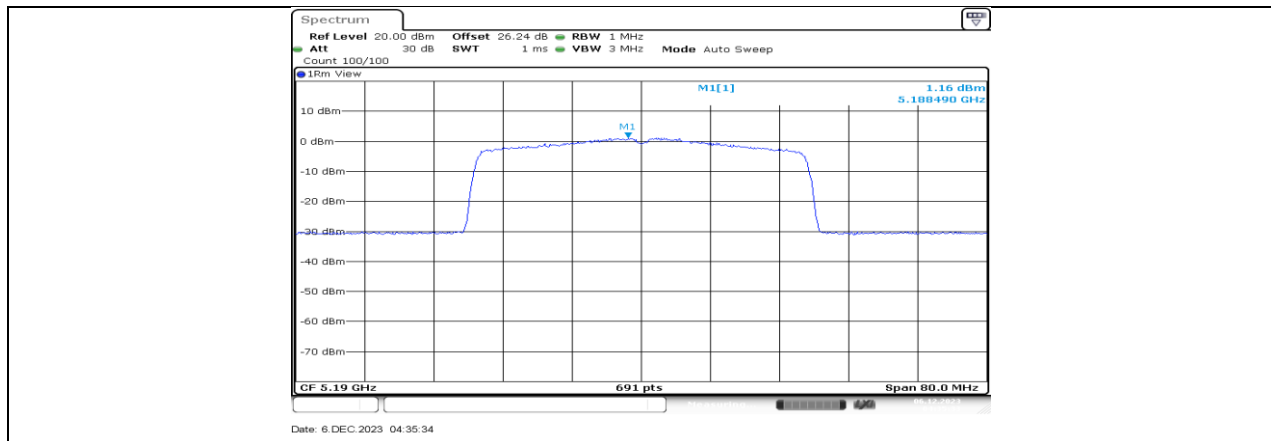
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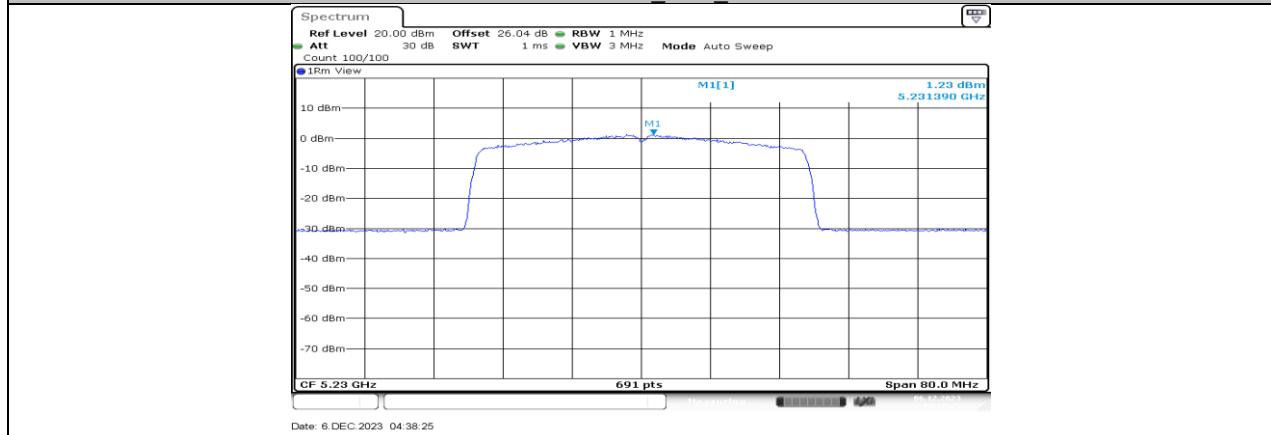


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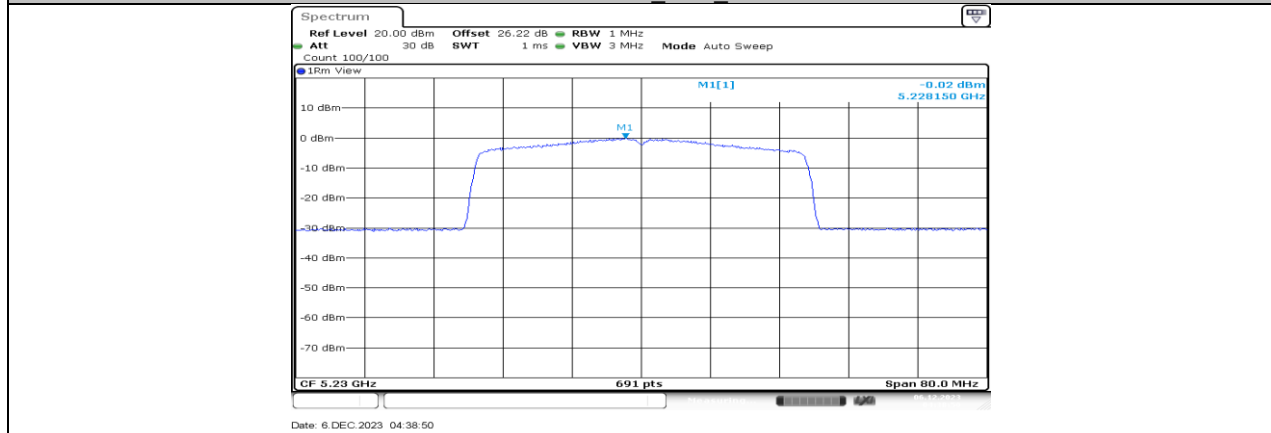
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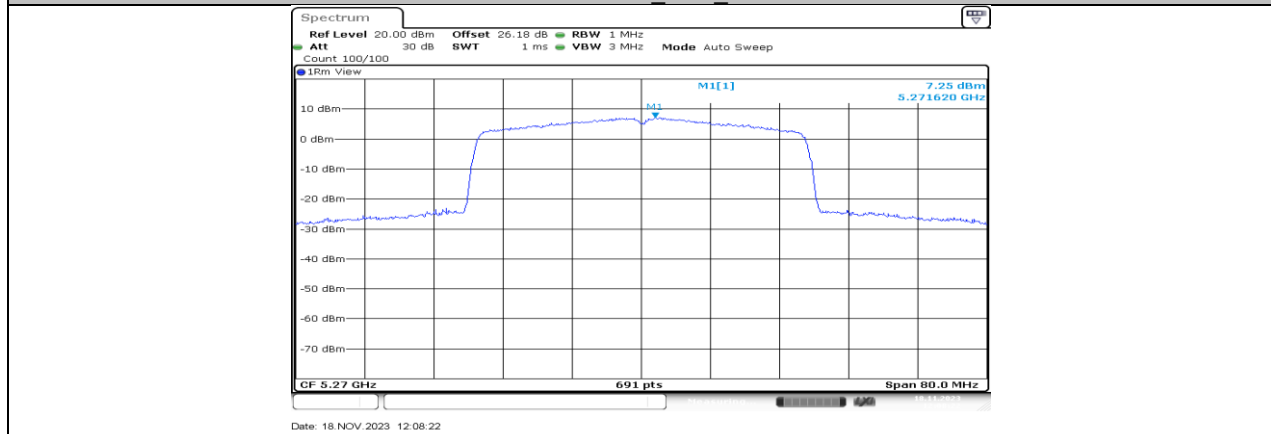
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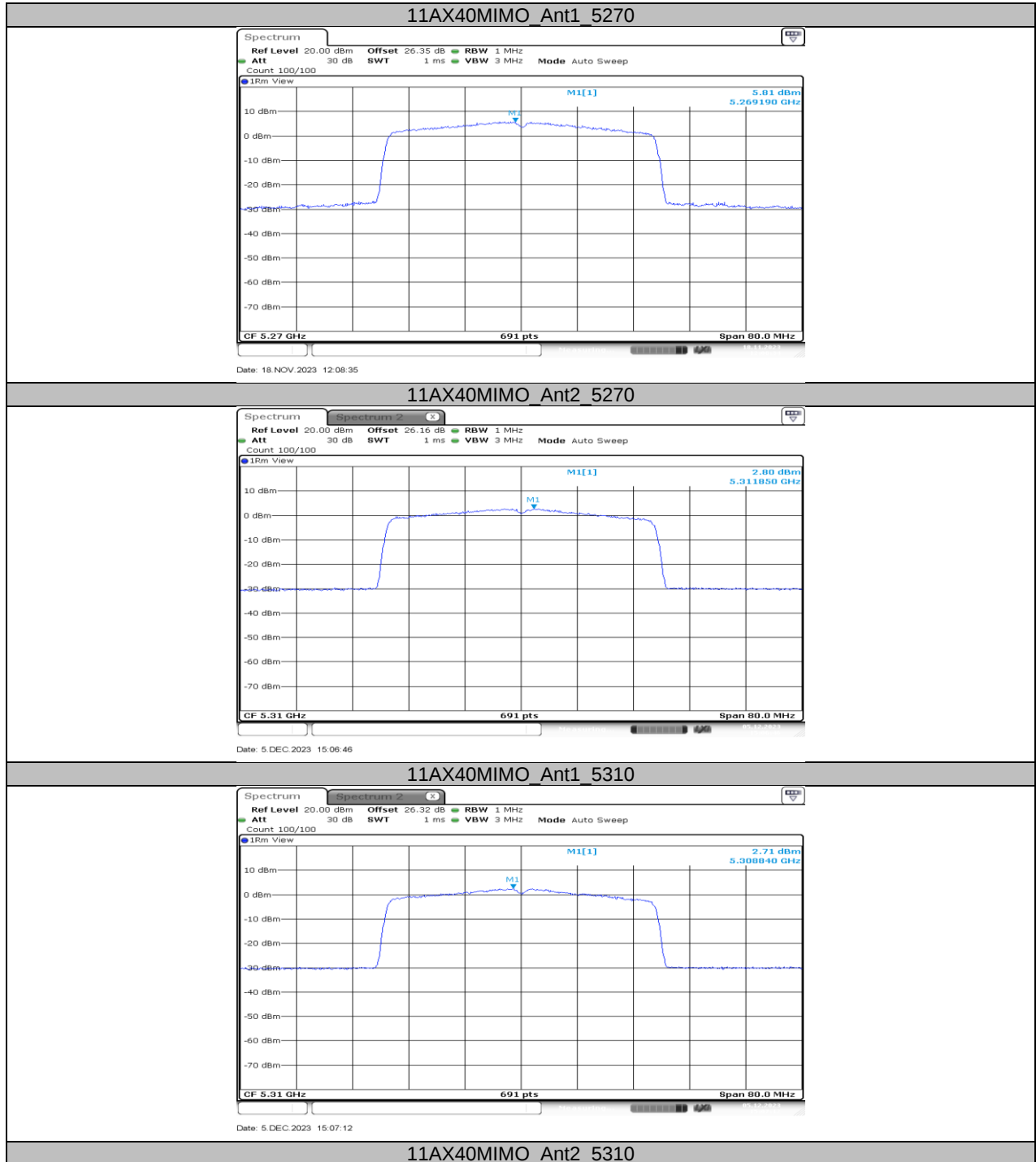


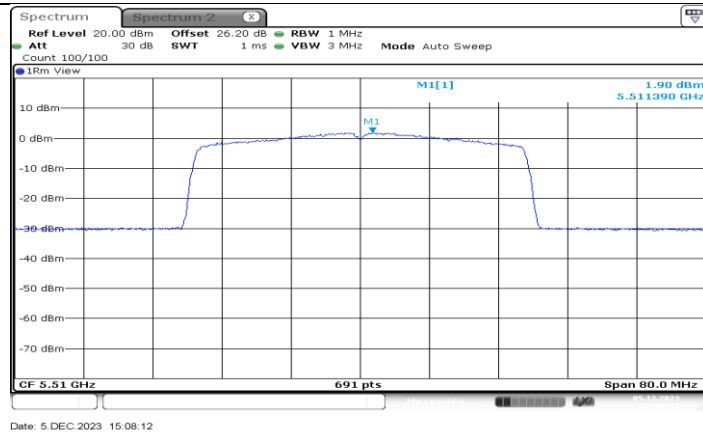
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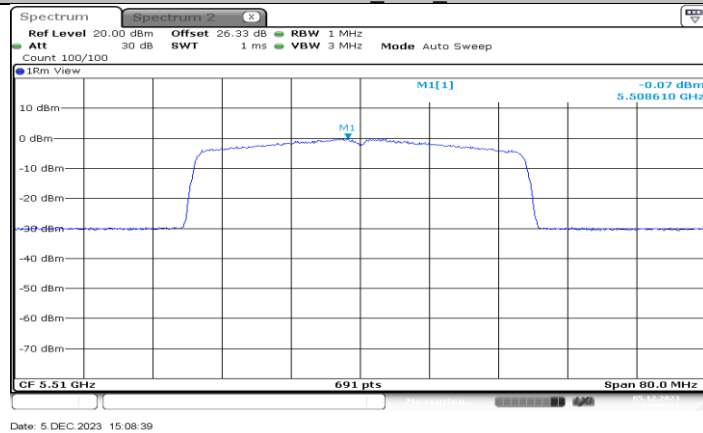
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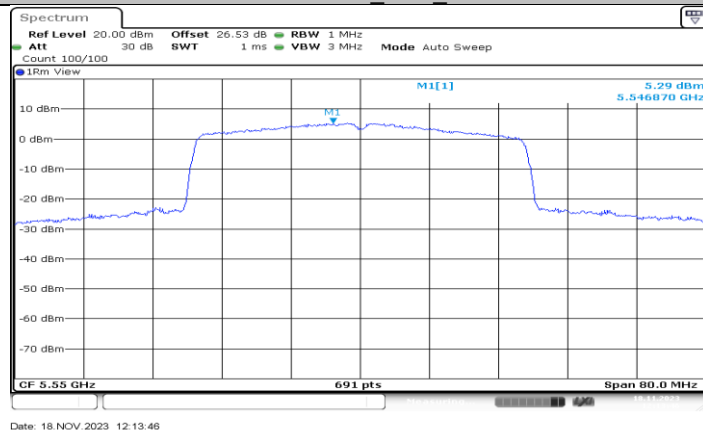
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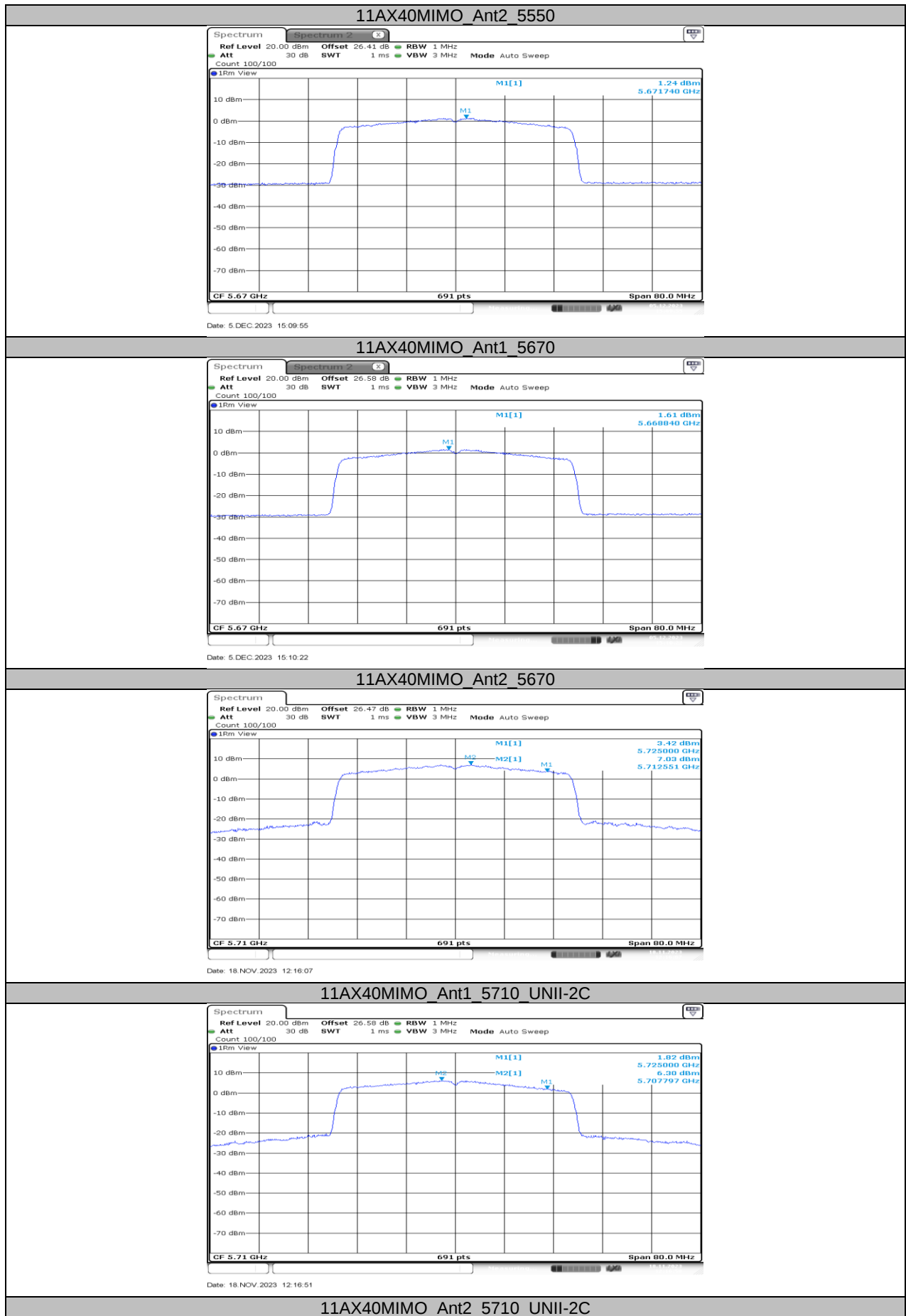


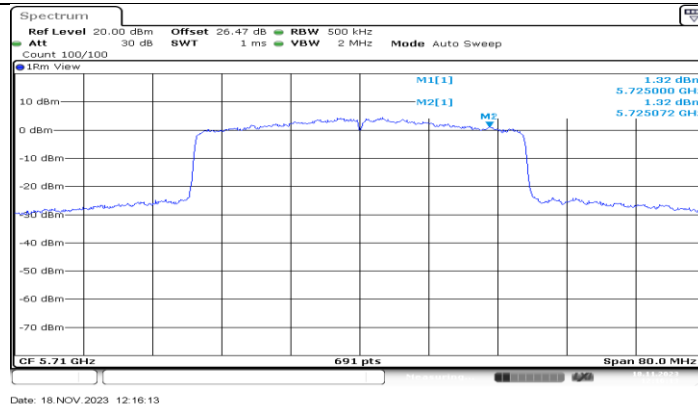
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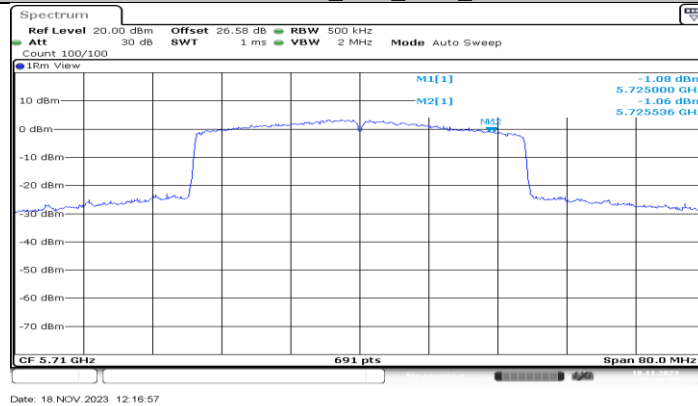
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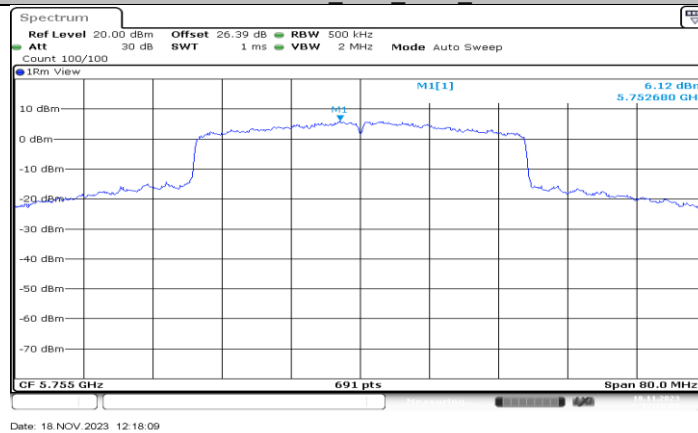




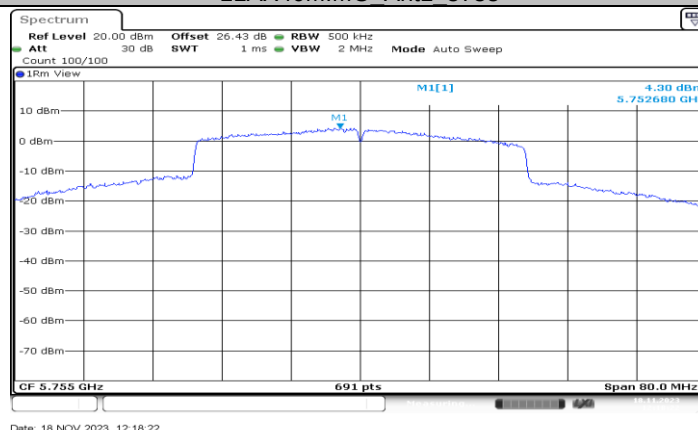
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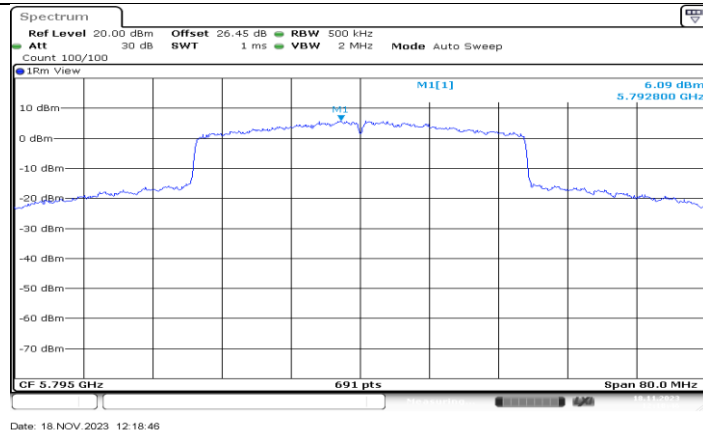
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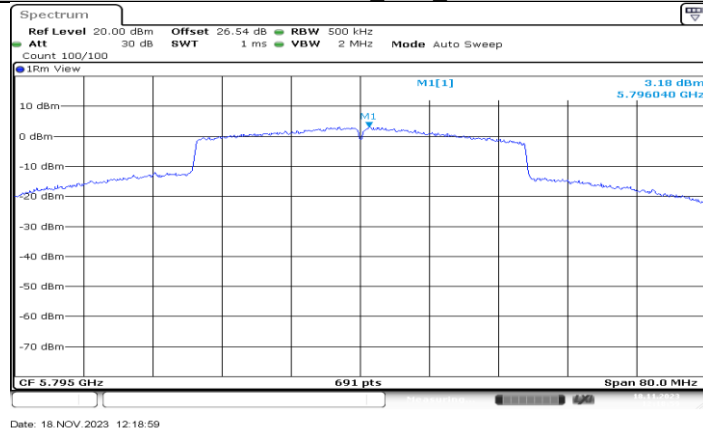
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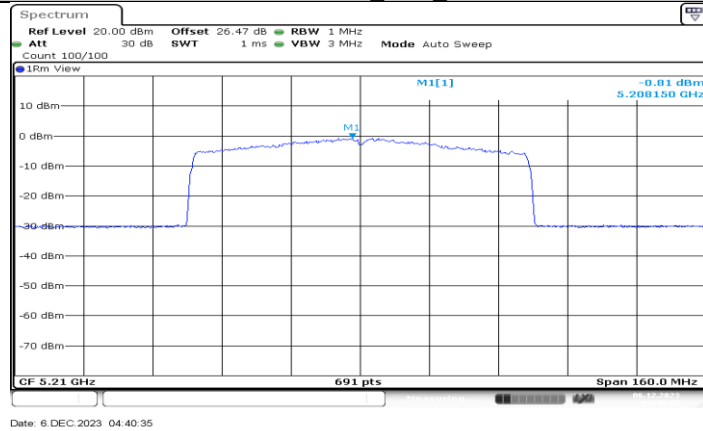
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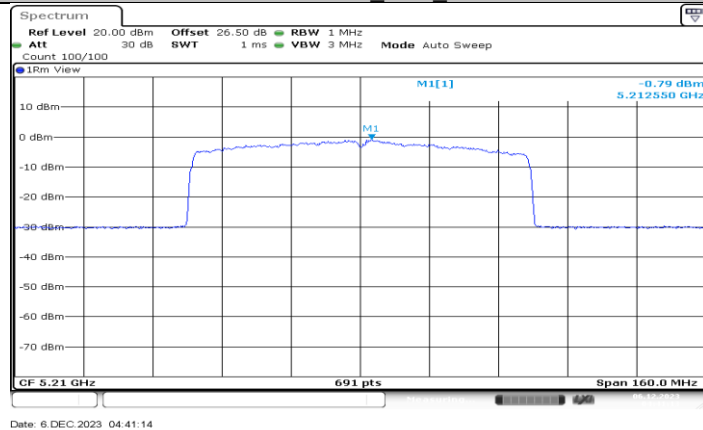
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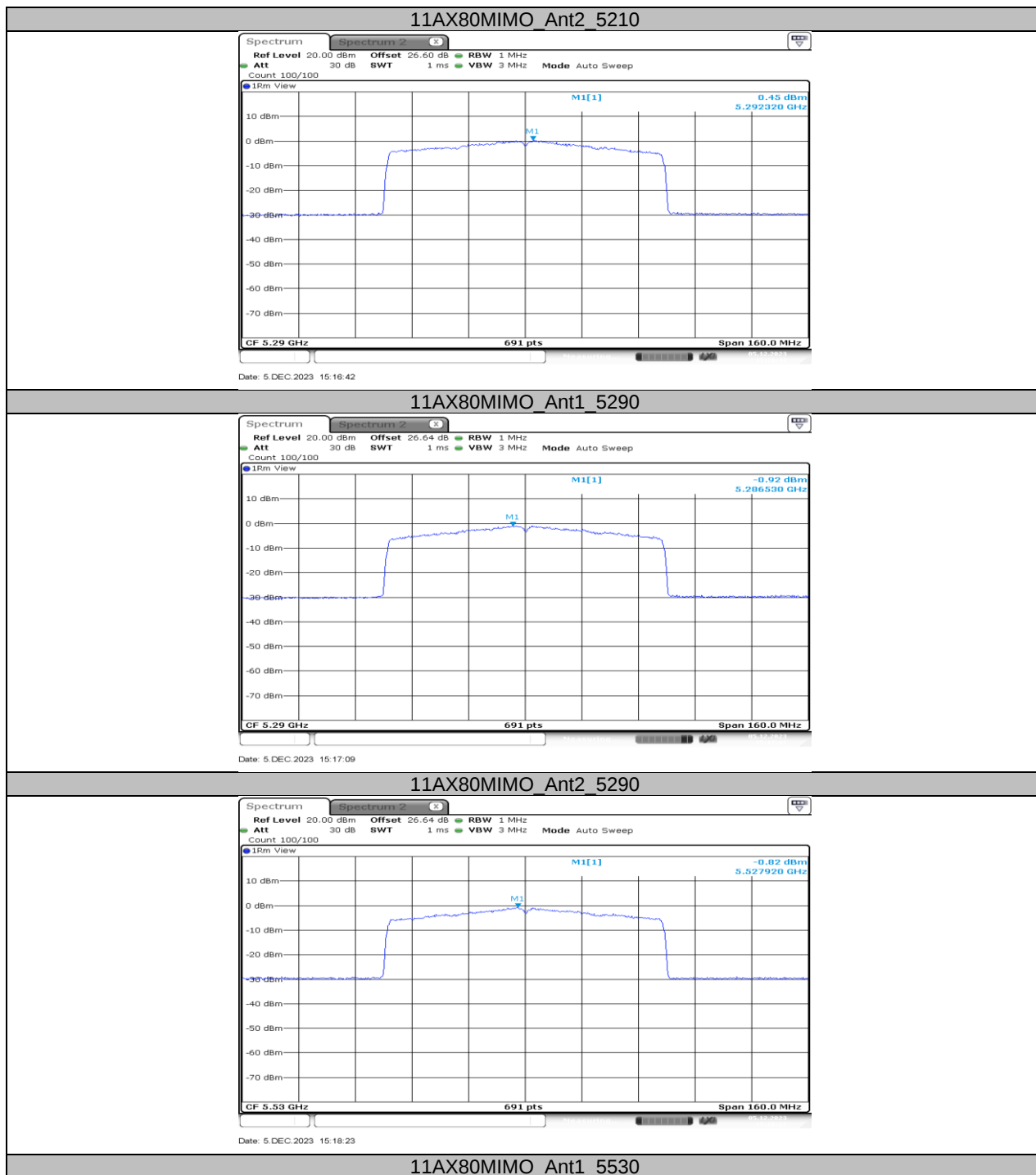


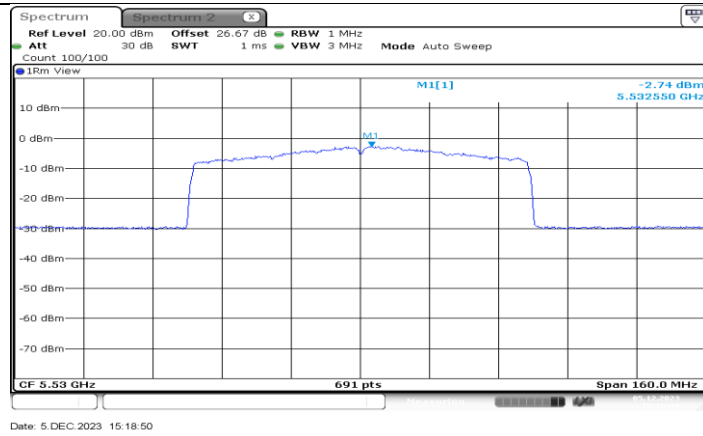
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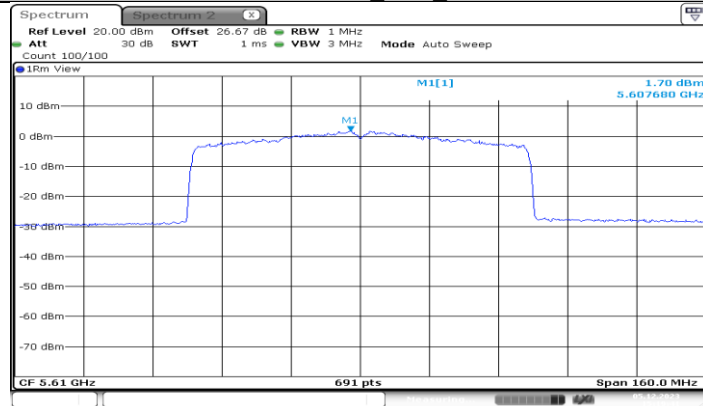






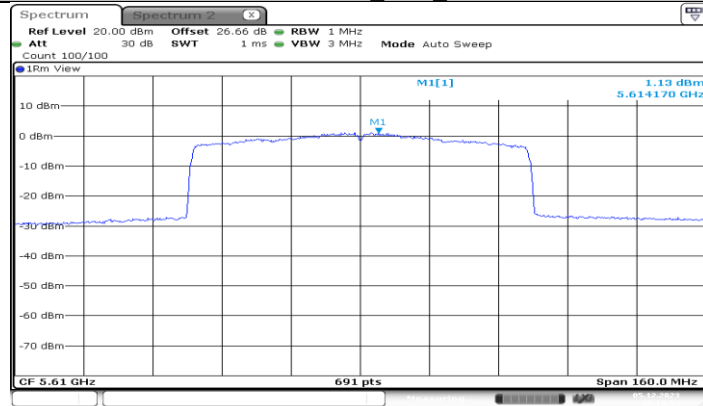
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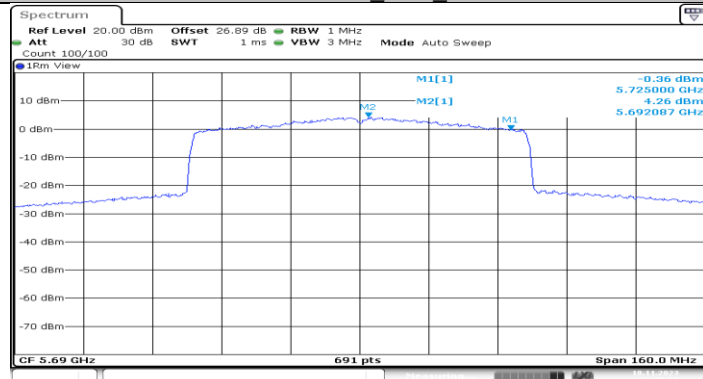
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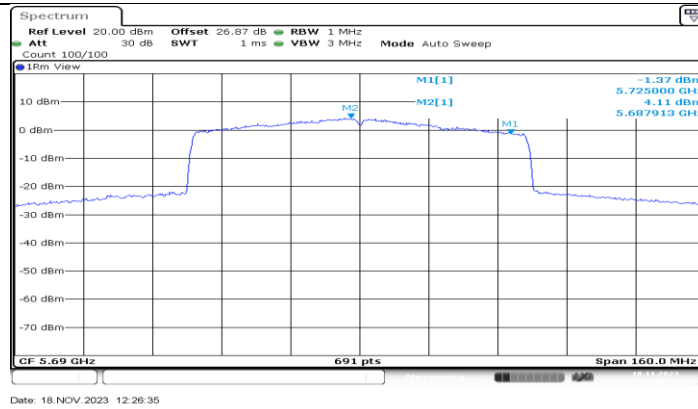
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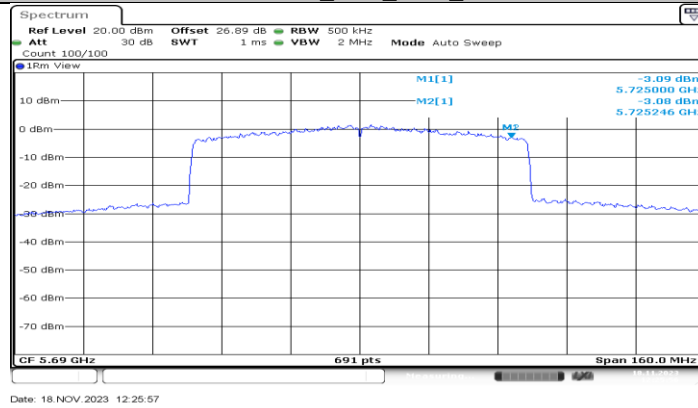


Date: 18 NOV 2023 12:25:51

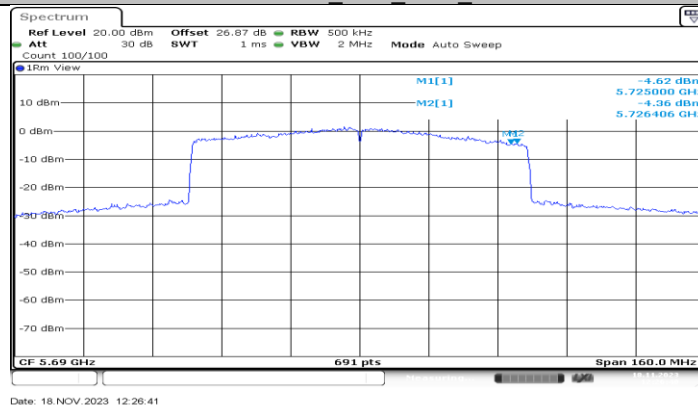
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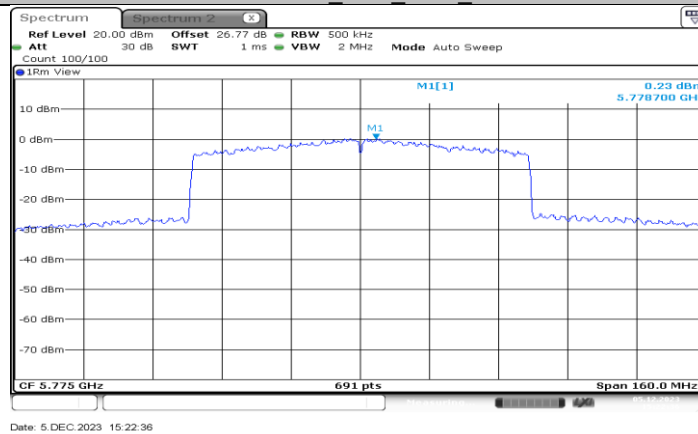
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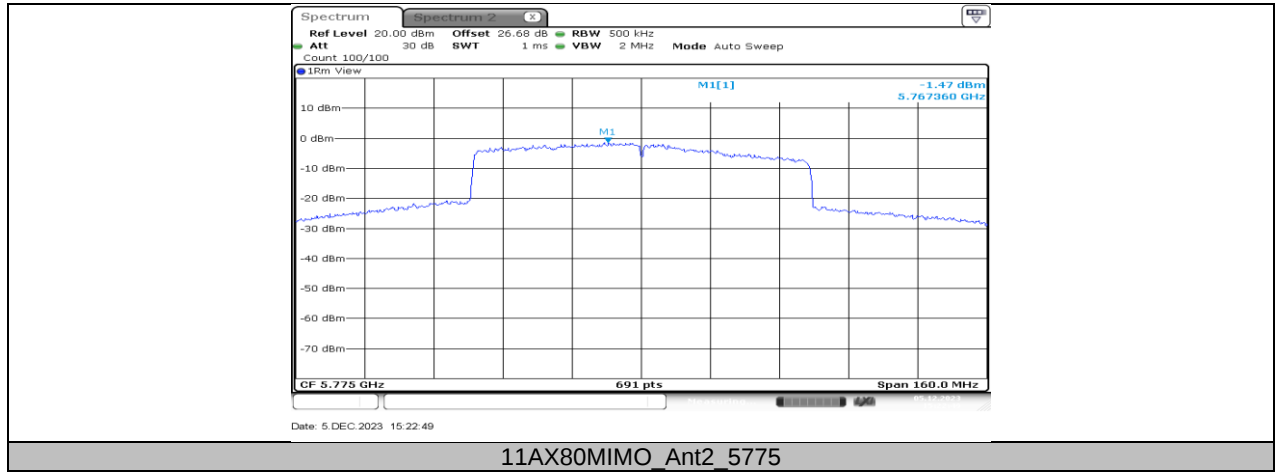
11AX80MIMO_Ant1_5690_UNII-3



11AX80MIMO_Ant2_5690_UNII-3



11AX80MIMO_Ant1_5775



11.6. APPENDIX D1: DUTY CYCLE

11.6.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11A-CDD	1.39	1.45	0.9586	95.86	0.18	0.72	1
11N20MIMO	1.31	1.37	0.9562	95.62	0.19	0.76	1
11N40MIMO	0.65	0.71	0.9155	91.55	0.38	1.54	2
11AC80MIMO	0.32	0.38	0.8421	84.21	0.75	3.13	4
11AX20MIMO	1.01	1.07	0.9439	94.39	0.25	0.99	1
11AX40MIMO	0.53	0.59	0.8983	89.83	0.47	1.89	2
11AX80MIMO	0.28	0.34	0.8235	82.35	0.84	3.57	4

Note:

Duty Cycle Correction Factor=10log (1/x).

Where: x is Duty Cycle (Linear)

Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.