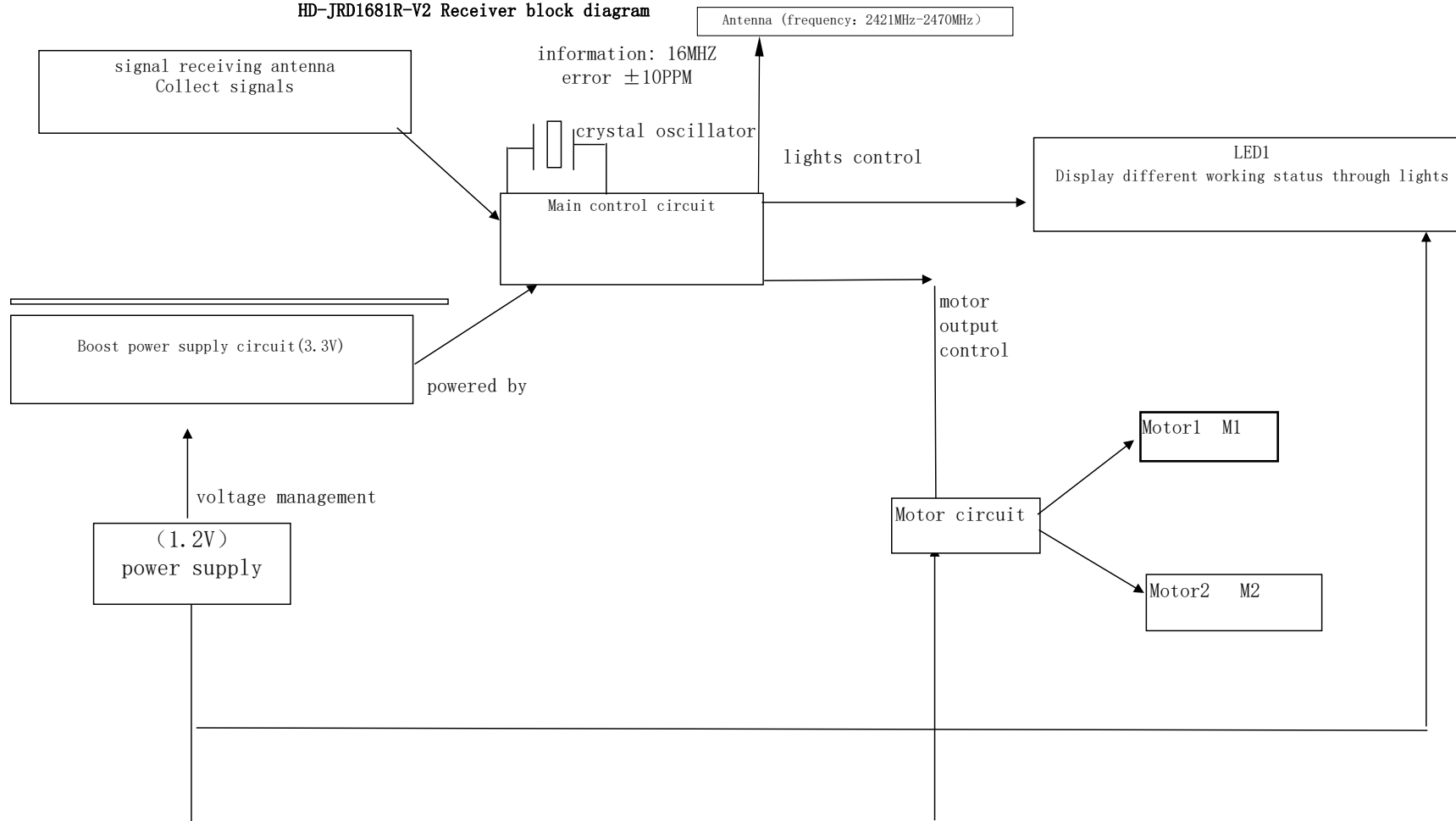


HD-JRD1681R-V2 Receiver block diagram



Description of working principle:

1. Power on the entire machine and then manage the voltage; supply power to each integrated IC circuit when the voltage reaches 3.3V
2. 2.4G component processes the signal received from the emitter through the antenna and sends the results to the main control MCU for further decoding
3. After the decoding method, the main control MCU guides the drive control and operates the 2 motors of the motor drive to run