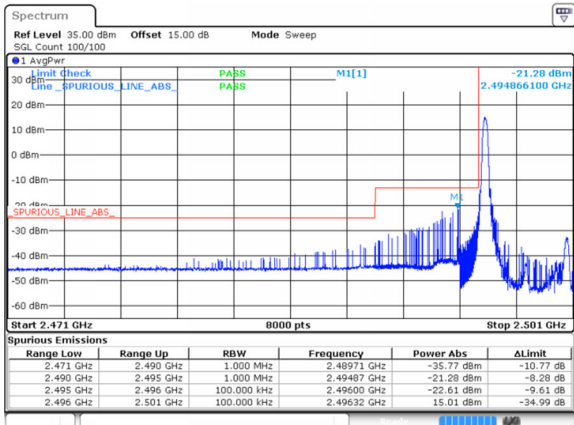


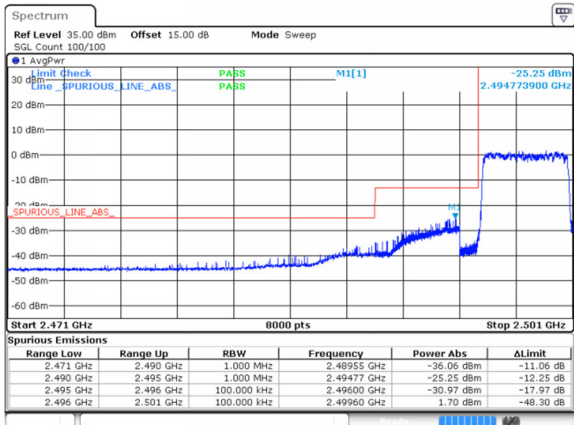
B41 , Normal

5MHz_Low_QPSK_1@0



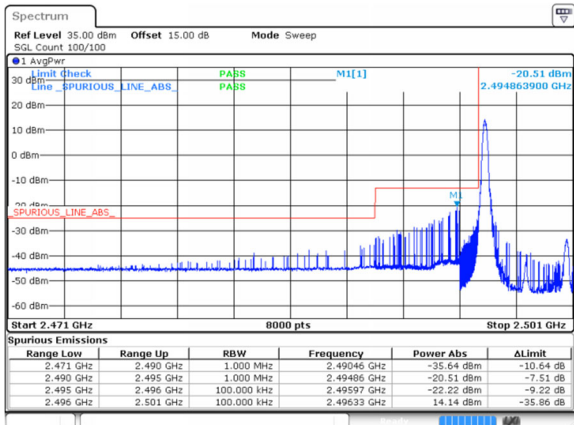
ProjectNo. RSHA240322001 Tester.Jason Lu
Date: 2 DEC 2024 13:34:35

5MHz_Low_QPSK_25@0



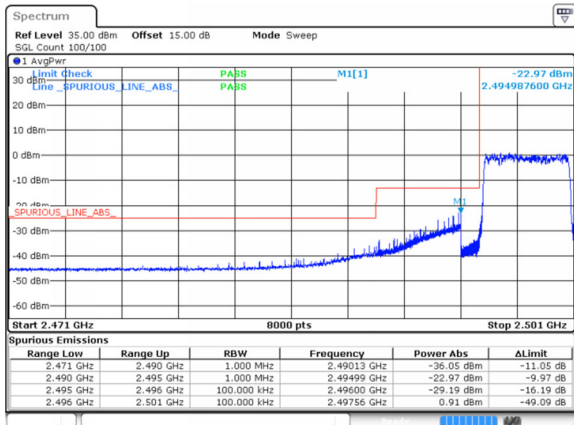
ProjectNo. RSHA240322001 Tester.Jason Lu
Date: 2 DEC 2024 13:34:49

5MHz_Low_16QAM_1@0



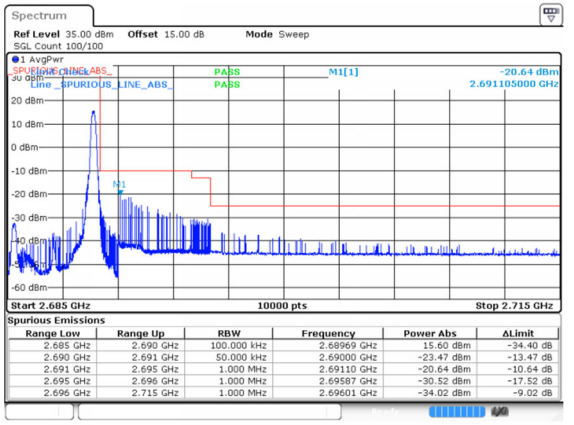
ProjectNo. RSHA240322001 Tester.Jason Lu
Date: 2 DEC 2024 13:35:03

5MHz_Low_16QAM_25@0



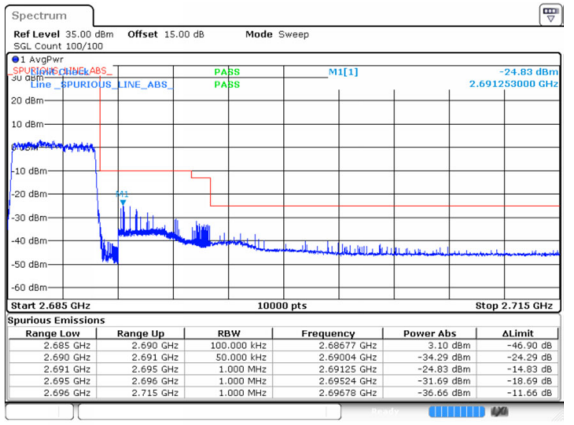
ProjectNo. RSHA240322001 Tester.Jason Lu
Date: 2 DEC 2024 13:35:17

5MHz_High_QPSK_1@24



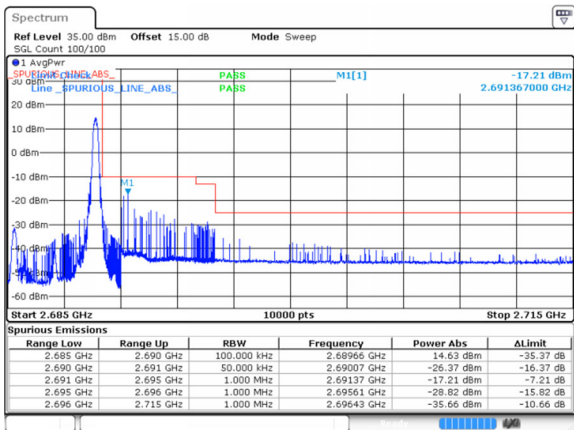
ProjectNo. RSHA240322001 Tester.Jason Lu
Date: 2 DEC 2024 13:35:35

5MHz_High_QPSK_25@0



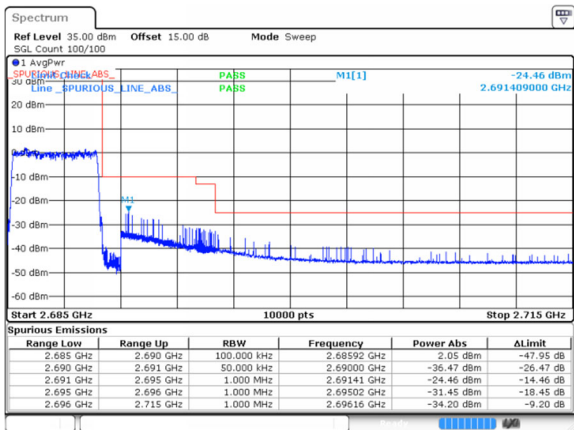
ProjectNo. RSHA240322001 Tester.Jason Lu
Date: 2 DEC 2024 13:35:48

5MHz_High_16QAM_1@24



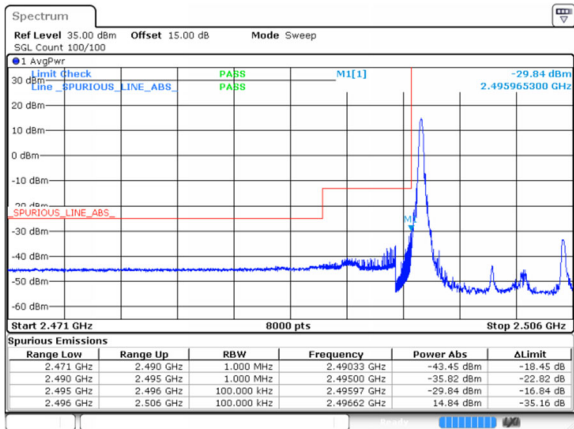
ProjectNo. RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:36:00

5MHz_High_16QAM_25@0



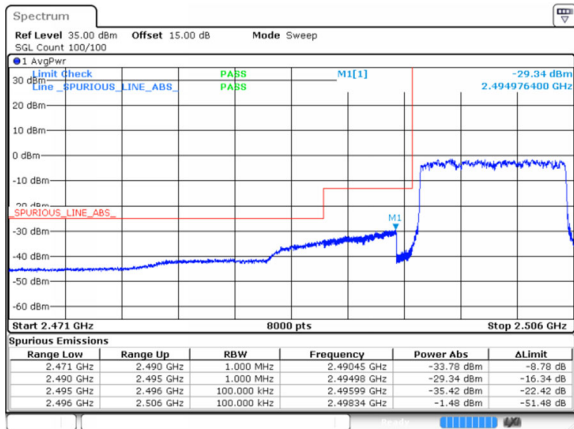
ProjectNo. RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:36:13

10MHz_Low_QPSK_1@0



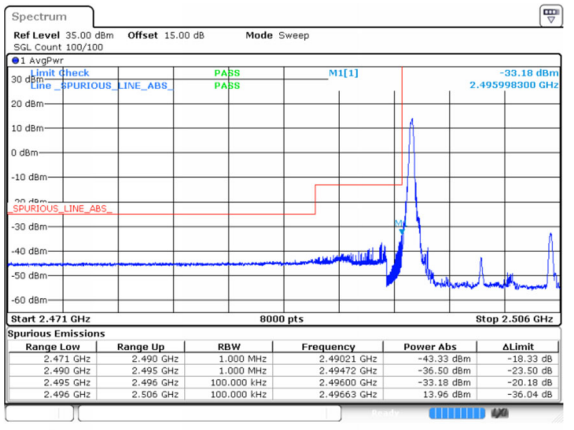
ProjectNo. RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:37:10

10MHz_Low_QPSK_50@0



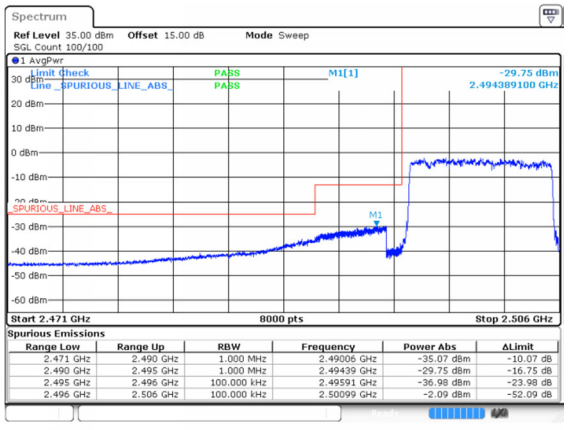
ProjectNo. RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:37:24

10MHz_Low_16QAM_1@0



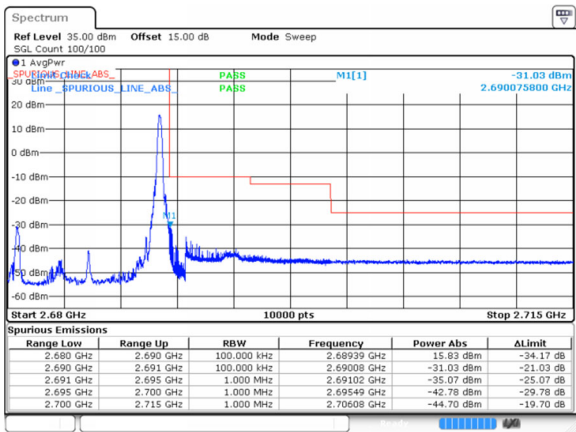
ProjectNo. RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:37:37

10MHz_Low_16QAM_50@0



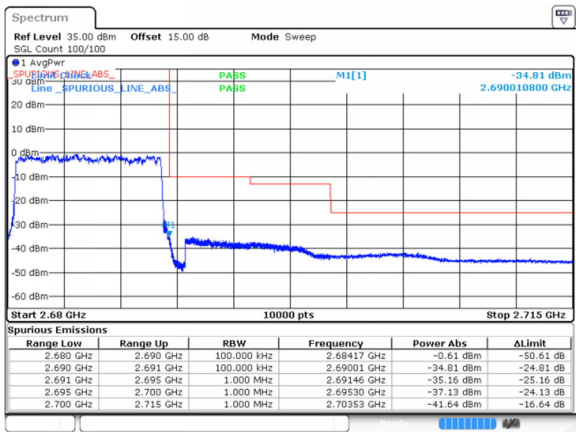
ProjectNo. RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:37:51

10MHz_High_QPSK_1@49



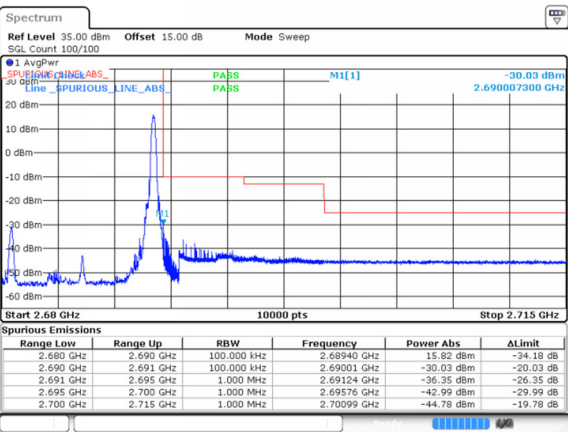
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:38:10

10MHz_High_QPSK_50@0



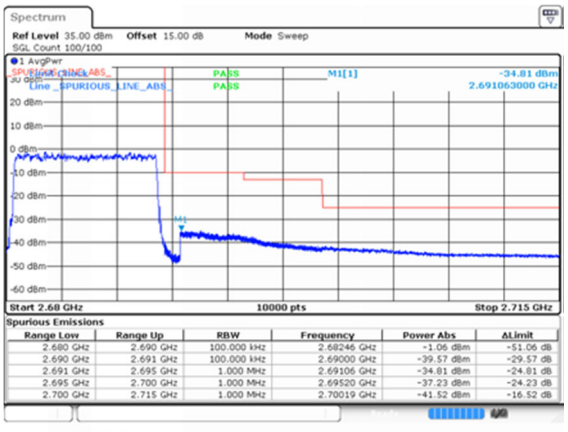
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:38:22

10MHz_High_16QAM_1@49



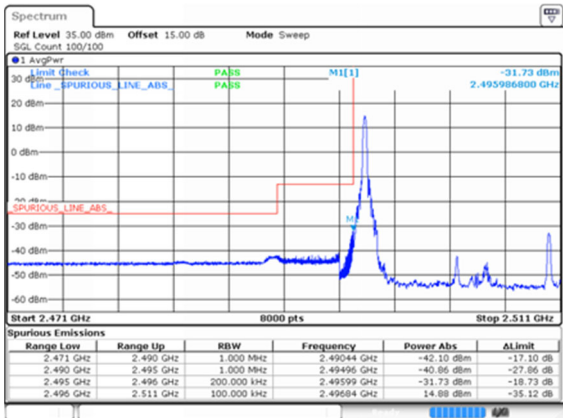
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:38:35

10MHz_High_16QAM_50@0



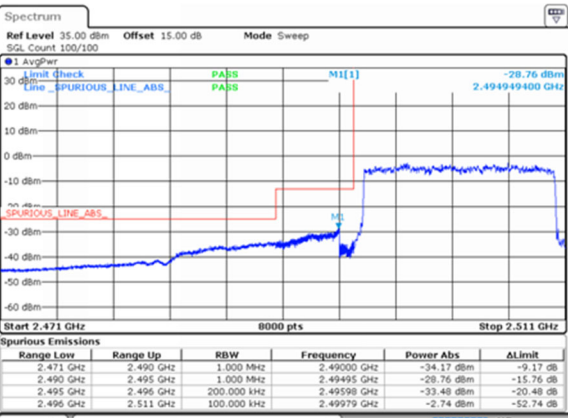
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:39:00

15MHz_Low_QPSK_1@0



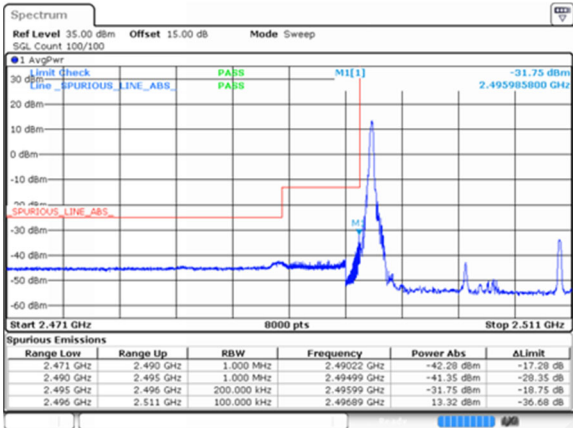
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:39:59

15MHz_Low_QPSK_75@0



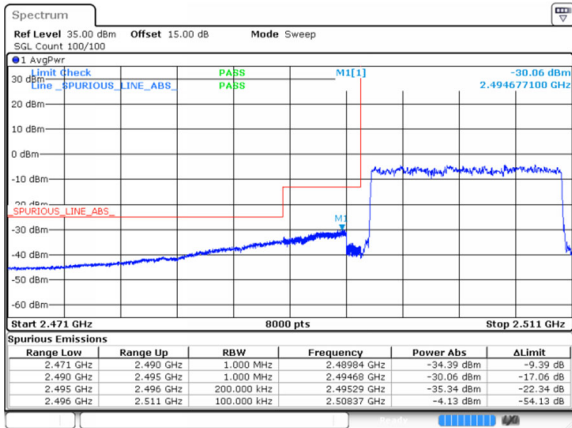
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:40:14

15MHz_Low_16QAM_1@0



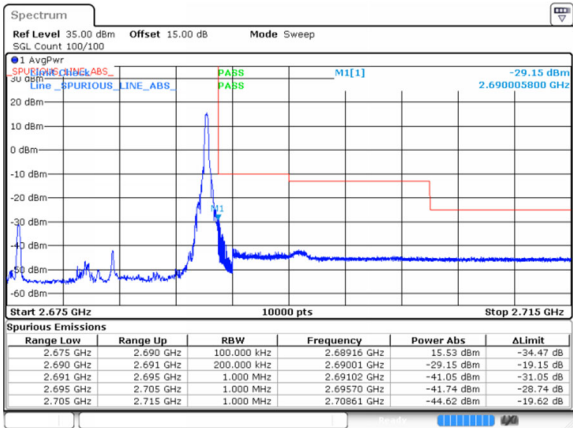
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:40:30

15MHz_Low_16QAM_75@0



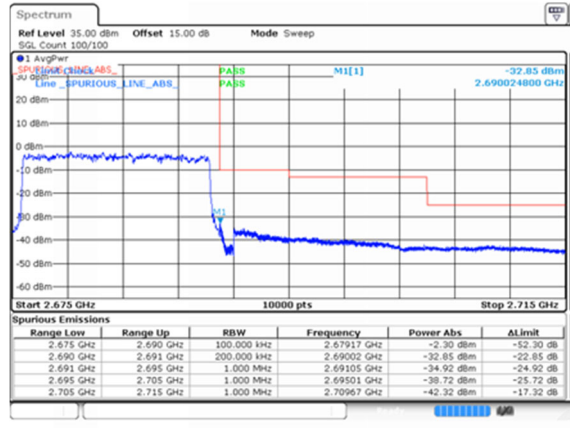
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Date: 2 DEC 2024 13:40:45

15MHz_High_QPSK_1@74



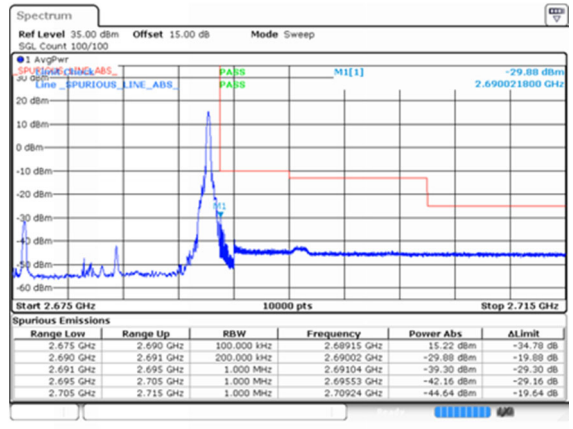
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:41:05

15MHz_High_QPSK_75@0



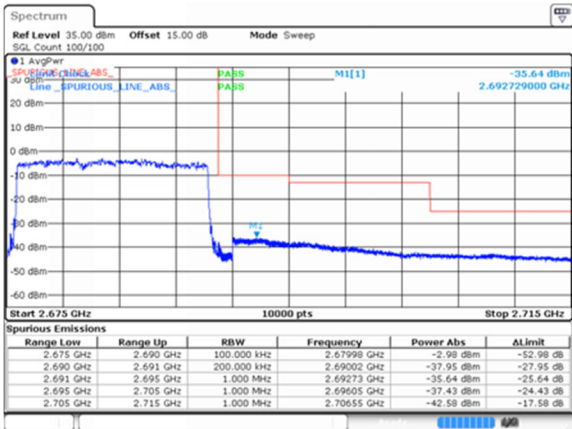
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:41:21

15MHz_High_16QAM_1@74



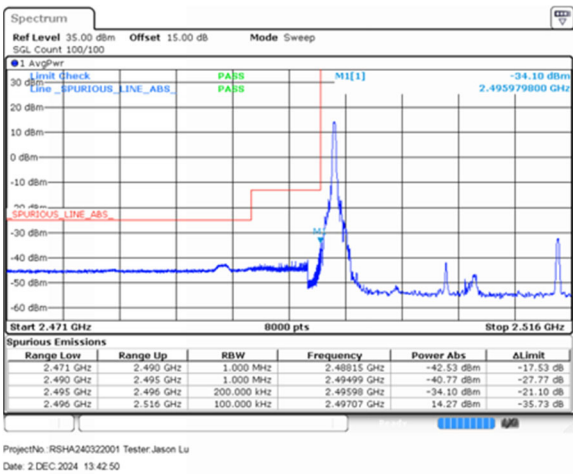
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Date: 2 DEC 2024 13:41:36

15MHz_High_16QAM_75@0

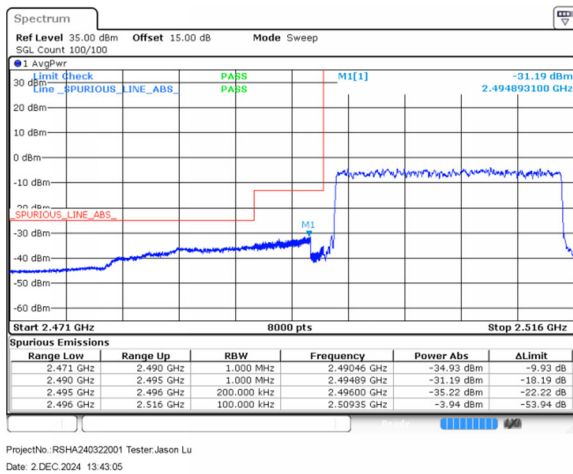


ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 2 DEC 2024 13:41:51

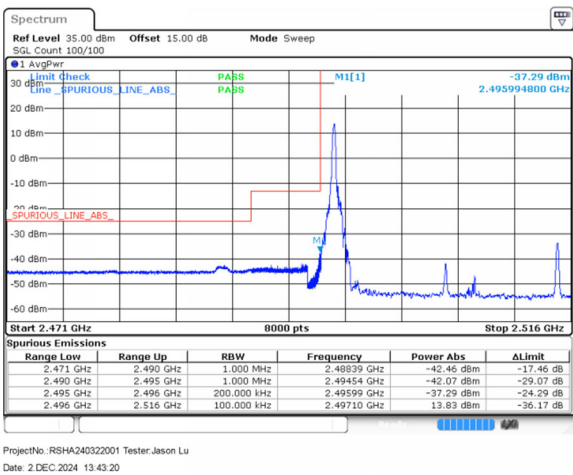
20MHz_Low_QPSK_1@0



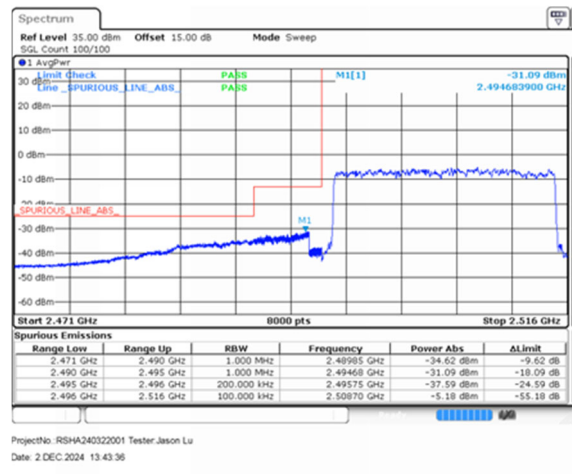
20MHz_Low_QPSK_100@0



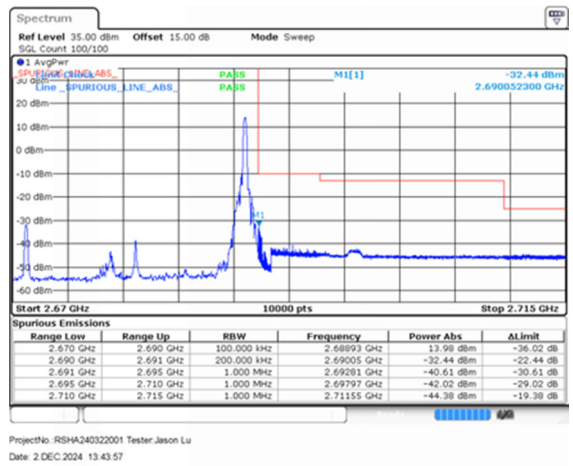
20MHz_Low_16QAM_1@0



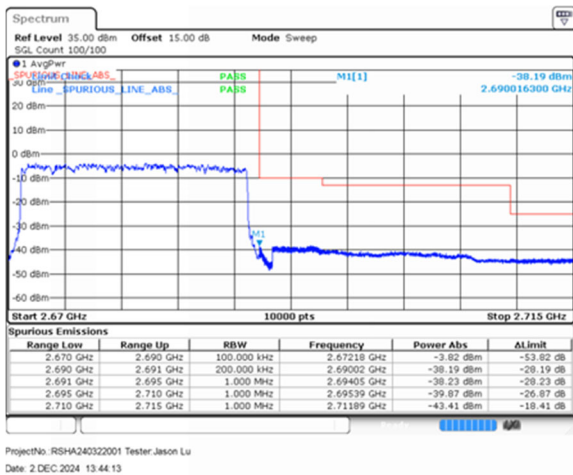
20MHz_Low_16QAM_100@0



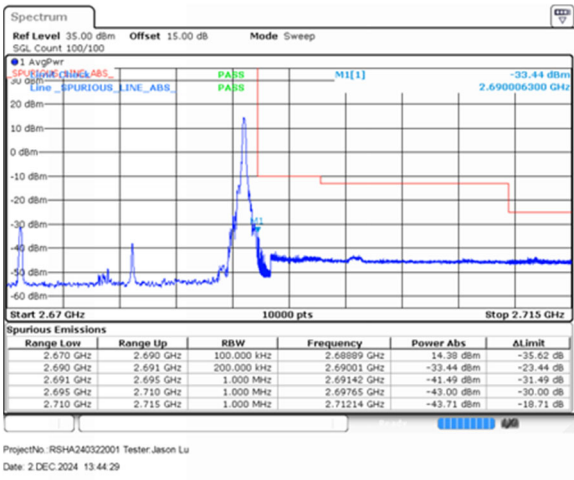
20MHz_High_QPSK_1@99



20MHz_High_QPSK_100@0



20MHz_High_16QAM_1@99



20MHz_High_16QAM_100@0

