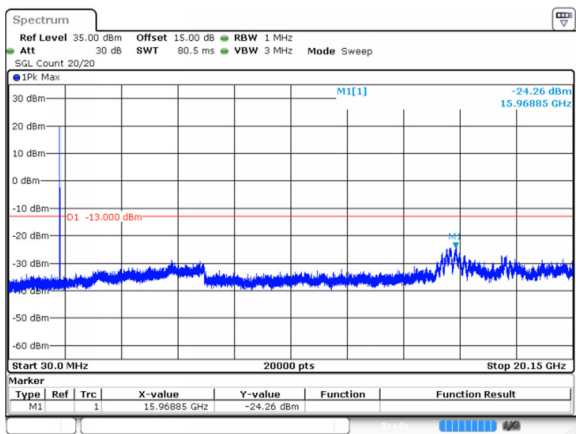
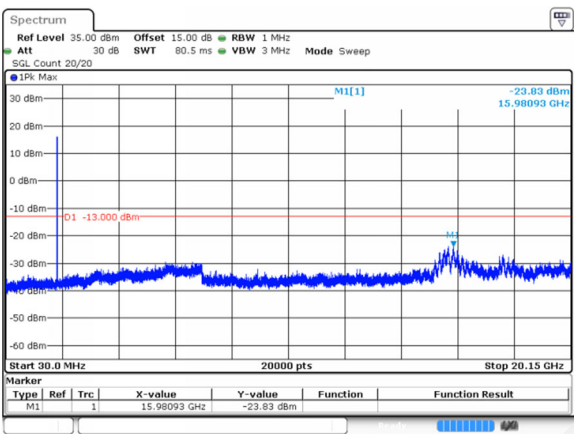


5MHz_Low_QPSK_1@0(30MHz~20.15GHz)



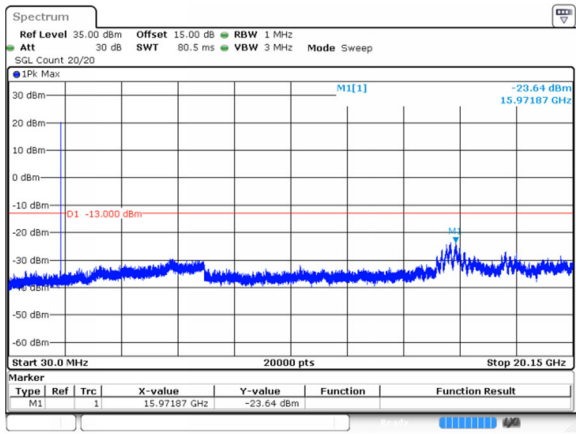
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 21:58:30

5MHz_Low_QPSK_25@0(30MHz~20.15GHz)



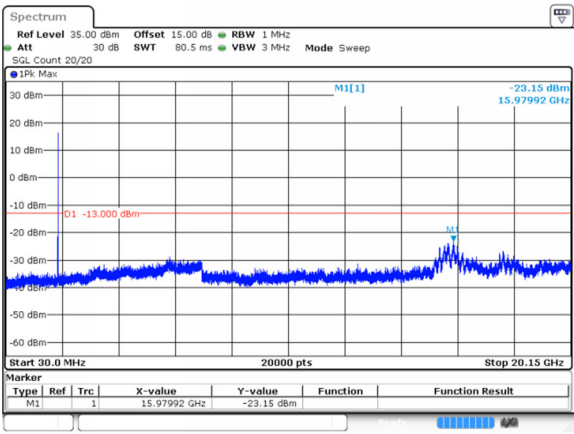
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 21:58:52

5MHz_Middle_QPSK_1@0(30MHz~20.15GHz)



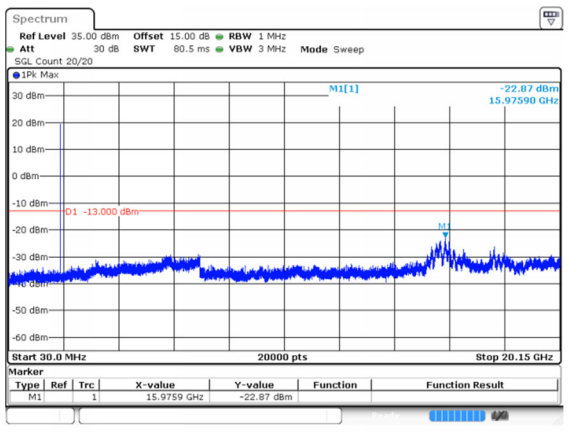
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 21:59:15

5MHz_Middle_QPSK_25@0(30MHz~20.15GHz)



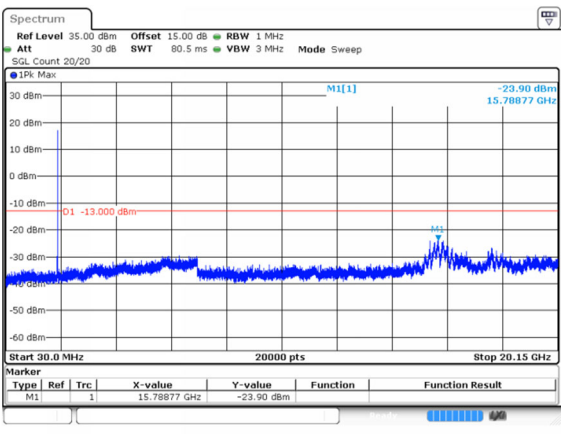
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 21:59:38

5MHz_High_QPSK_1@0(30MHz~20.15GHz)



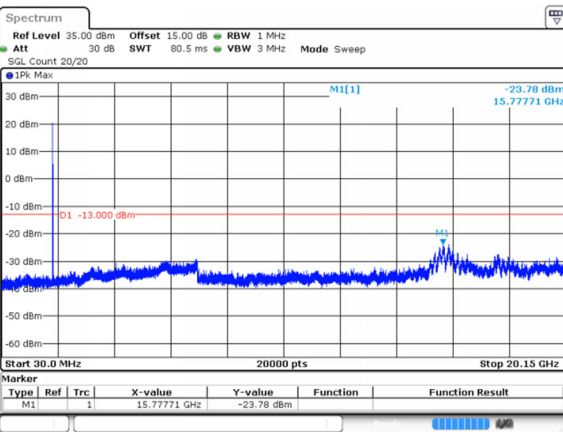
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:00:01

5MHz_High_QPSK_25@0(30MHz~20.15GHz)



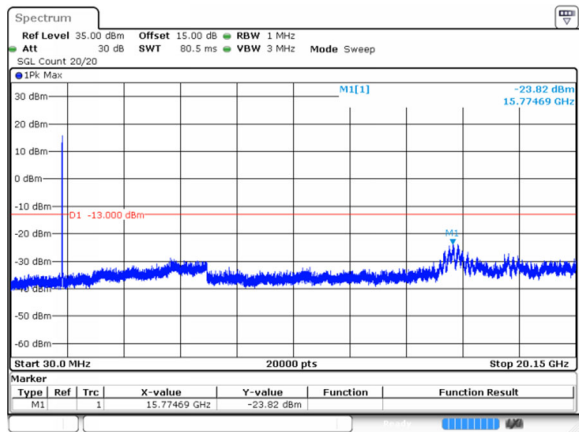
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:00:23

10MHz_Low_QPSK_1@0(30MHz~20.15GHz)



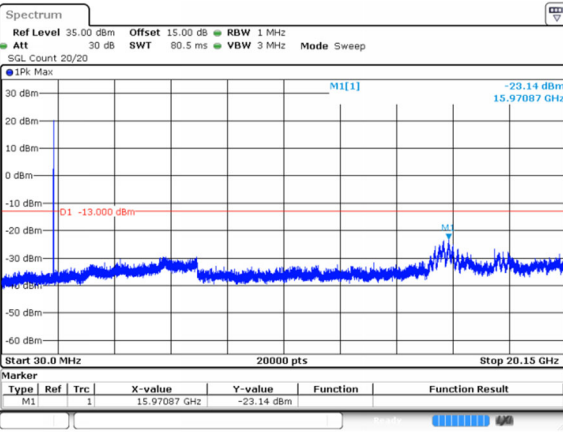
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:01:17

10MHz_Low_QPSK_50@0(30MHz~20.15GHz)



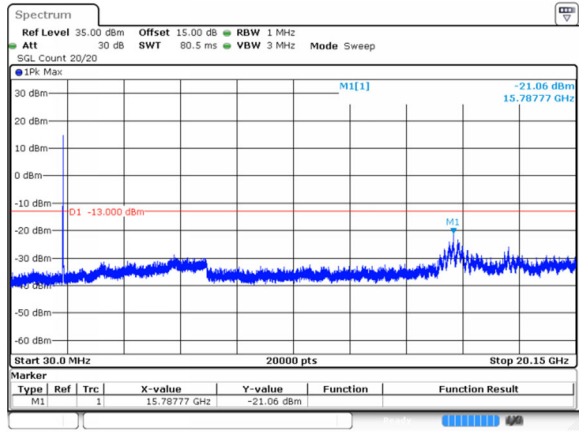
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:01:40

10MHz_Middle_QPSK_1@0(30MHz~20.15GHz)



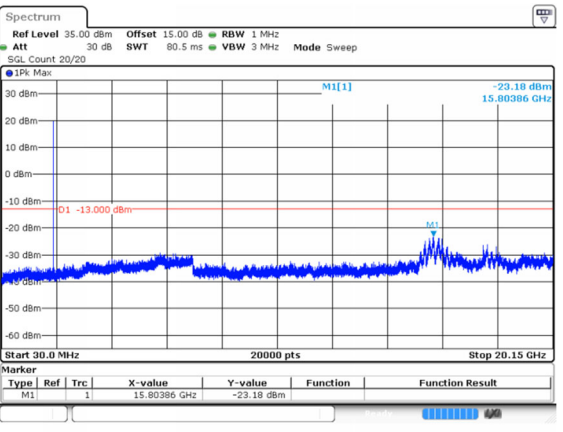
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:02:02

10MHz_Middle_QPSK_50@0(30MHz~20.15GHz)



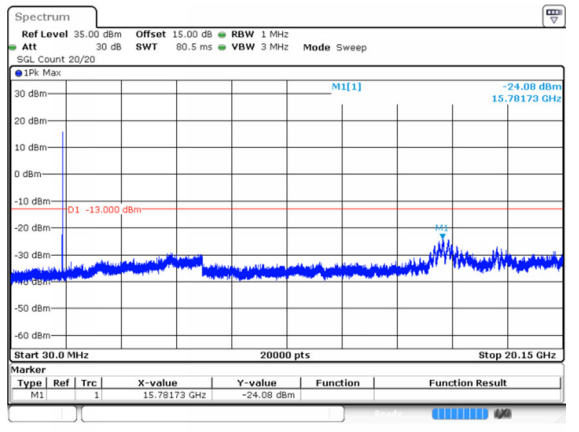
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:02:25

10MHz_High_QPSK_1@0(30MHz~20.15GHz)



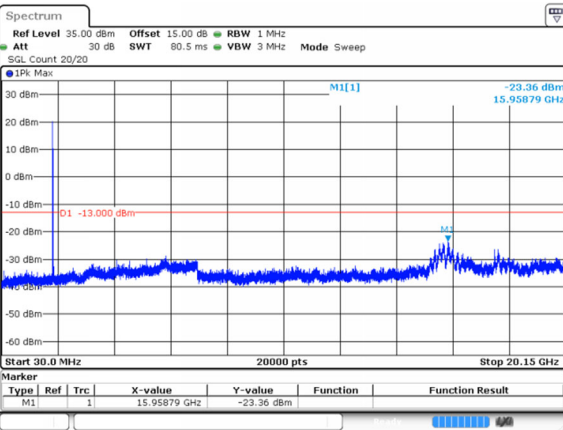
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:02:47

10MHz_High_QPSK_50@0(30MHz~20.15GHz)



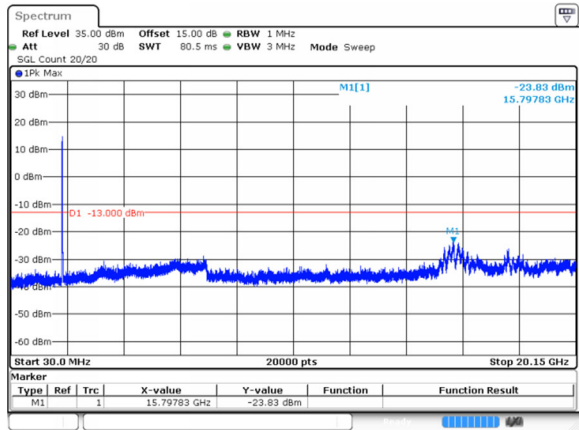
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:03:10

15MHz_Low_QPSK_1@0(30MHz~20.15GHz)



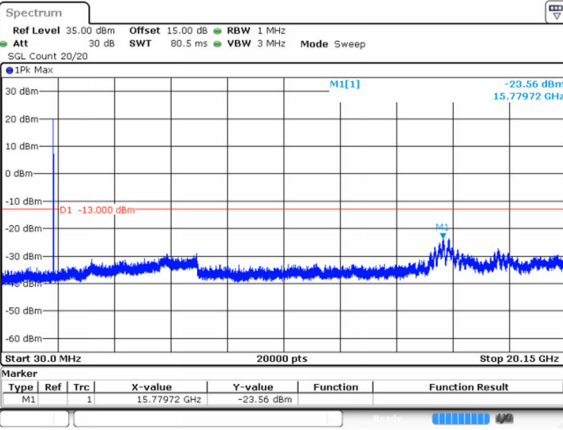
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:05:05

15MHz_Low_QPSK_75@0(30MHz~20.15GHz)



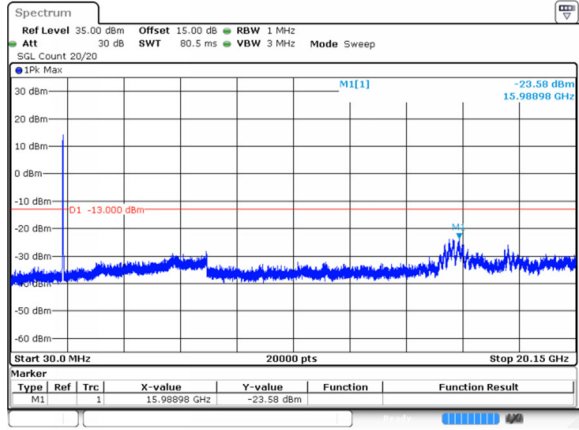
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:05:29

15MHz_Middle_QPSK_1@0(30MHz~20.15GHz)



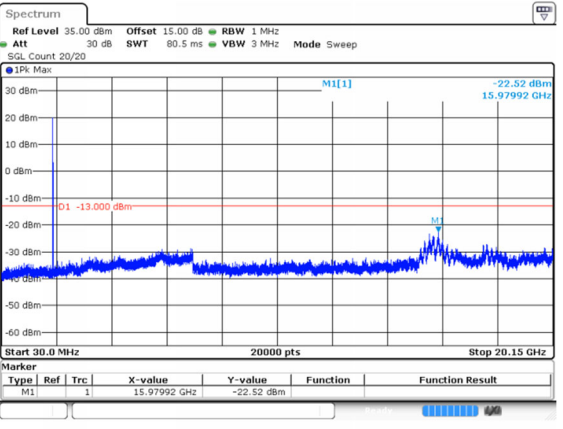
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:05:52

15MHz_Middle_QPSK_75@0(30MHz~20.15GHz)



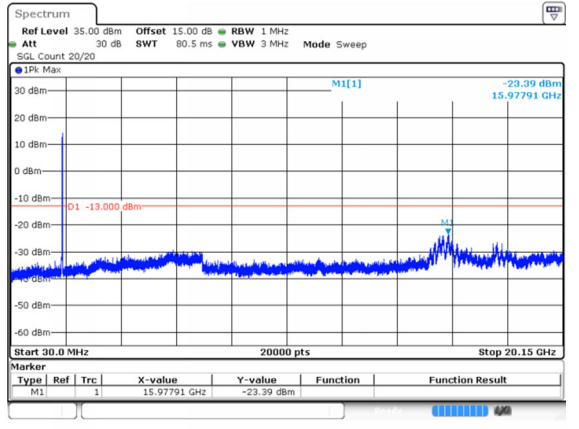
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:06:16

15MHz_High_QPSK_1@0(30MHz~20.15GHz)



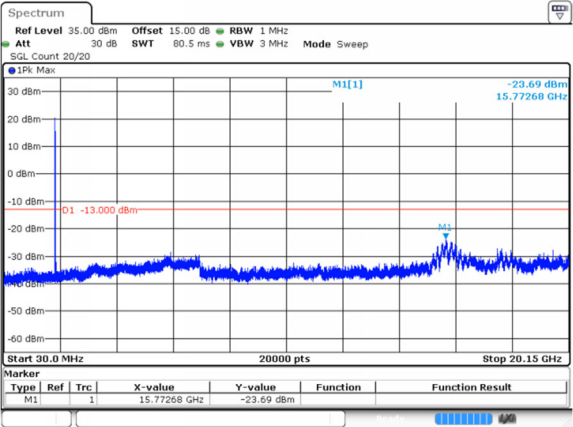
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:06:38

15MHz_High_QPSK_75@0(30MHz~20.15GHz)



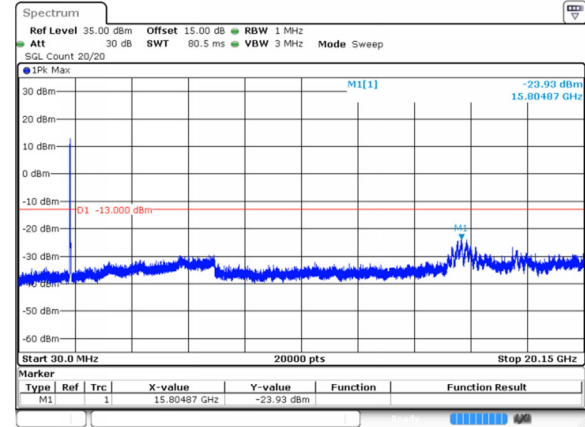
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:07:02

20MHz_Low_QPSK_1@0(30MHz~20.15GHz)



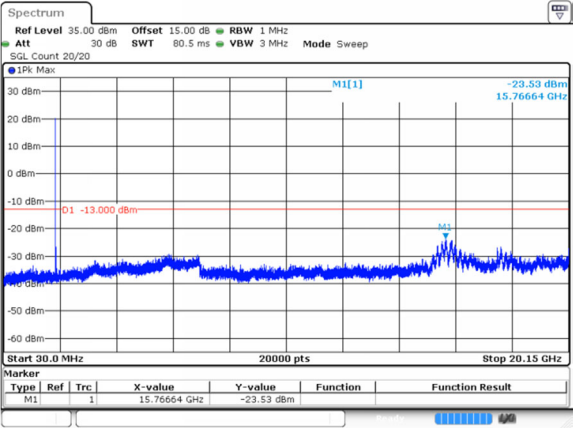
ProjectNo.: RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:07:56

20MHz_Low_QPSK_100@0(30MHz~20.15GHz)



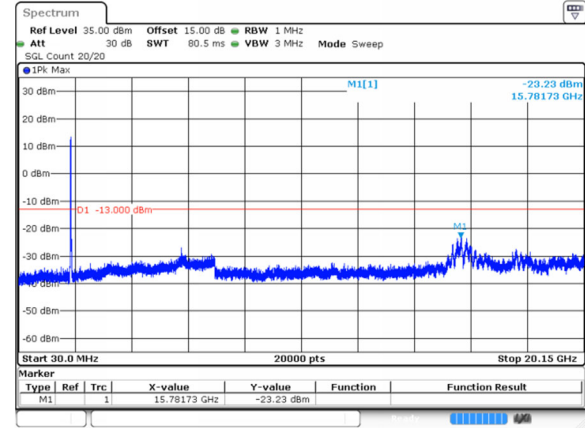
ProjectNo.: RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:08:20

20MHz_Middle_QPSK_1@0(30MHz~20.15GHz)



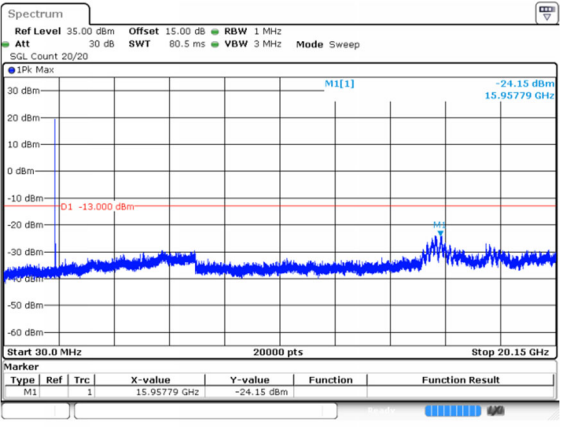
ProjectNo.: RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:08:43

20MHz_Middle_QPSK_100@0(30MHz~20.15GHz)



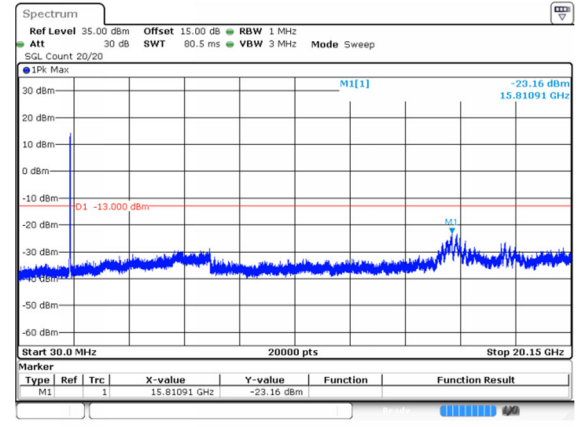
ProjectNo.: RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:09:06

20MHz_High_QPSK_1@0(30MHz~20.15GHz)



ProjectNo.: RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:09:29

20MHz_High_QPSK_100@0(30MHz~20.15GHz)



ProjectNo.: RSHA240322001 Tester:Jason Lu
Date: 29 NOV 2024 22:09:54