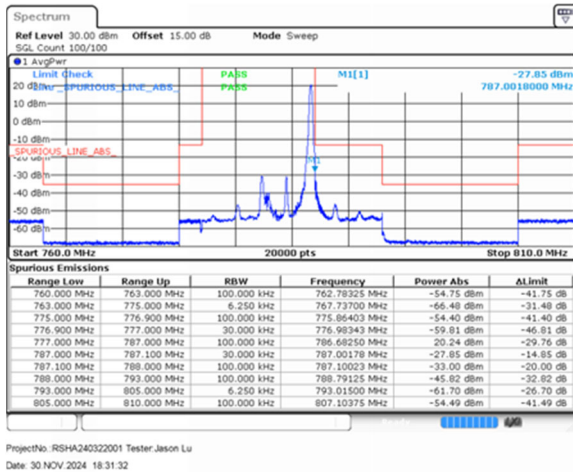
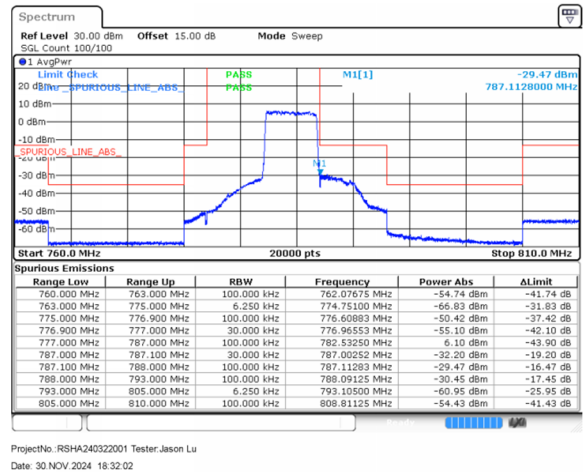


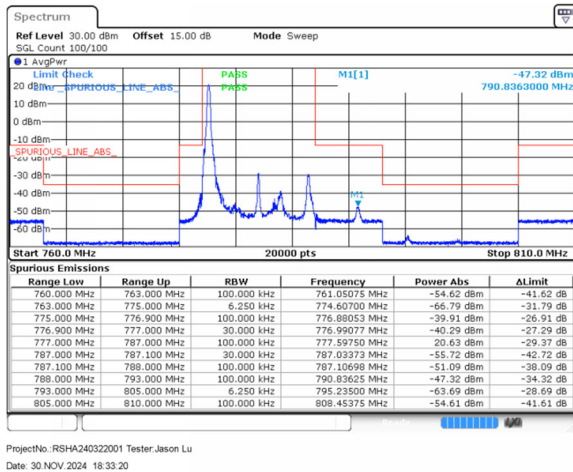
5MHz_High_16QAM_1@24



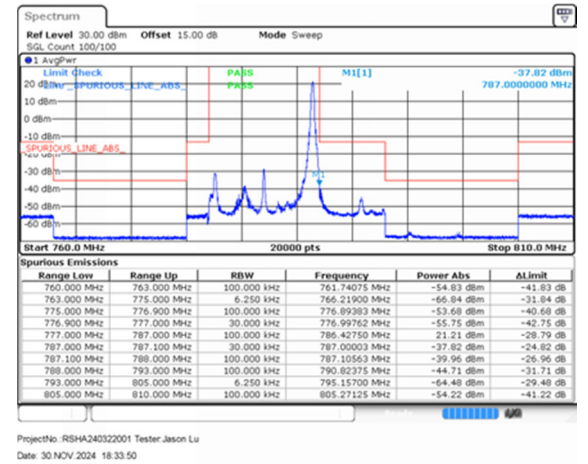
5MHz_High_16QAM_25@0



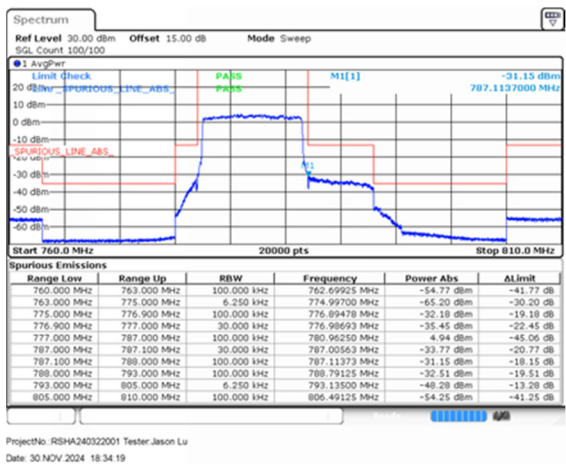
10MHz_Middle_QPSK_1@0



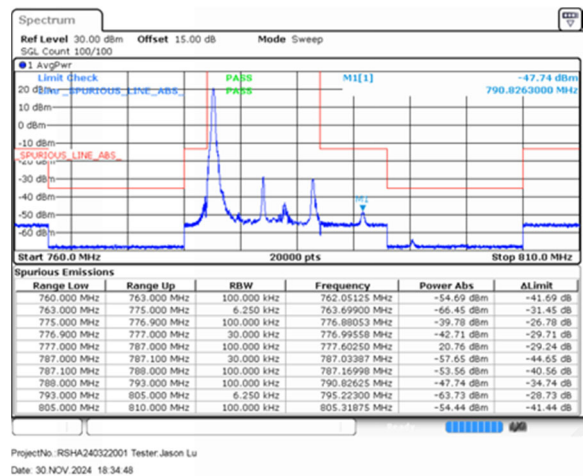
10MHz_Middle_QPSK_1@49



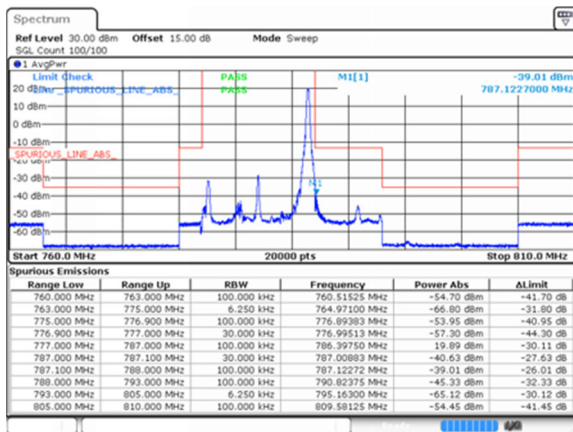
10MHz_Middle_QPSK_50@0



10MHz_Middle_16QAM_1@0

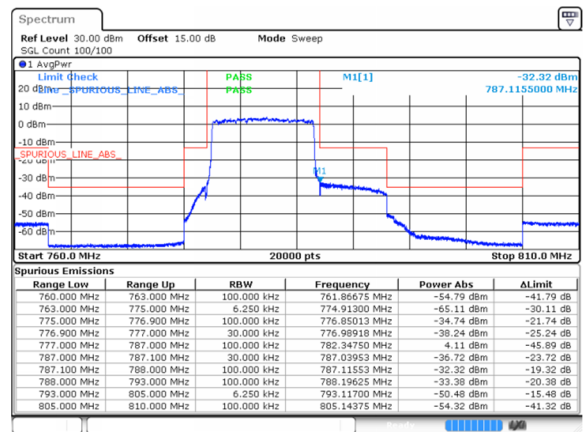


10MHz_Middle_16QAM_1@49



ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:35:18

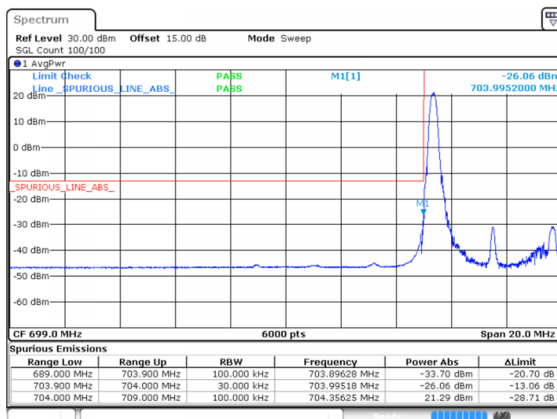
10MHz_Middle_16QAM_50@0



ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:36:17

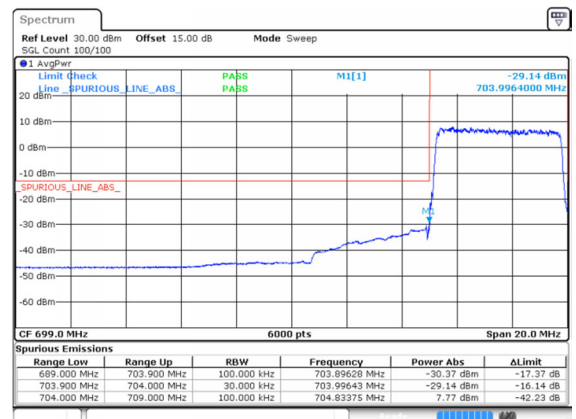
B17, Normal

5MHz_Low_QPSK_1@0



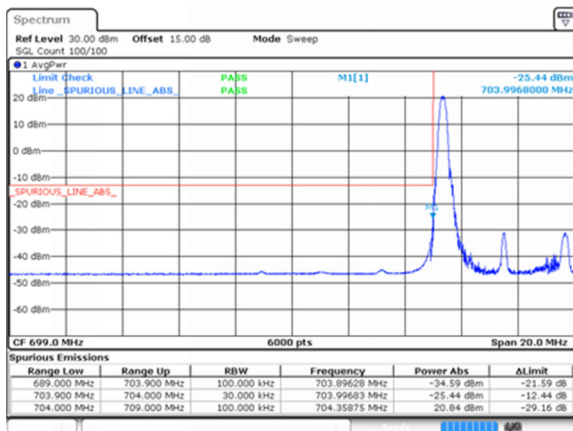
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:45:43

5MHz_Low_QPSK_25@0



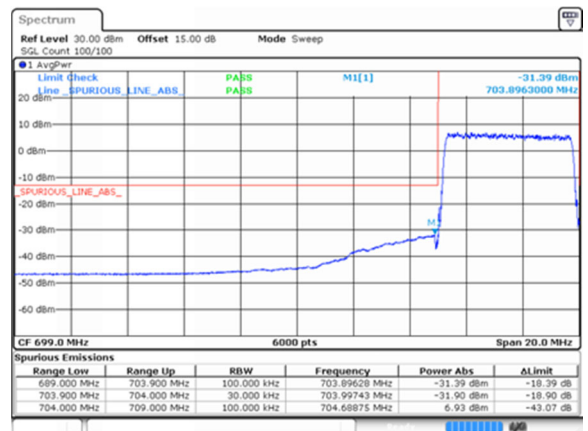
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Date: 30 NOV 2024 18:46:31

5MHz_Low_16QAM_1@0



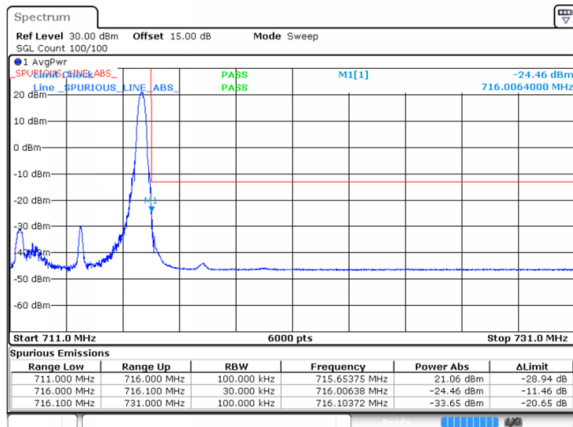
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Date: 30 NOV 2024 18:47:20

5MHz_Low_16QAM_25@0



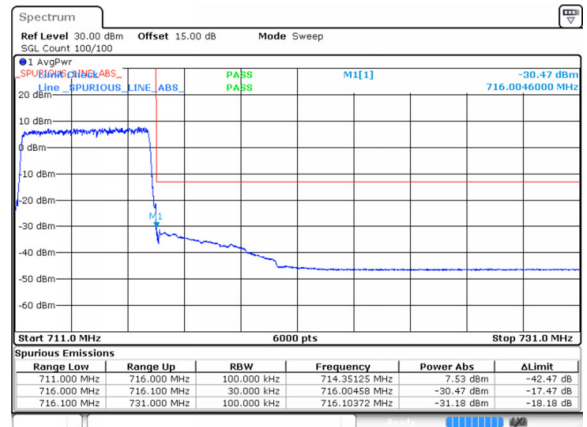
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Date: 30 NOV 2024 18:48:09

5MHz_High_QPSK_1@24



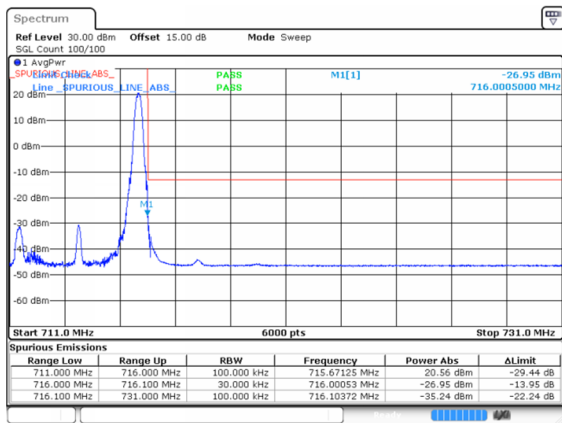
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Date: 30 NOV 2024 18:49:04

5MHz_High_QPSK_25@0



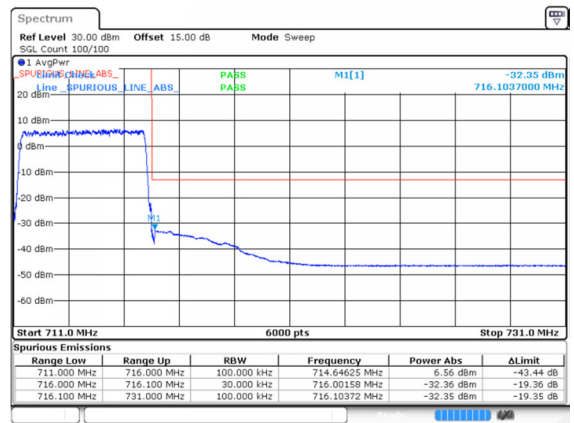
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Date: 30 NOV 2024 18:49:53

5MHz_High_16QAM_1@24



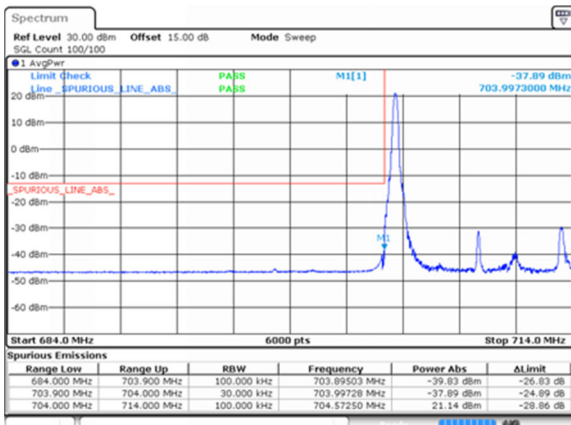
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Date: 30 NOV 2024 18:50:41

5MHz_High_16QAM_25@0



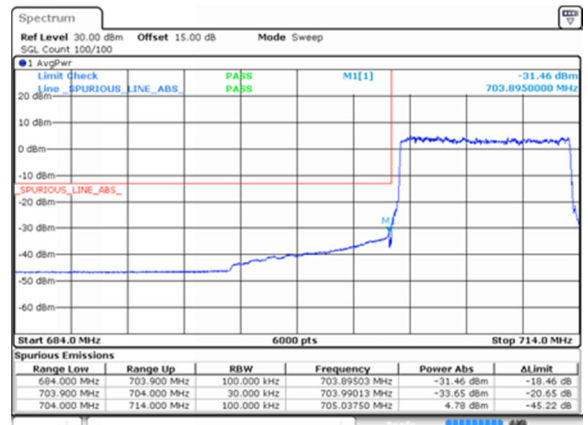
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Date: 30 NOV 2024 18:51:29

10MHz_Low_QPSK_1@0



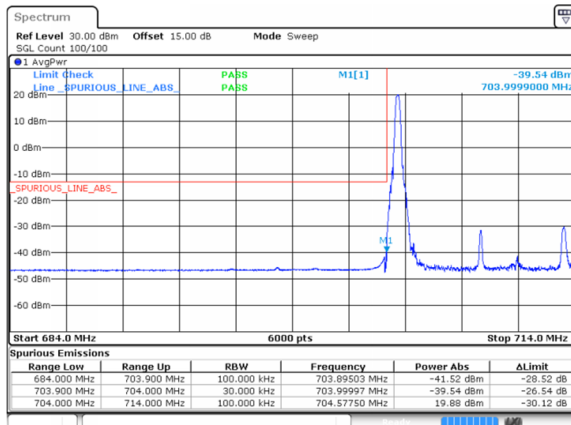
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Date: 30 NOV 2024 18:53:08

10MHz_Low_QPSK_50@0



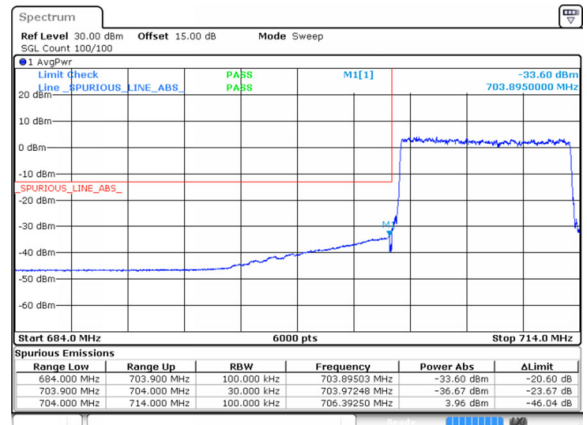
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:54:08

10MHz_Low_16QAM_1@0



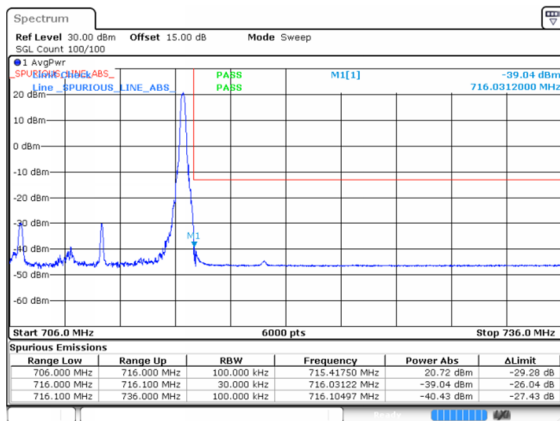
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Date: 30 NOV 2024 18:55:10

10MHz_Low_16QAM_50@0



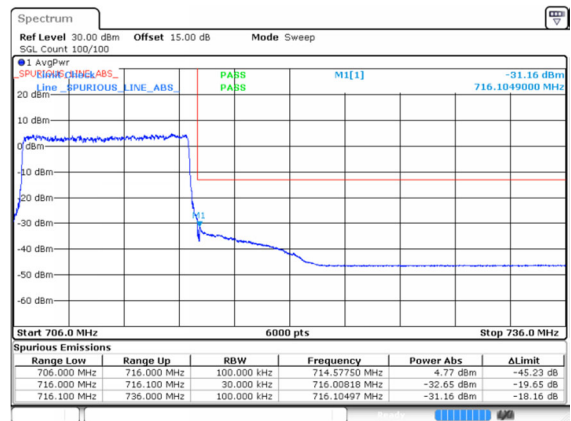
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Date: 30 NOV 2024 18:56:10

10MHz_High_QPSK_1@49



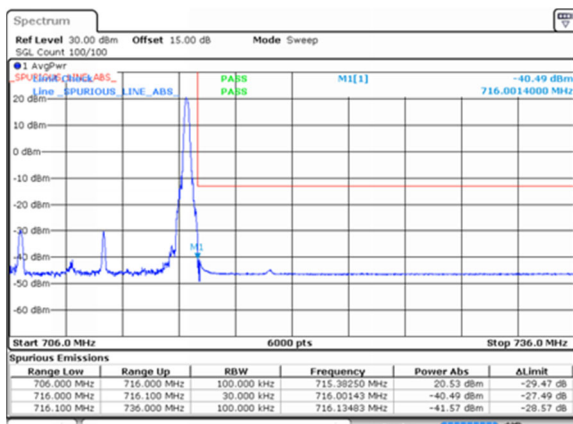
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Date: 30 NOV 2024 18:57:16

10MHz_High_QPSK_50@0



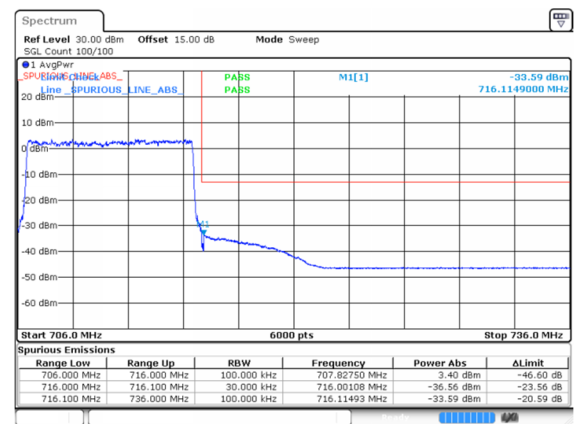
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:58:15

10MHz_High_16QAM_1@49



ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:59:14

10MHz_High_16QAM_50@0



ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:01:12