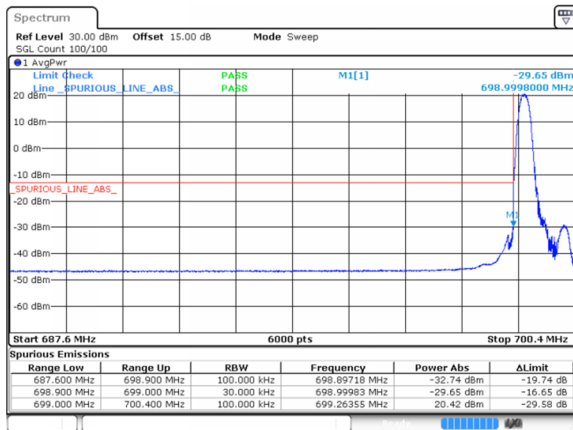
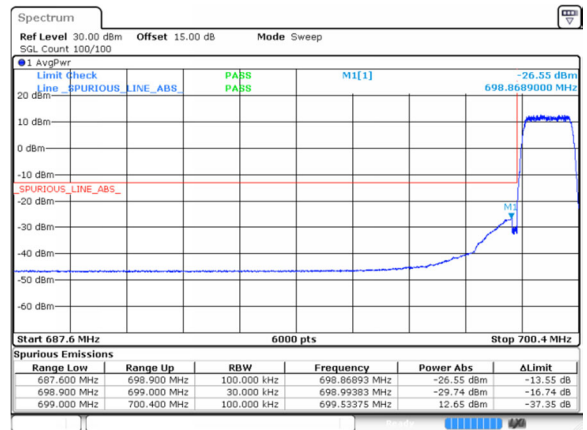


1.4MHz_Low_16QAM_1@0



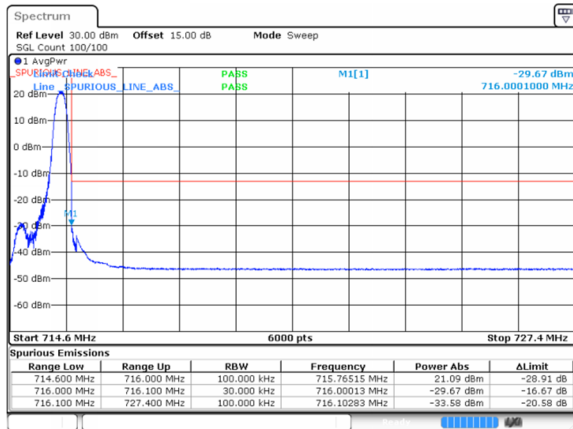
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:00:13

1.4MHz_Low_16QAM_6@0



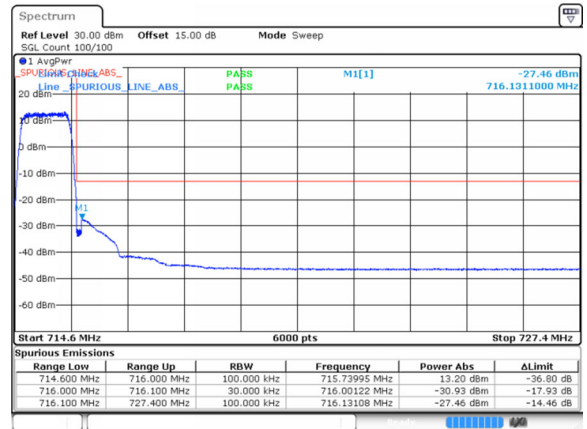
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:00:52

1.4MHz_High_QPSK_1@5



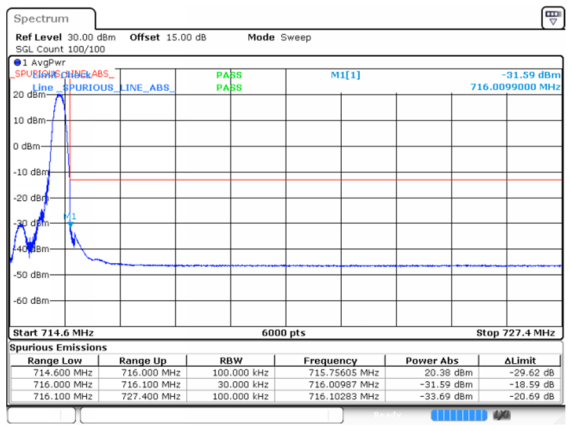
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:01:38

1.4MHz_High_QPSK_6@0



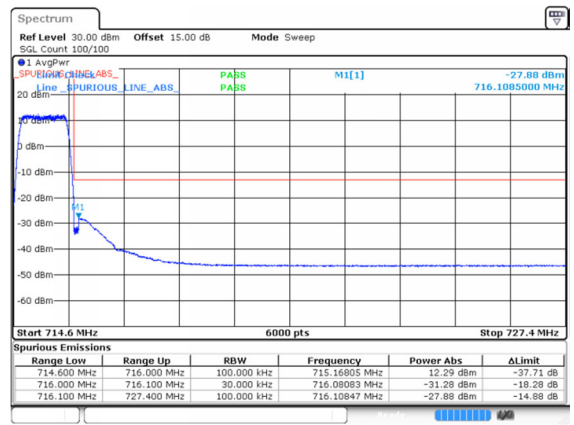
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:02:17

1.4MHz_High_16QAM_1@5



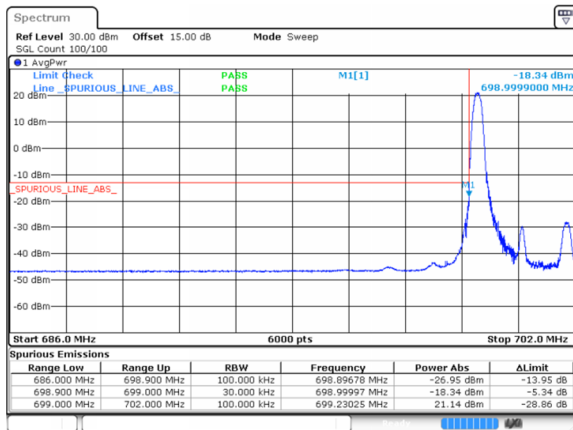
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:02:56

1.4MHz_High_16QAM_6@0



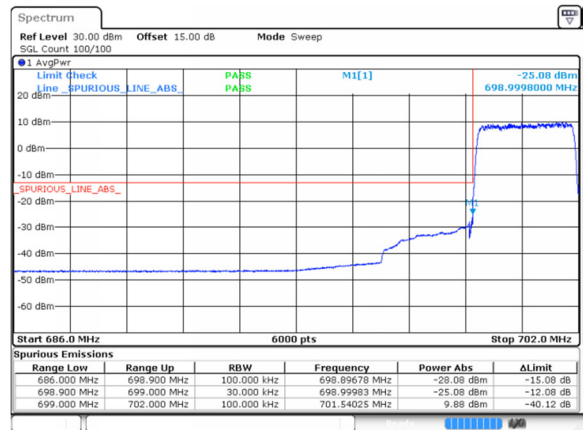
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:03:34

3MHz_Low_QPSK_1@0



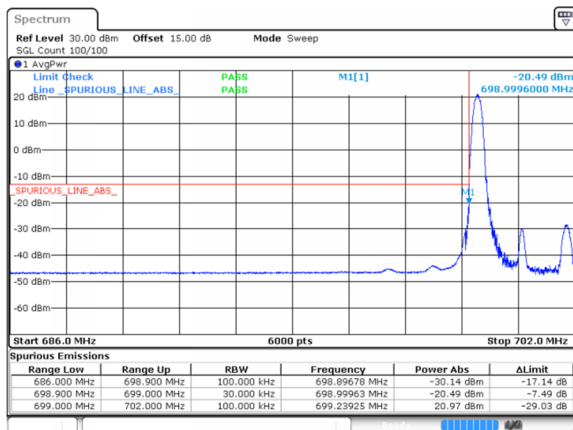
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:05:03

3MHz_Low_QPSK_15@0



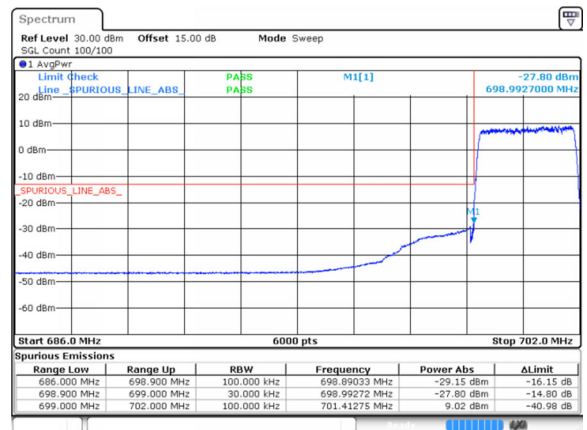
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:05:46

3MHz_Low_16QAM_1@0



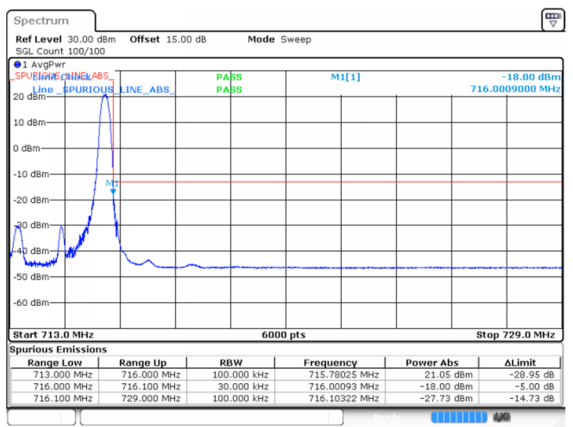
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:06:29

3MHz_Low_16QAM_15@0



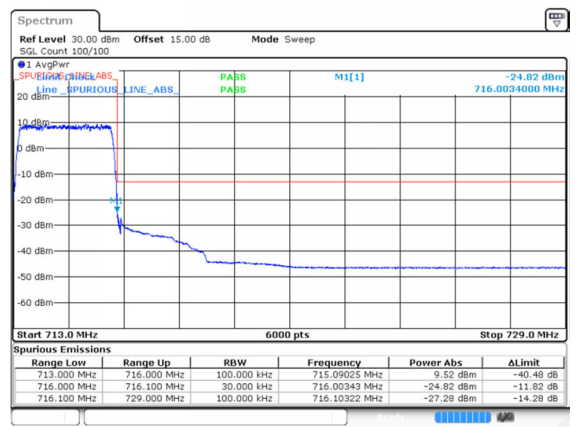
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:07:12

3MHz_High_QPSK_1@14



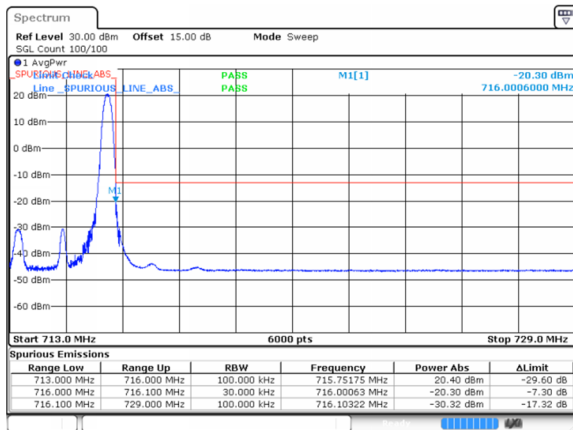
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:08:01

3MHz_High_QPSK_15@0



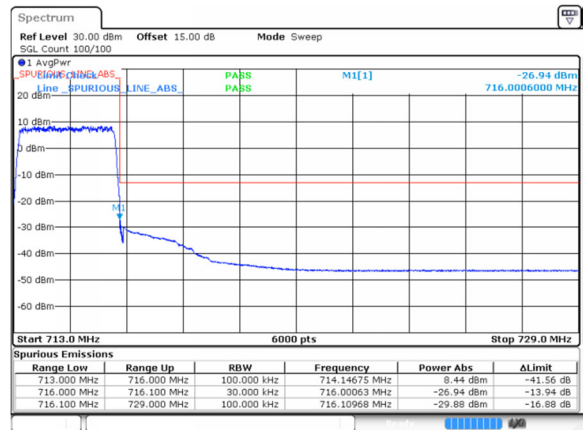
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:08:44

3MHz_High_16QAM_1@14



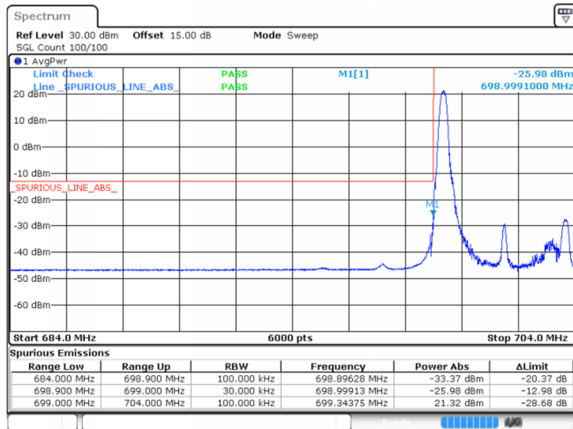
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:09:26

3MHz_High_16QAM_15@0



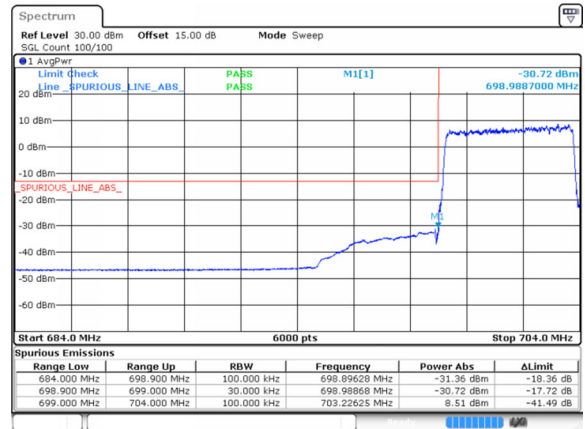
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:10:09

5MHz_Low_QPSK_1@0



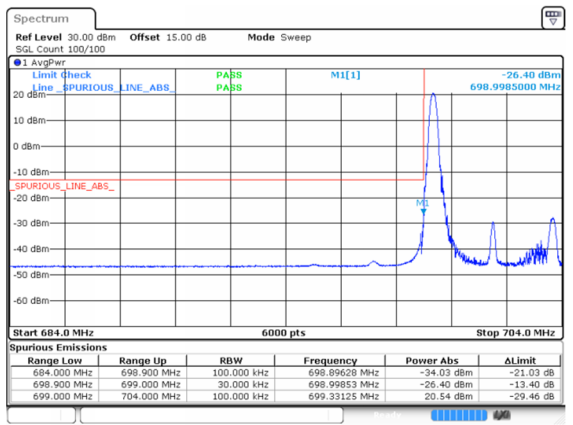
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:11:36

5MHz_Low_QPSK_25@0



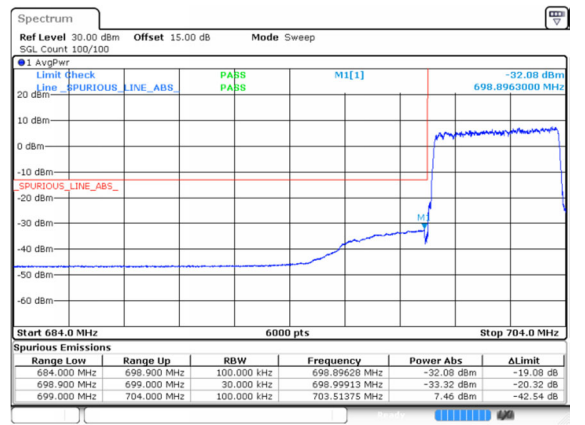
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:12:24

5MHz_Low_16QAM_1@0



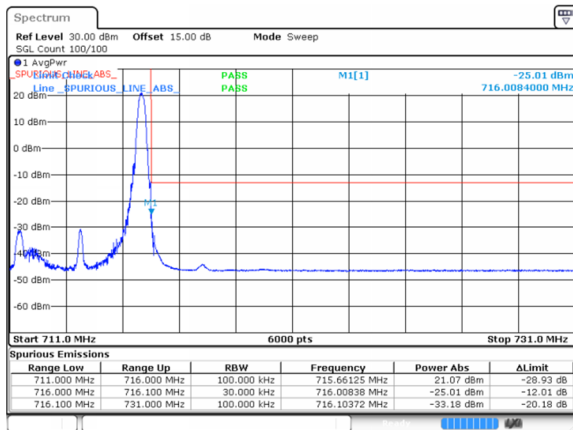
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:13:14

5MHz_Low_16QAM_25@0



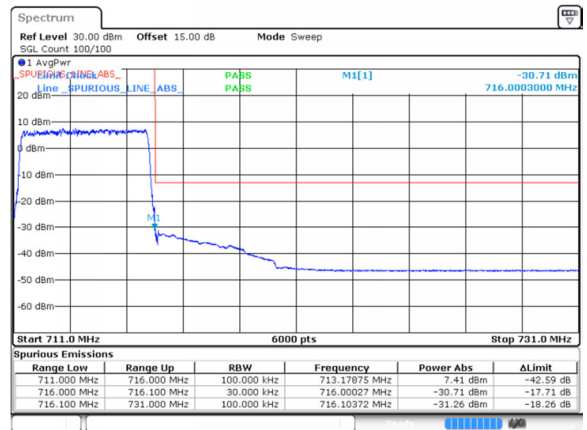
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:14:02

5MHz_High_QPSK_1@24



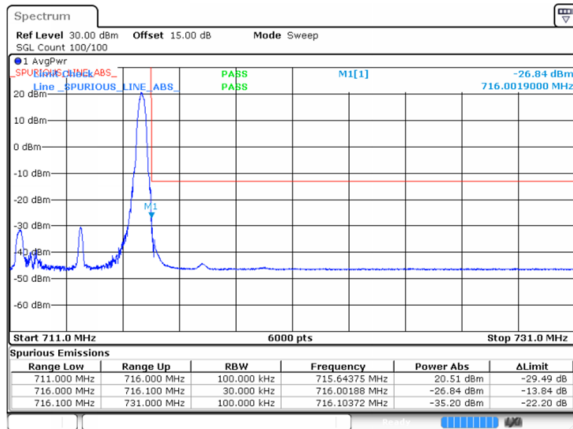
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:14:58

5MHz_High_QPSK_25@0



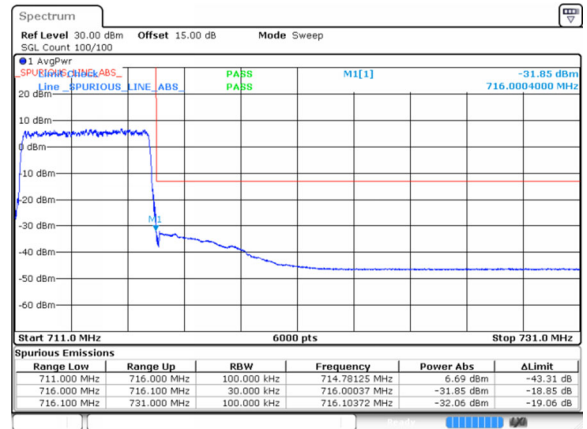
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:15:46

5MHz_High_16QAM_1@24



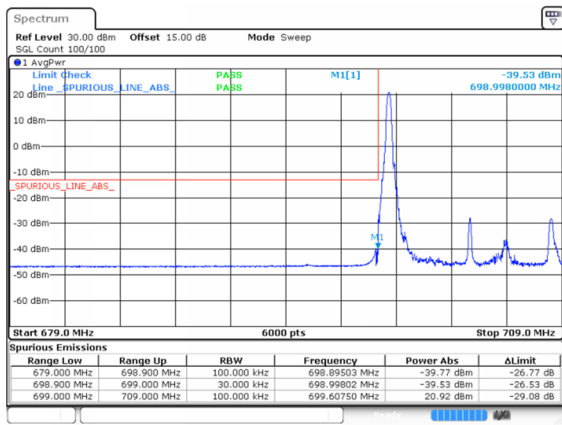
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:16:35

5MHz_High_16QAM_25@0



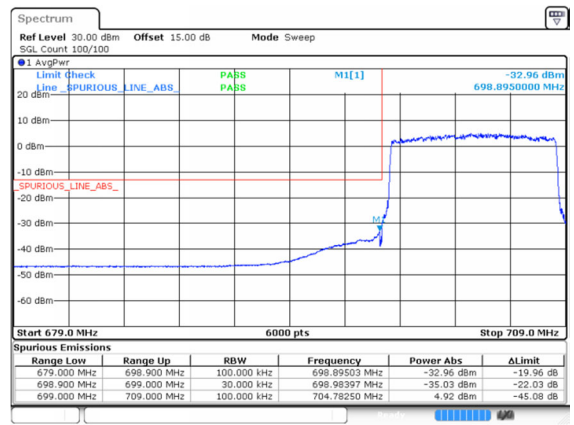
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:17:23

10MHz_Low_QPSK_1@0



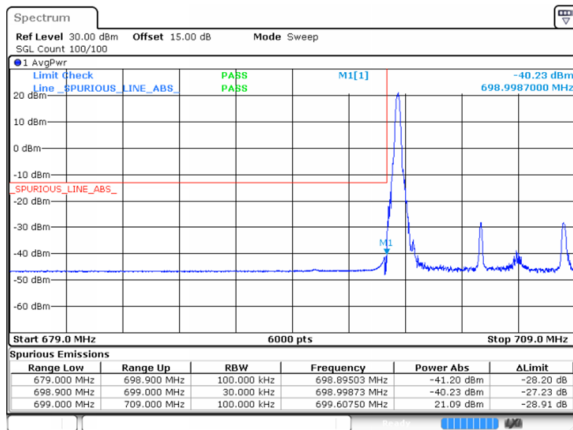
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:19:03

10MHz_Low_QPSK_50@0



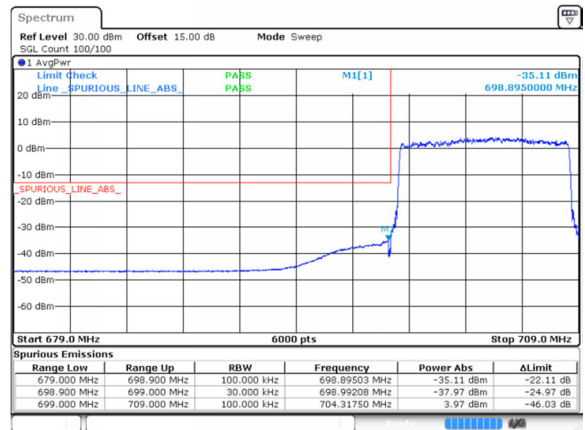
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:20:07

10MHz_Low_16QAM_1@0



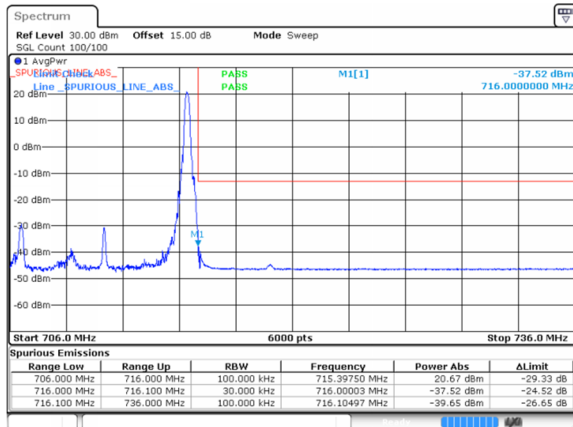
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:21:07

10MHz_Low_16QAM_50@0



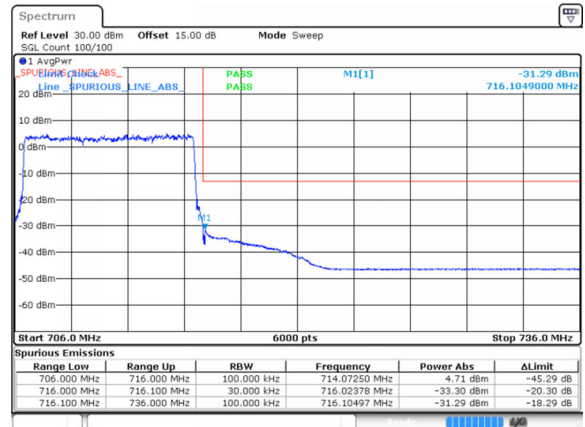
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:22:11

10MHz_High_QPSK_1@49



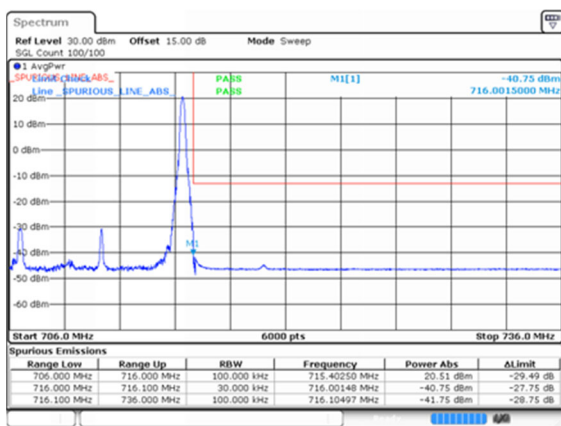
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:23:17

10MHz_High_QPSK_50@0



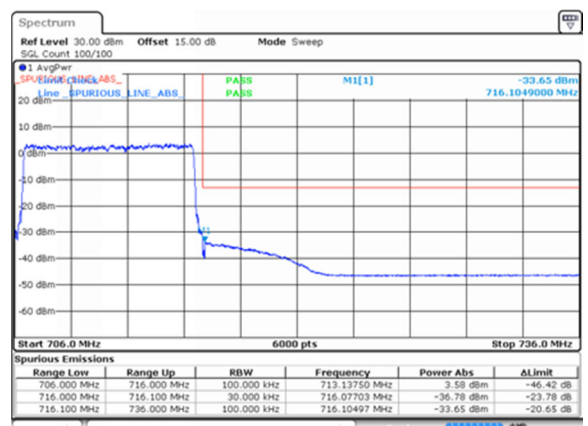
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:24:16

10MHz_High_16QAM_1@49



ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:25:15

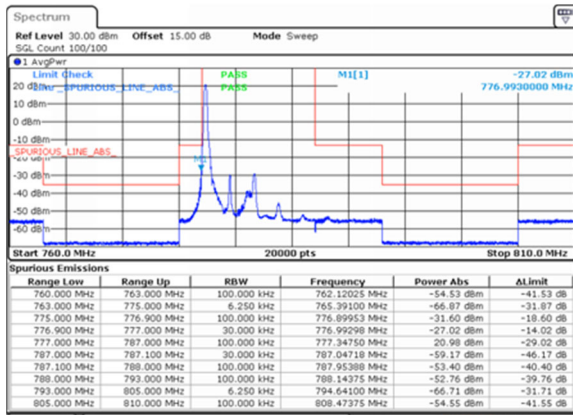
10MHz_High_16QAM_50@0



ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:27:13

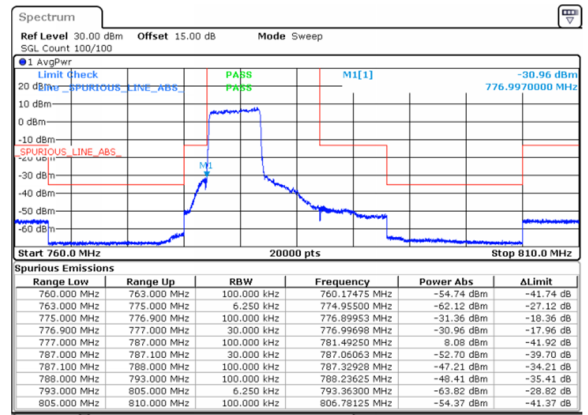
B13 , Normal

5MHz_Low_QPSK_1@0



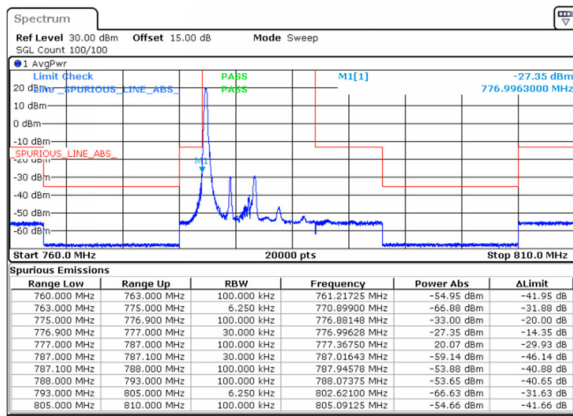
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:28:16

5MHz_Low_QPSK_25@0



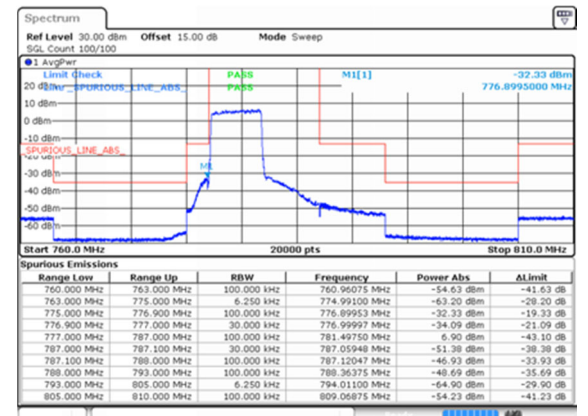
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:28:46

5MHz_Low_16QAM_1@0



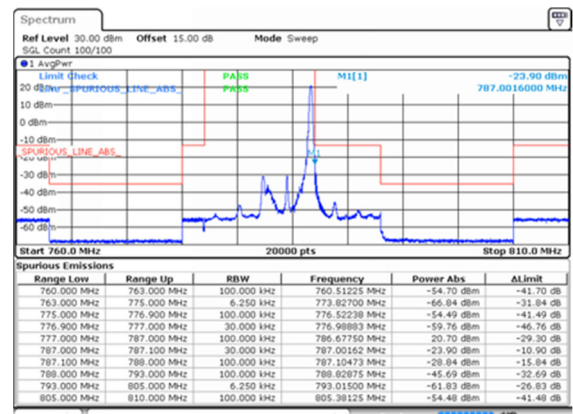
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:29:16

5MHz_Low_16QAM_25@0



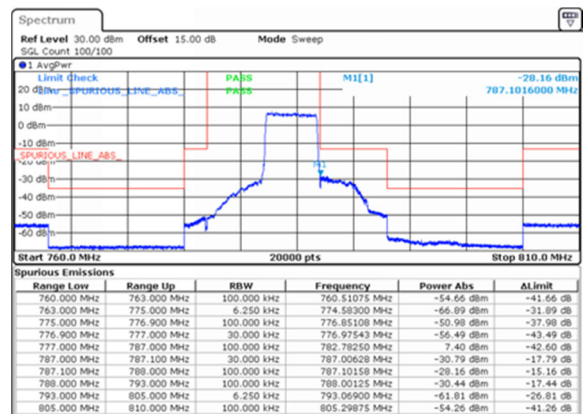
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:29:46

5MHz_High_QPSK_1@24



ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:30:33

5MHz_High_QPSK_25@0



ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 18:31:02