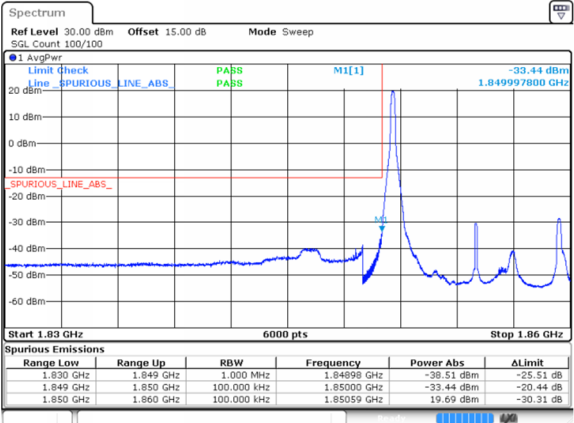
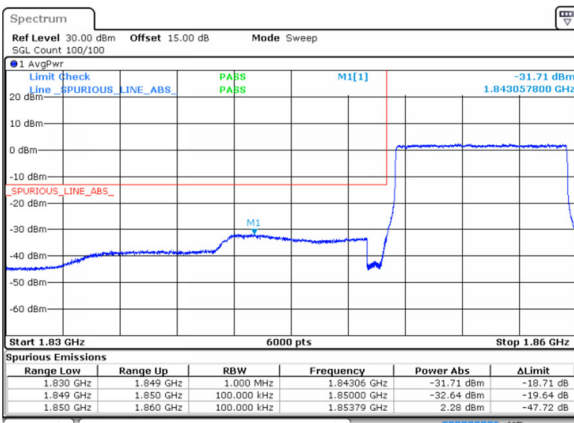


10MHz_Low_QPSK_1@0



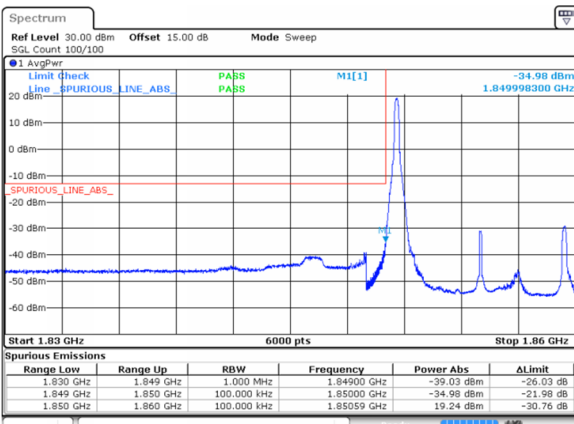
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:17:10

10MHz_Low_QPSK_50@0



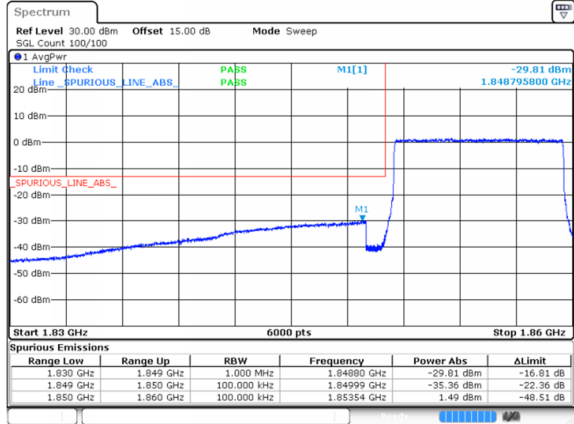
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:17:50

10MHz_Low_16QAM_1@0



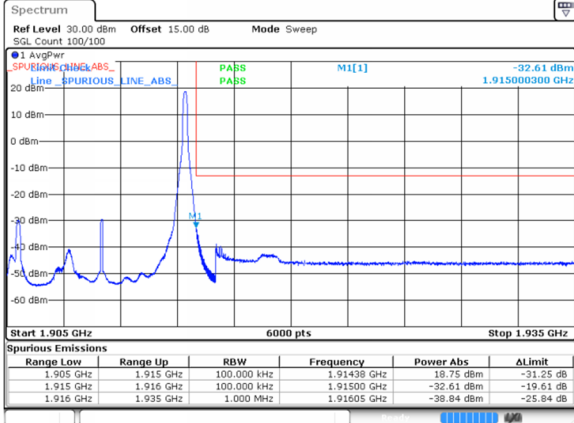
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:18:29

10MHz_Low_16QAM_50@0



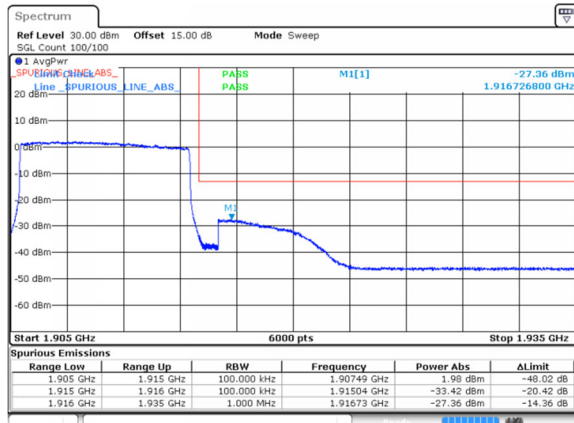
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:19:09

10MHz_High_QPSK_1@49



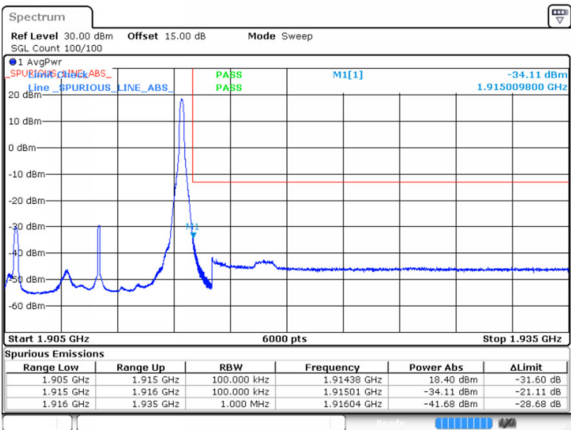
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:20:06

10MHz_High_QPSK_50@0



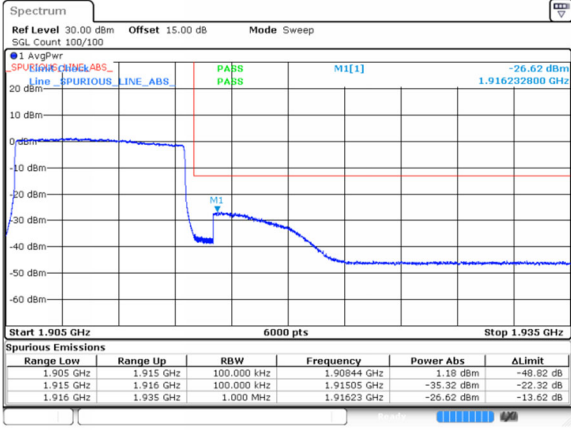
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:20:46

10MHz_High_16QAM_1@49



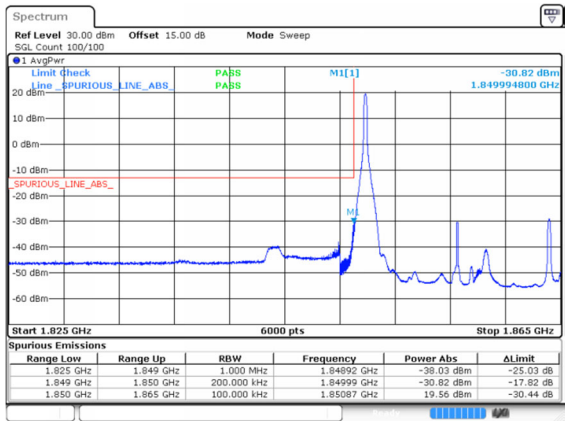
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:21:25

10MHz_High_16QAM_50@0



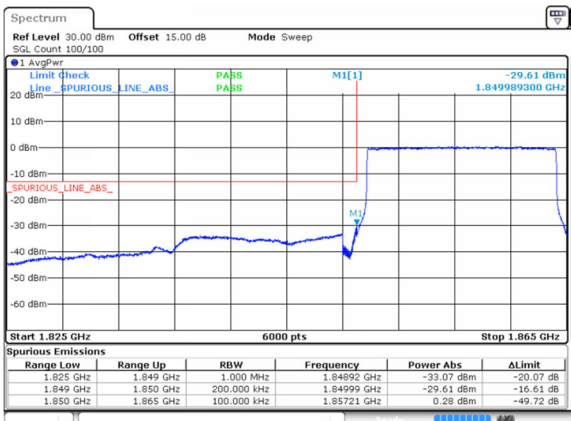
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:22:45

15MHz_Low_QPSK_1@0



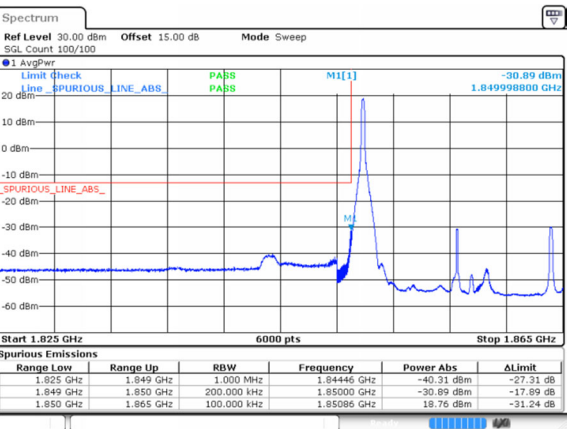
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:24:22

15MHz_Low_QPSK_75@0



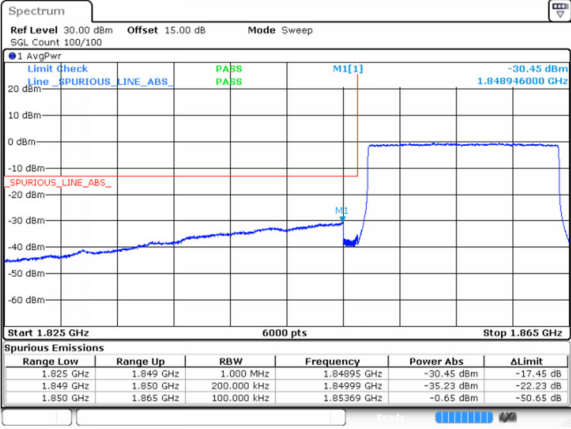
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:25:12

15MHz_Low_16QAM_1@0



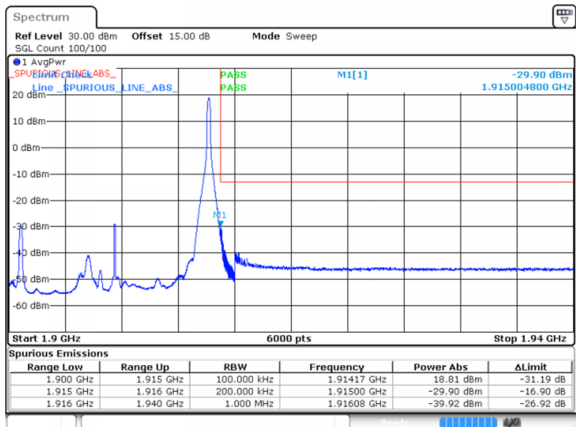
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:26:01

15MHz_Low_16QAM_75@0



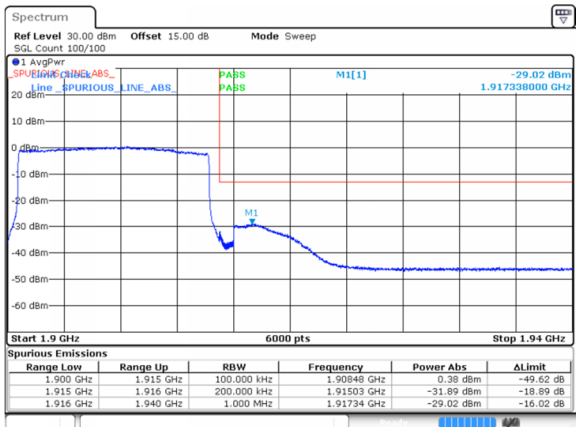
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:26:52

15MHz_High_QPSK_1@74



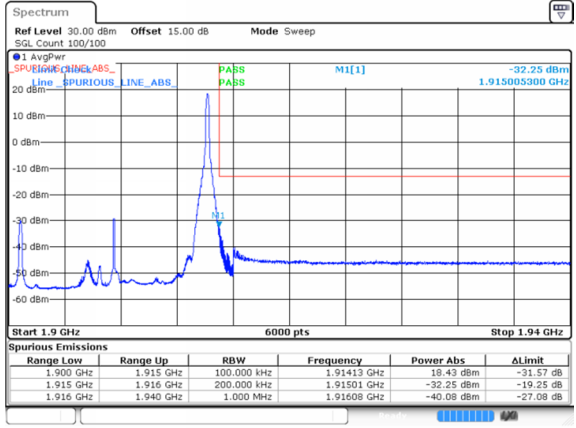
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:27:49

15MHz_High_QPSK_75@0



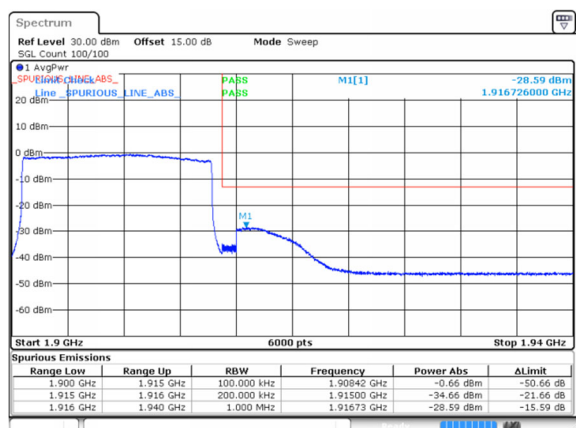
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:28:40

15MHz_High_16QAM_1@74



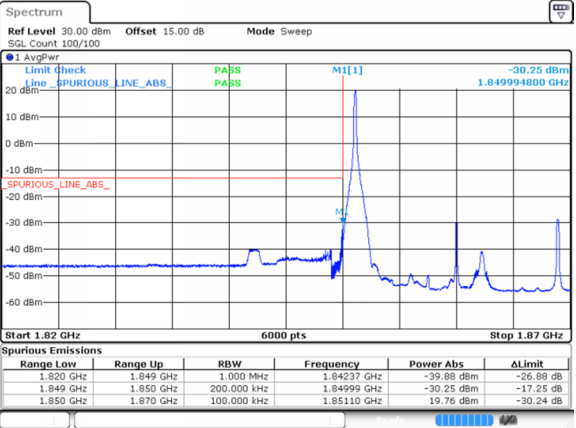
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:29:28

15MHz_High_16QAM_75@0



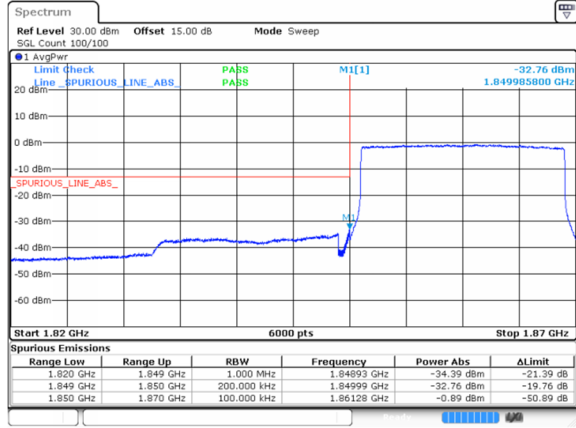
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:30:19

20MHz_Low_QPSK_1@0



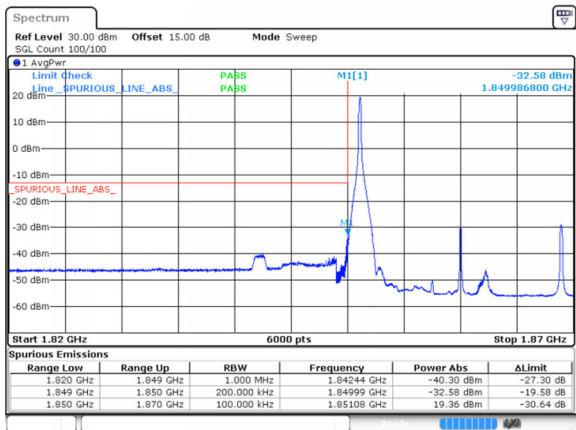
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:32:06

20MHz_Low_QPSK_100@0



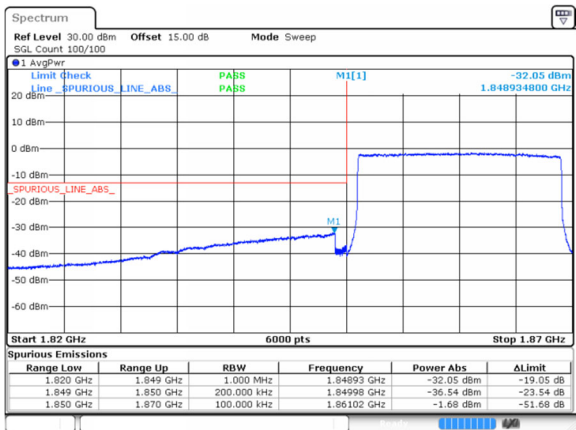
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:33:08

20MHz_Low_16QAM_1@0



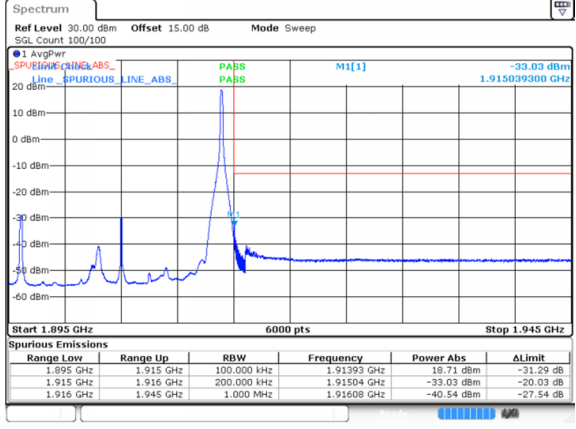
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:34:08

20MHz_Low_16QAM_100@0



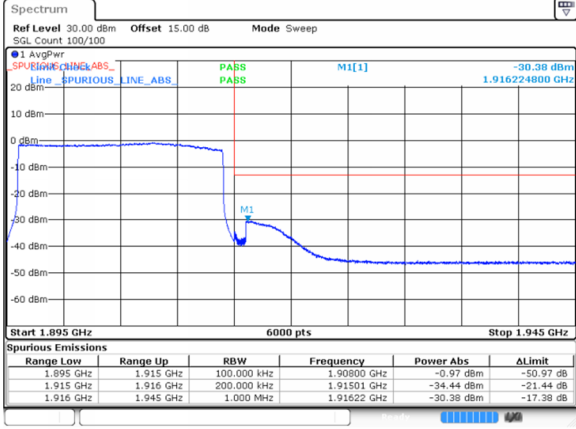
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:35:09

20MHz_High_QPSK_1@99



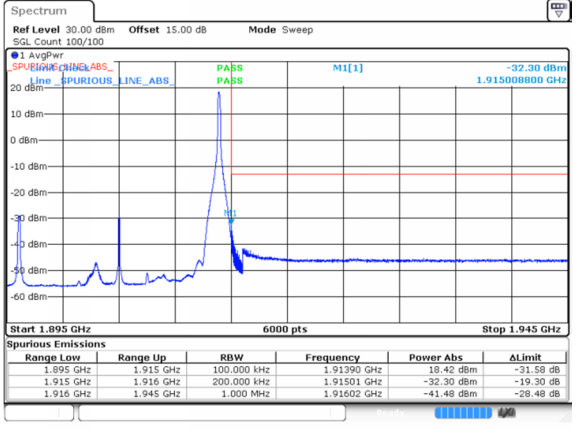
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:36:22

20MHz_High_QPSK_100@0



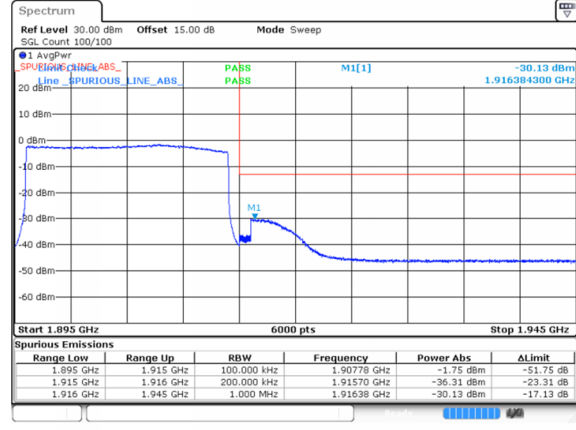
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:37:23

20MHz_High_16QAM_1@99



ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:38:22

20MHz_High_16QAM_100@0



ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:39:24