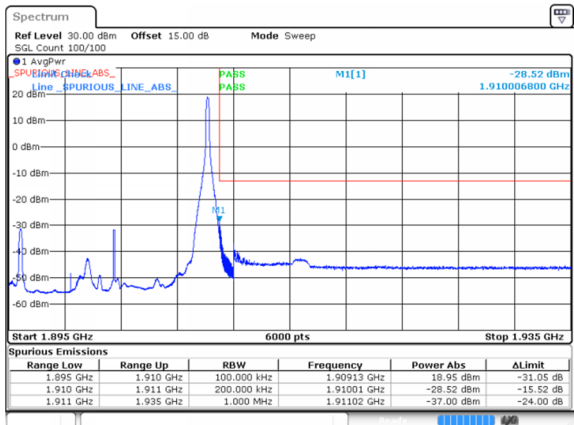
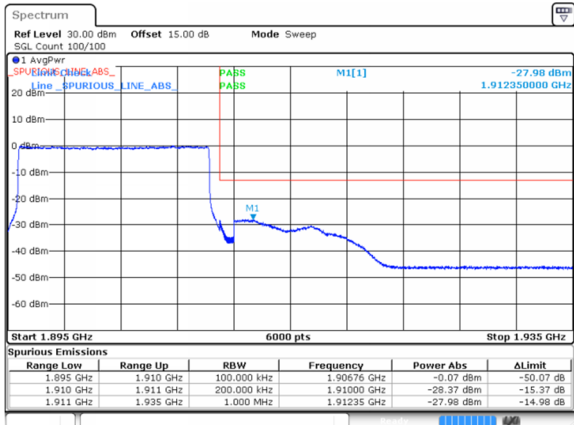


15MHz_High_QPSK_1@74



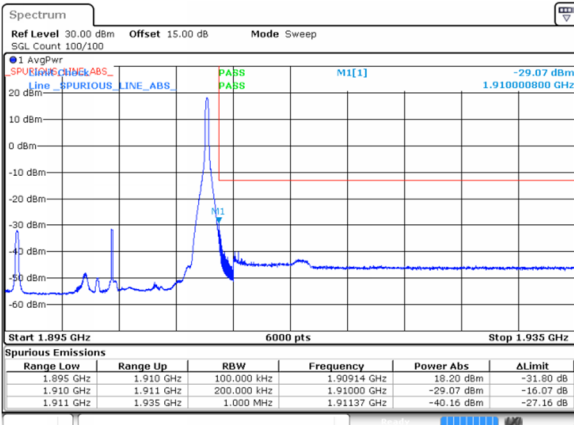
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 16:19:16

15MHz_High_QPSK_75@0



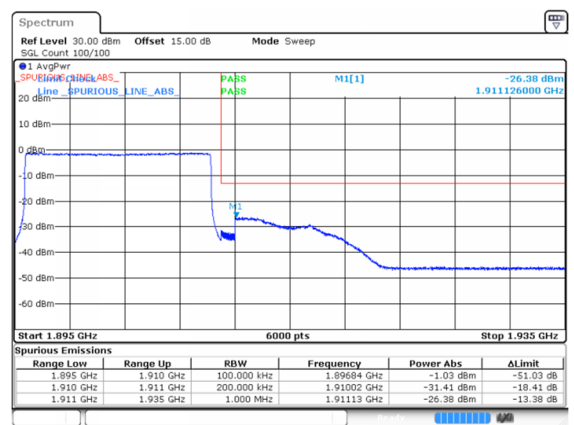
ProjectNo.: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 16:20:05

15MHz_High_16QAM_1@74



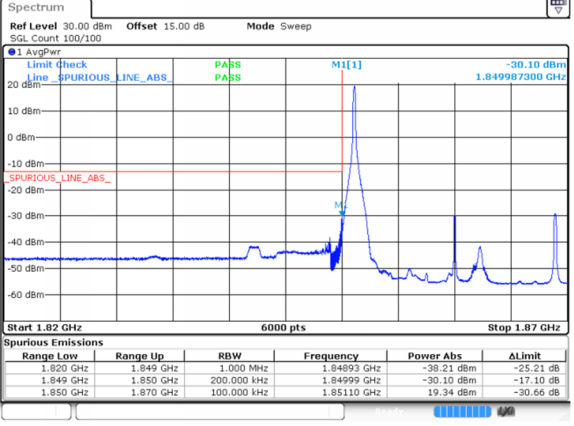
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 16:20:58

15MHz_High_16QAM_75@0



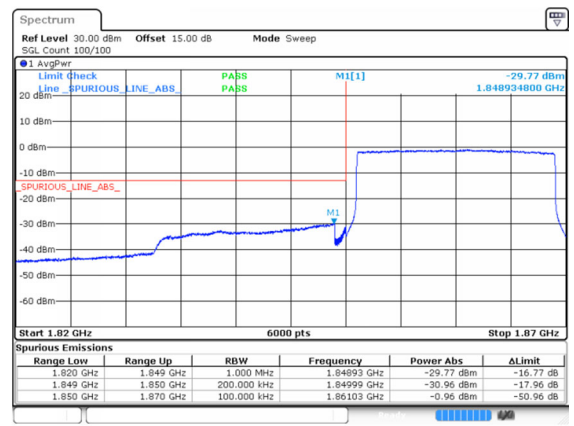
ProjectNo.: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 16:21:46

20MHz_Low_QPSK_1@0



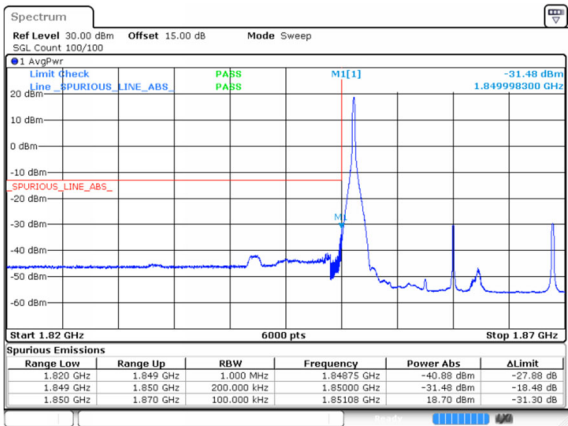
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 16:23:25

20MHz_Low_QPSK_100@0



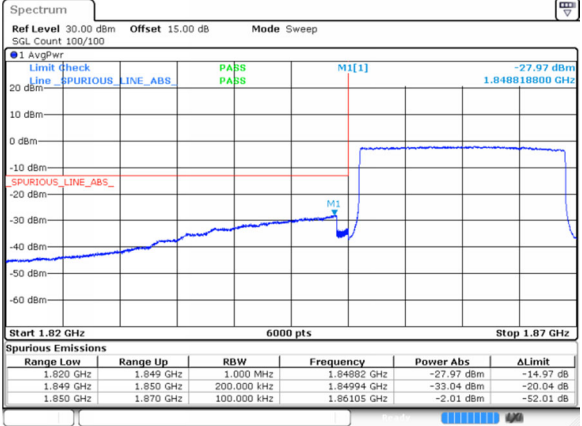
ProjectNo.: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 16:24:25

20MHz_Low_16QAM_1@0



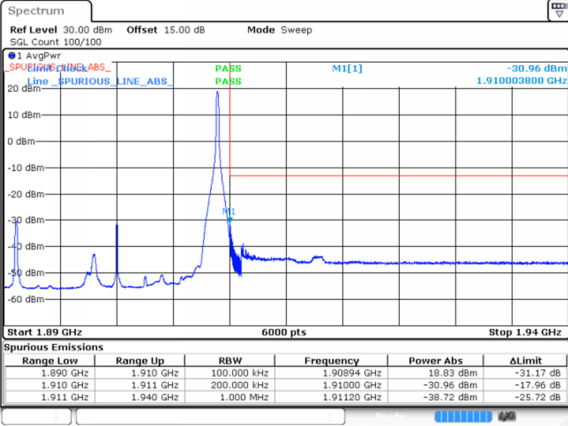
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV. 2024 16:25:23

20MHz_Low_16QAM_100@0



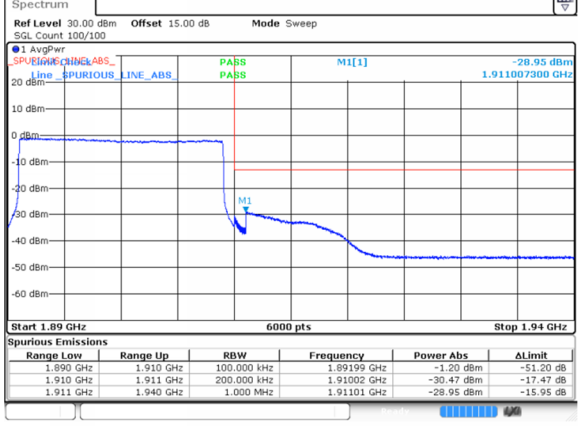
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV. 2024 16:26:23

20MHz_High_QPSK_1@99



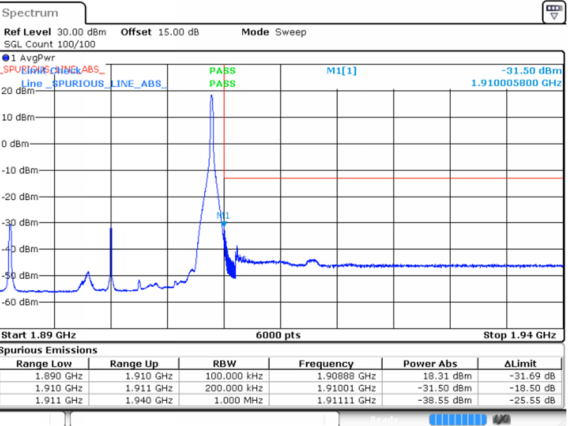
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV. 2024 16:27:29

20MHz_High_QPSK_100@0



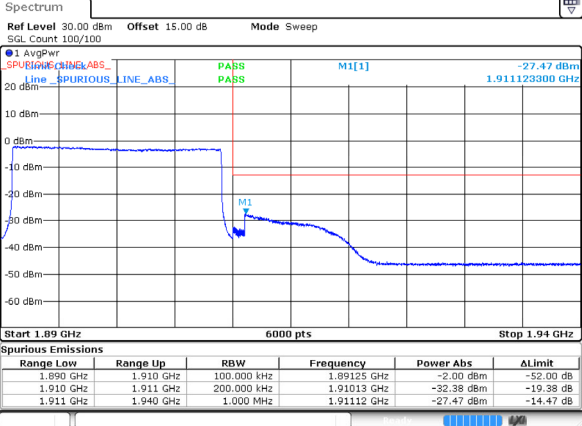
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV. 2024 16:28:28

20MHz_High_16QAM_1@99



ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV. 2024 16:29:32

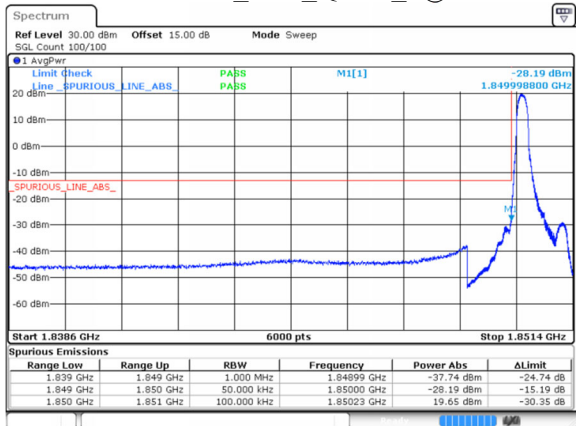
20MHz_High_16QAM_100@0



ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV. 2024 16:30:31

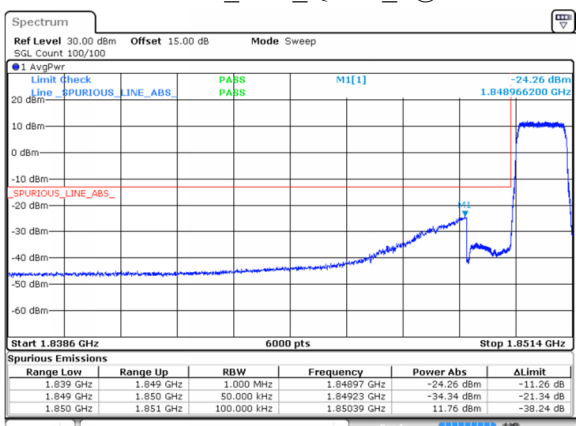
B25 , Normal

1.4MHz_Low_QPSK_1@0



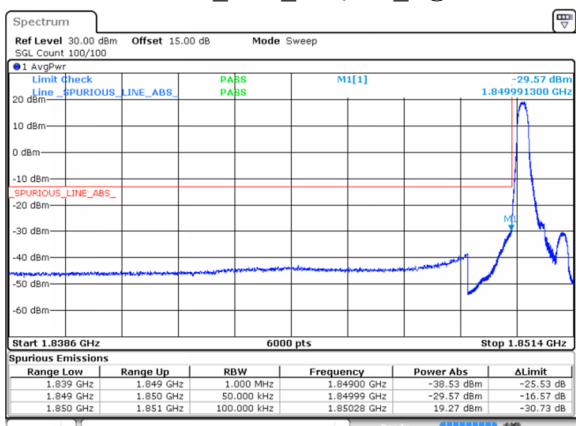
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:02:05

1.4MHz_Low_QPSK_6@0



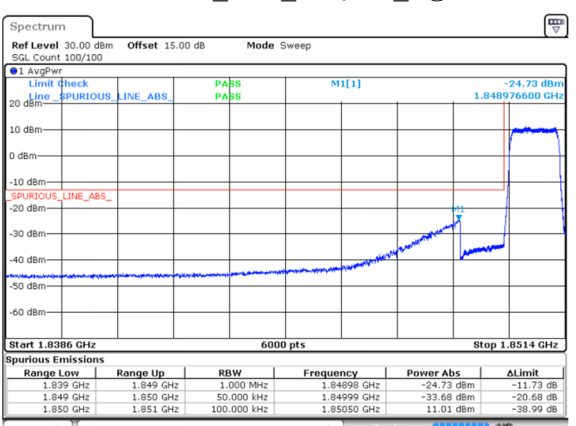
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:02:31

1.4MHz_Low_16QAM_1@0



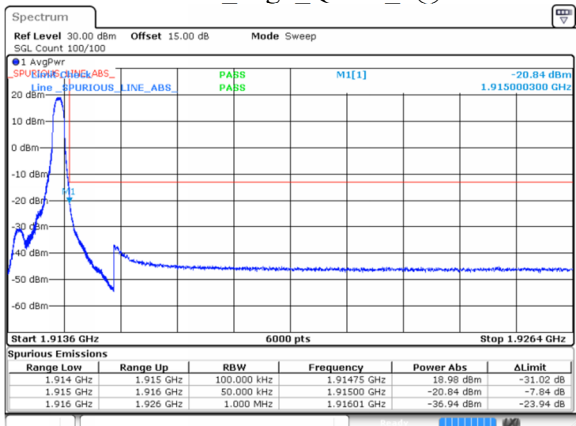
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:02:57

1.4MHz_Low_16QAM_6@0



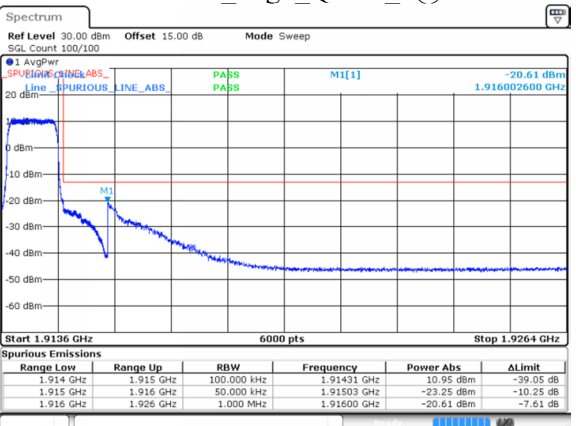
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:03:28

1.4MHz_High_QPSK_1@5



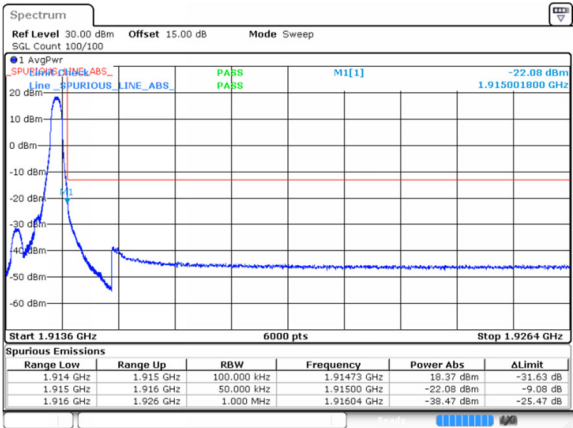
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:04:03

1.4MHz_High_QPSK_6@0



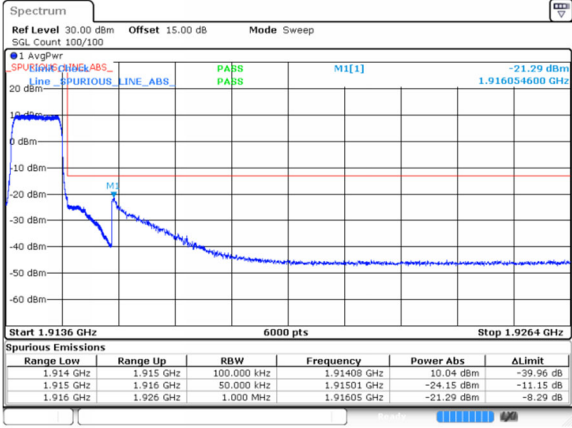
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:04:29

1.4MHz_High_16QAM_1@5



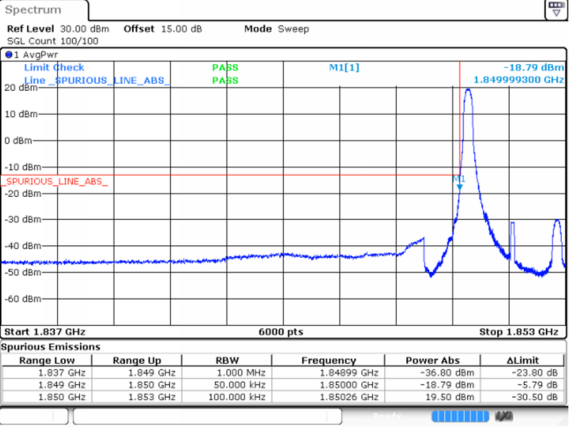
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:04:55

1.4MHz_High_16QAM_6@0



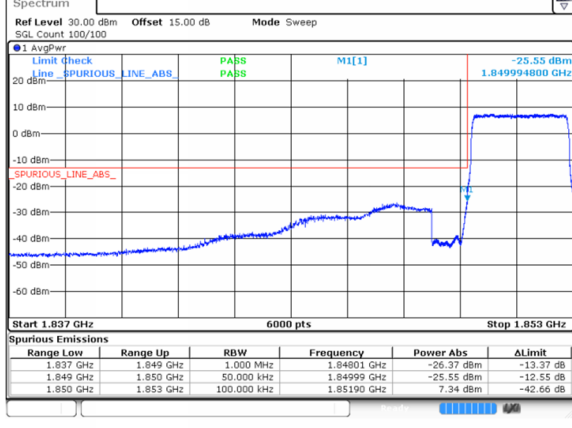
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:05:21

3MHz_Low_QPSK_1@0



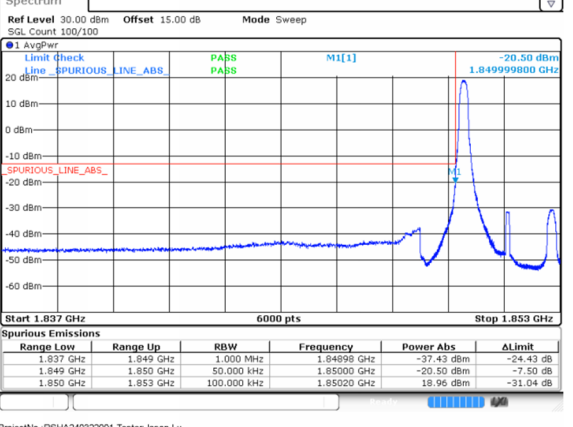
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:06:52

3MHz_Low_QPSK_15@0



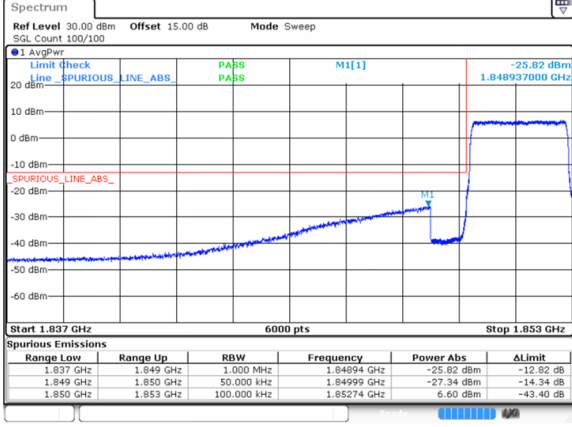
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:07:21

3MHz_Low_16QAM_1@0



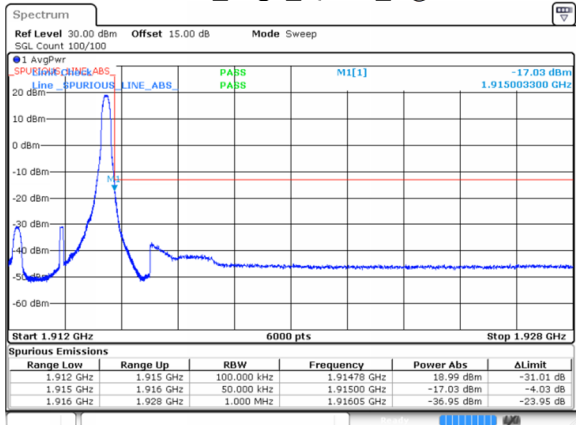
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:07:50

3MHz_Low_16QAM_15@0



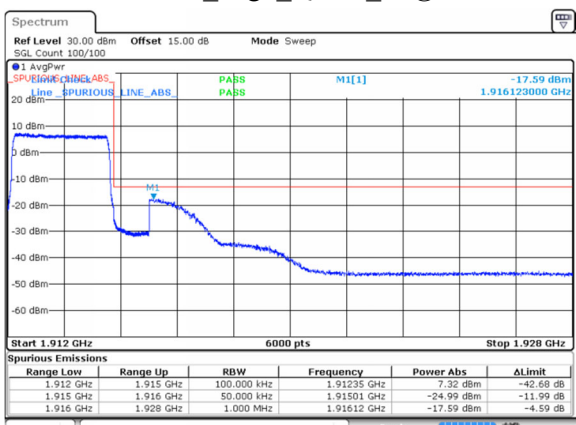
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:08:20

3MHz_High_QPSK_1@14



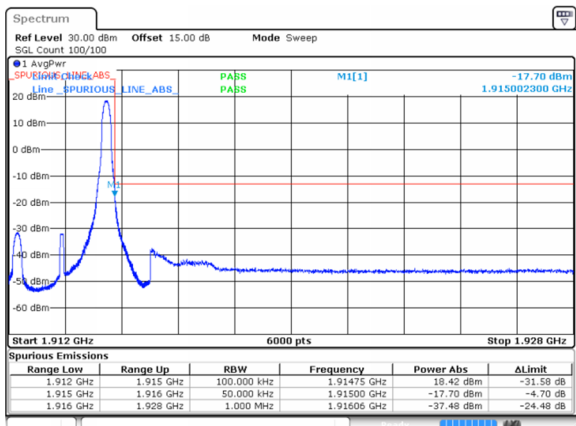
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:08:58

3MHz_High_QPSK_15@0



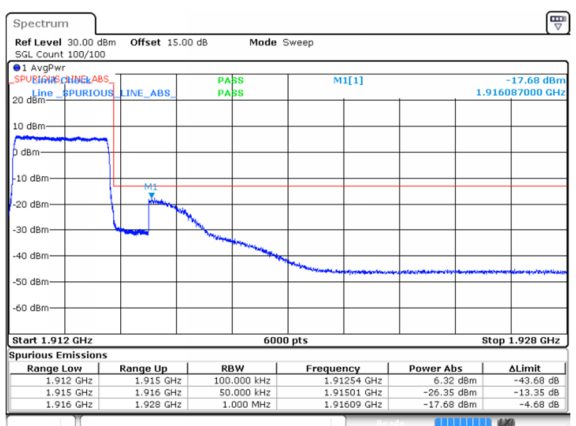
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:09:28

3MHz_High_16QAM_1@14



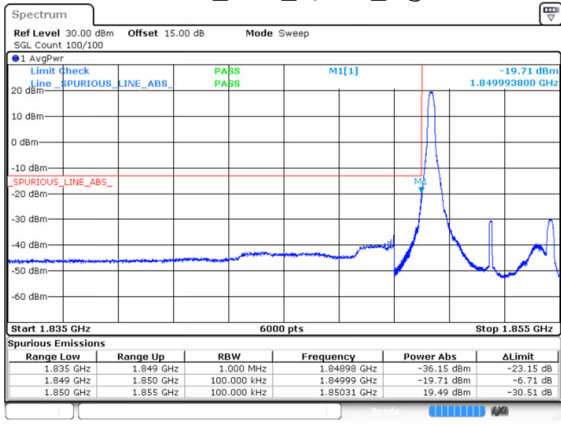
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:09:57

3MHz_High_16QAM_15@0



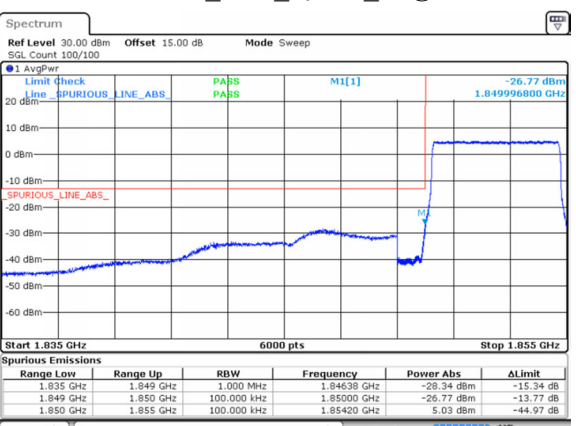
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:10:27

5MHz_Low_QPSK_1@0



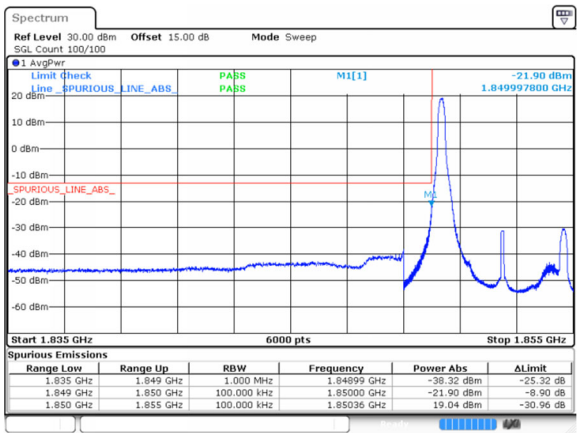
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:12:10

5MHz_Low_QPSK_25@0



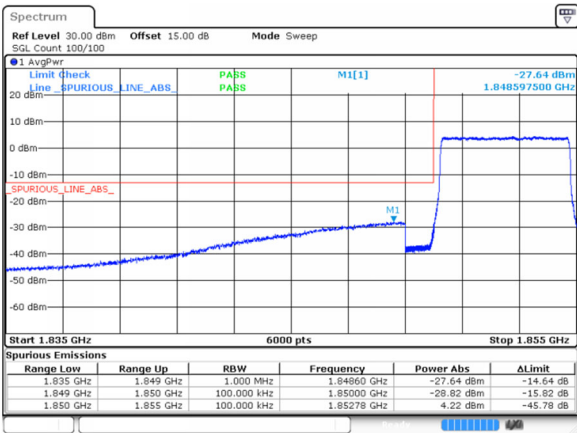
ProjectNo.:RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:12:39

5MHz_Low_16QAM_1@0



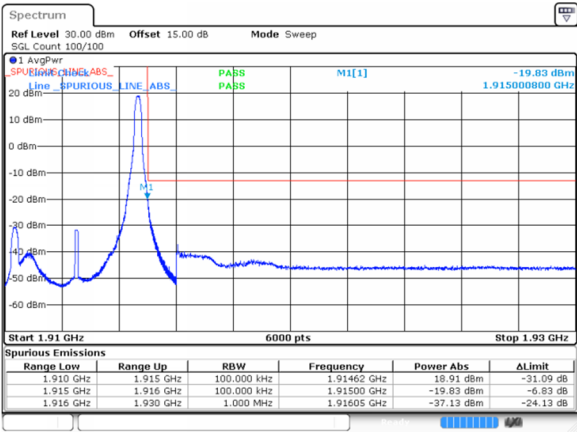
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:13:08

5MHz_Low_16QAM_25@0



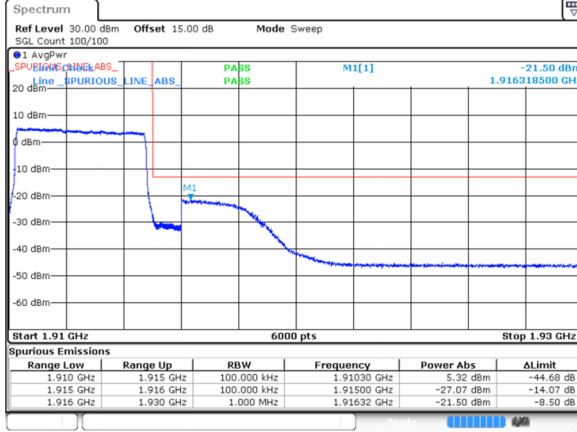
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:13:37

5MHz_High_QPSK_1@24



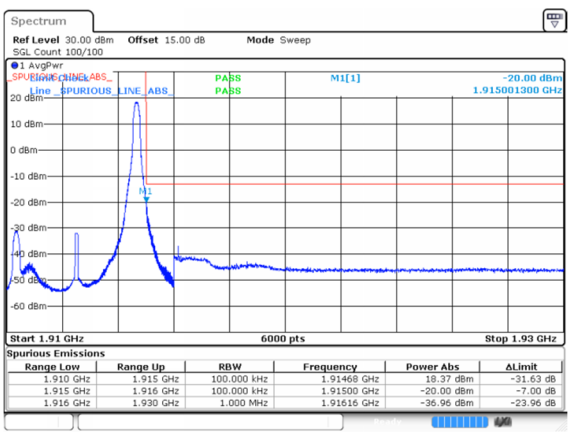
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:14:13

5MHz_High_QPSK_25@0



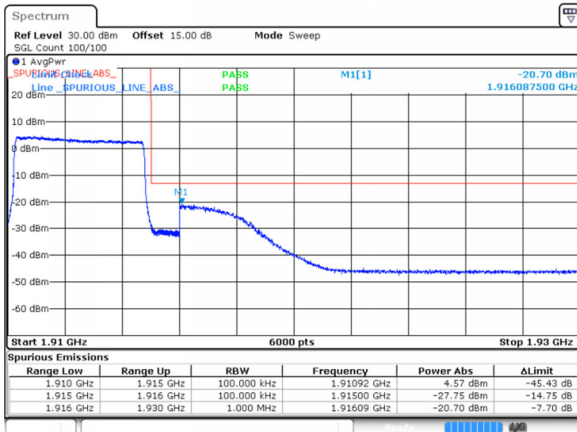
ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:14:45

5MHz_High_16QAM_1@24



ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:15:14

5MHz_High_16QAM_25@0



ProjectNo: RSHA240322001 Tester:Jason Lu
Date: 30 NOV 2024 19:15:43