User Guide

Amphenol - Wi-Fi (IOT) module

W105C_EVK

Document information

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Revision History

Version	Date	Note
1.0	2023-04-21	Created

Aim of this Document

The aim of this document is to give a user guide of W105C Demo Board.

Contents

1. Introduction

W105C modules designed base on RTL8710B chip solution, The SOC module is a highly intelligent platform for the Internet of Everything that contains a low-power Wi-Fi connectivity solution on one package.

W105C_EVK has one Micro USB port which is used for power supply and log read.
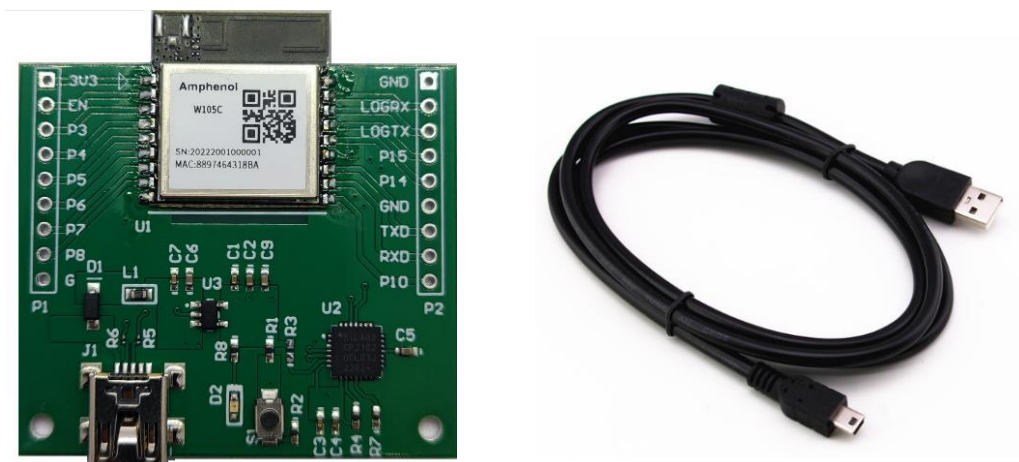
2. EVK Pattern

Figure 2-1 EVK Pattern

Table 2-1 Components List

No.	Description
1	W105C EVB
2	USB A to USB Mini-B cable

3. PIN description
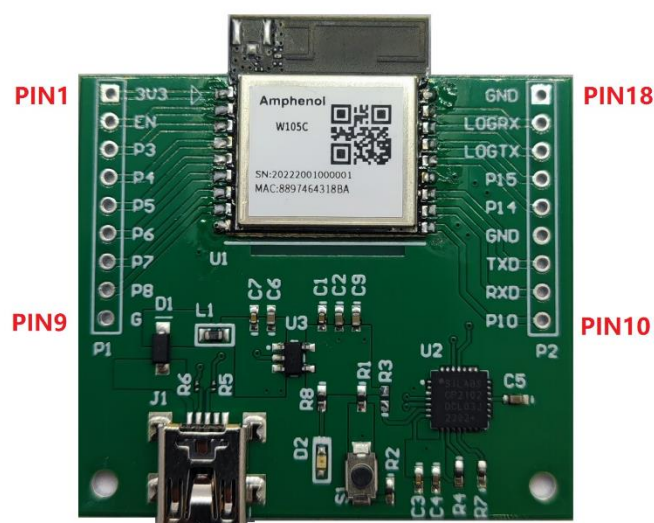
Figure 3-1 PIN MAP

Table 3-1 PIN Description

Pin Number	Name	Pin Number	Name
1	VDD	10	NC
2	Chip_Enable	11	GPIOA_18(UART0_RXD)*
3	GPIOA_14	12	GPIOA_23(UART0_TXD)*
4	GPIOA_15	13	GND
5	GPIOA_0	14	GPIOA_19
6	GPIOA_12	15	GPIOA_22
7	NC	16	GPIOA_30(UART_LOG_TXD)*
8	GPIOA_5	17	GPIOA_29(UART_LOG_RXD)*
9	GND	18	GND

Note:
- More information about usage of UART ports, please contact local Amphenol FAE;

Table 3-2 UART connection

UART from Host controller	Normal	Log
UART_TXD of Host controller	UART0_RXD (PIN11)	UART_LOG_RXD (PIN17)
UART_RXD of Host controller	UART0_TXD (PIN12)	UART_LOG_TXD (PIN16)

Table 3-3 Electrical Character

| Electrical Character	
Interfaces	GPIO;UART;SPI;I2C
Power supply-VDD	3.0V to 3.6V

4. Power on

Connect EVK to PC by USB A to USB Mini-B cable, and then read port status through device manager.
The following status means port work normally

Ports (COM & LPT)
  Silicon Labs CP210x USB to UART Bridge (COM5)
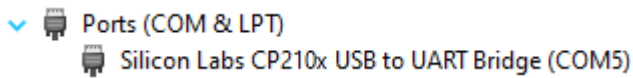
If port don't work normally, please to download and install driver from the following link:
https://www.silabs.com/developers/usb-to-uart-bridge-vcp-drivers?tab=downloads

Default Baud rate: 115200
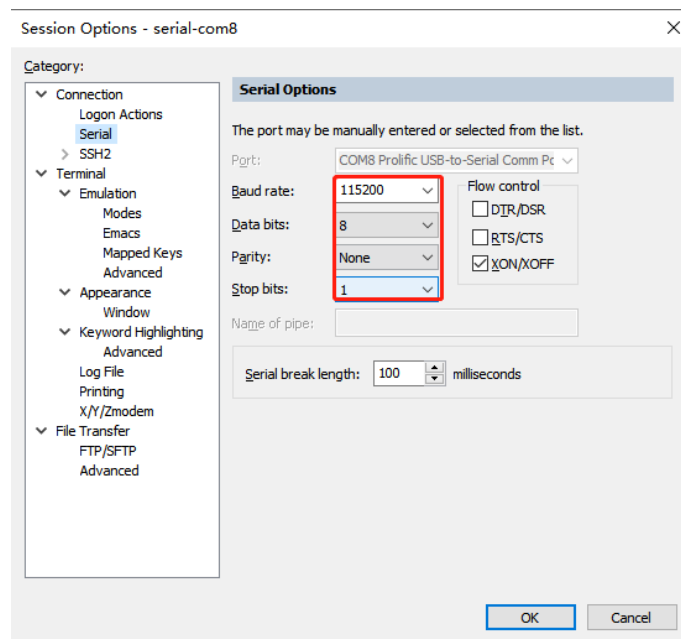
5. AT commands

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