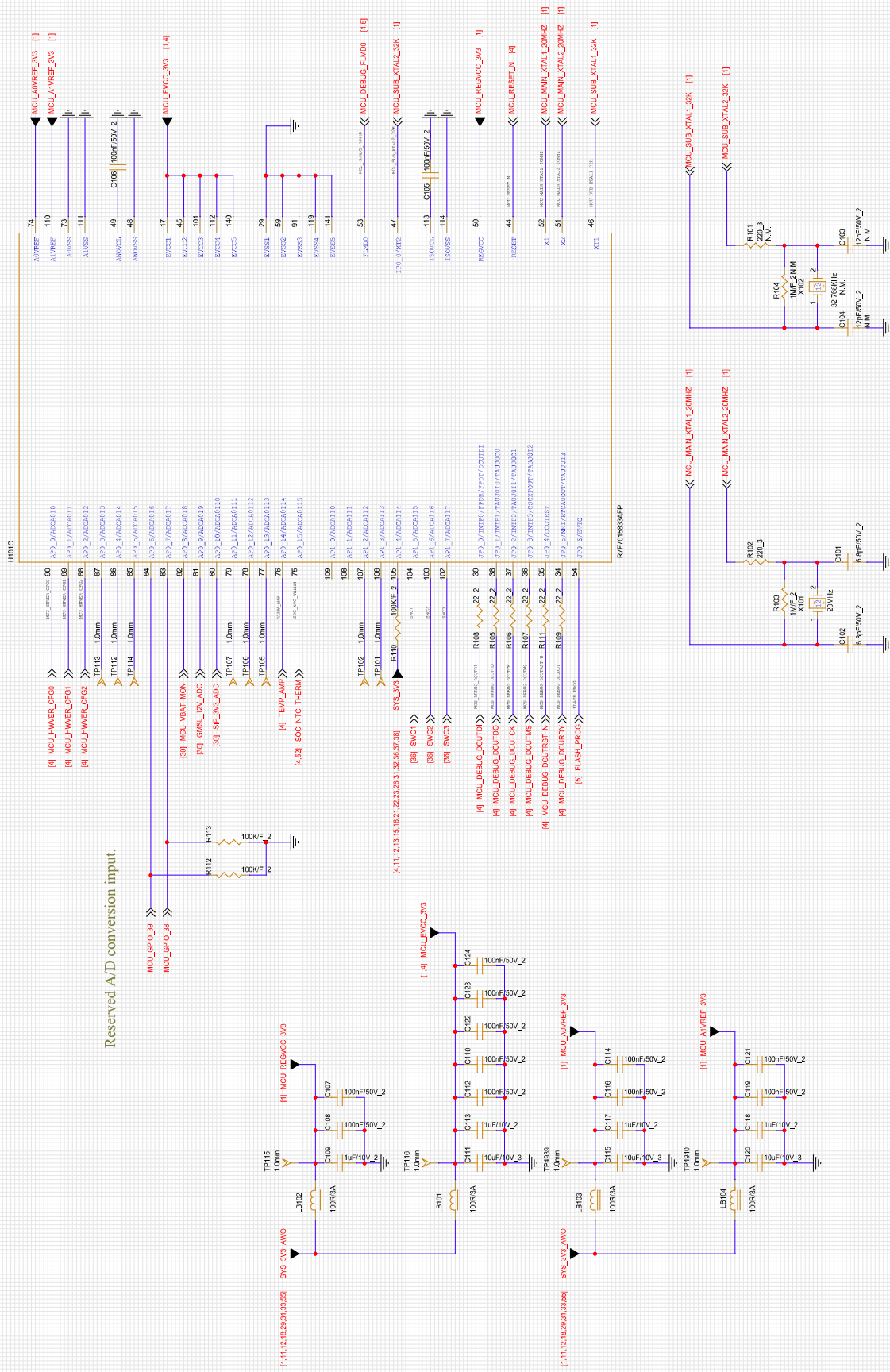
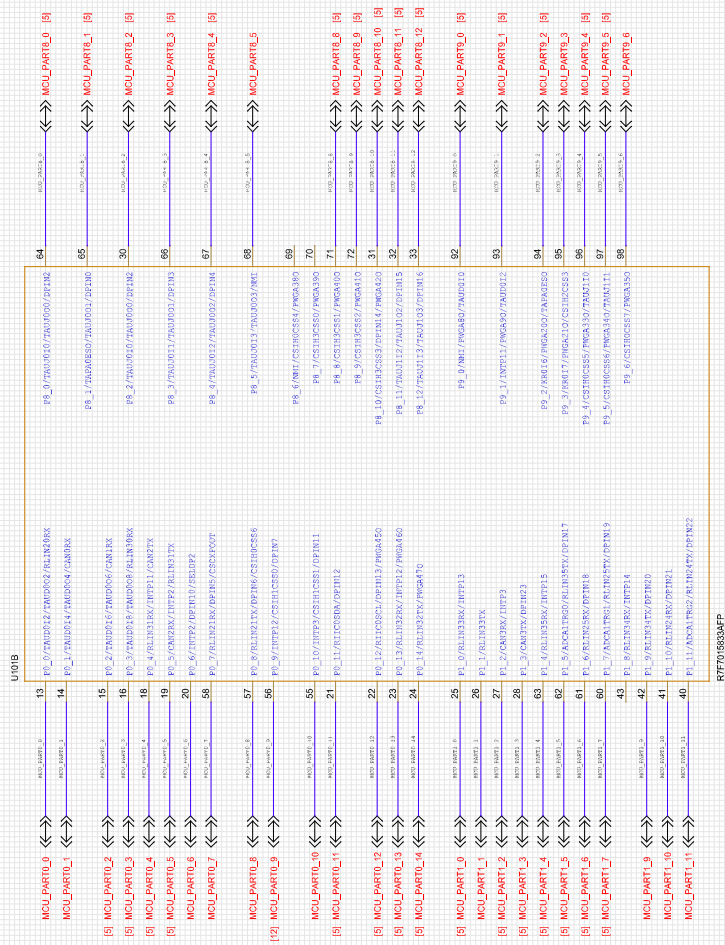
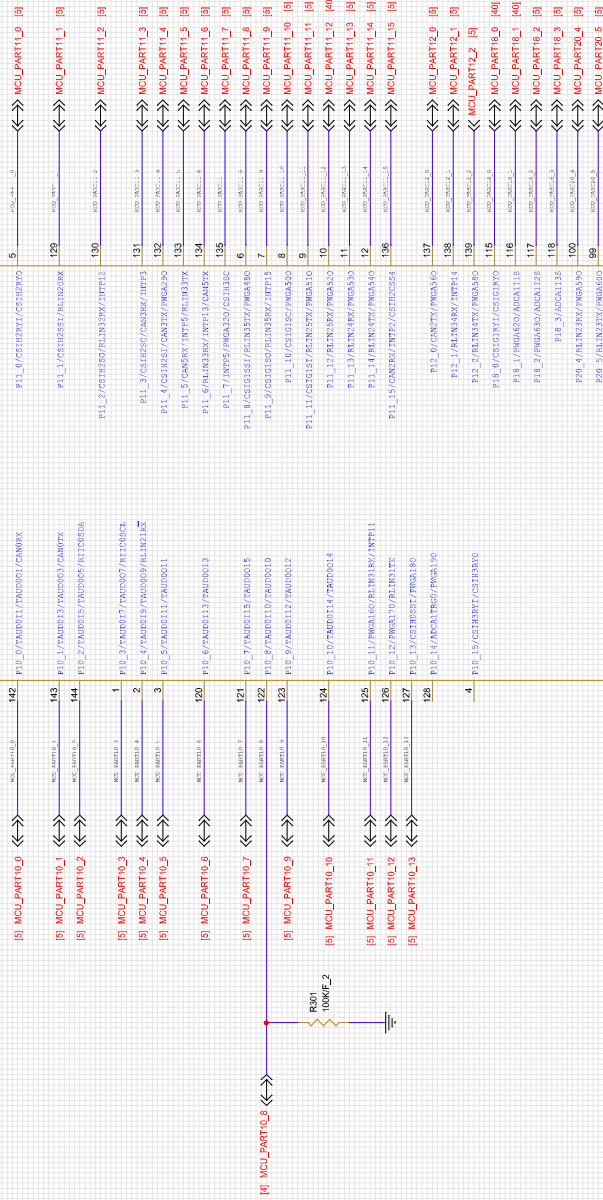






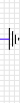
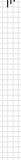
U00A



R1701583MFP

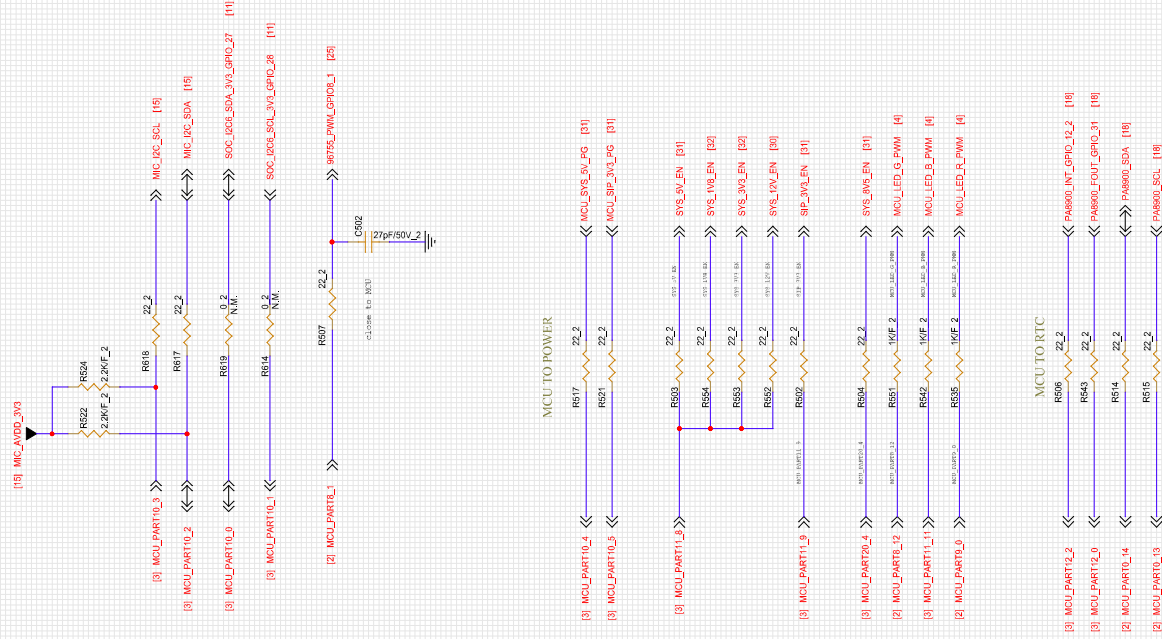
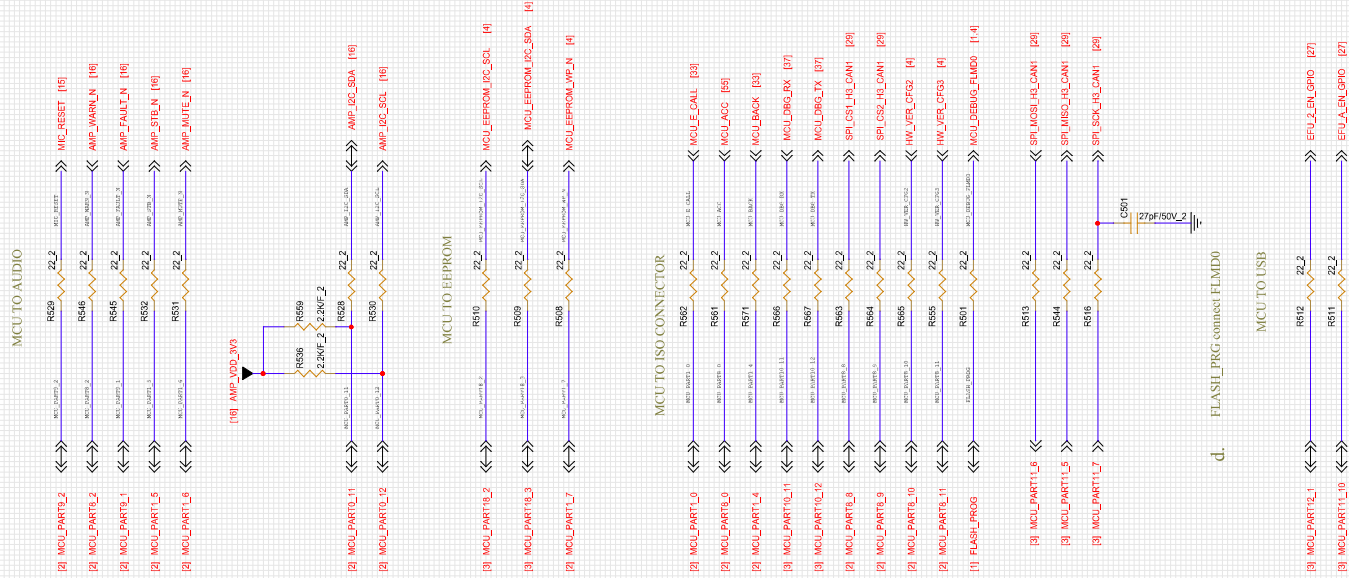
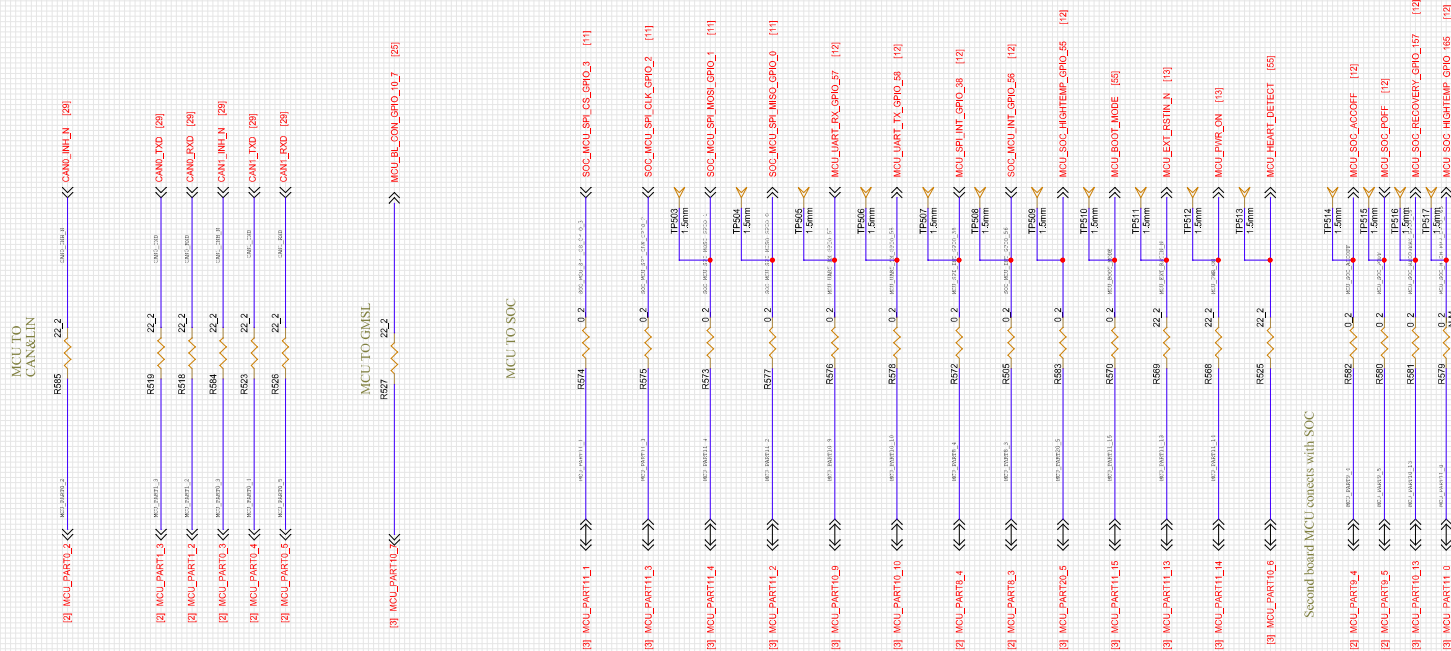


Could be other usage.

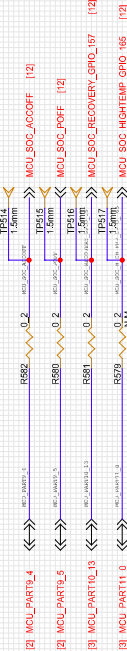


b. Modify NTC for CDP SID



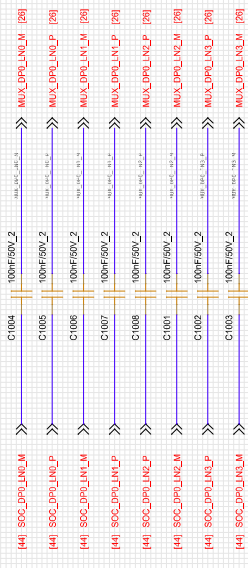
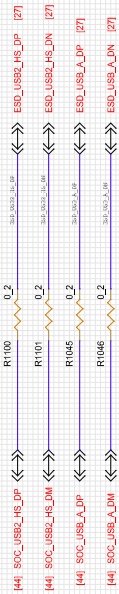


Second board MCU connects with SOC



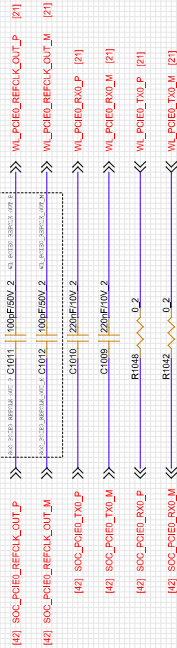
FLASH PRG connect FLMD0

SOC TO USB DP

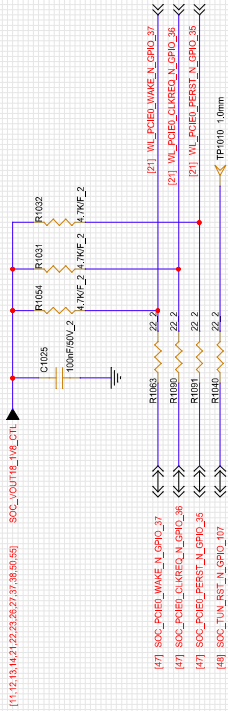


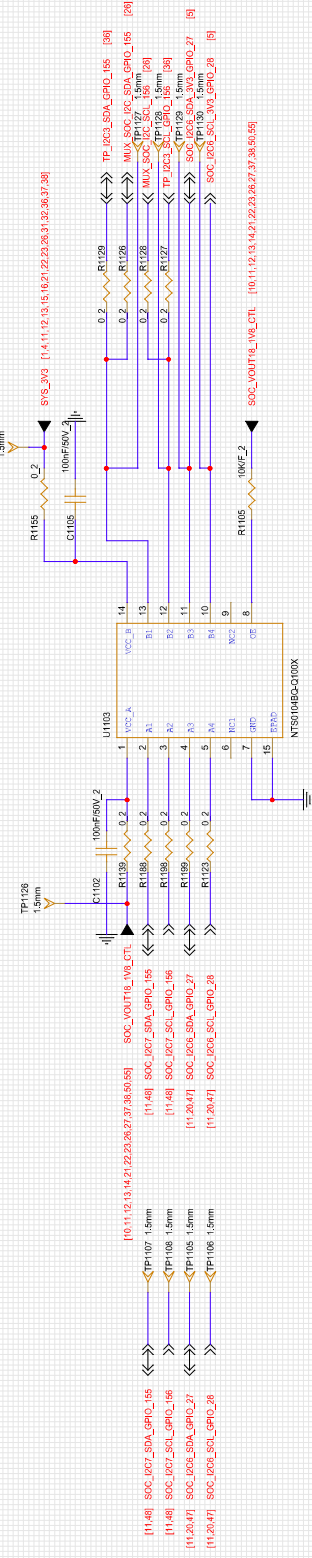
SOC TO BT&WIFI

TO BE CHECKED !!



COUPLED CAP : TX close to SOC , RX close to device





```

[47] SOC_UART4_TXD_GPIO_45 >> R117 22_2 BT_UART_RX [21]
[47] SOC_UART4_RXD_GPIO_46 << R118 22_2 BT_UART_TX [21]
[47] SOC_UART4_RTS_N_GPIO_44 >> R119 22_2 BT_UART_CTS [21]
[47] SOC_UART4_CTS_N_GPIO_43 << R120 22_2 BT_UART_RTS [21]

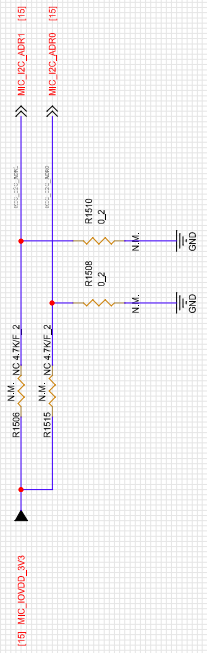
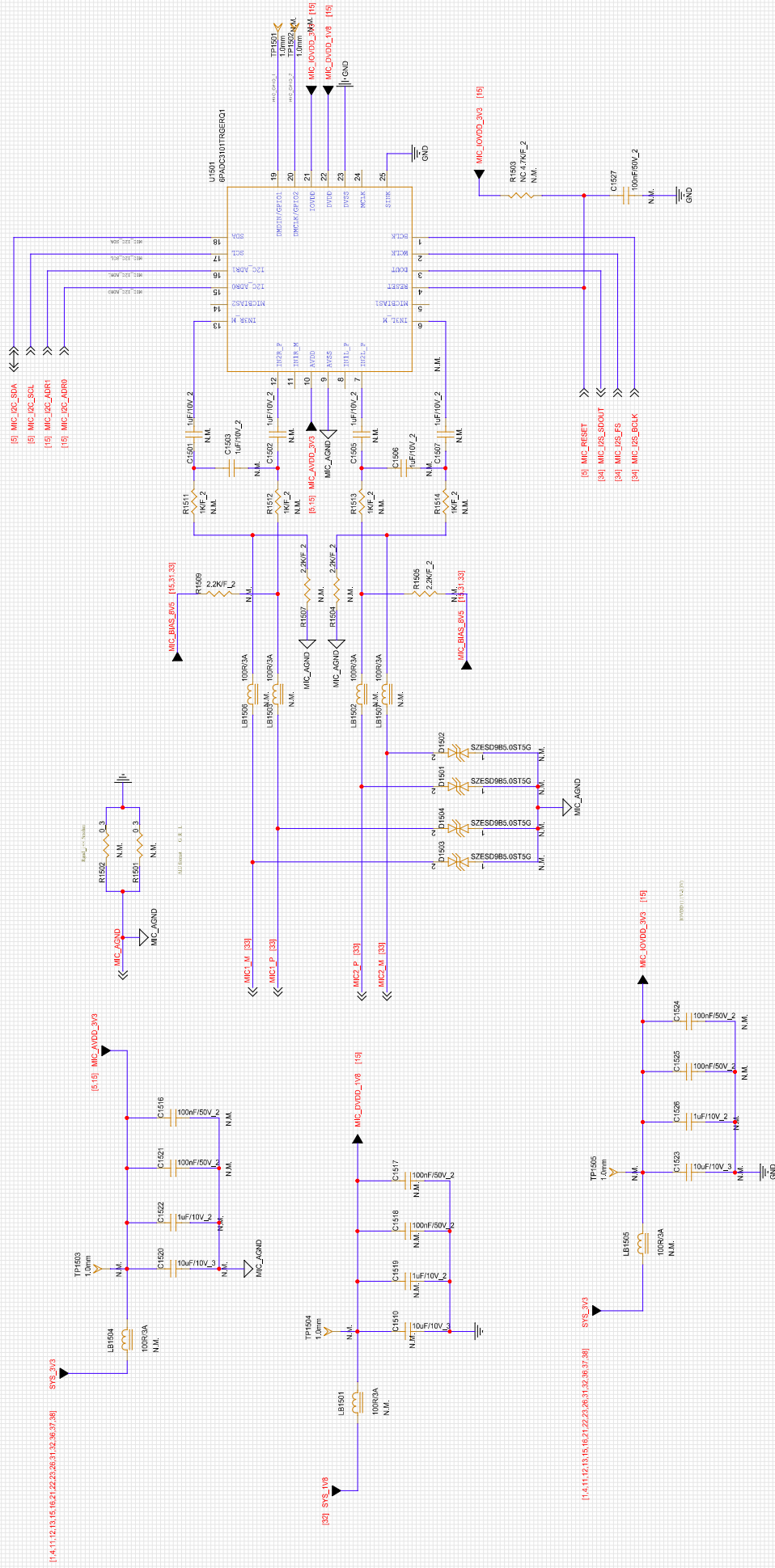
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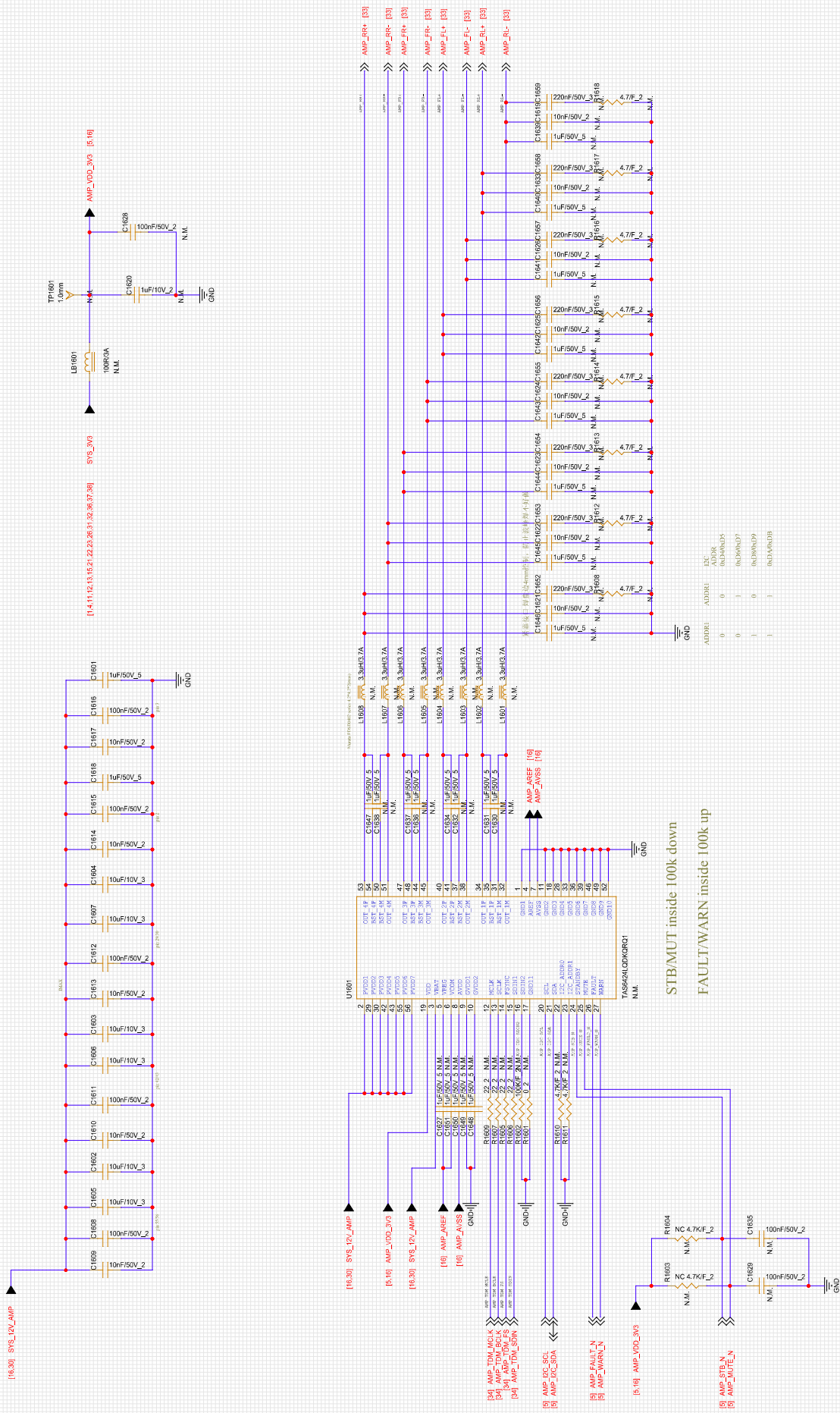




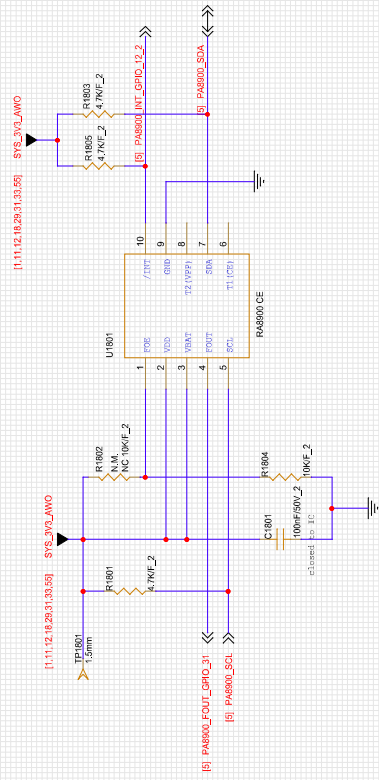


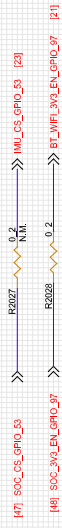
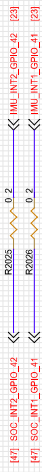
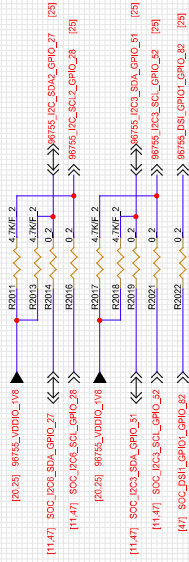
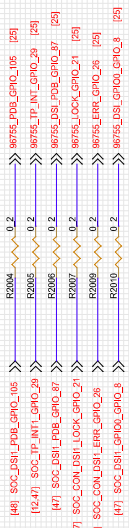
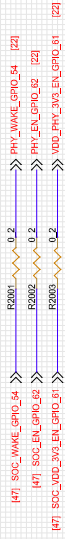




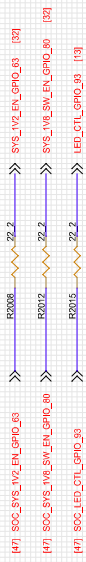








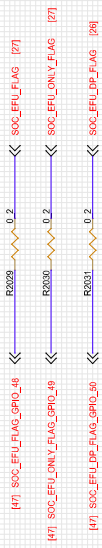
## SOC TO POWER



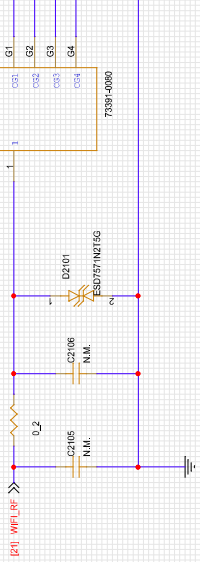
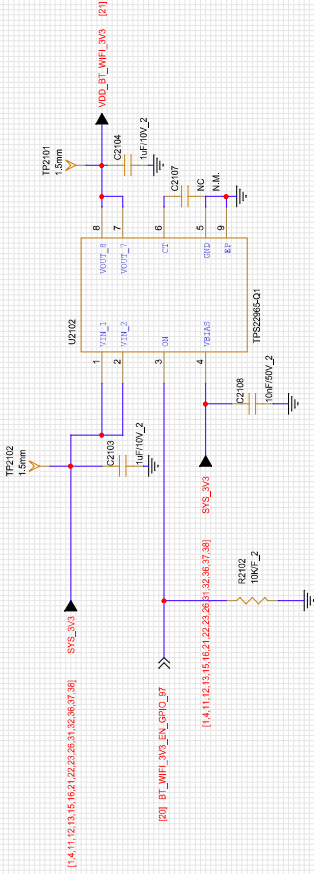
## SOC TO LCD&CAM&DP



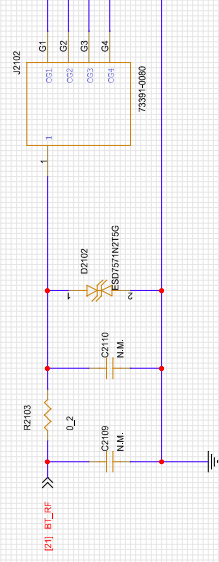
## SOC TO USB EFU FLAG



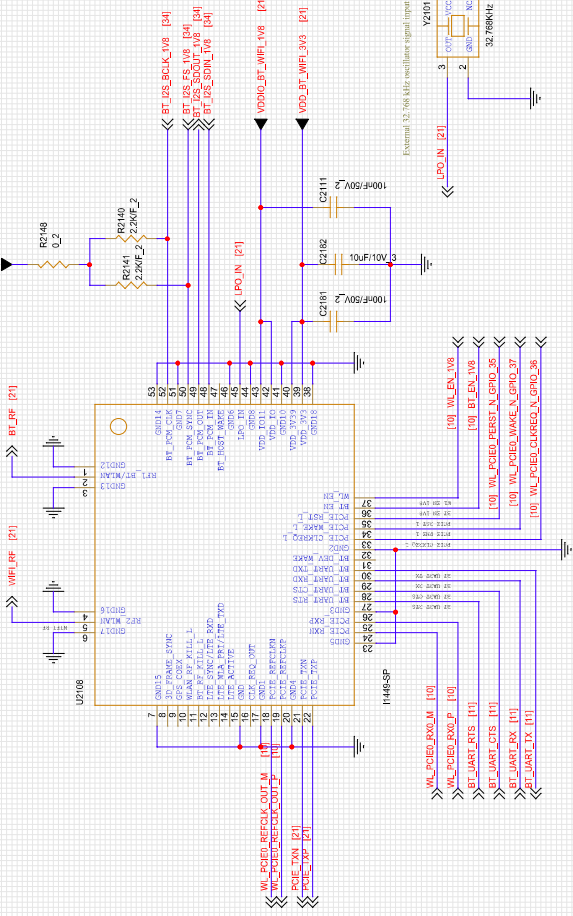
[10,11,12,13,14,21,22,23,26,27,37,38,50,56] SOC\_VOUT16\_V16\_CTL 100KΩ B2101 VDDIO\_BT\_WFL\_V16 [21]



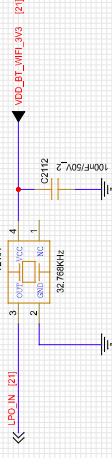
<< WL\_PCIE\_TX0\_P [10] C2113 100F50V 2 << PCIe\_TXP [21]  
<< WL\_PCIE\_TX0\_M [10] C2114 100F50V 2 << PCIe\_TXN [21]

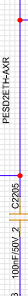
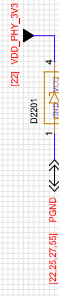
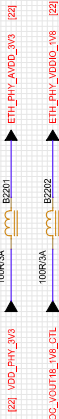


[10,11,12,13,14,21,22,23,26,27,37,38,50,56] SOC\_VOUT18\_V18\_CTL



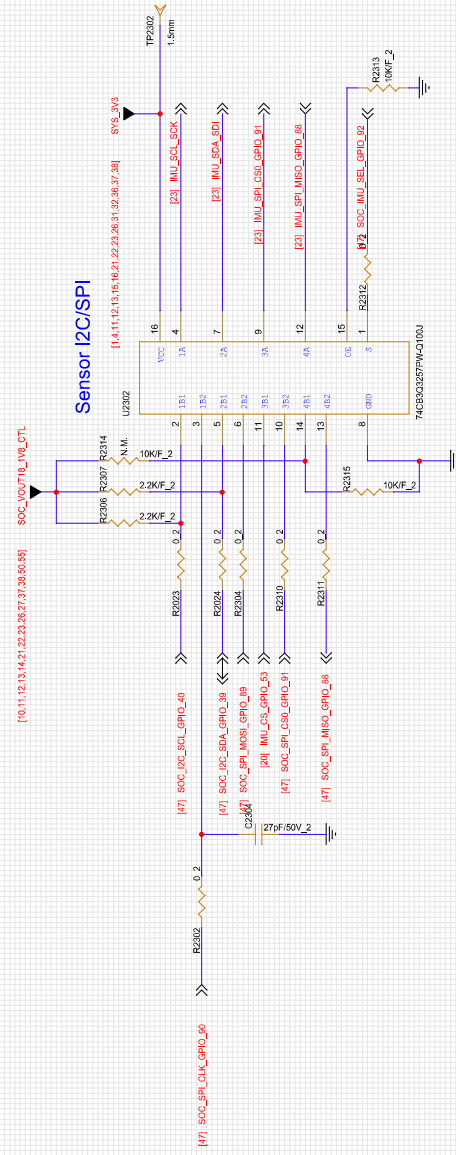
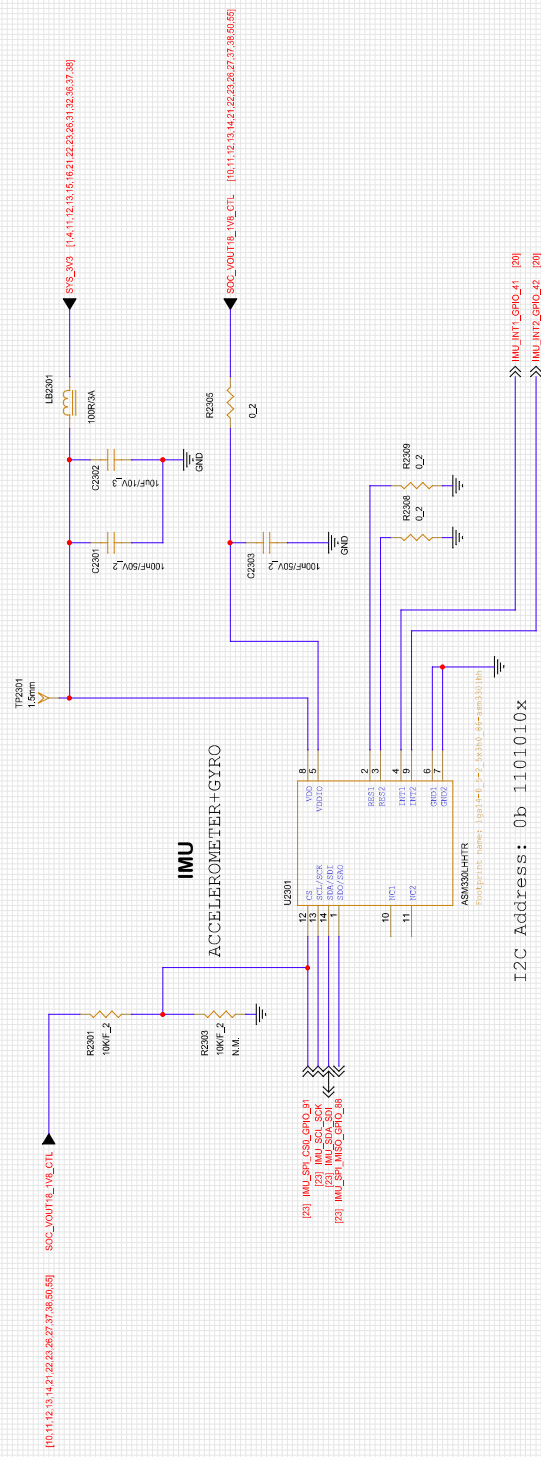
External 32.768 kHz oscillator signal input



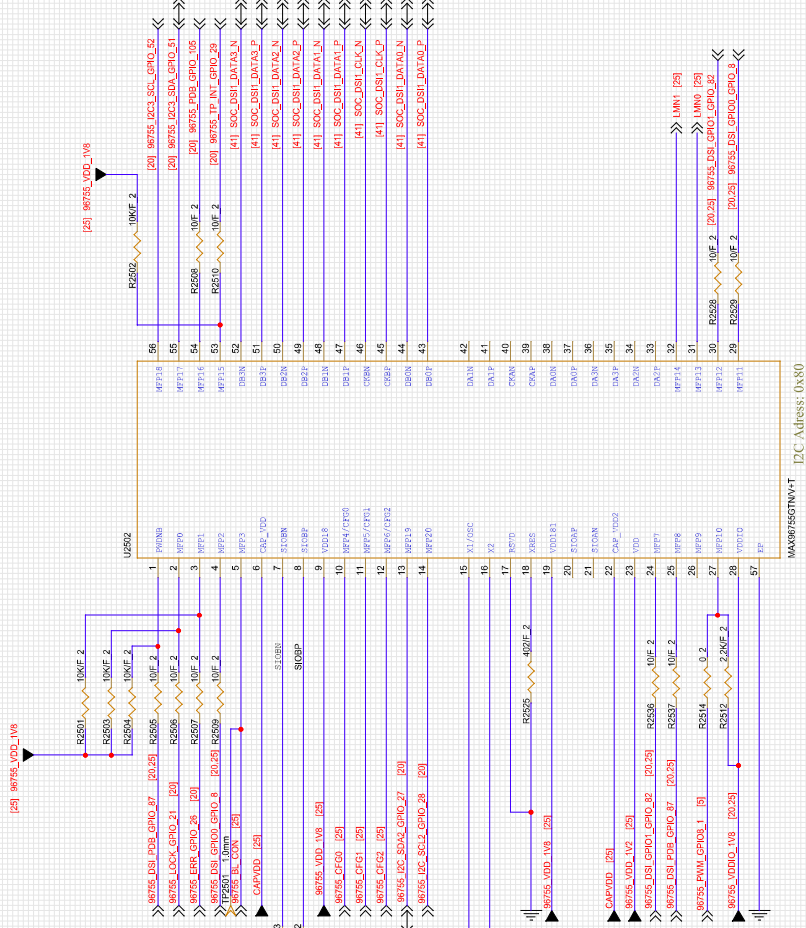
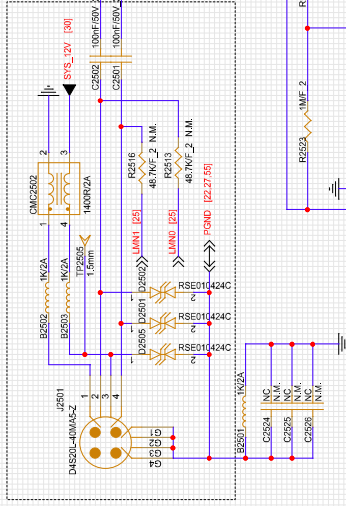


Strap Configuration Pins  
keep closed to IC pins



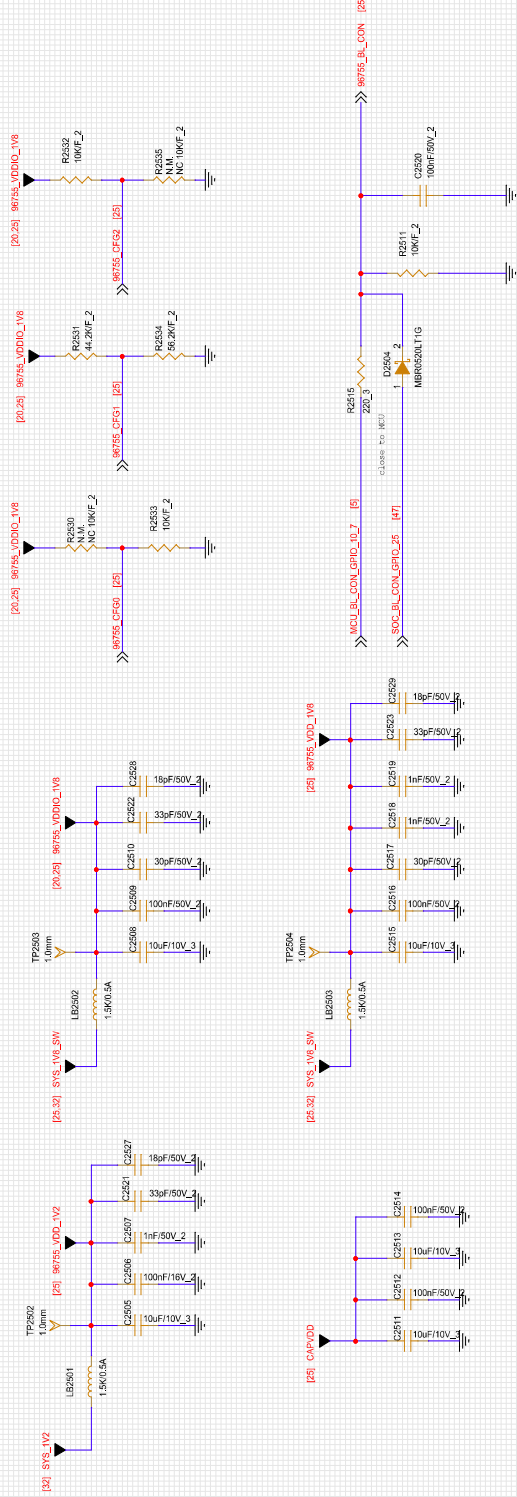


diff 100ohm±10%

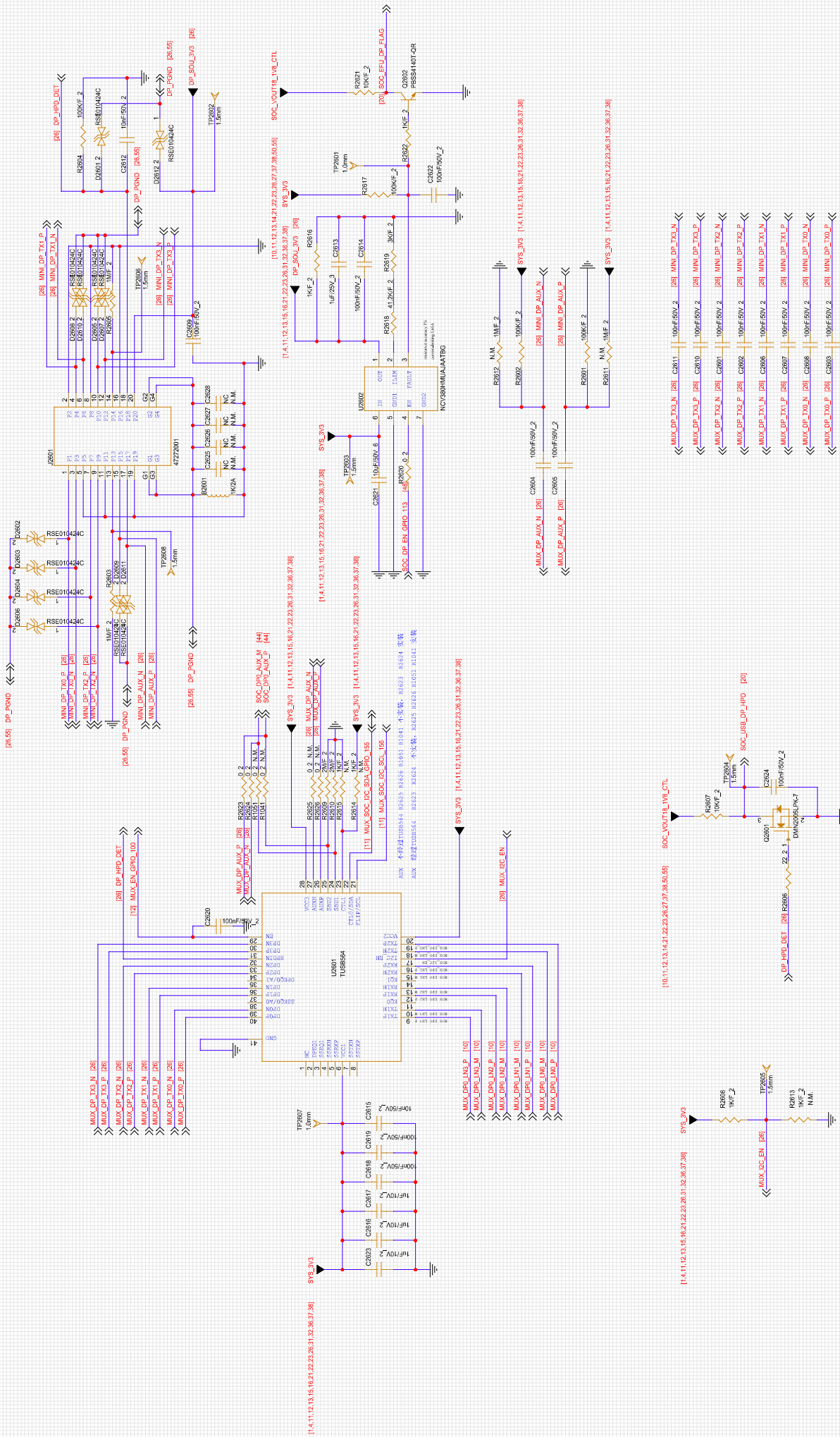


等长, 差分  
阻抗100ohm ±10%

MAX8150TMMT I2C Address: 0x80

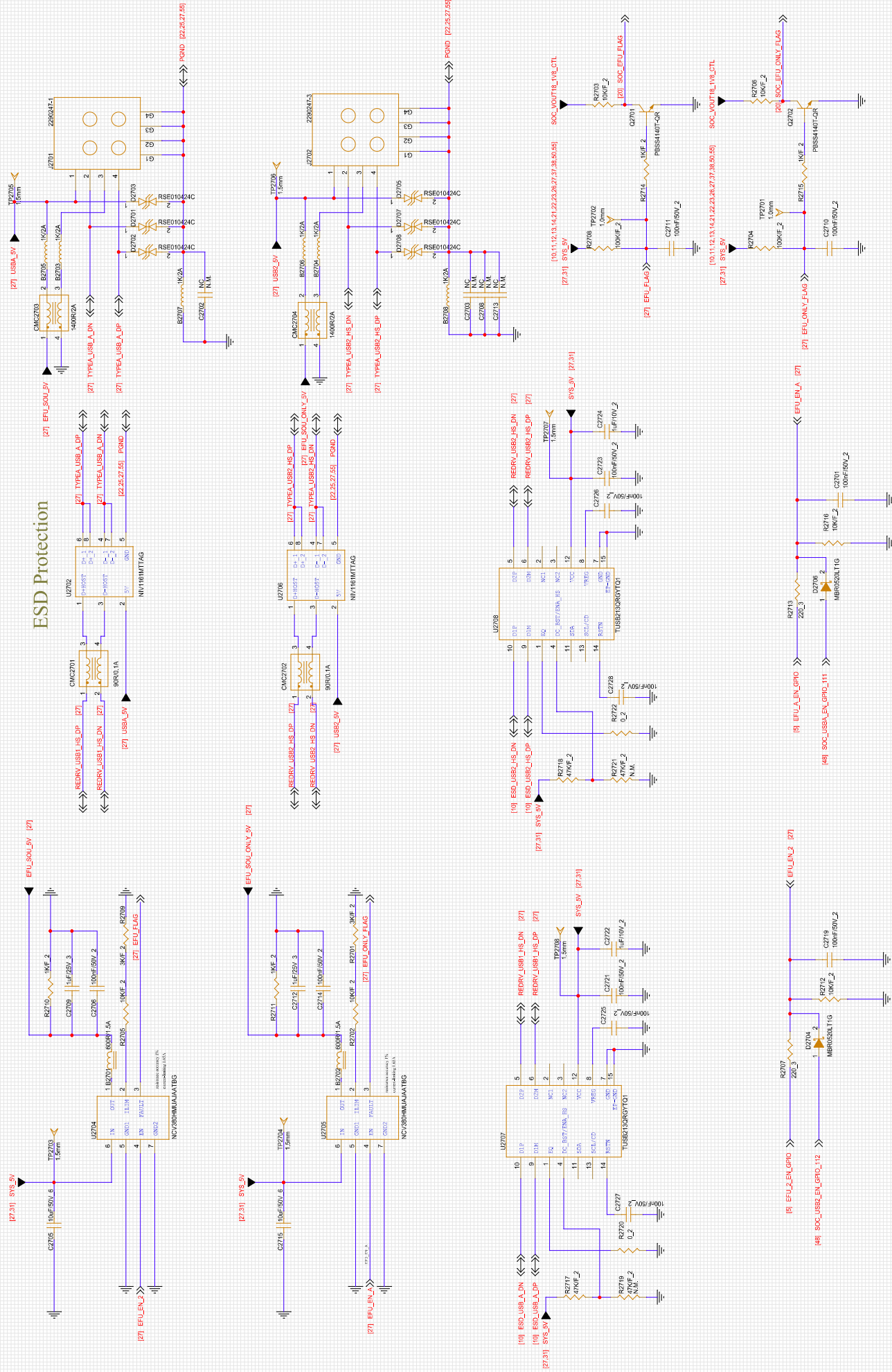


COAX,  
GMSL2\_3Chps

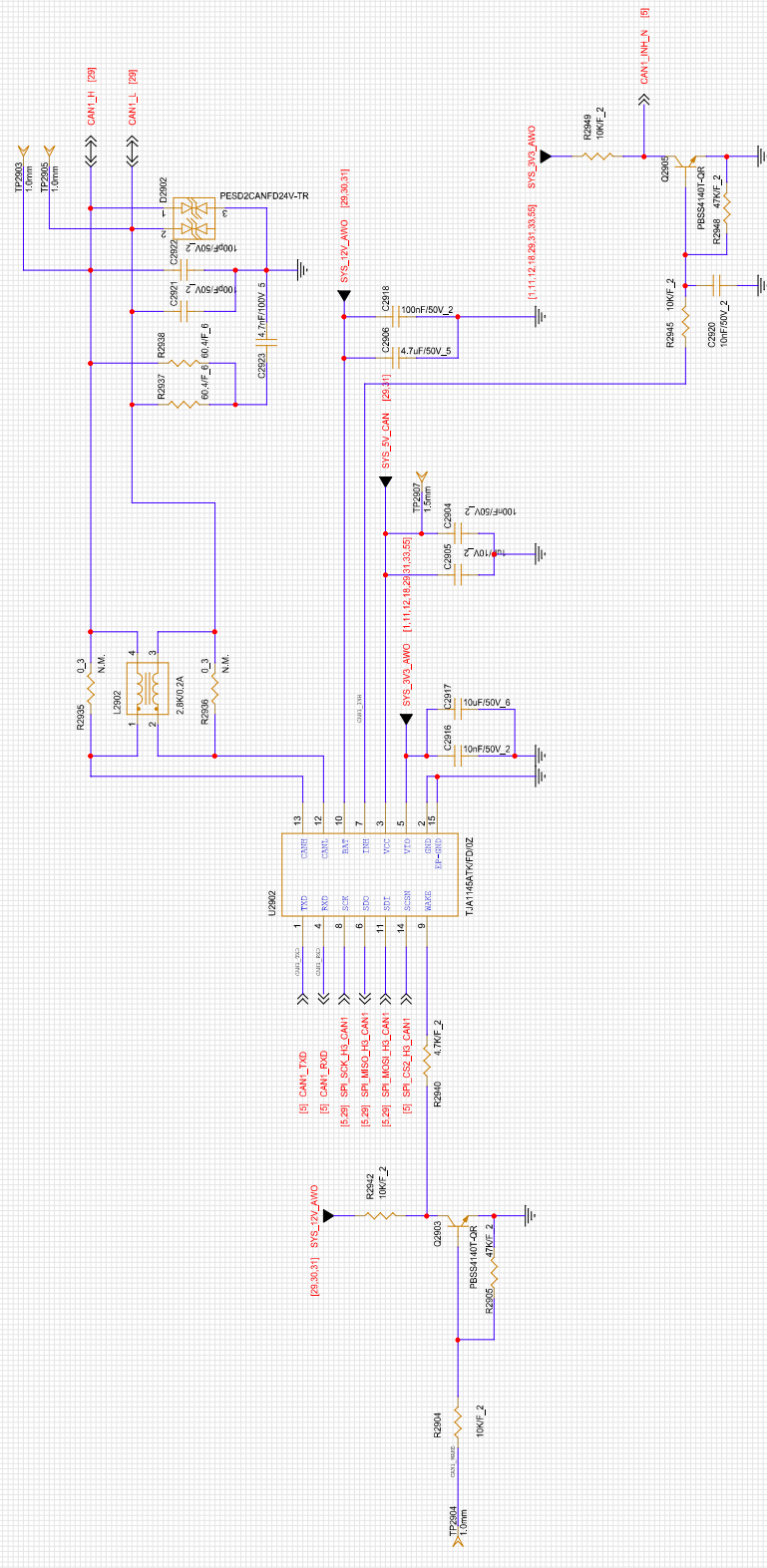


## USB VBUS Protection

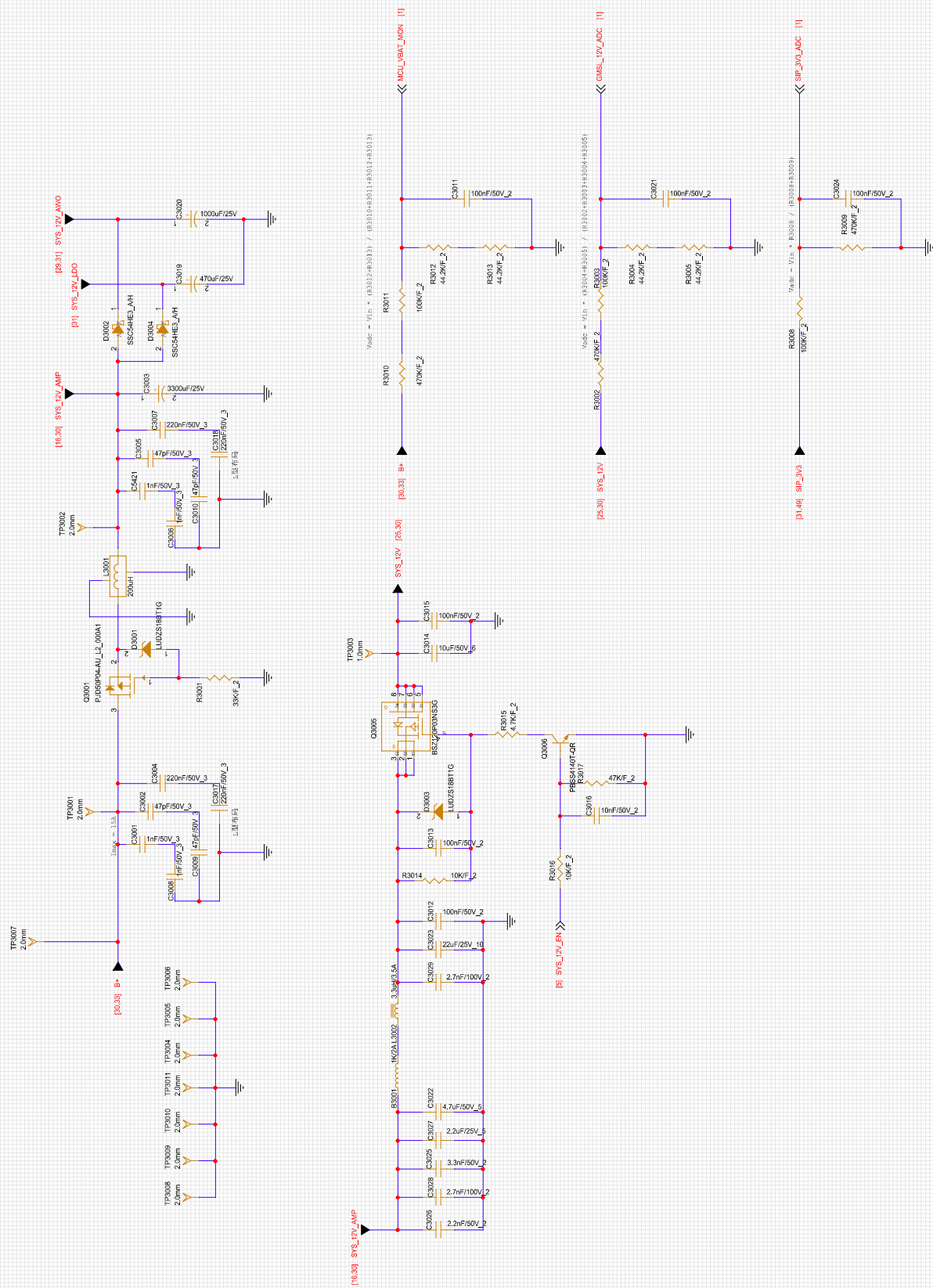
## ESD Protection



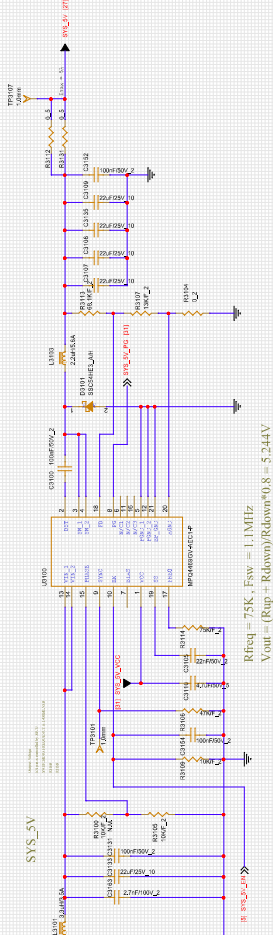
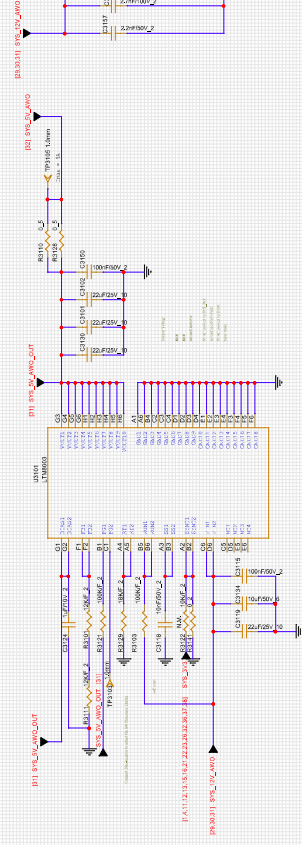






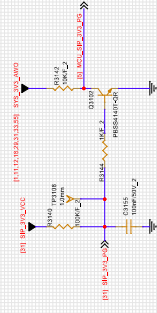
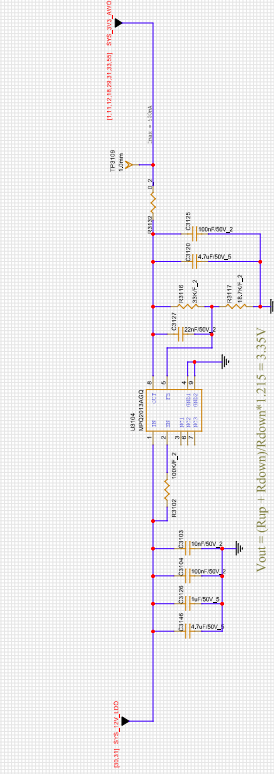


# SYS\_5V\_AWO

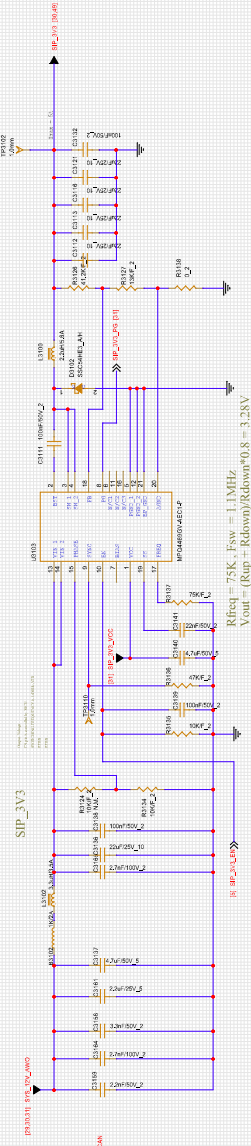


$$R_{fsw} = 75K, F_{sw} = 1MHz$$

$$V_{out} = (R_{up} + R_{down})/R_{down} * 0.8 = 5.244V$$

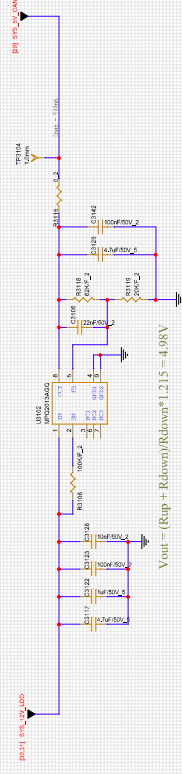


$$V_{out} = (R_{up} + R_{down})/R_{down} * 1.215 = 3.35V$$

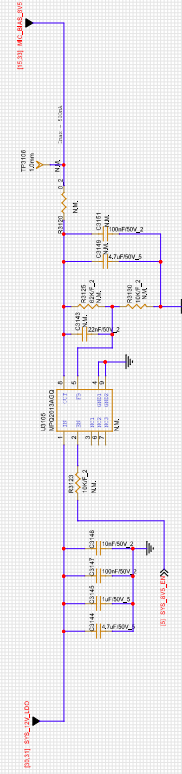


$$R_{fsw} = 75K, F_{sw} = 1MHz$$

$$V_{out} = (R_{up} + R_{down})/R_{down} * 0.8 = 3.28V$$



$$V_{out} = (R_{up} + R_{down})/R_{down} * 1.215 = 4.98V$$



$$V_{out} = (R_{up} + R_{down})/R_{down} * 1.215 = 8.74V$$

