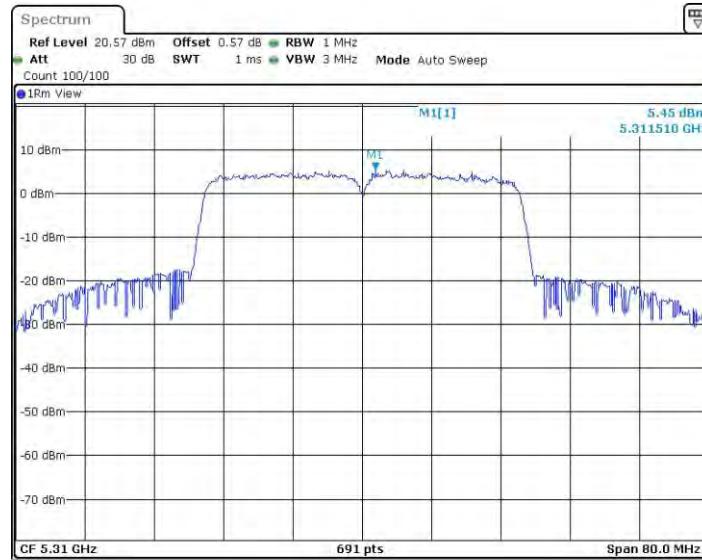
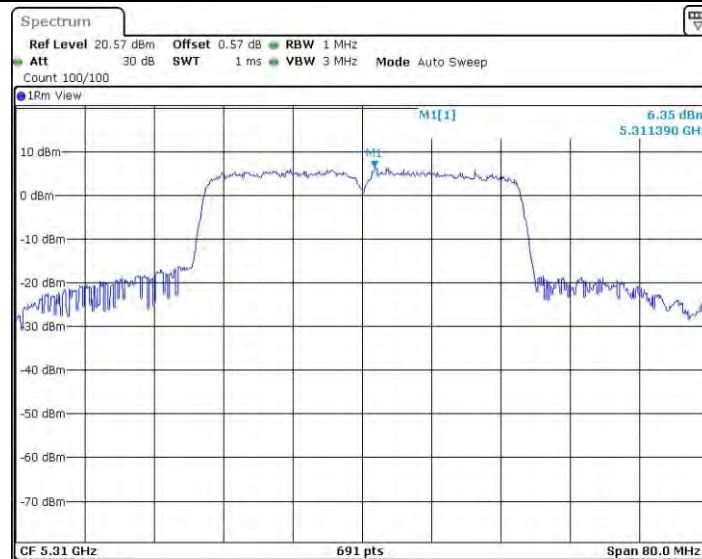
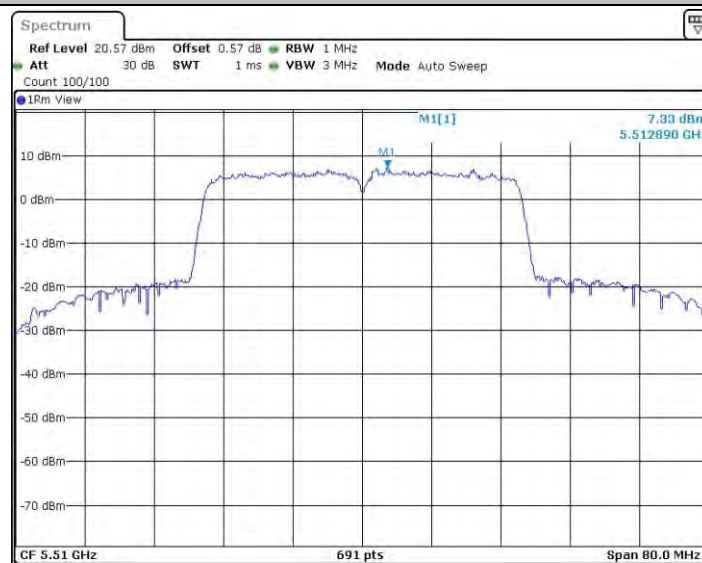




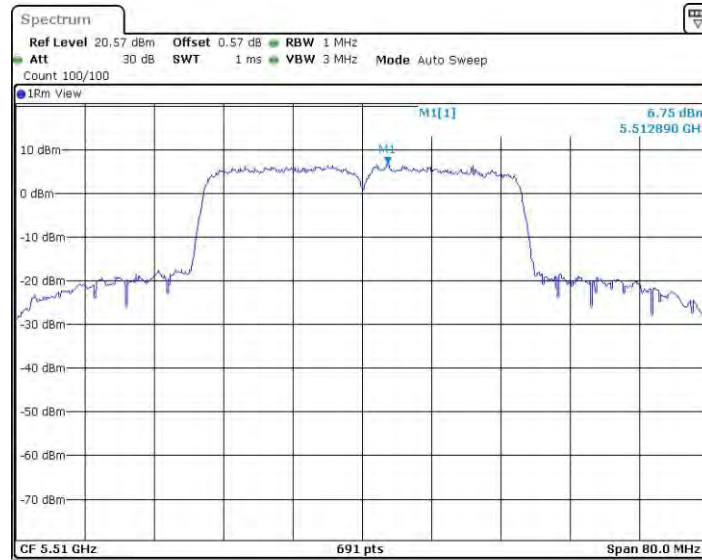
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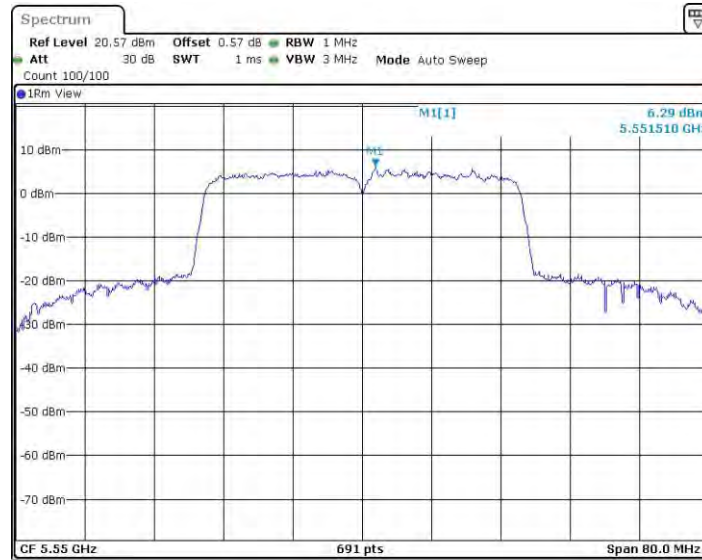
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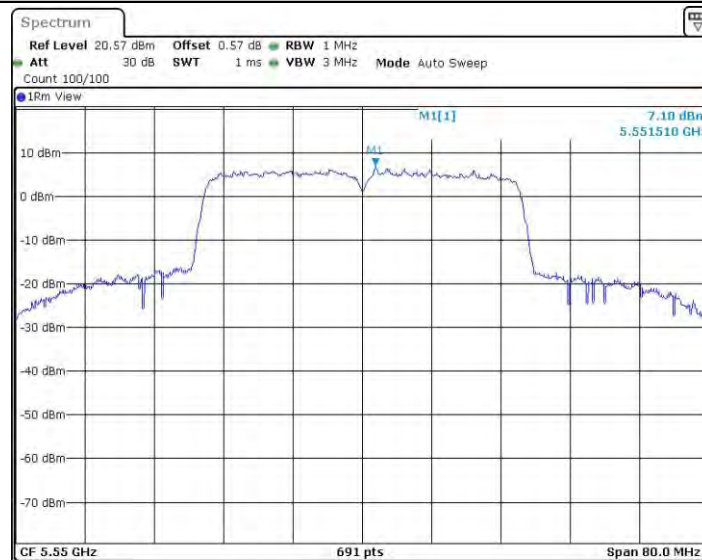
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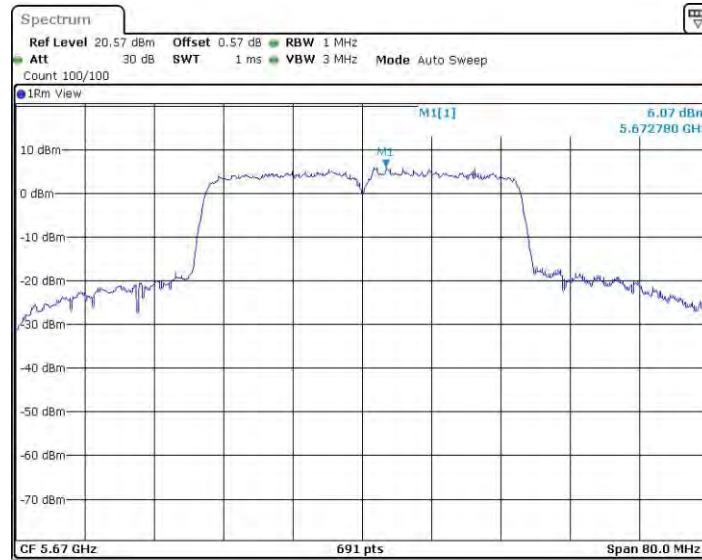
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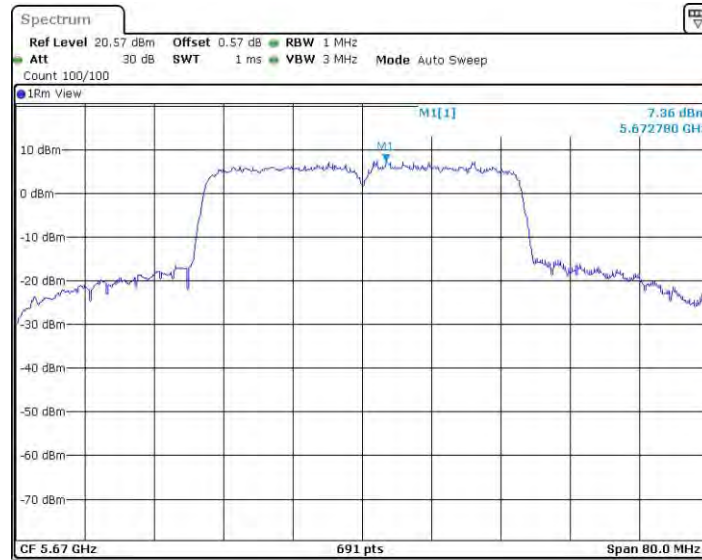
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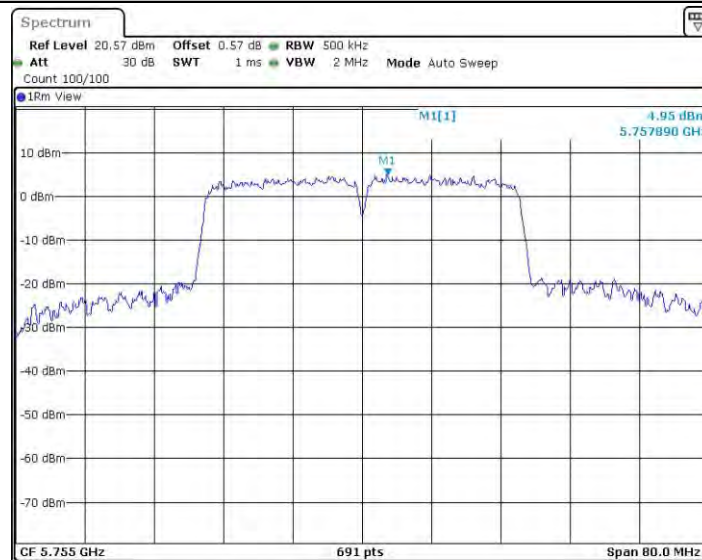
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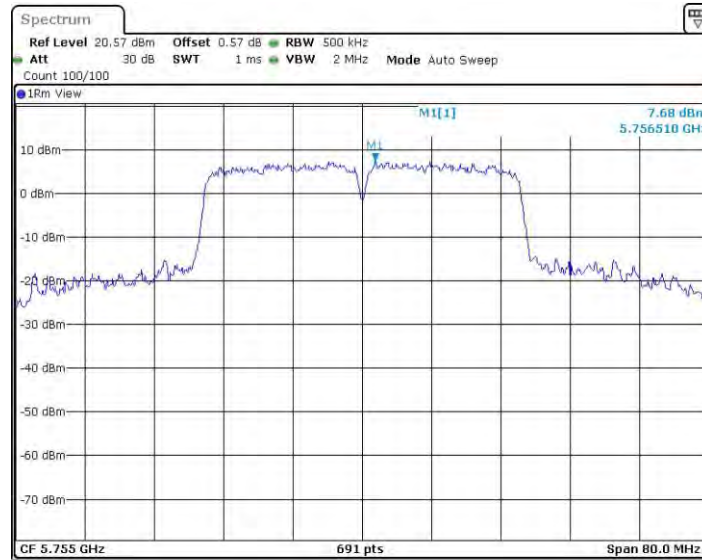
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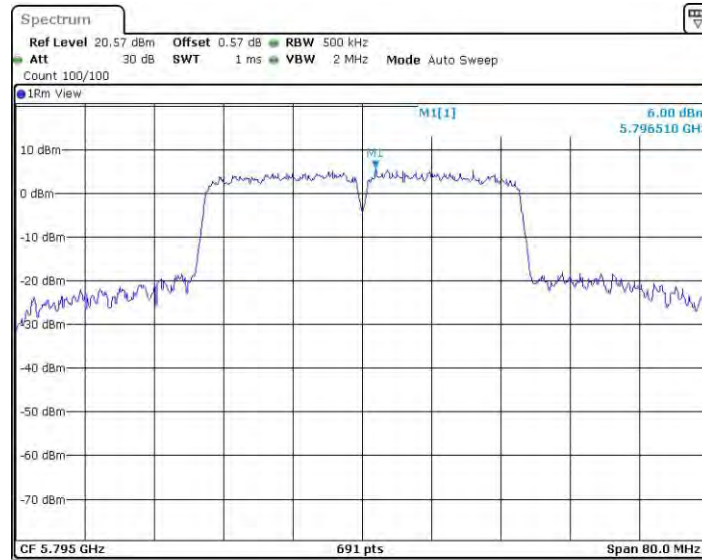
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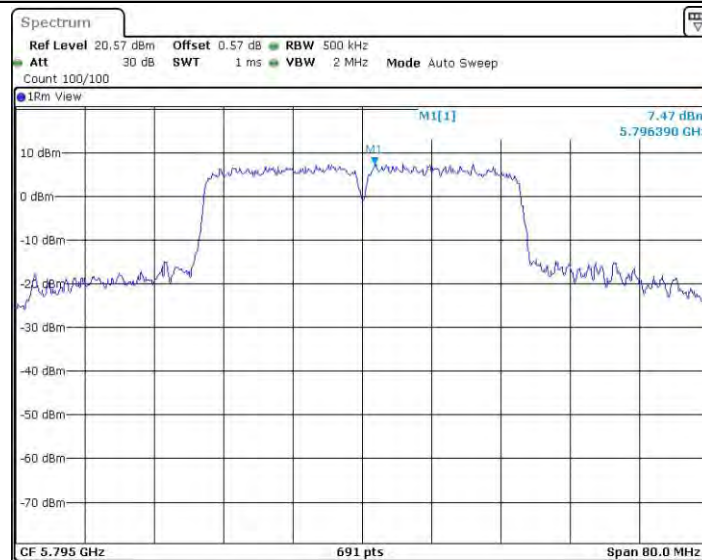
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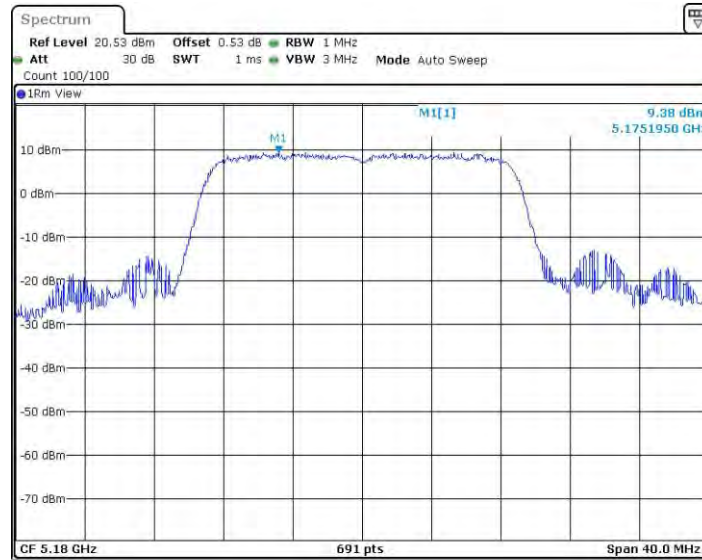
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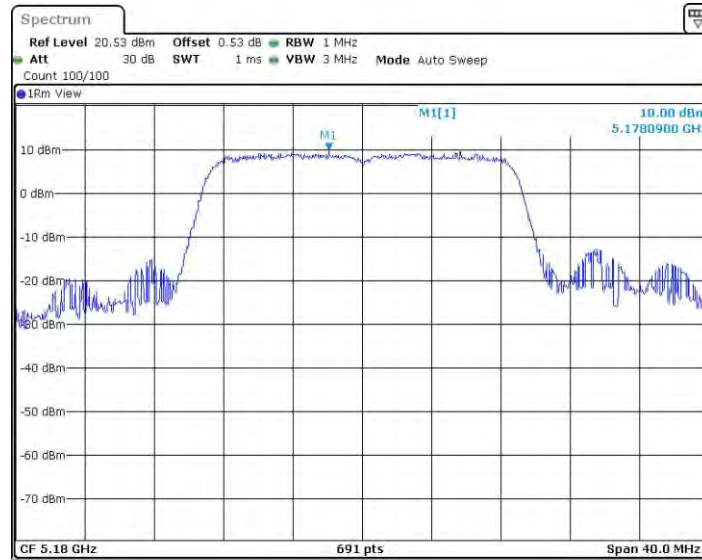
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11N40SISO_Ant1_5795



11AC20SISO_Ant0_5180



11AC20SISO_Ant1_5180



11AC20SISO_Ant0_5200



11AC20SISO_Ant1_5200



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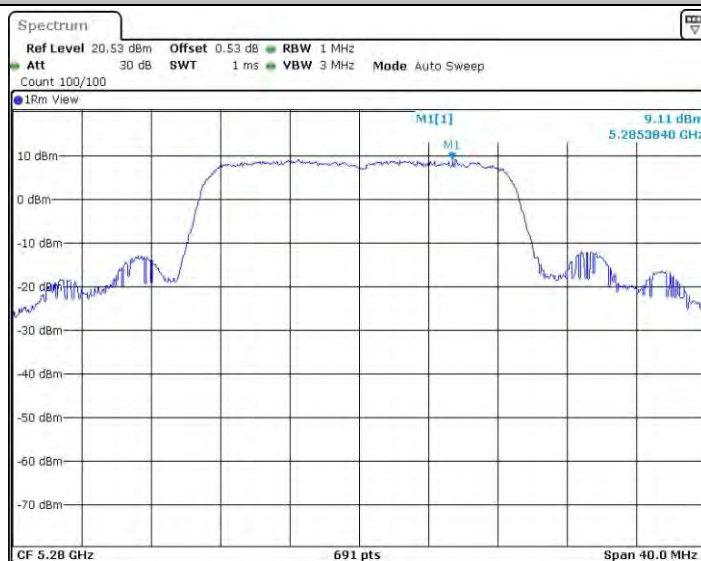
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11AC20SISO_Ant1_5260



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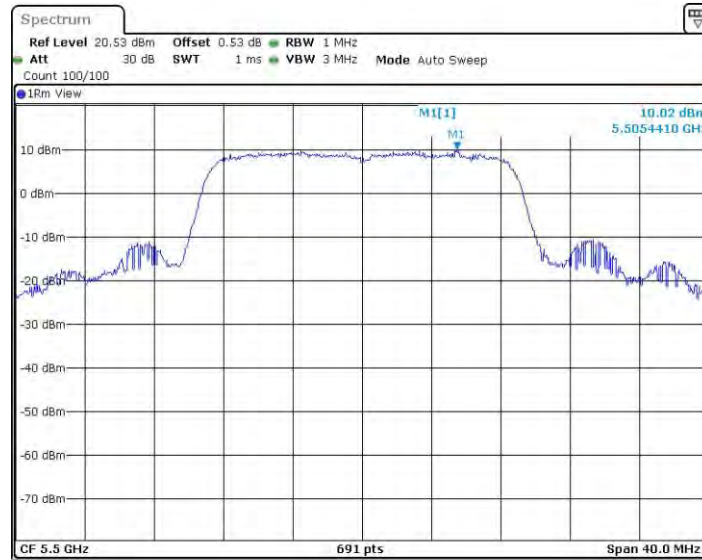
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11AC20SISO_Ant1_5500



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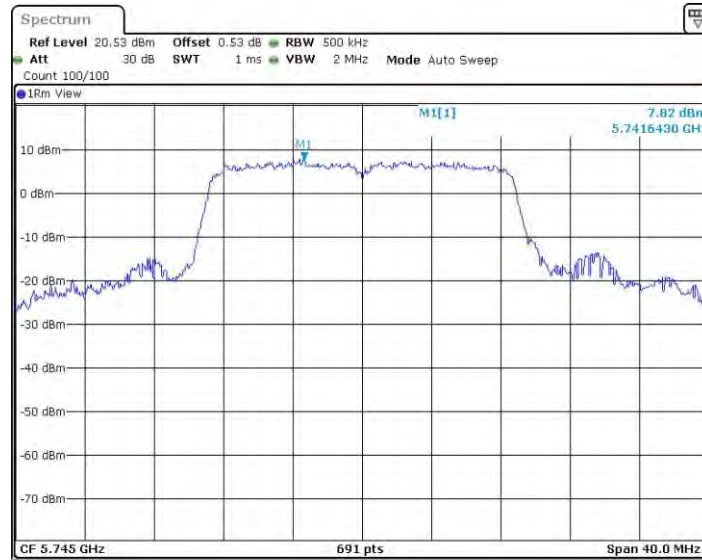
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11AC20SISO_Ant1_5700



11AC20SISO_Ant0_5745



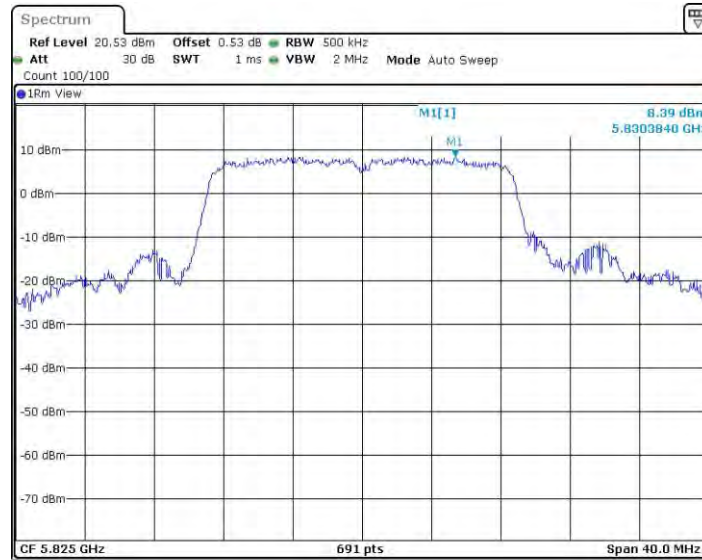
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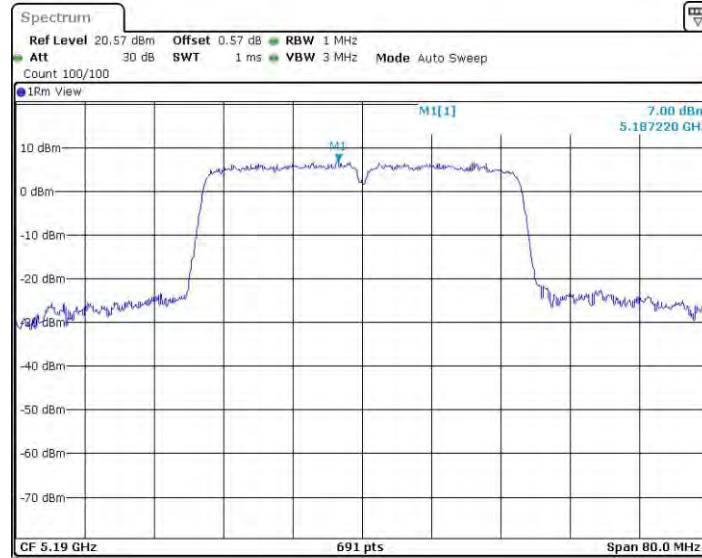
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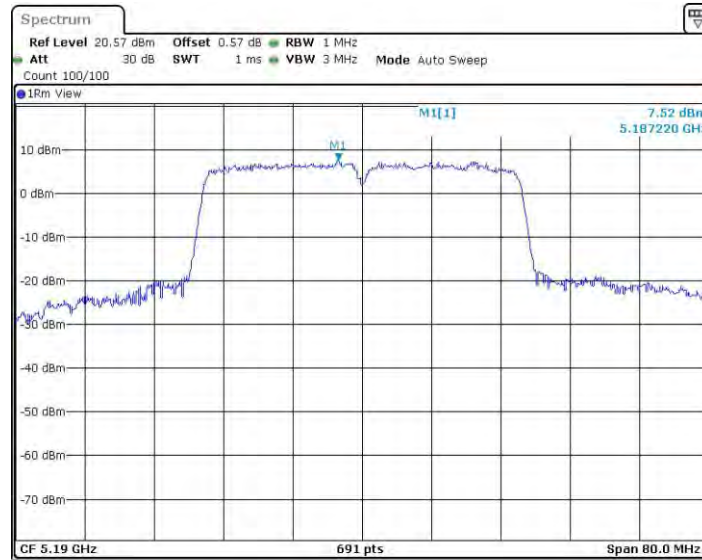
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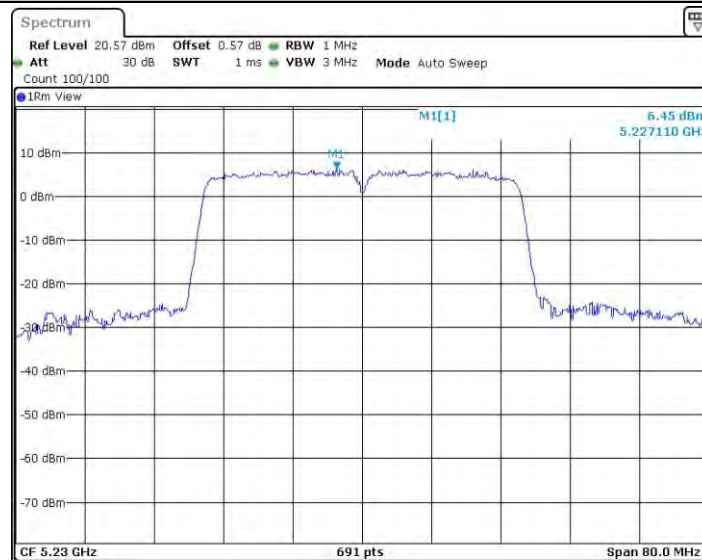
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11AC40SISO_Ant0_5190



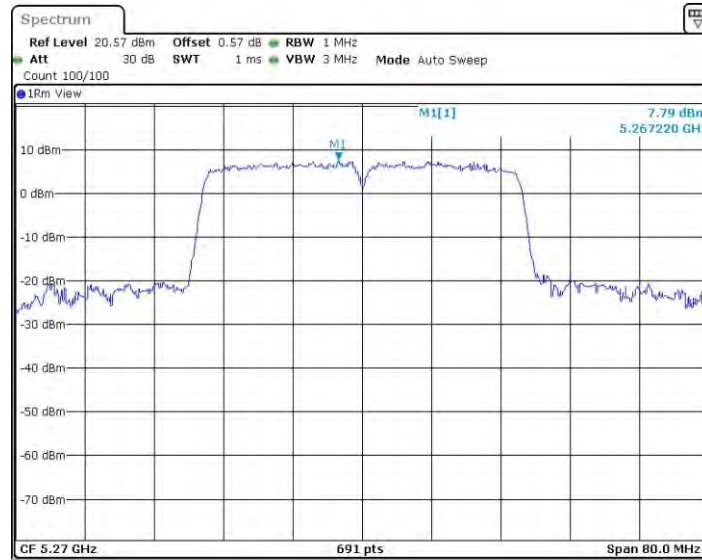
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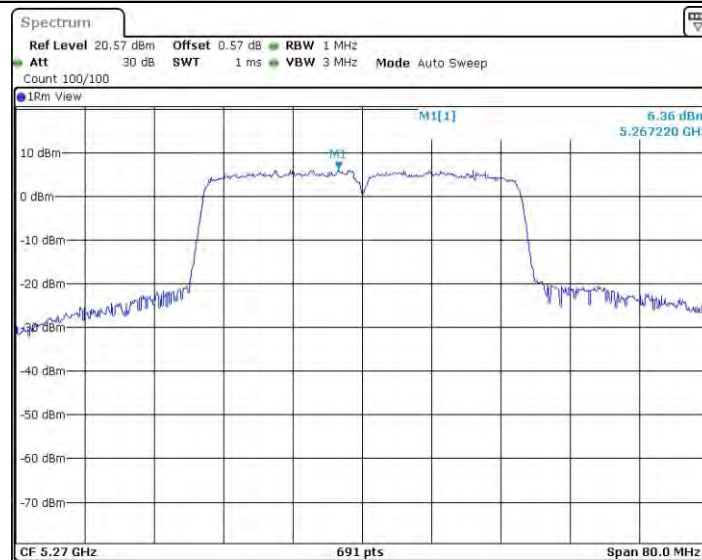
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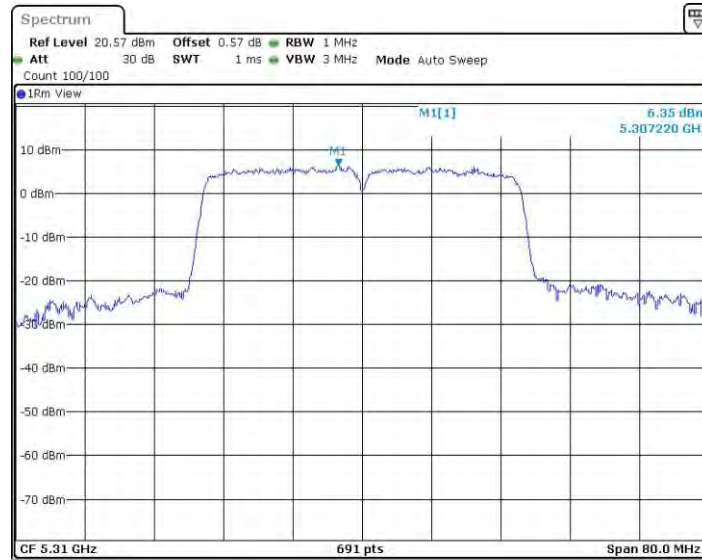
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11AC40SISO_Ant0_5310



11AC40SISO_Ant1_5310



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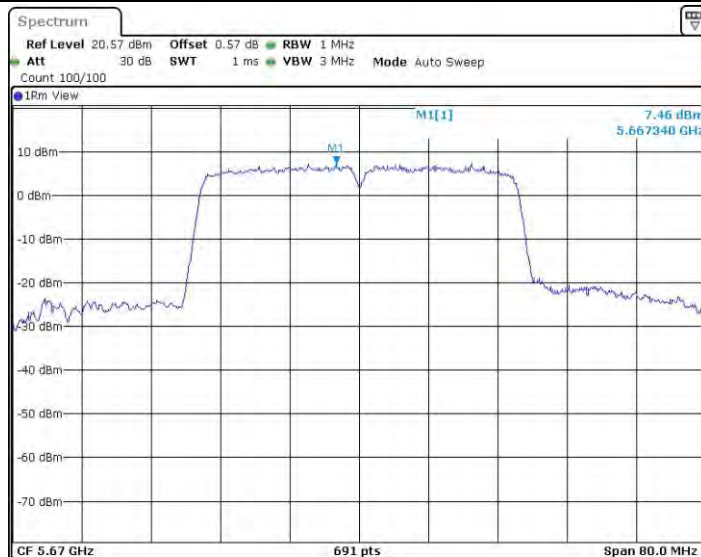
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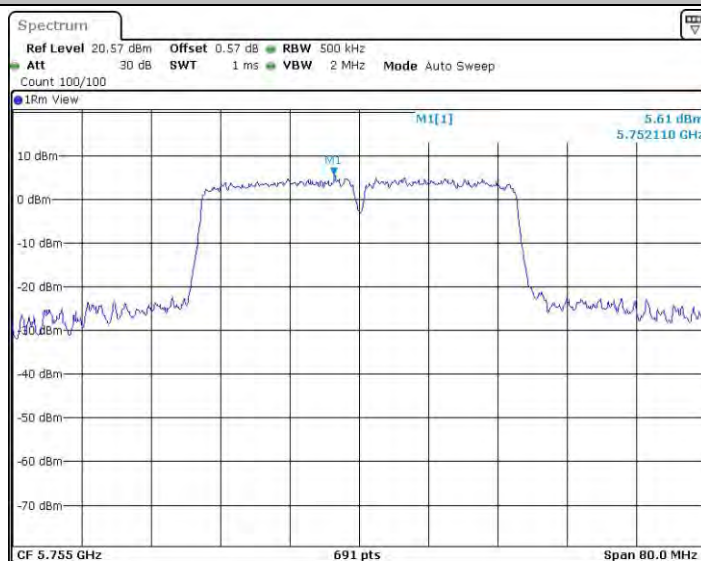
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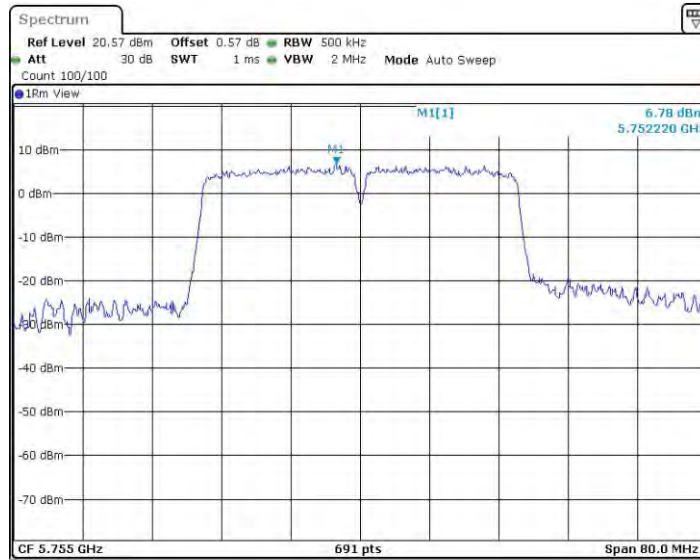
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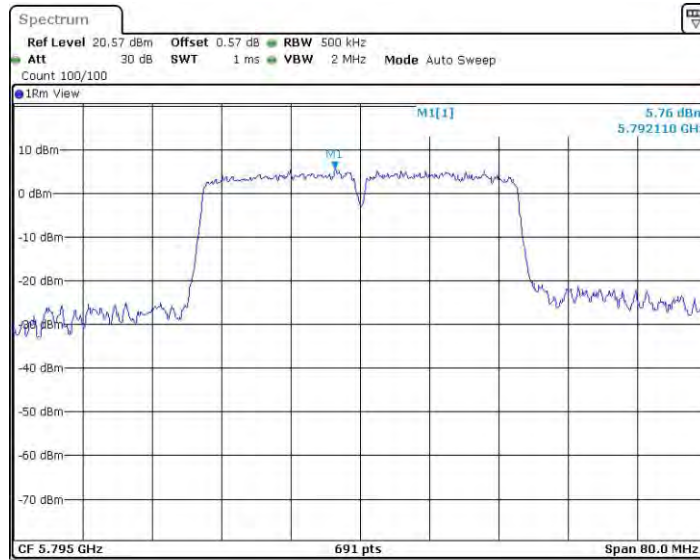
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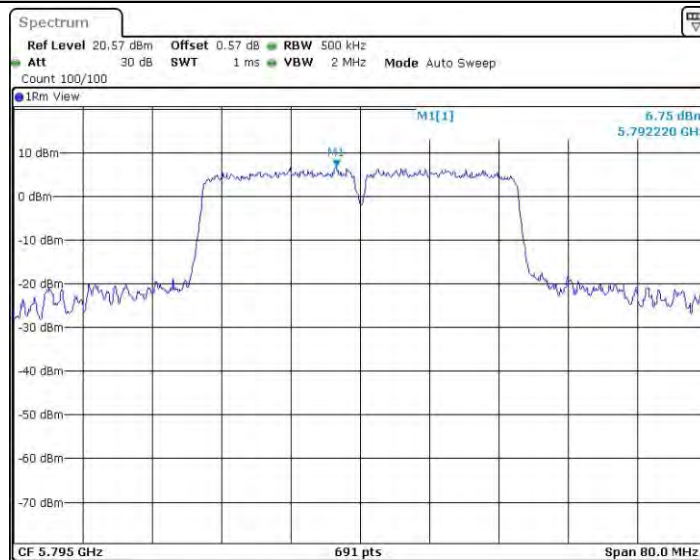
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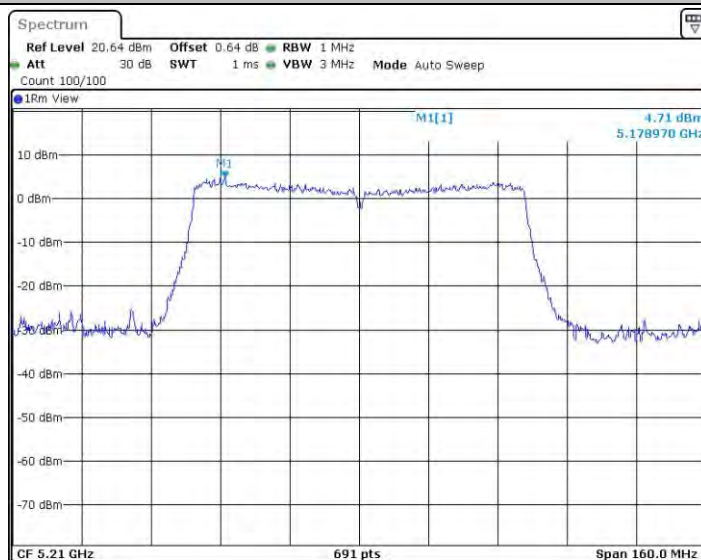
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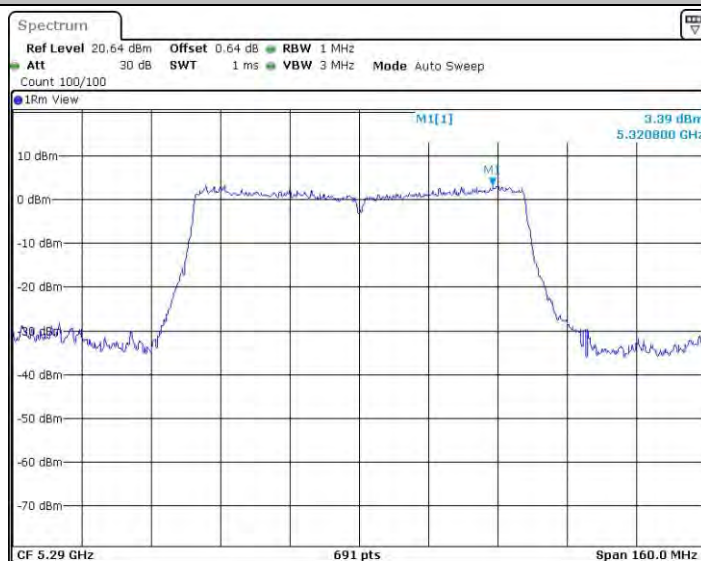
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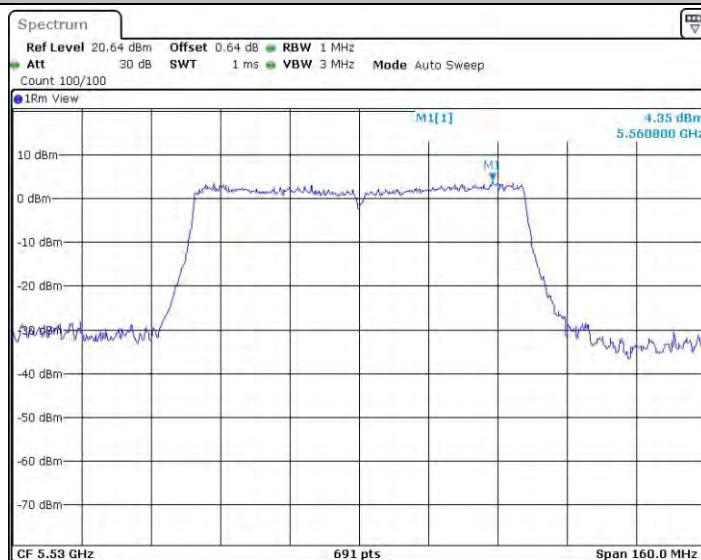
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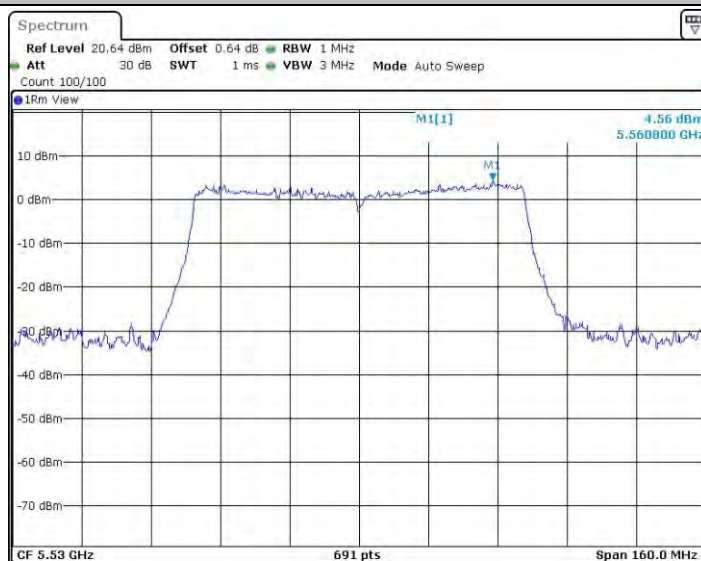
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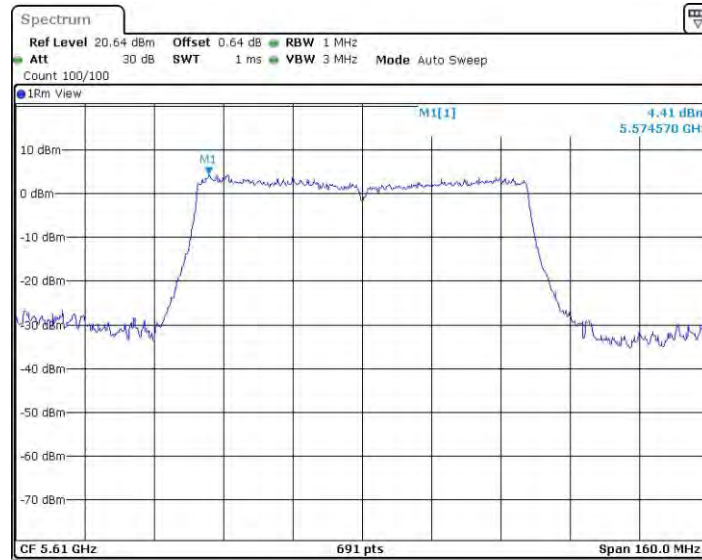
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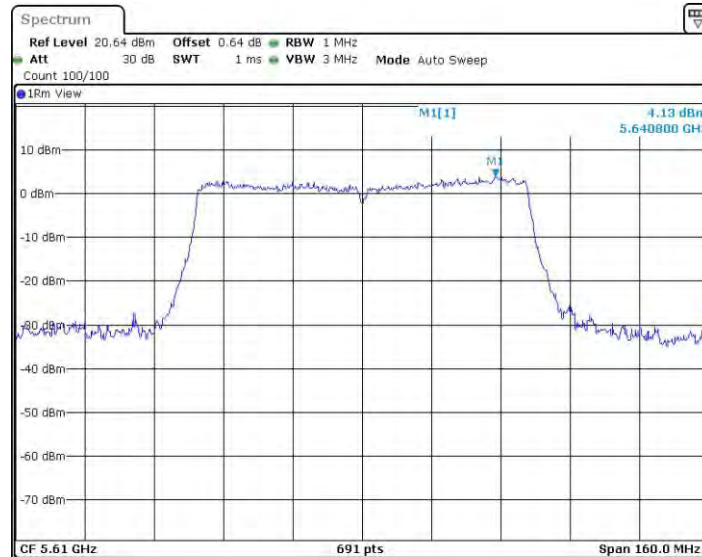
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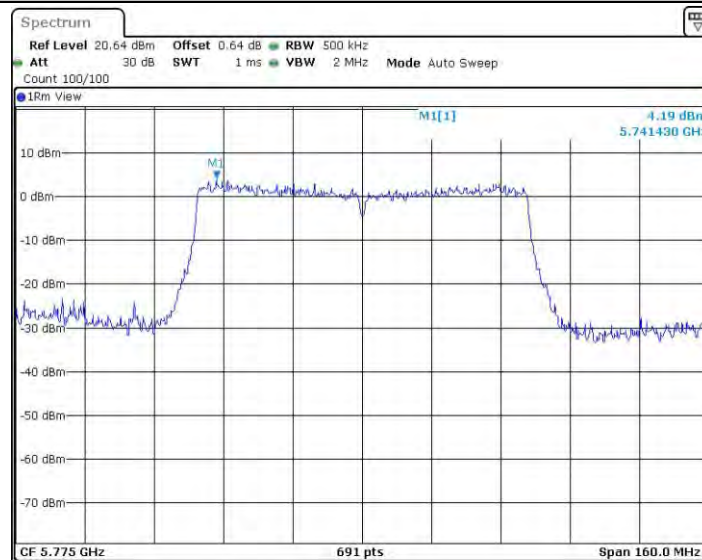
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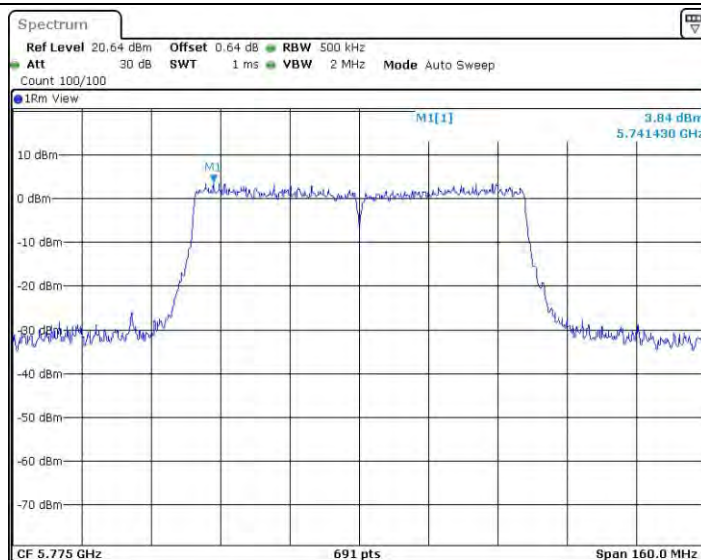
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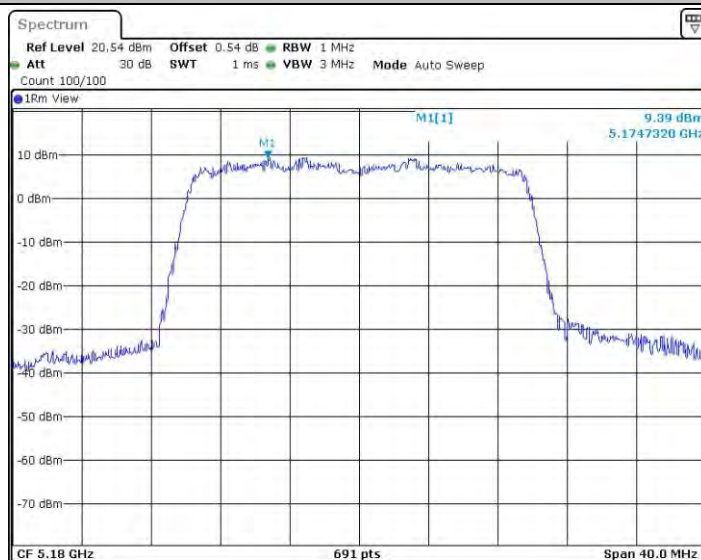
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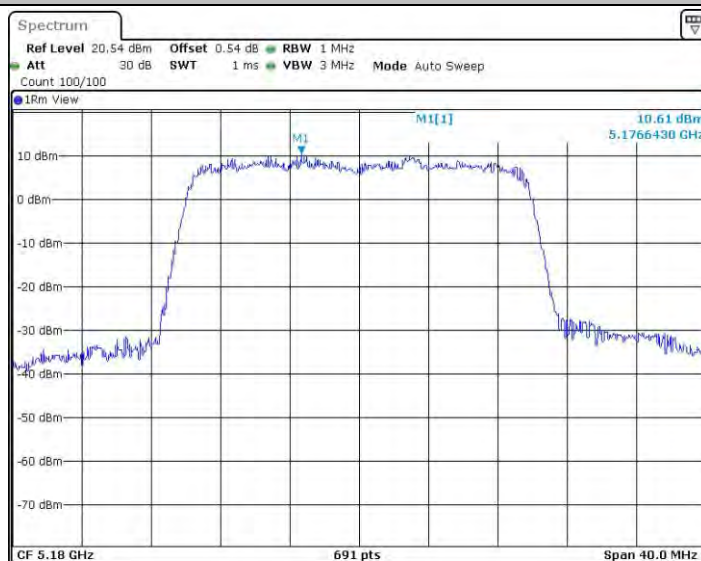
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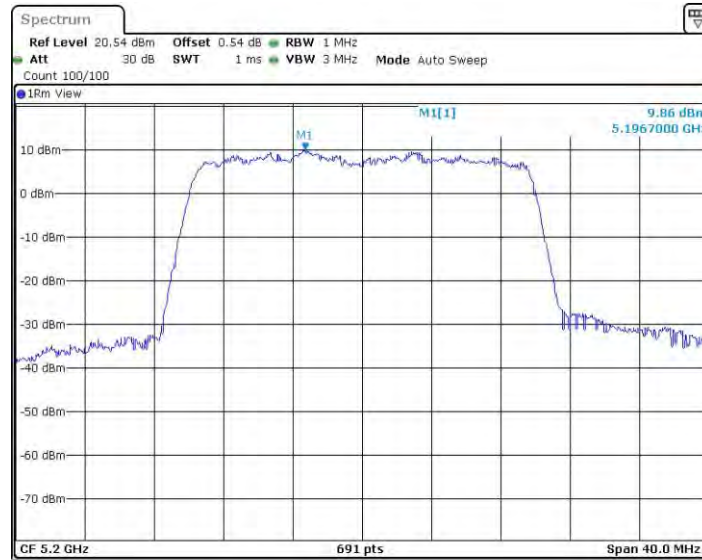
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11AX20SISO_Ant1_5180



11AX20SISO_Ant0_5200



11AX20SISO_Ant1_5200



11AX20SISO_Ant0_5240



11AX20SISO_Ant1_5240



11AX20SISO_Ant0_5260



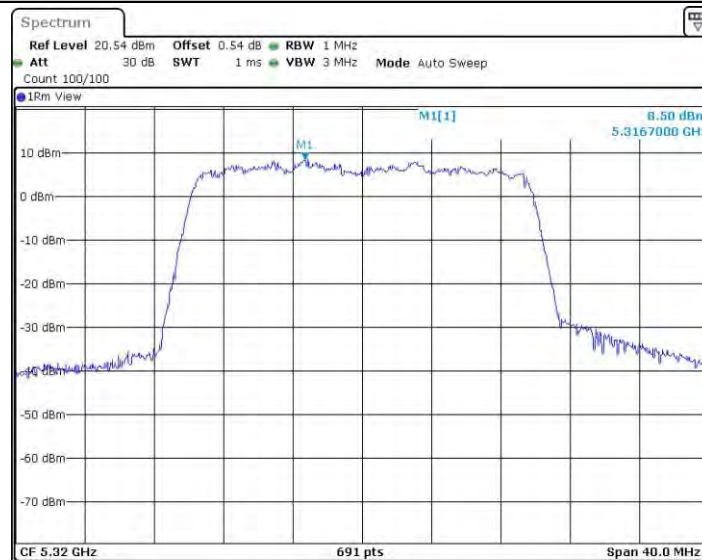
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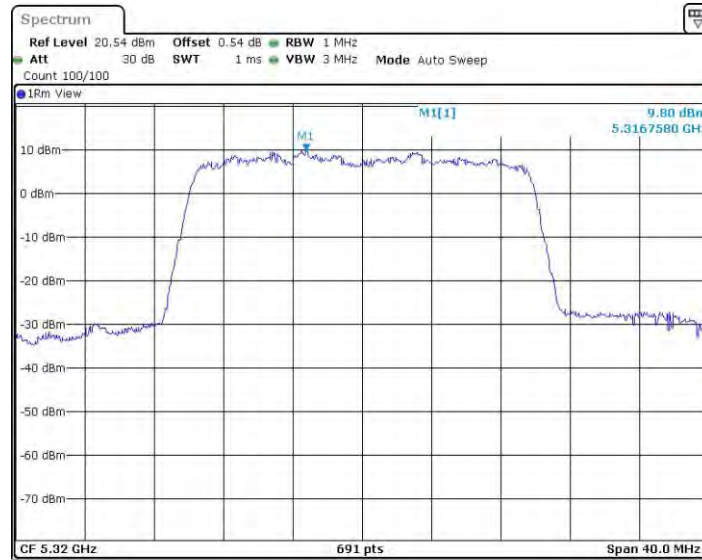
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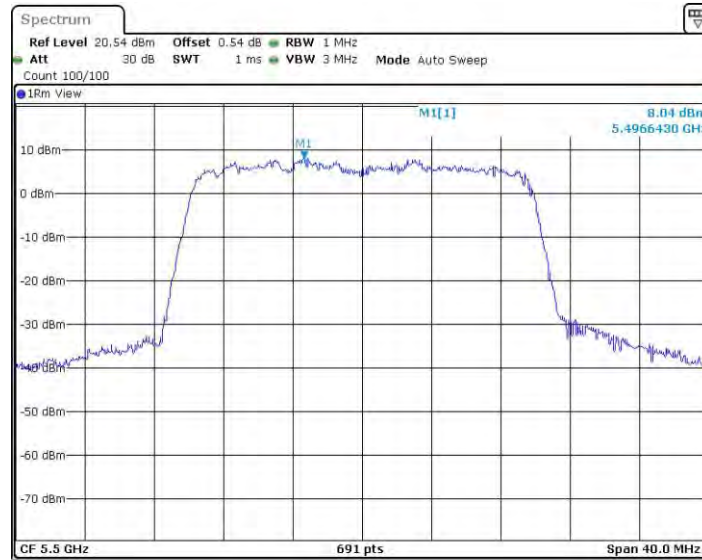
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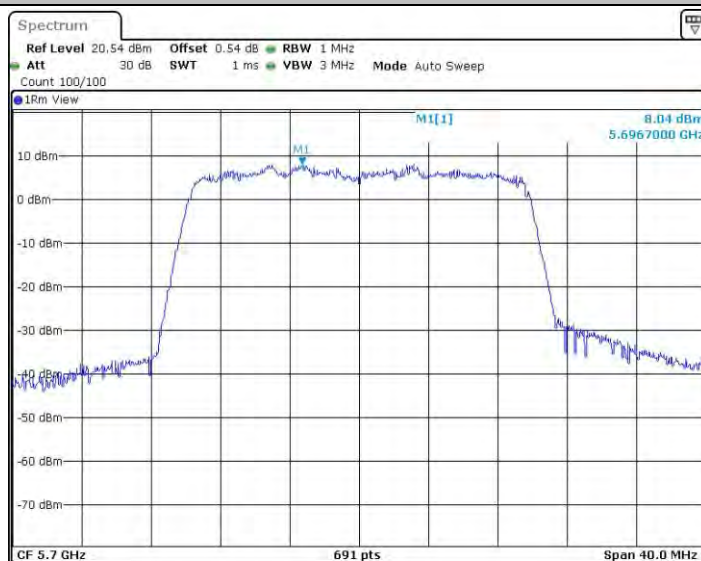
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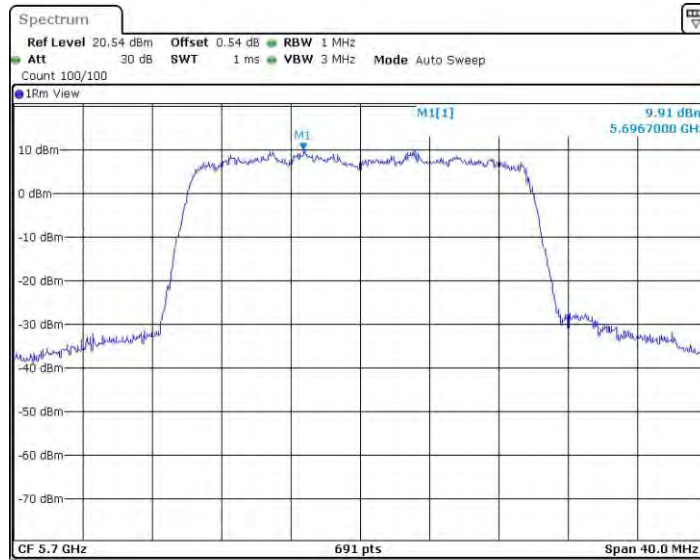
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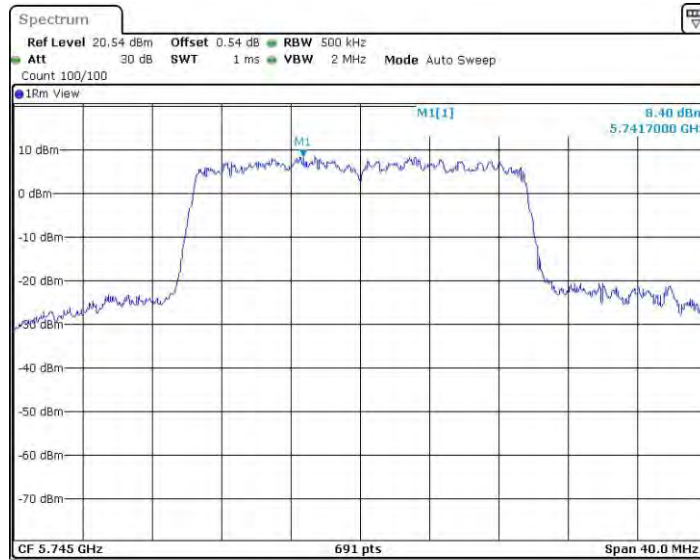
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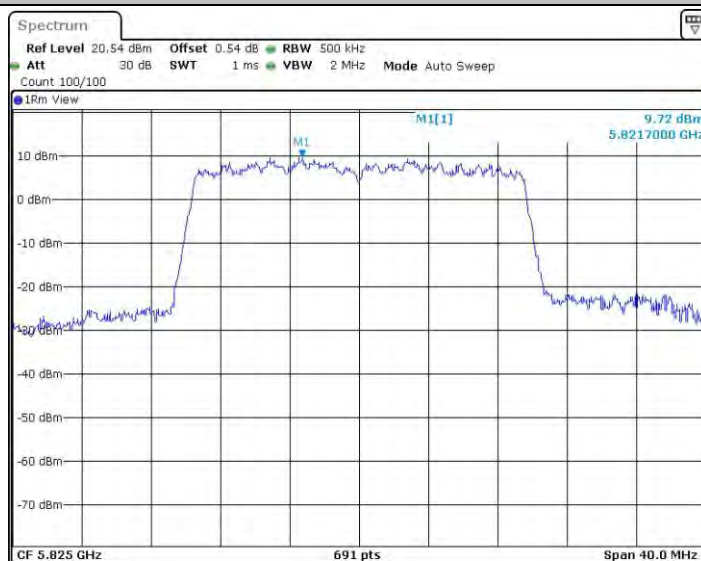
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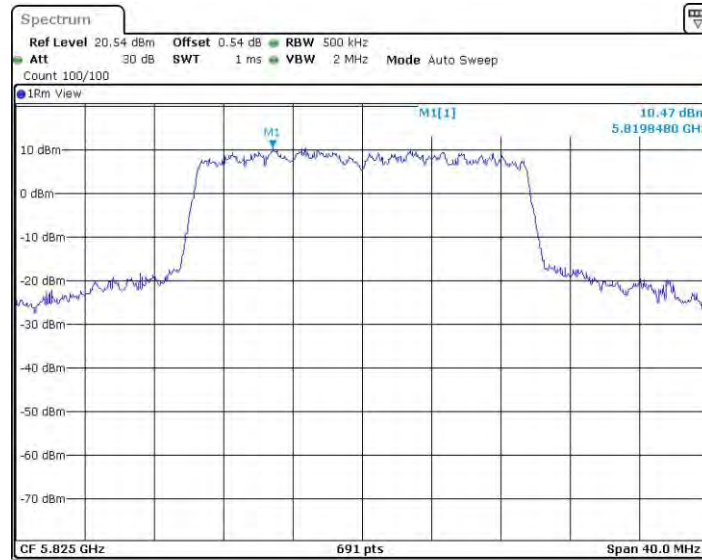
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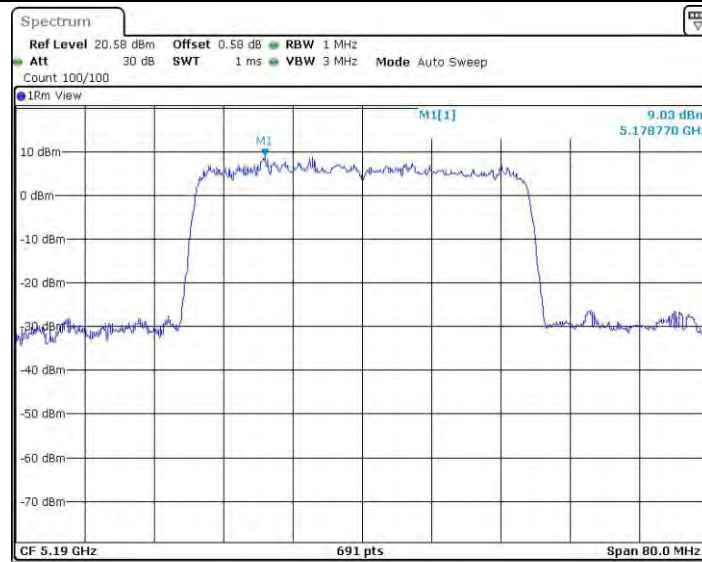
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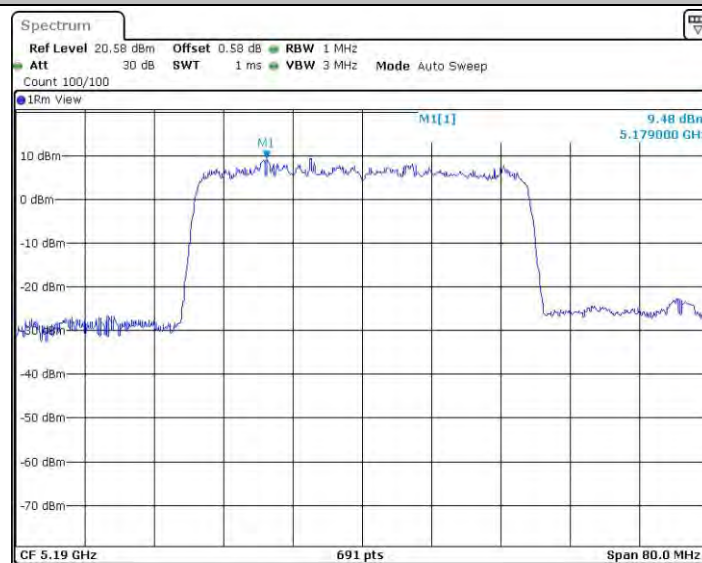
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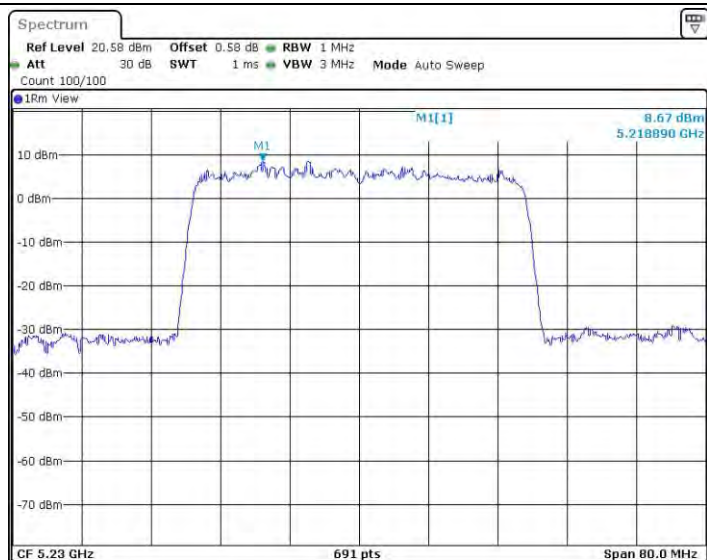
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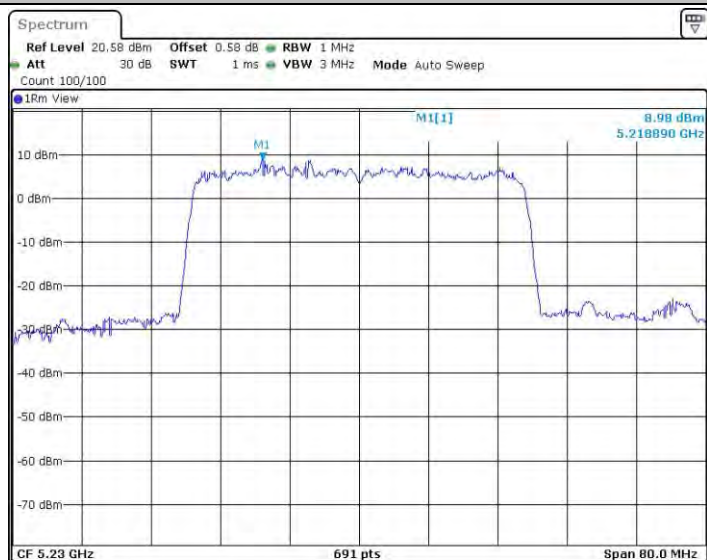
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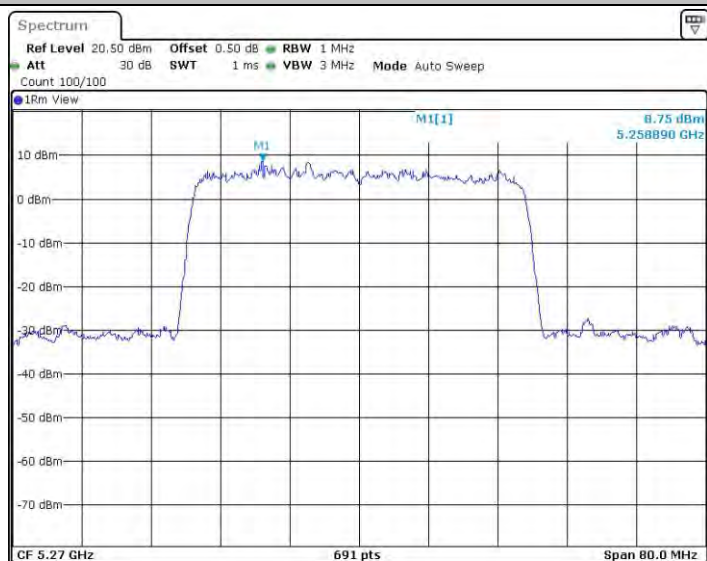
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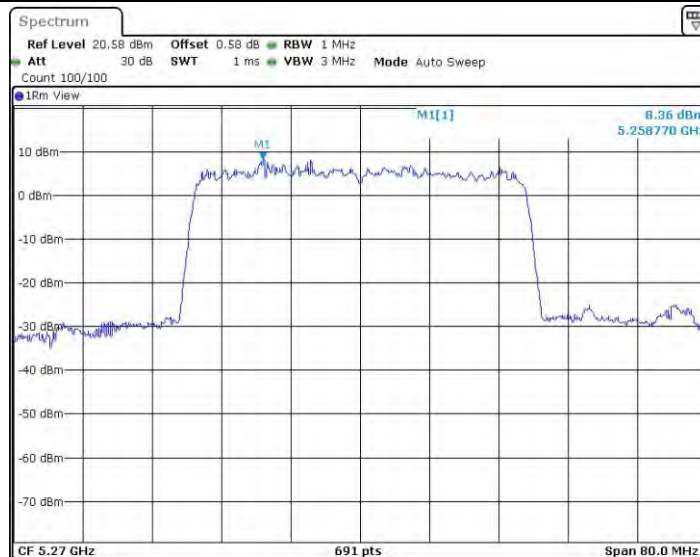
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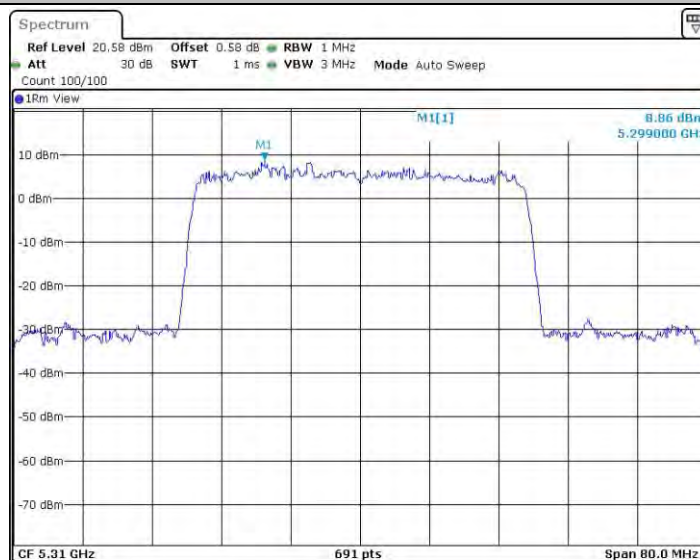
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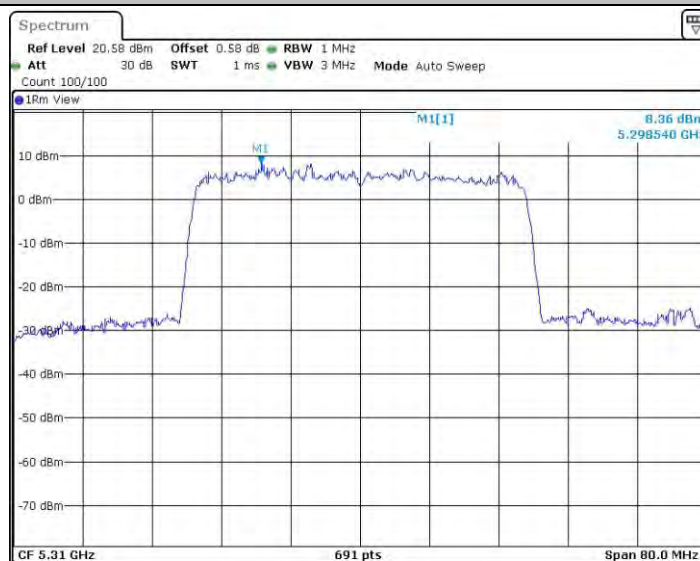
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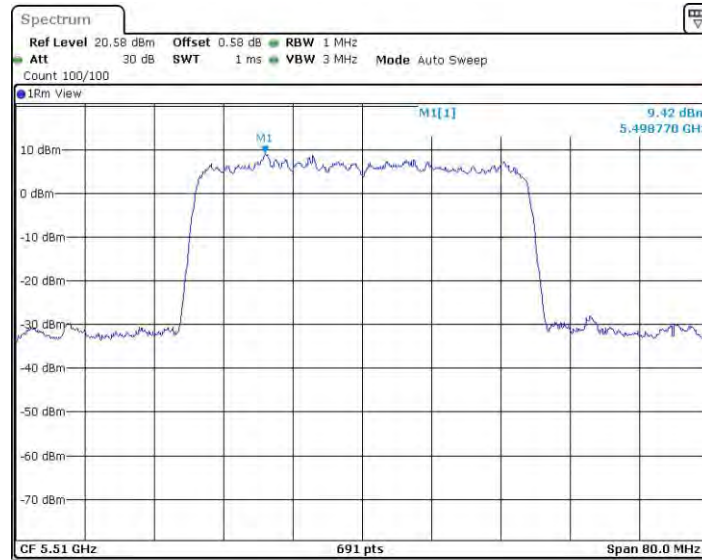
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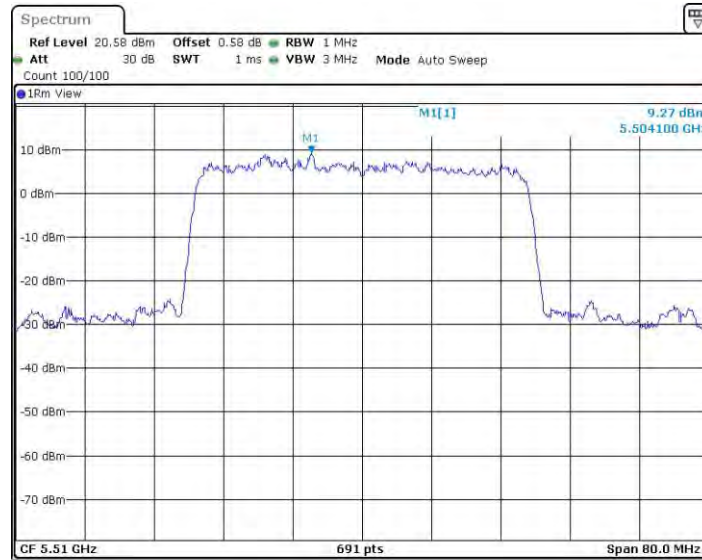
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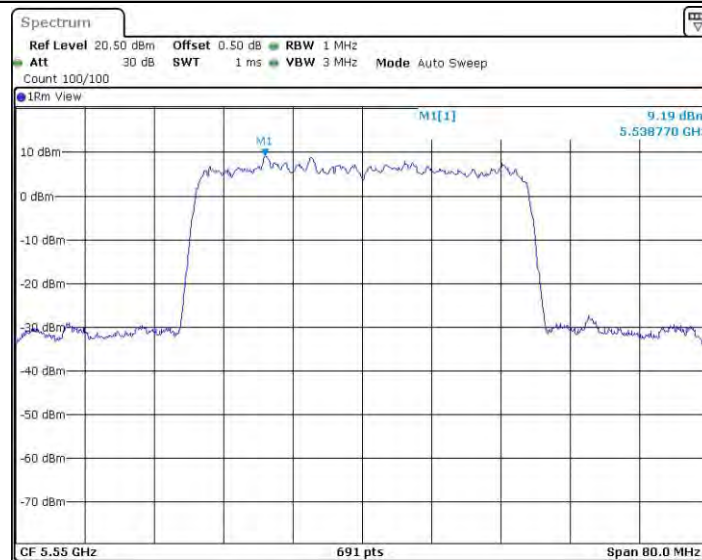
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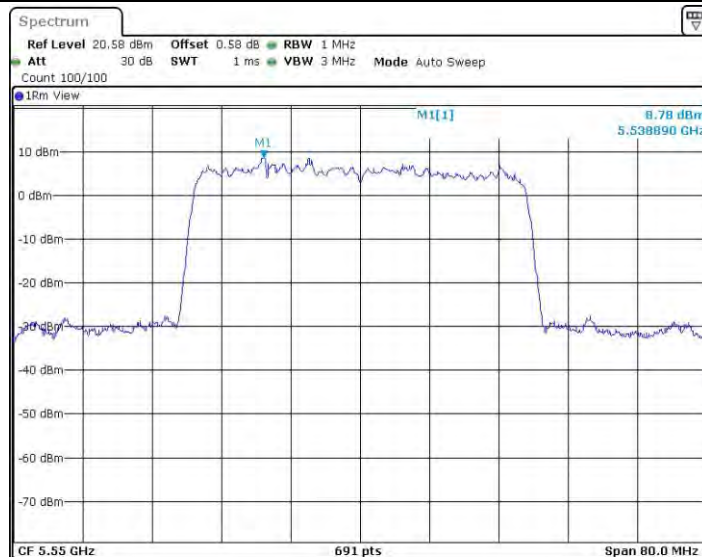
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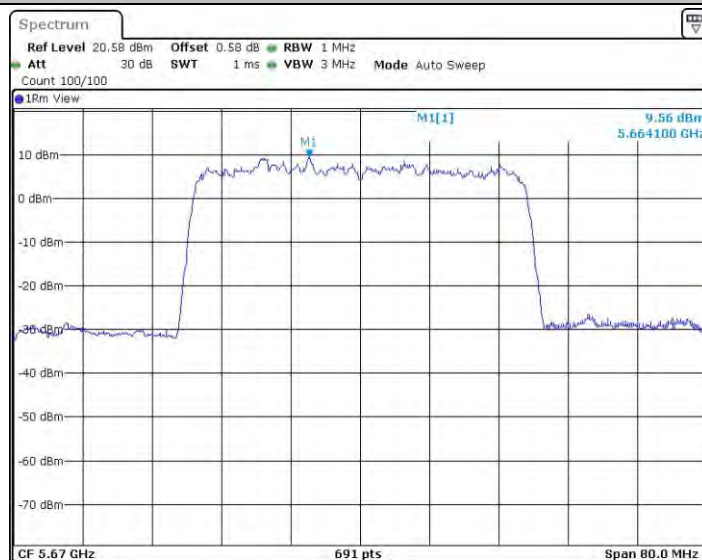
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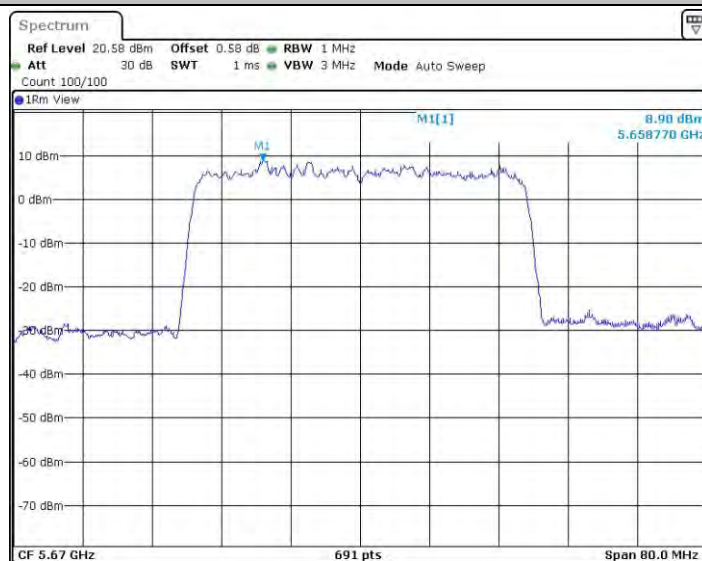
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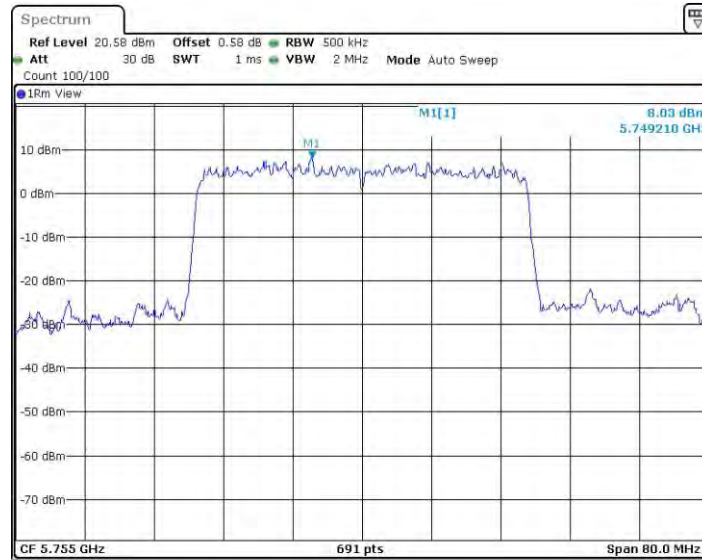
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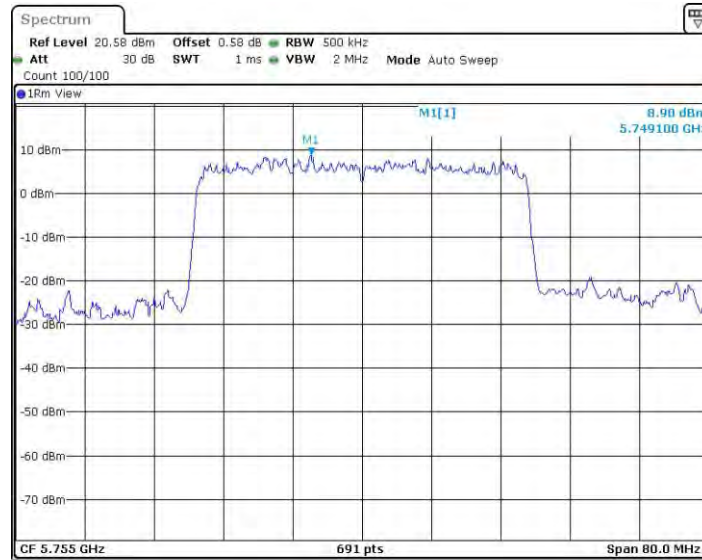
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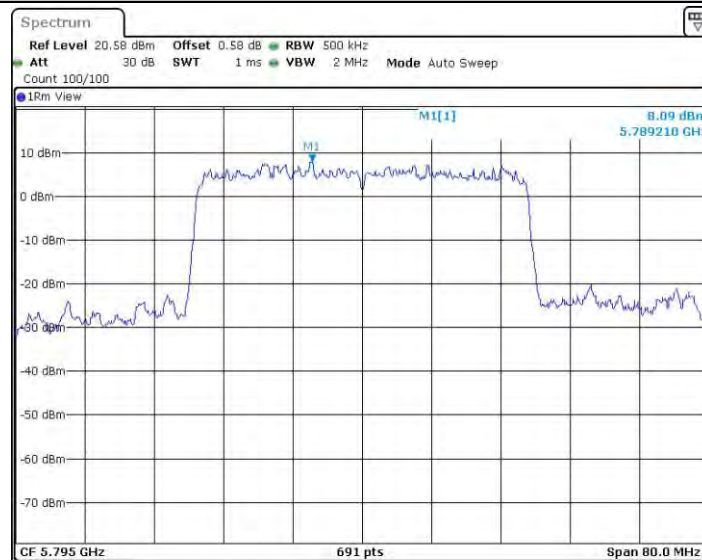
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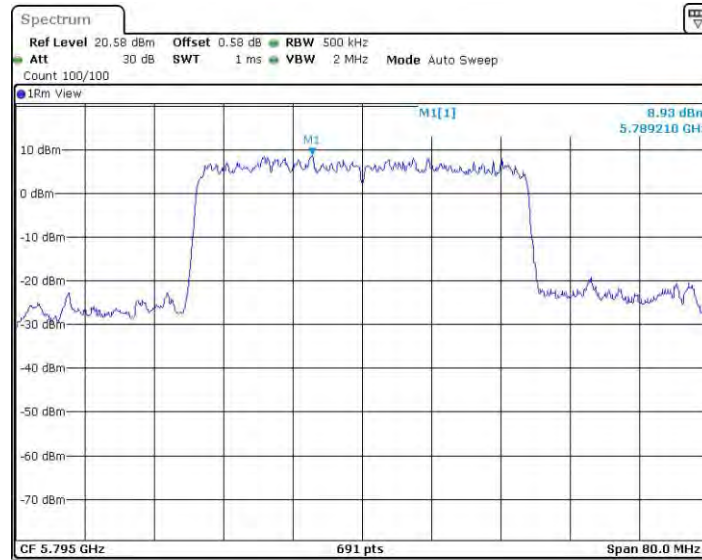
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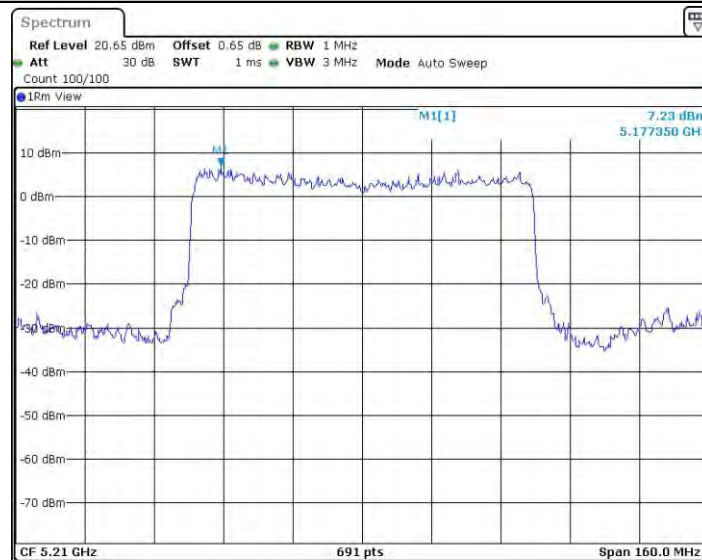
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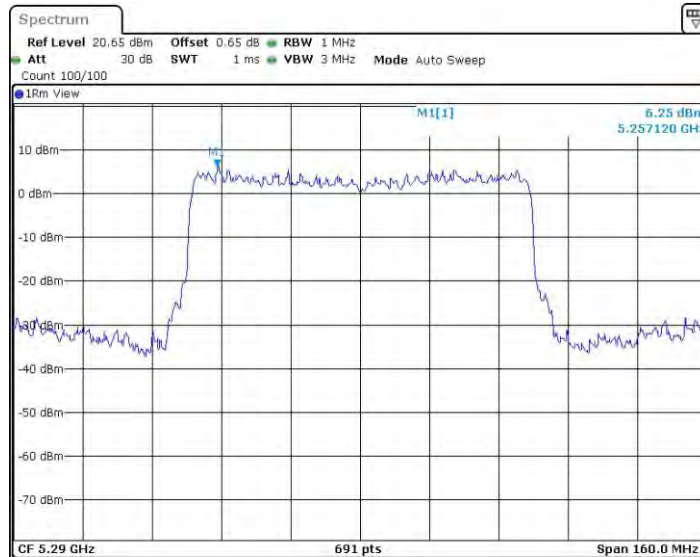
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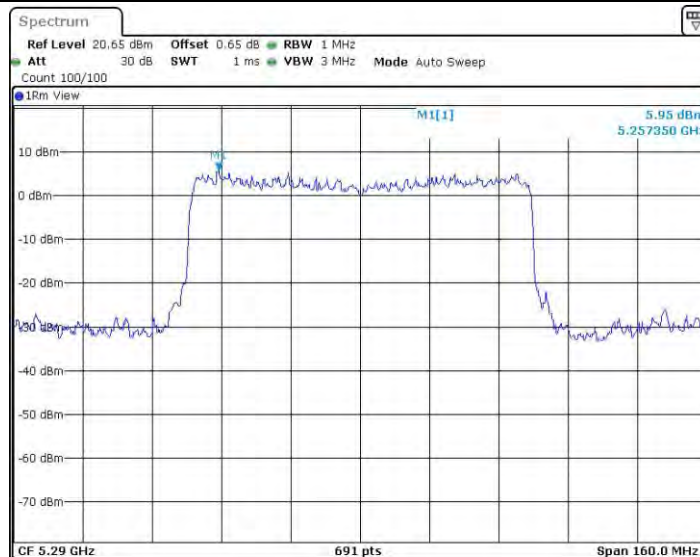
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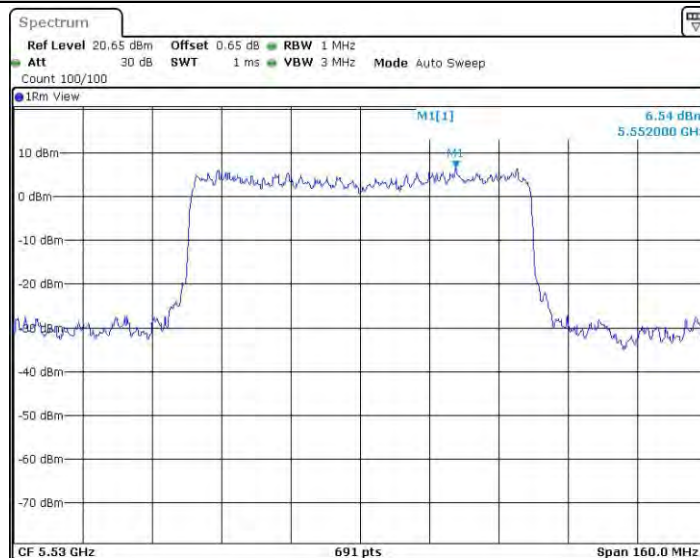
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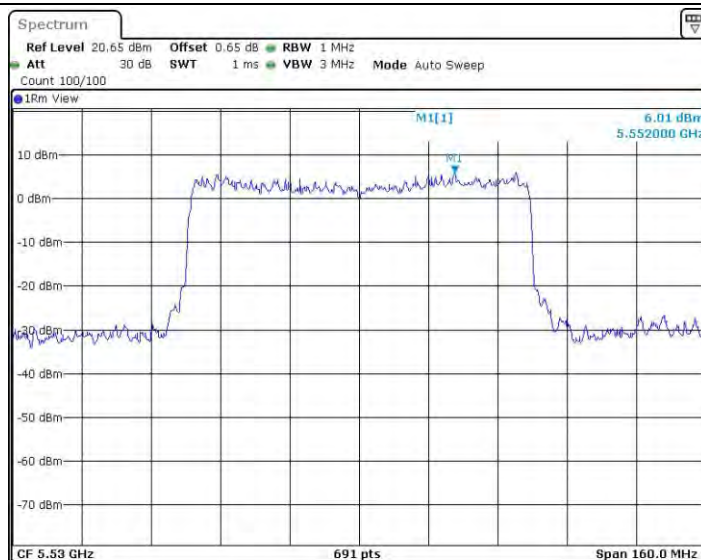
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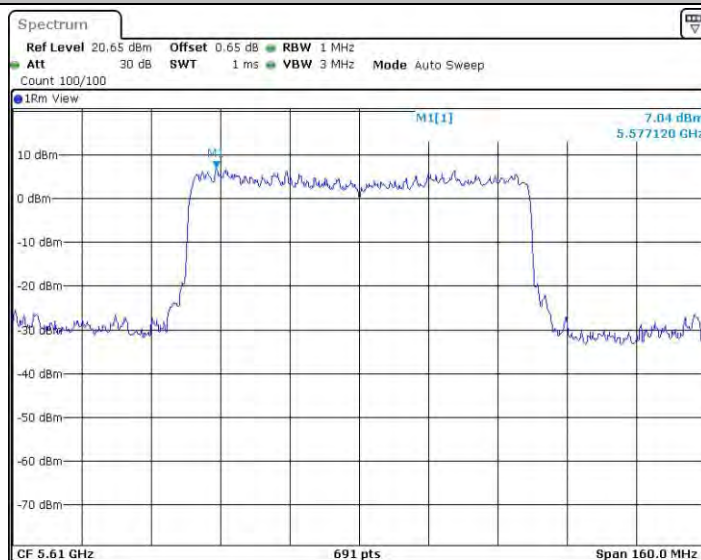
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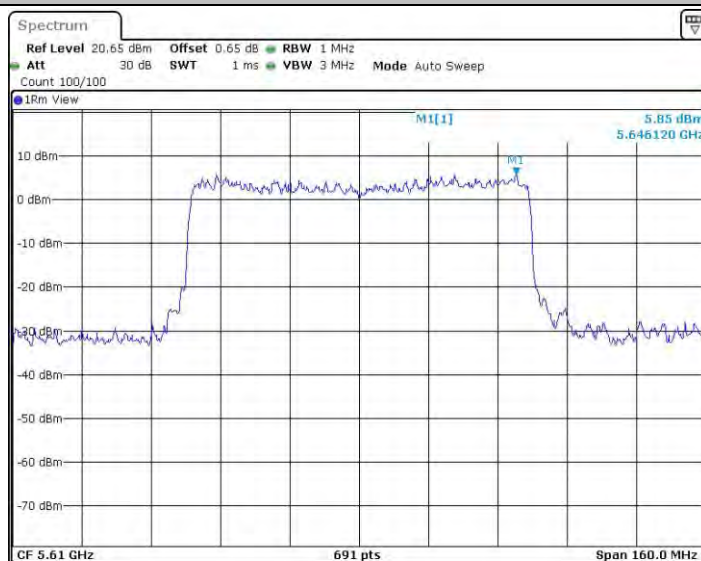
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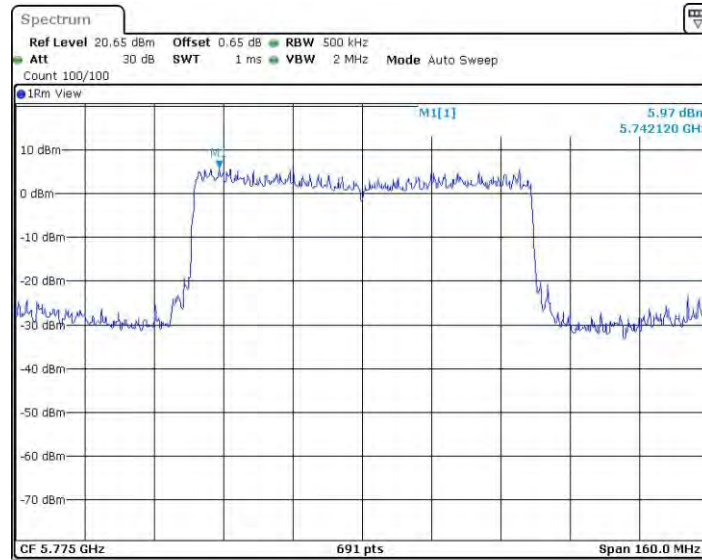
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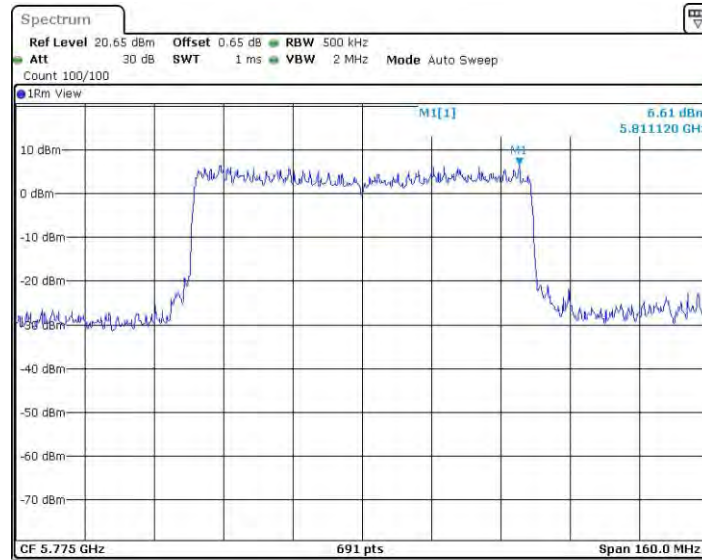
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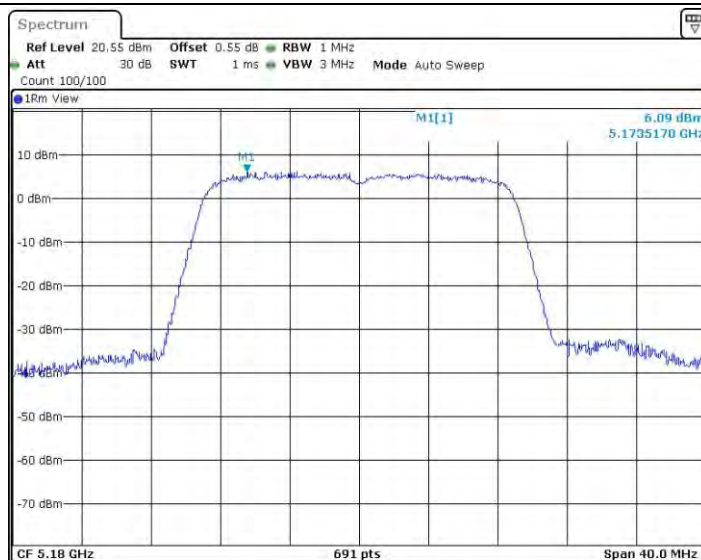
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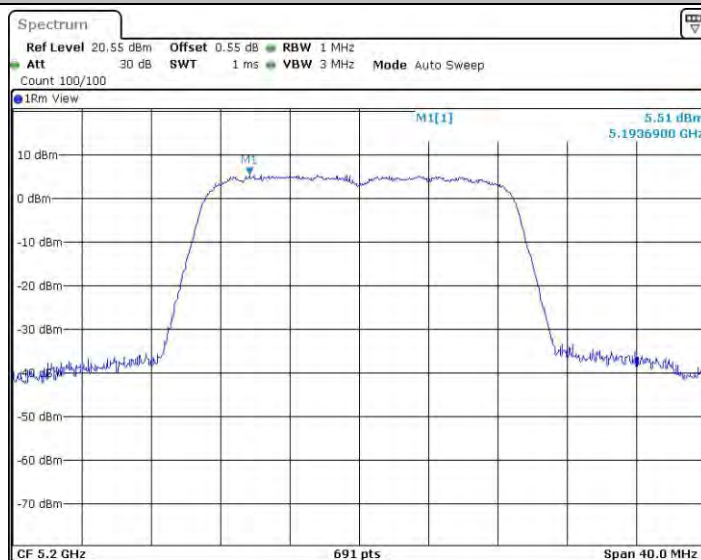
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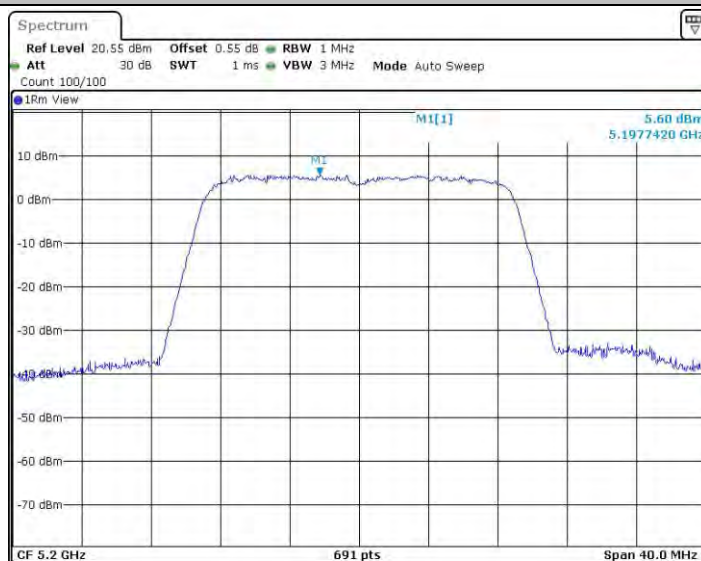
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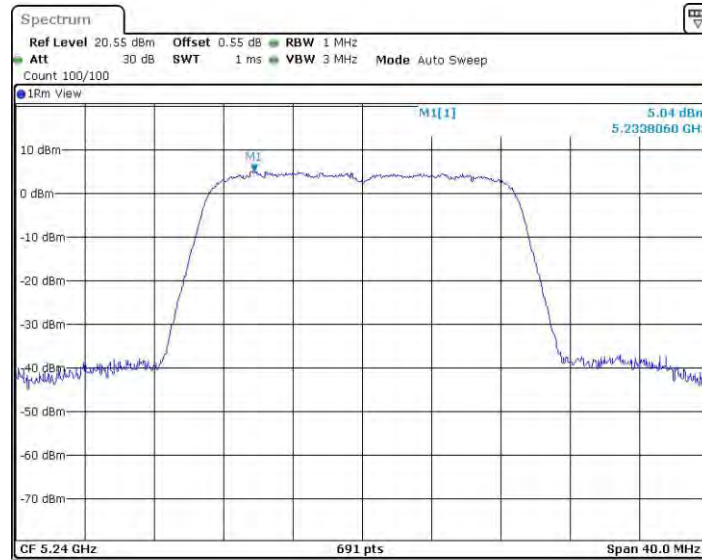
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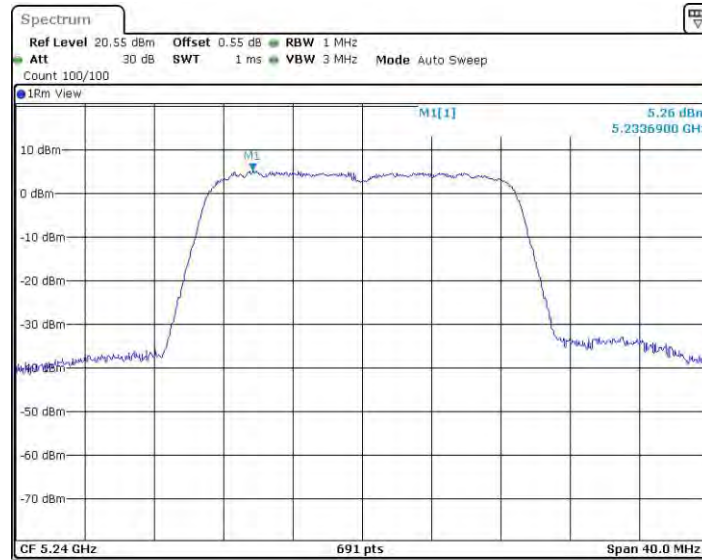
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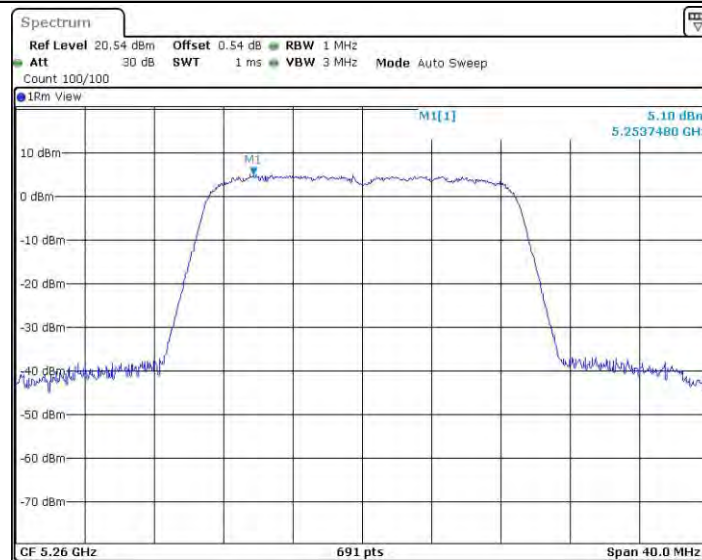
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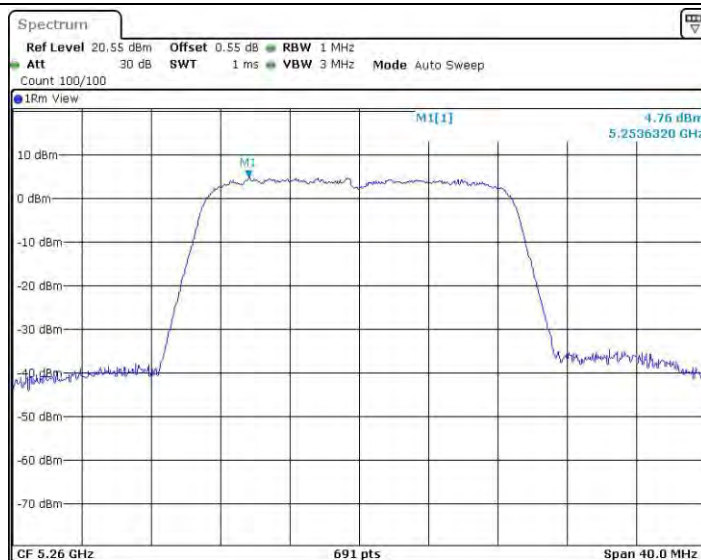
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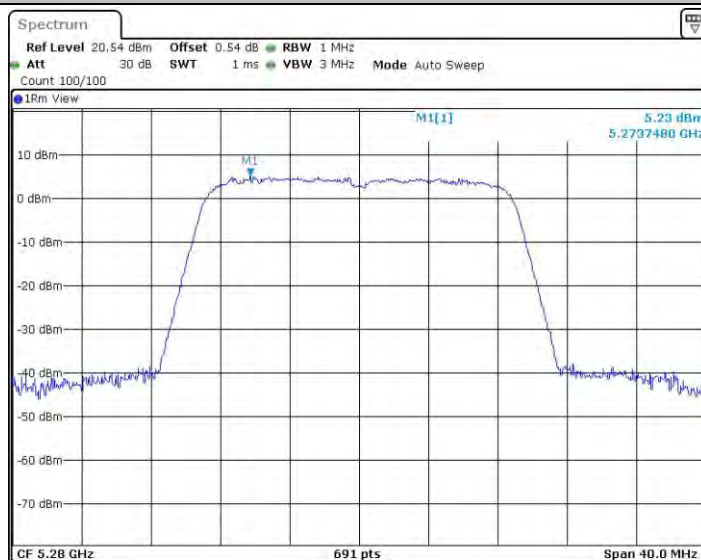
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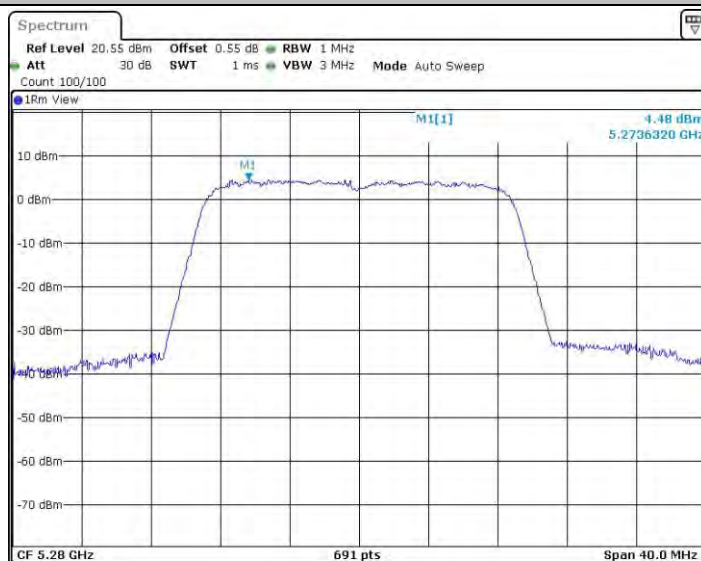
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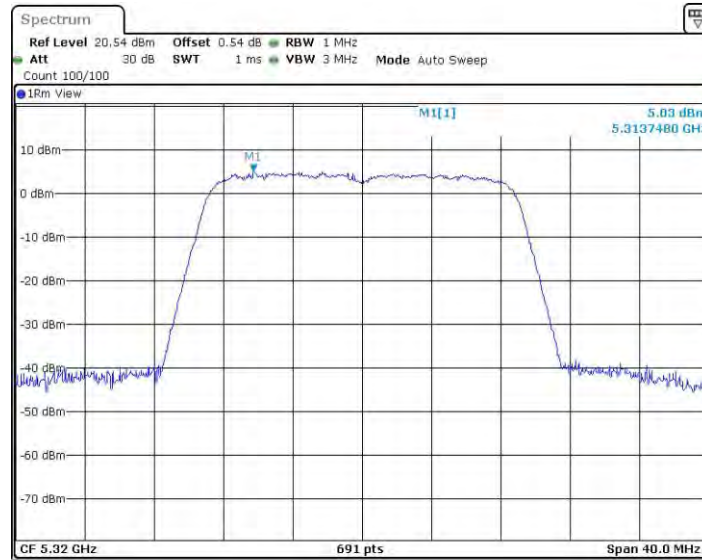
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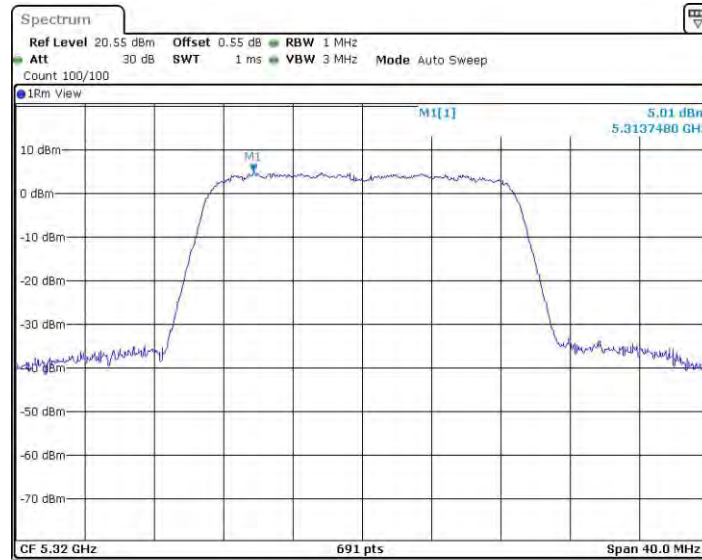
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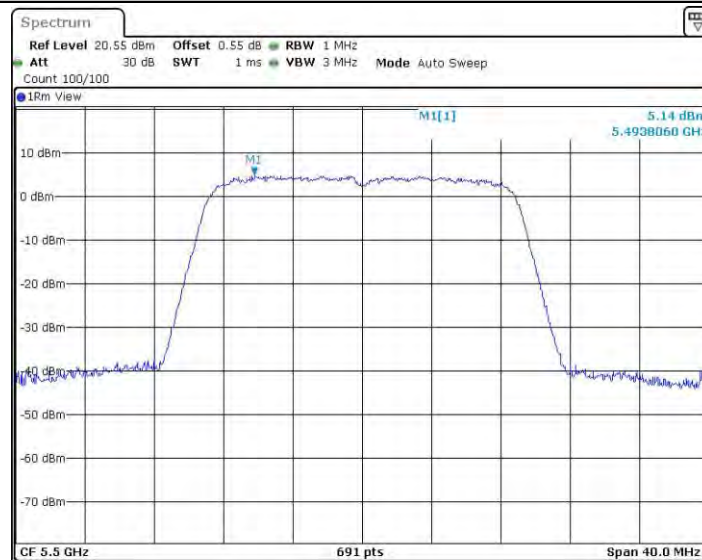
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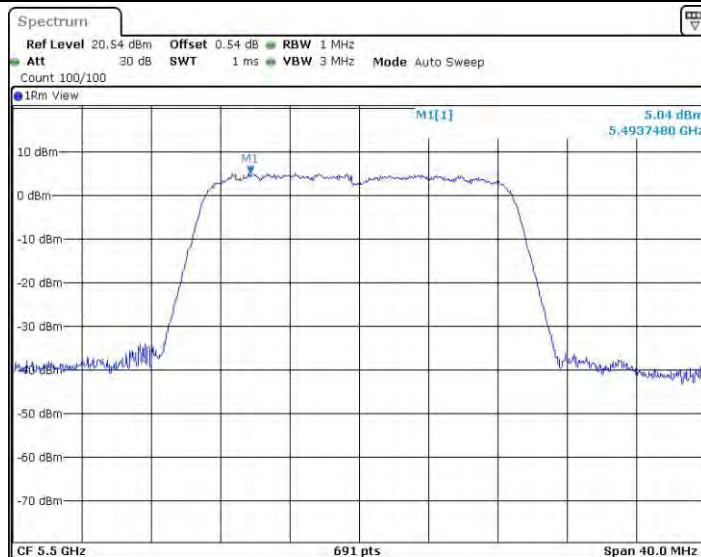
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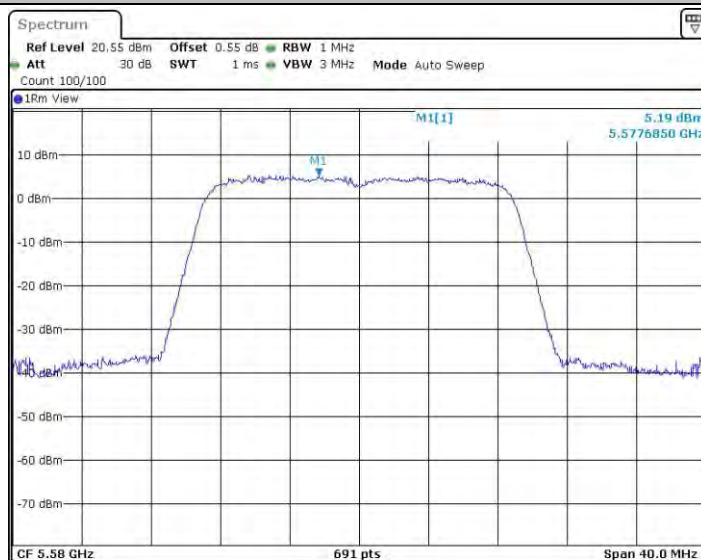
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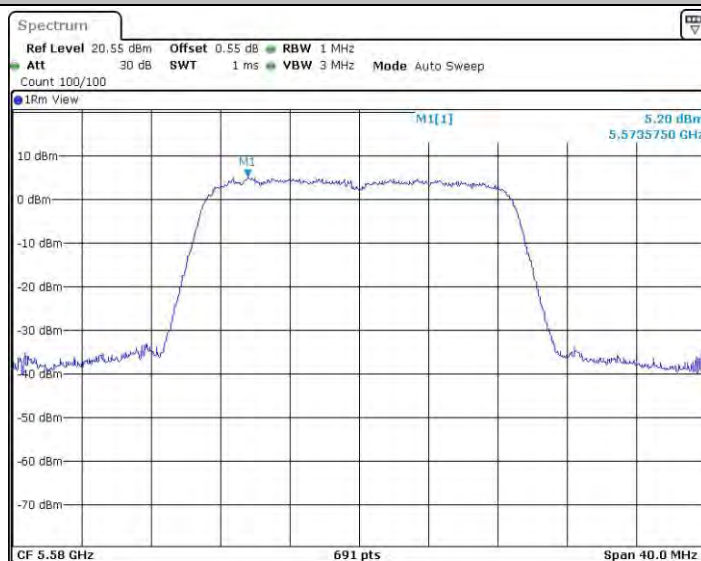
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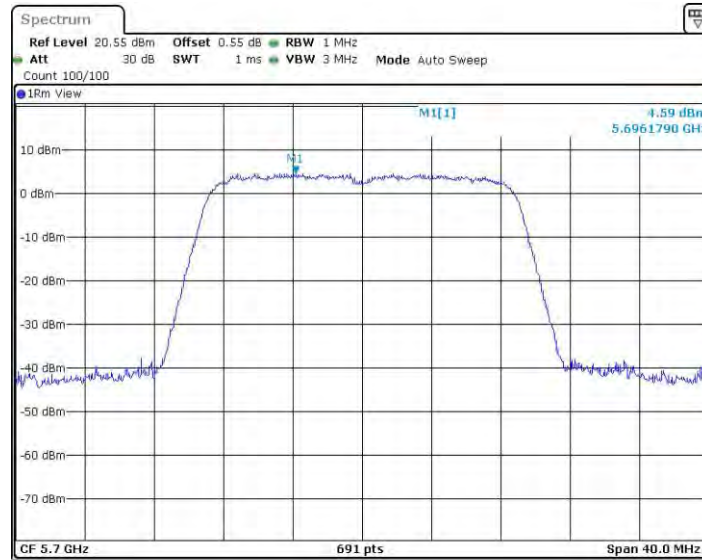
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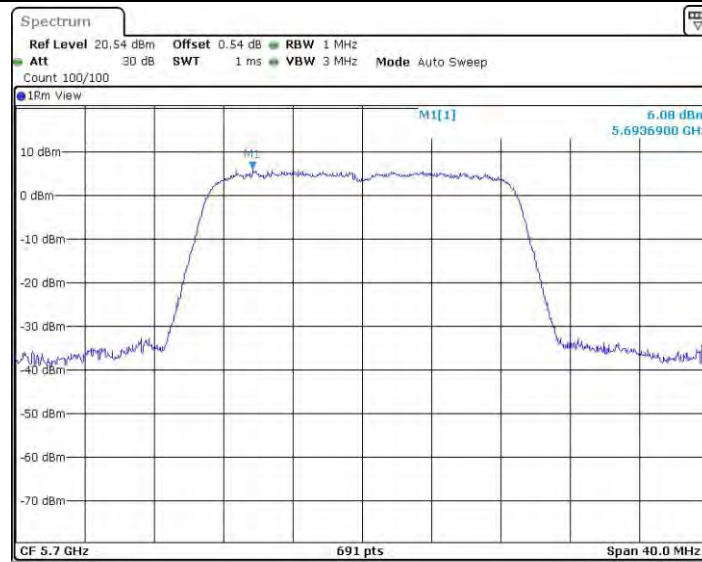
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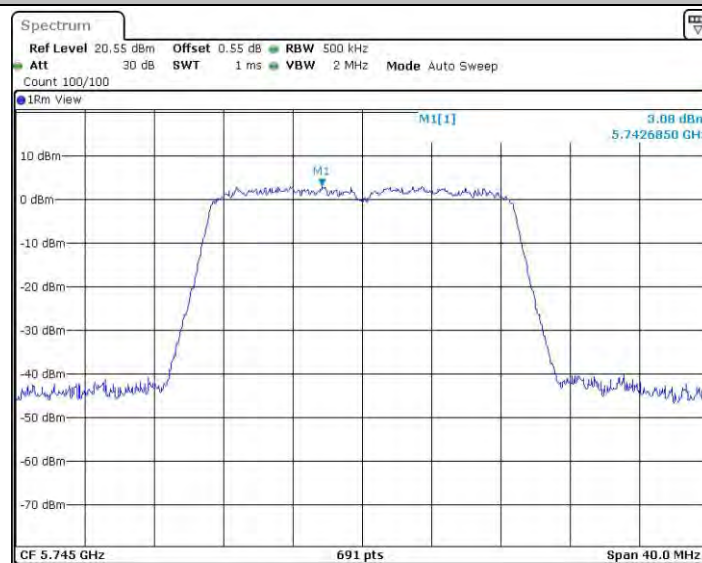
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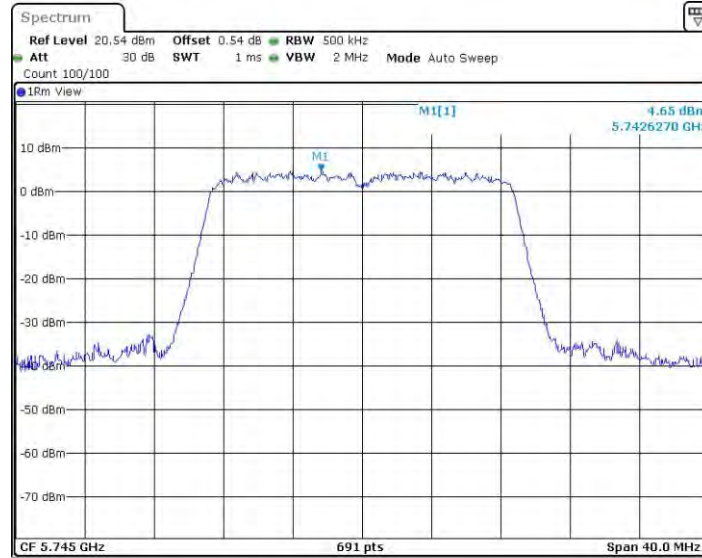
11N20MIMO_Ant0_5700



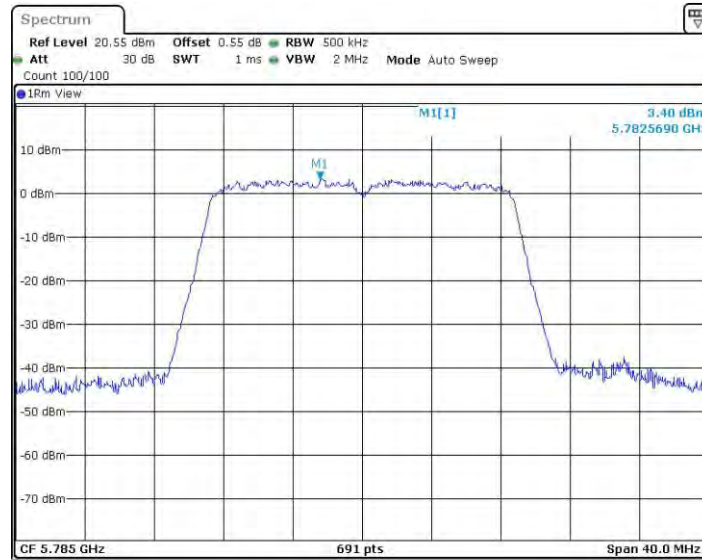
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11N20MIMO_Ant0_5745



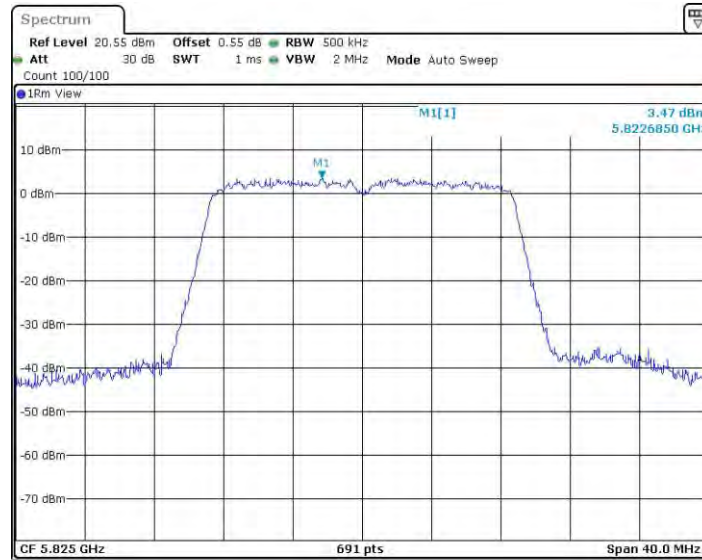
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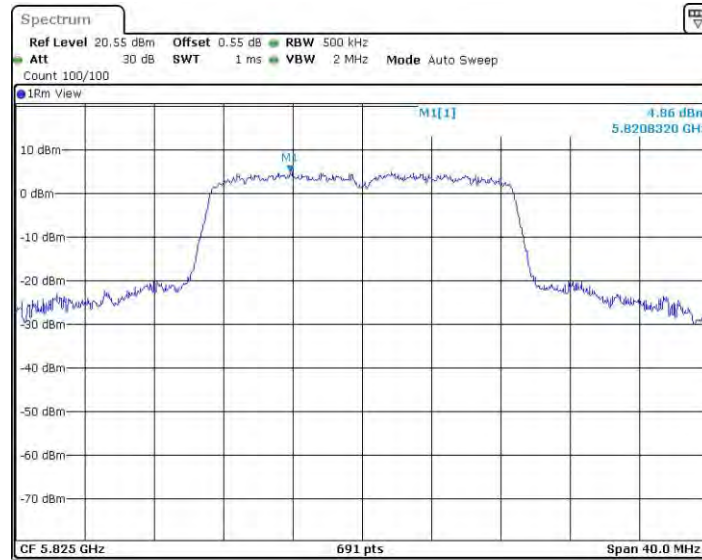
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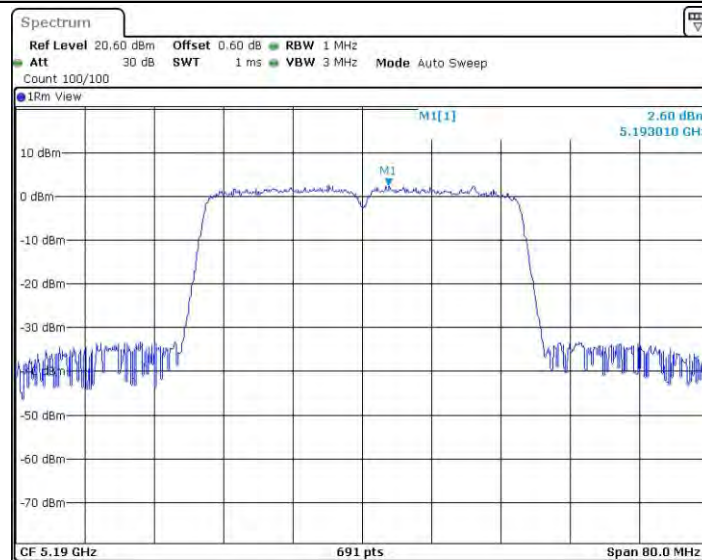
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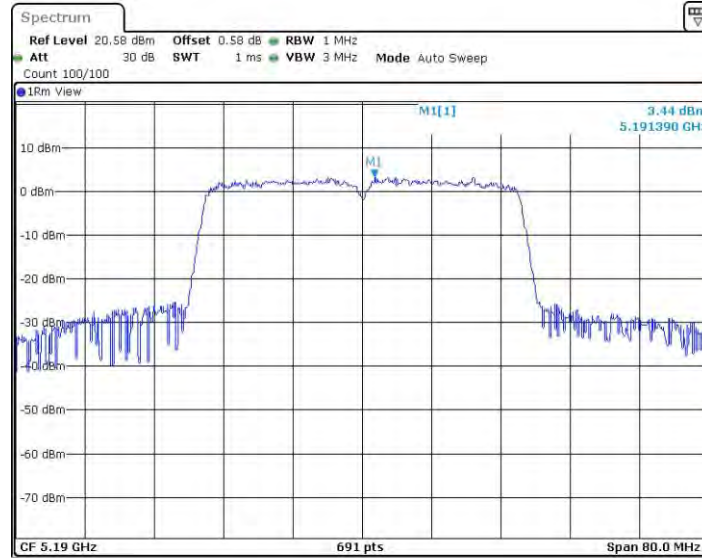
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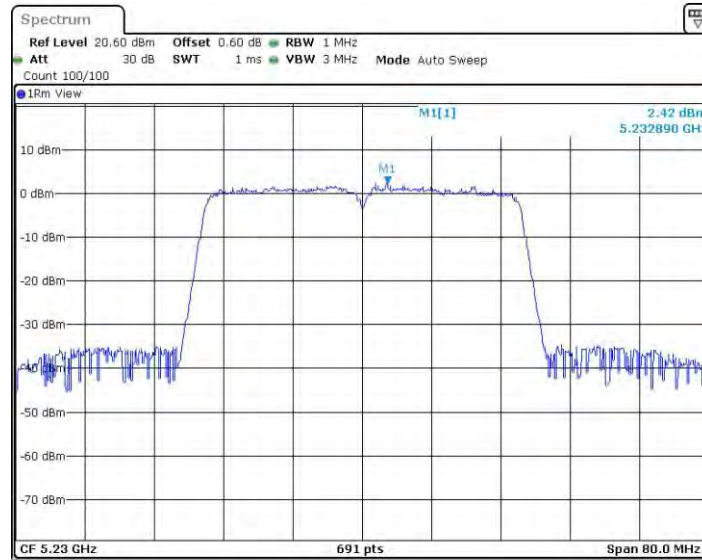
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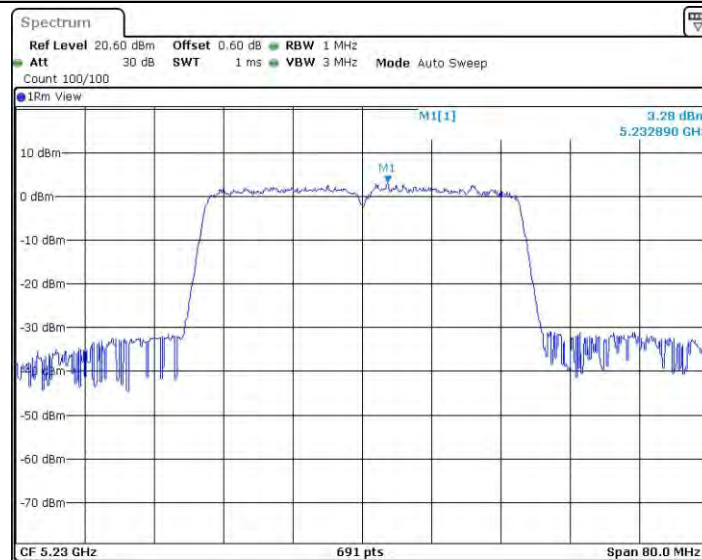
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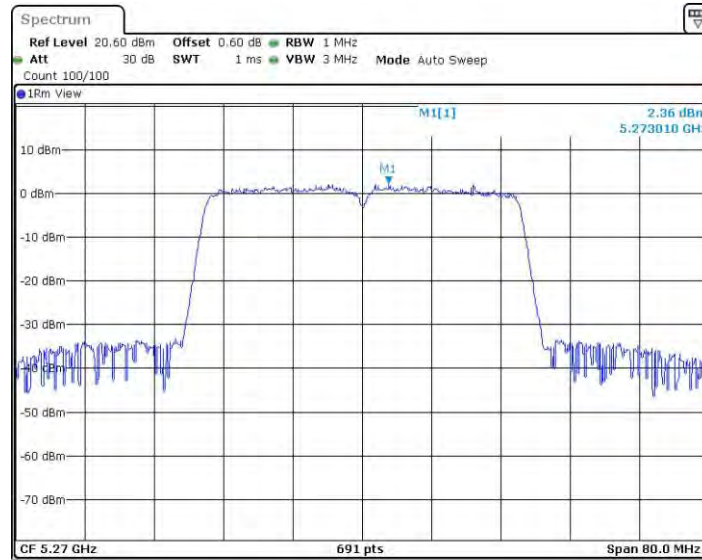
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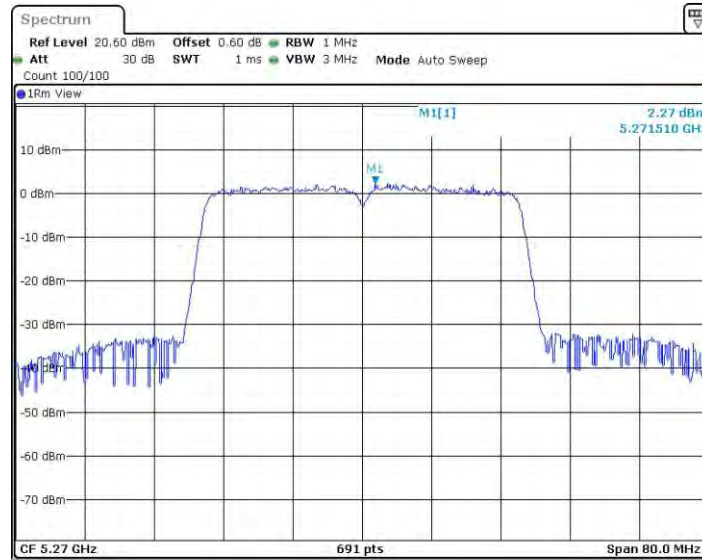
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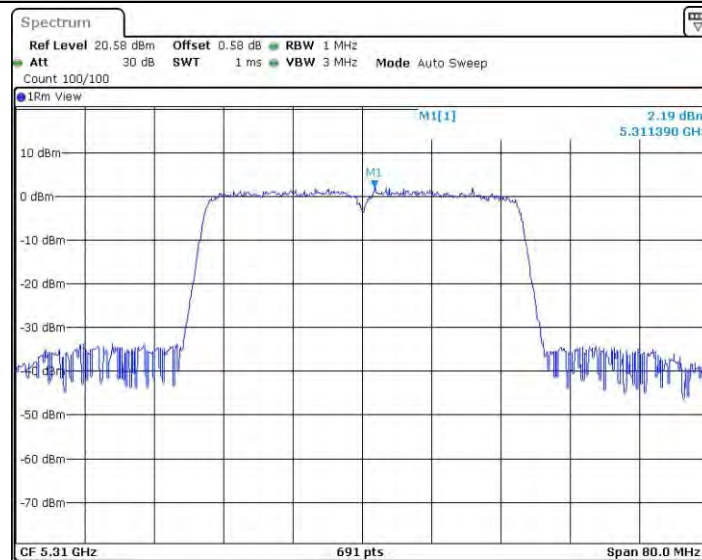
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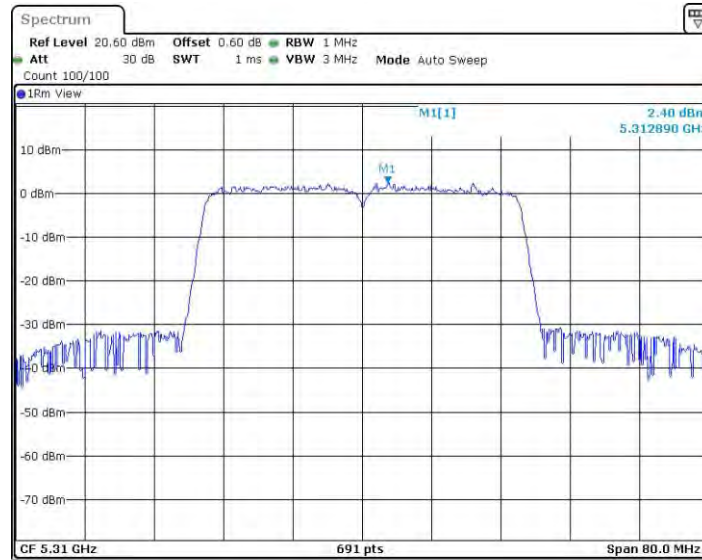
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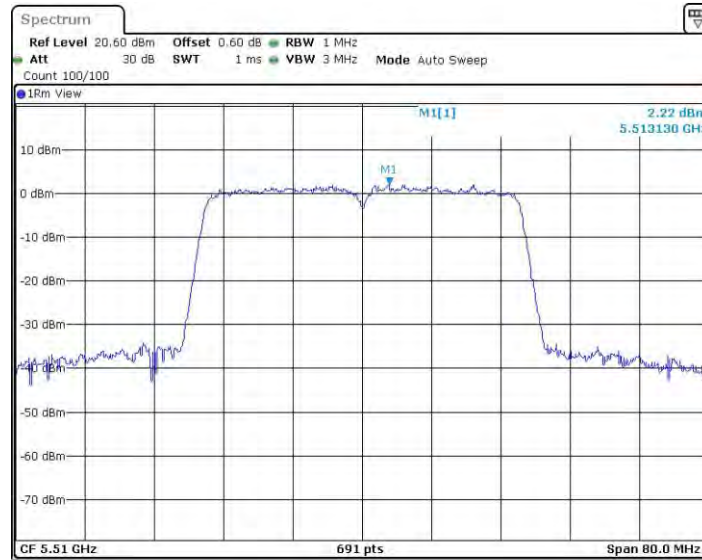
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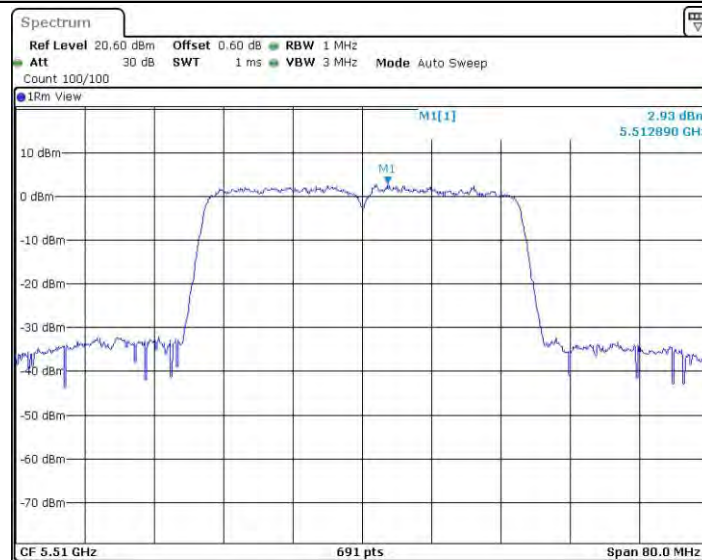
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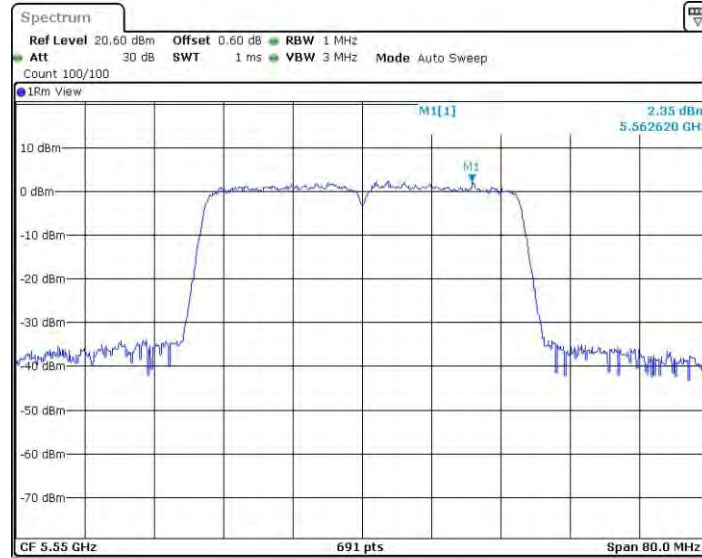
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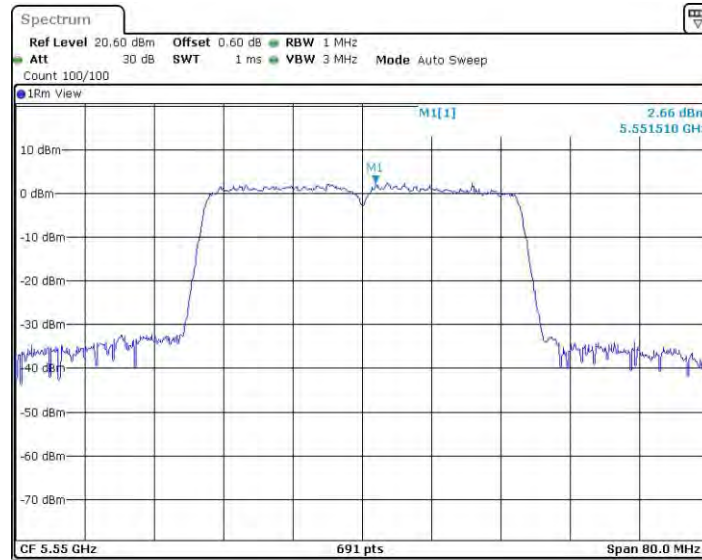
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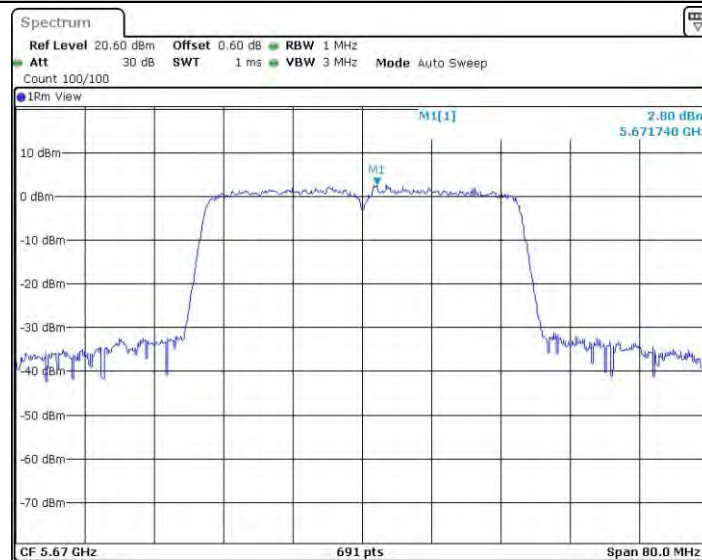
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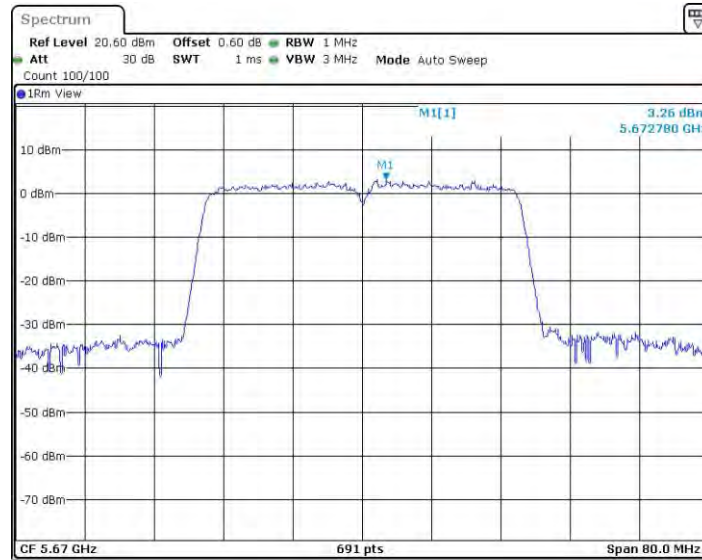
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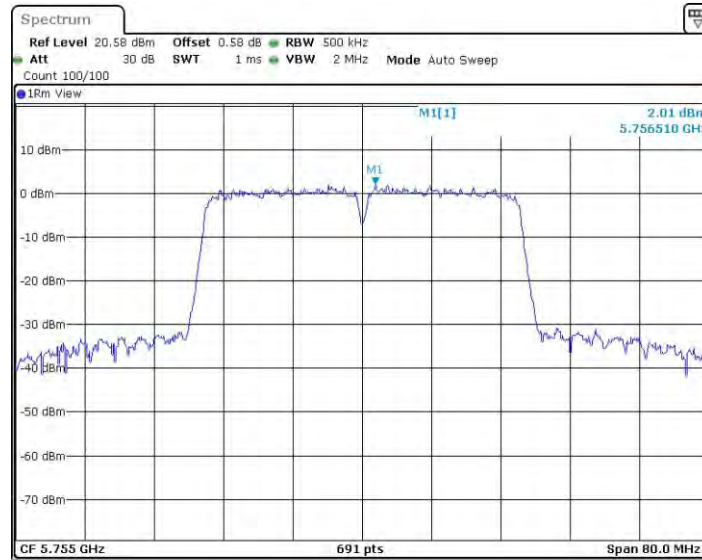
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11N40MIMO_Ant0_5670



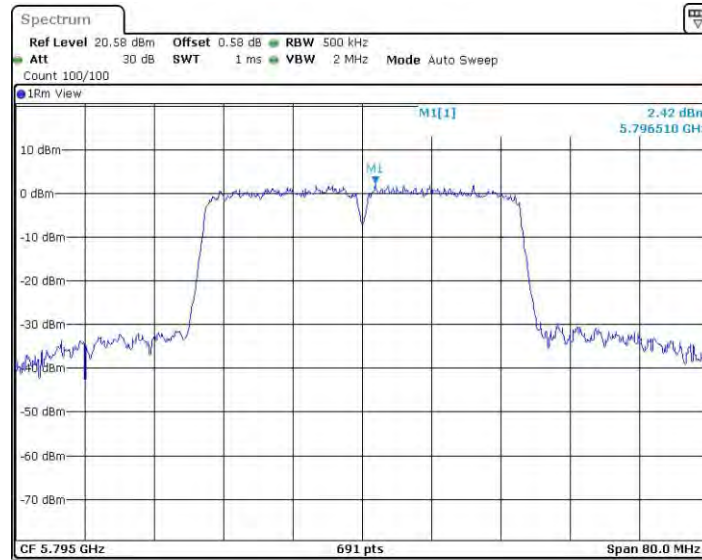
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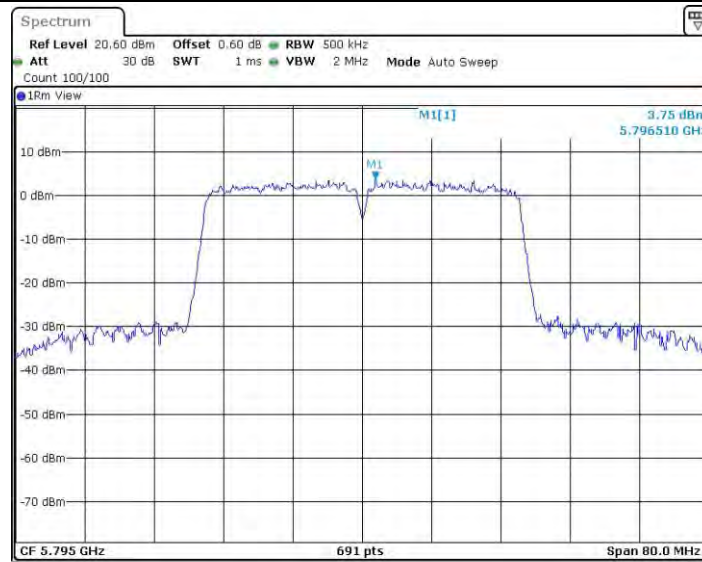
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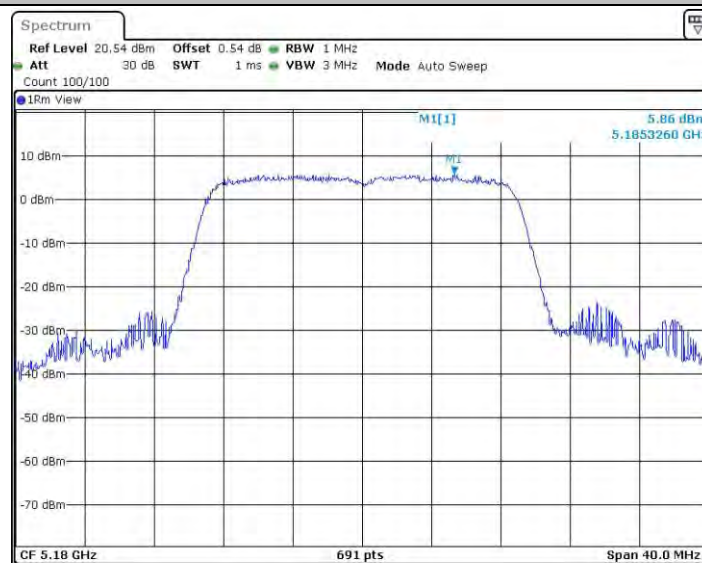
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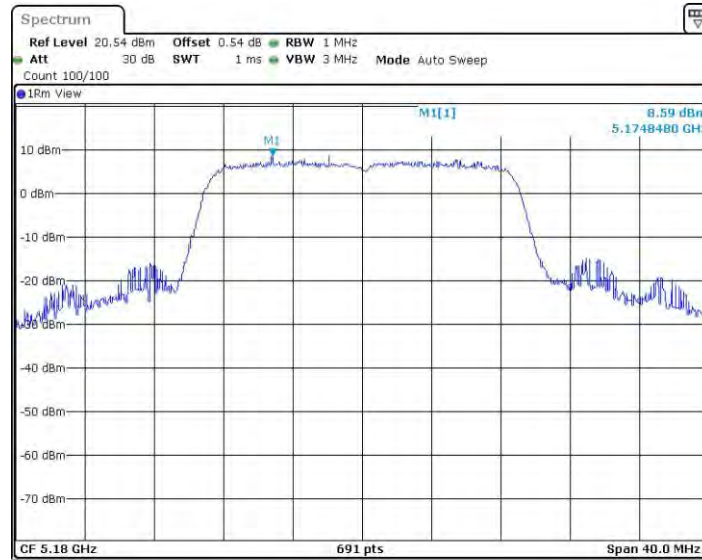
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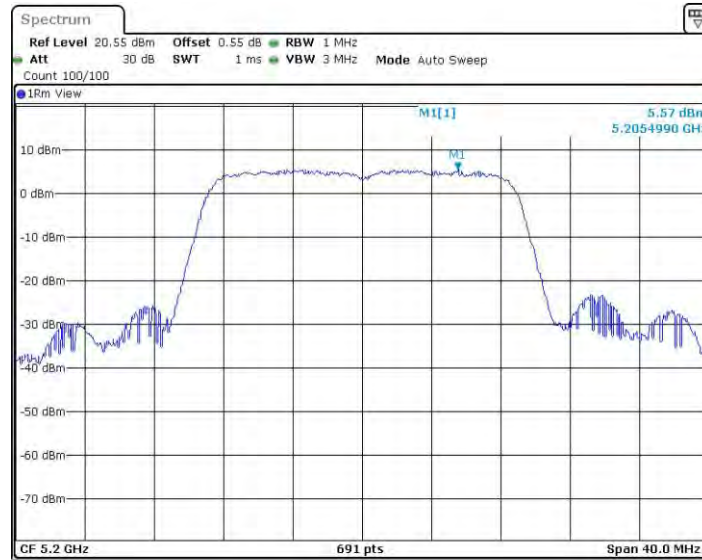
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11AC20MIMO_Ant0_5180



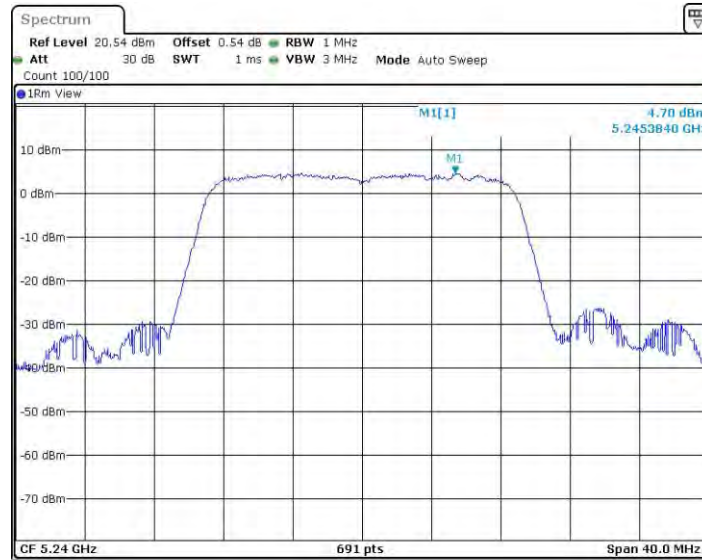
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11AC20MIMO_Ant0_5200



11AC20MIMO_Ant1_5200



11AC20MIMO_Ant0_5240



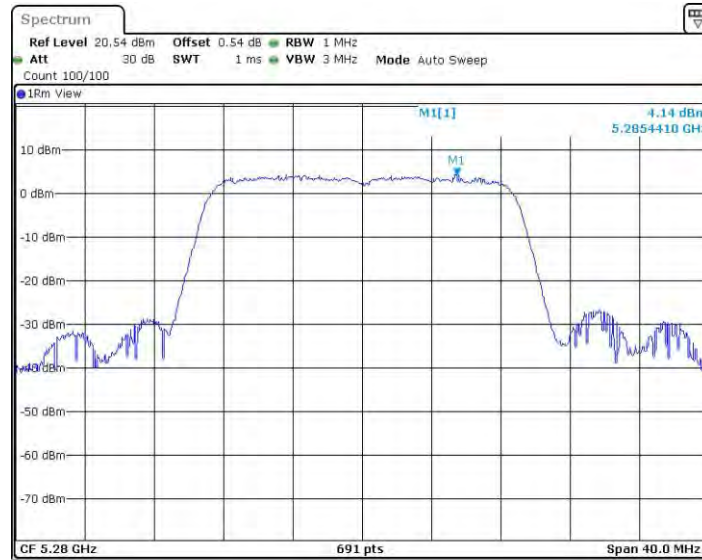
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11AC20MIMO_Ant0_5260



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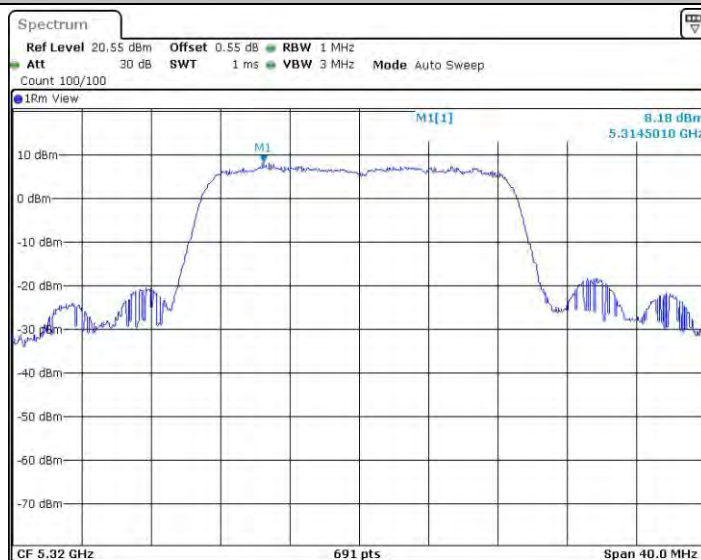
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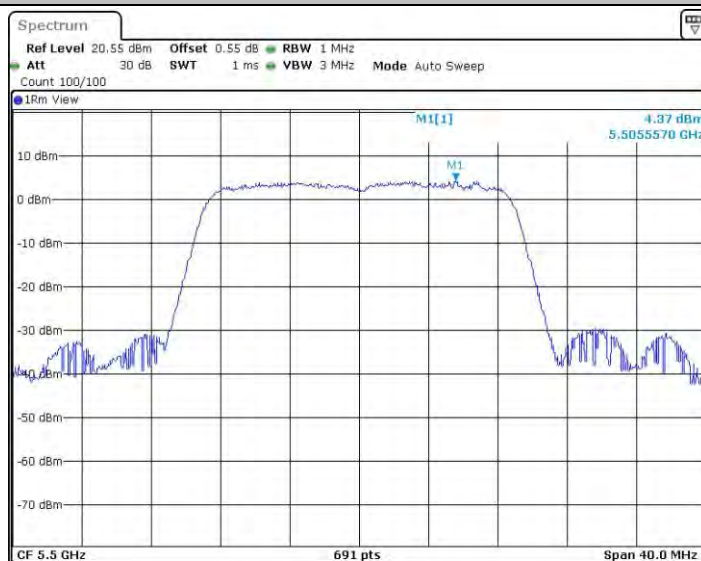
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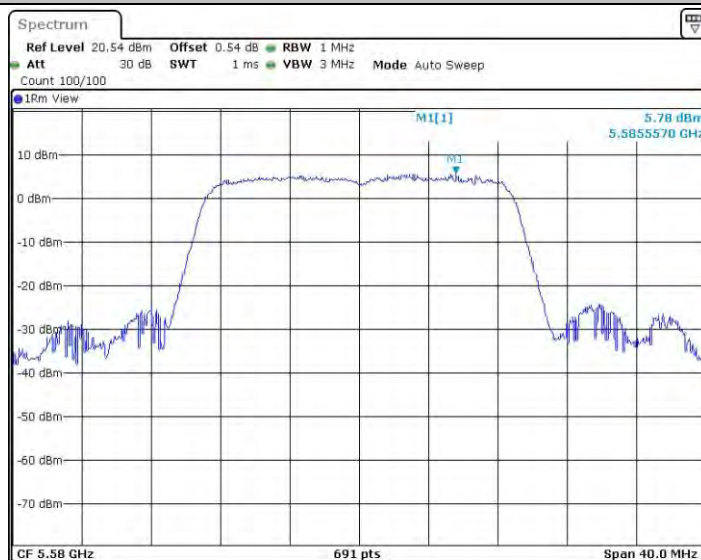
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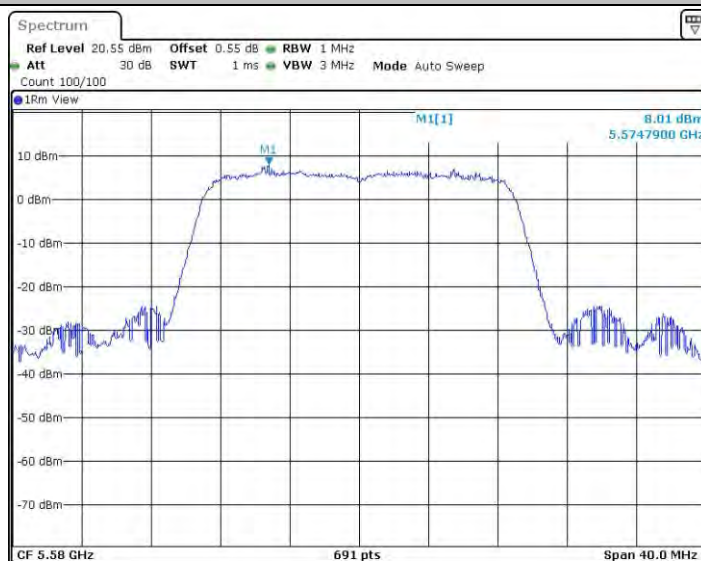
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11AC20MIMO_Ant1_5500



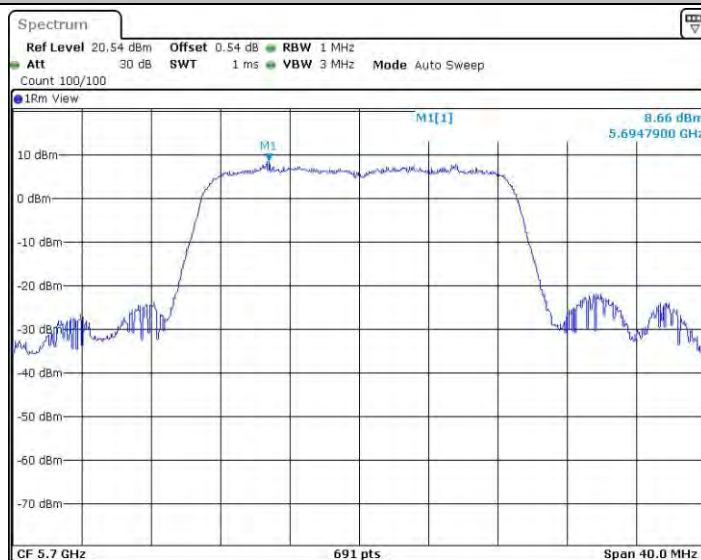
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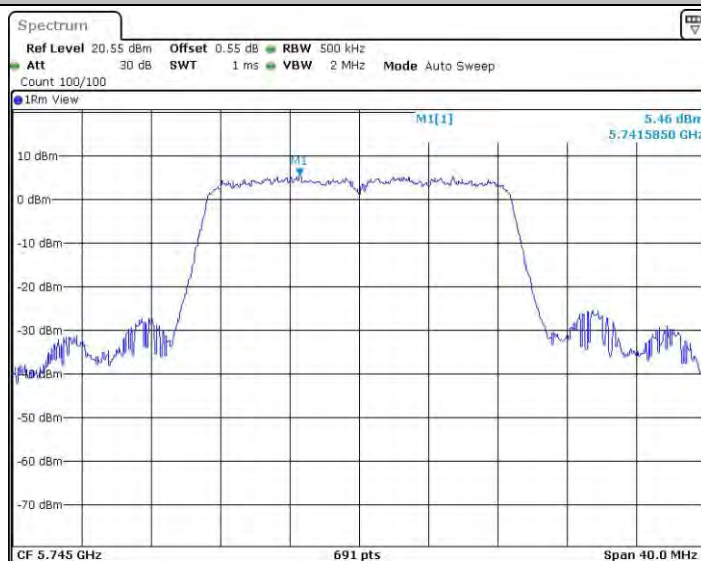
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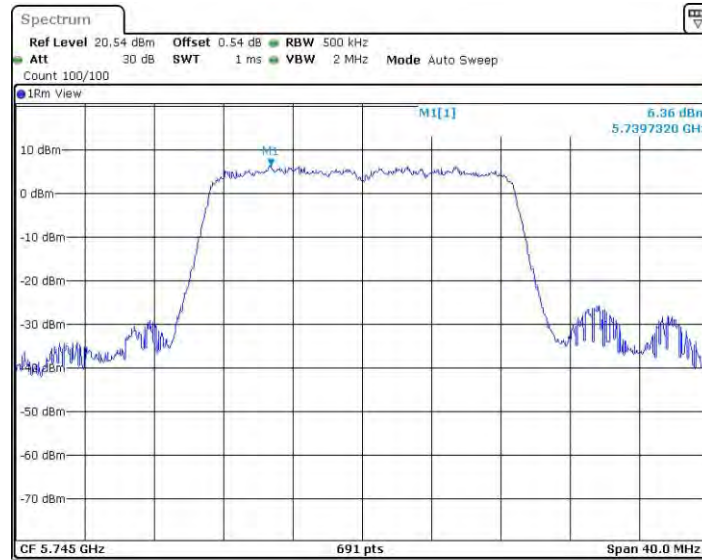
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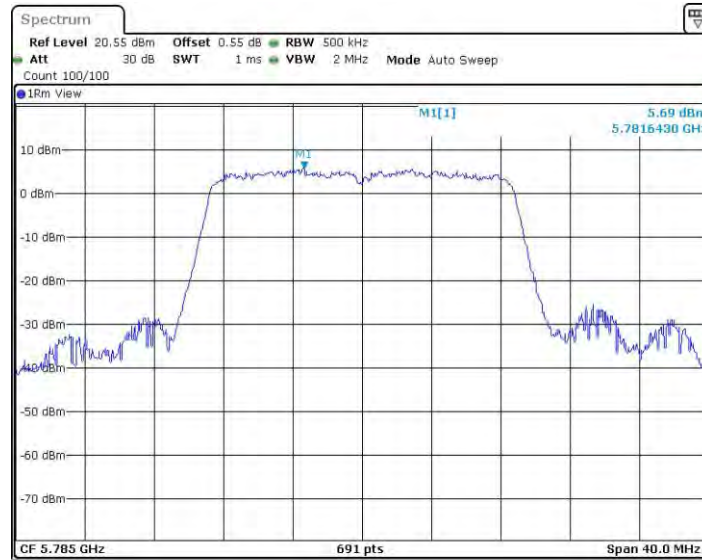
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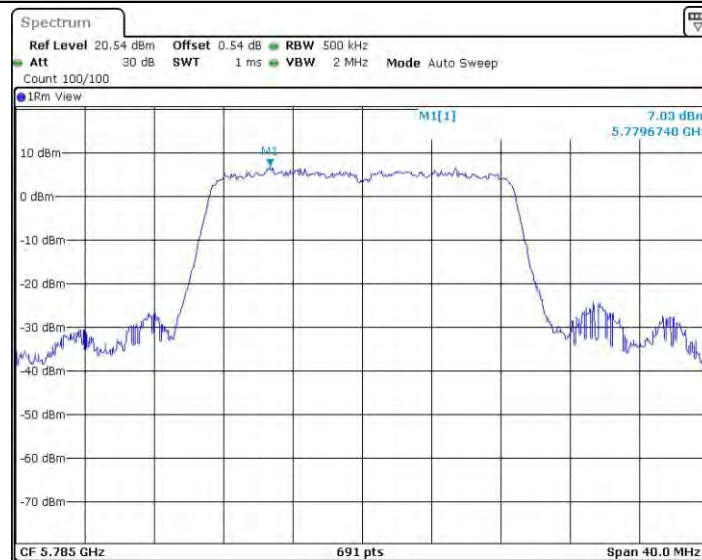
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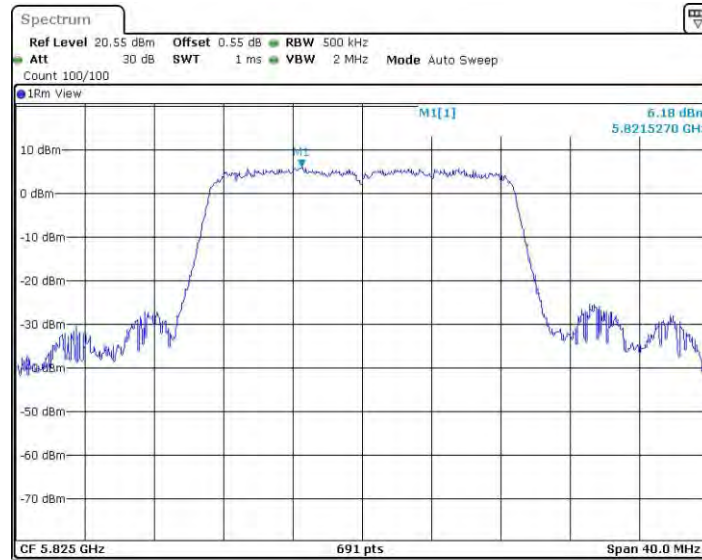
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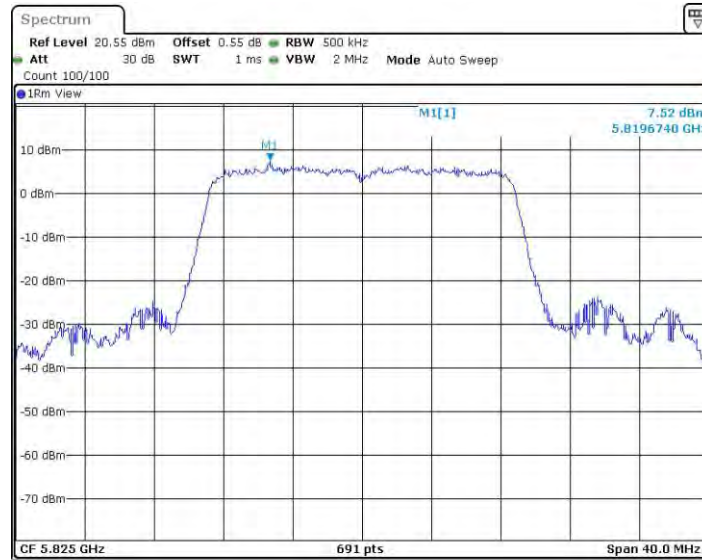
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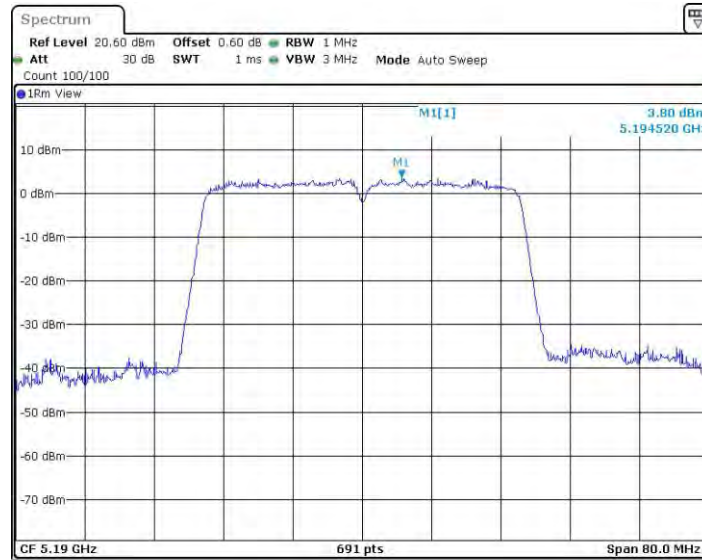
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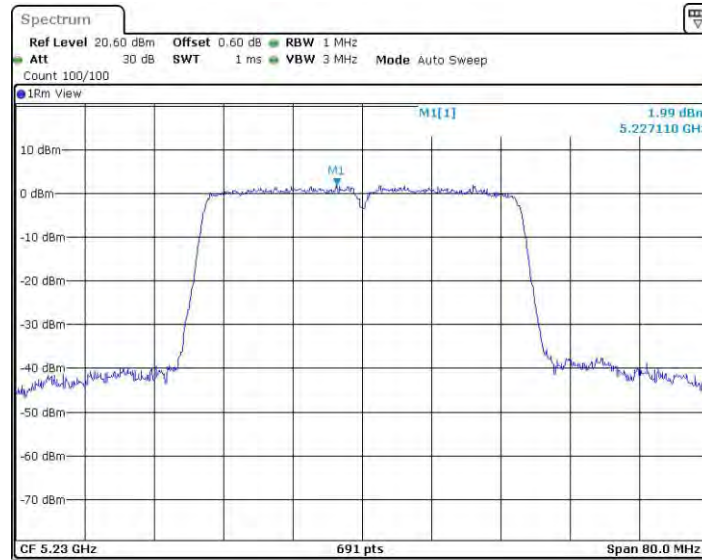
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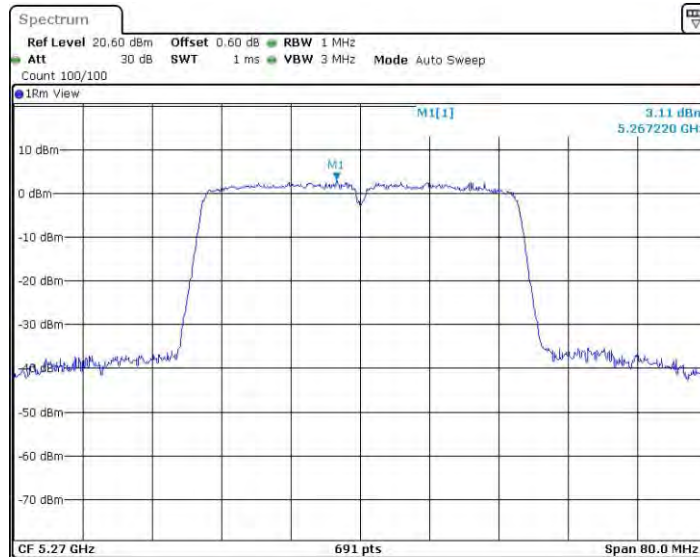
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11AC40MIMO_Ant0_5230



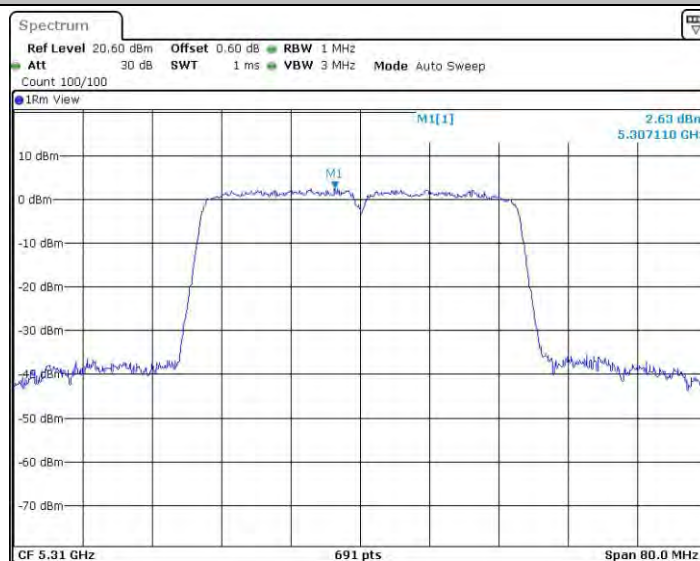
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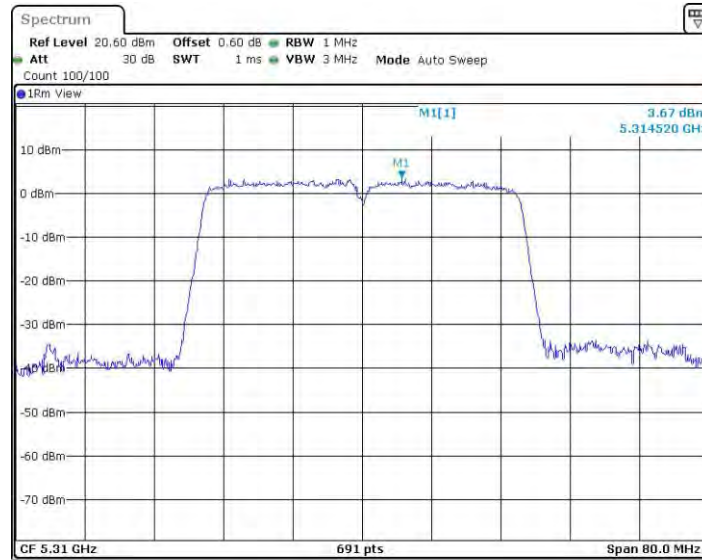
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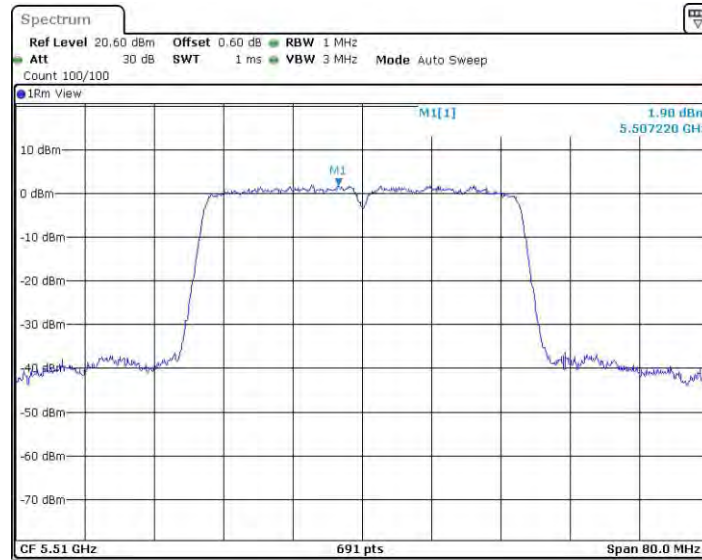
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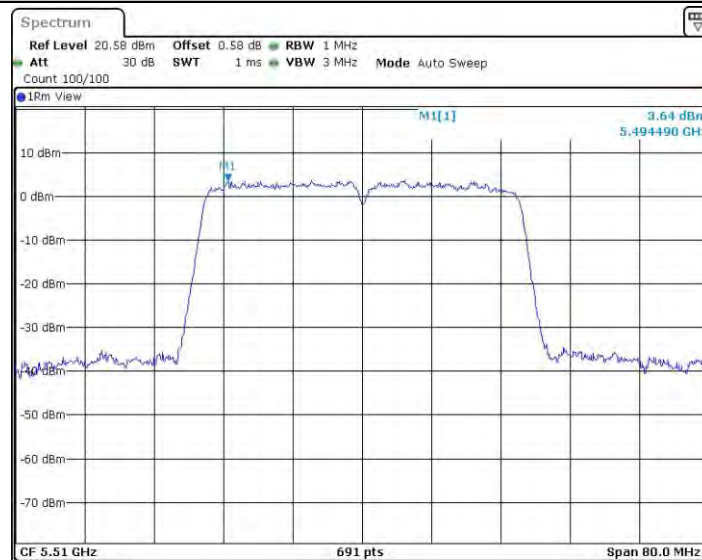
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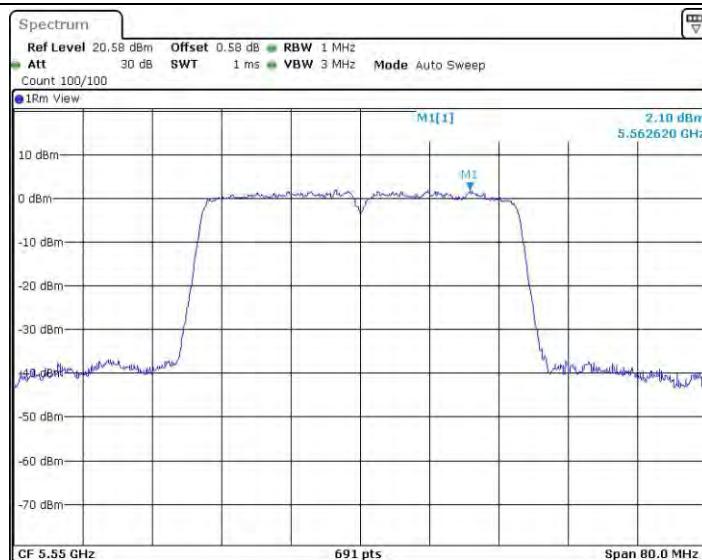
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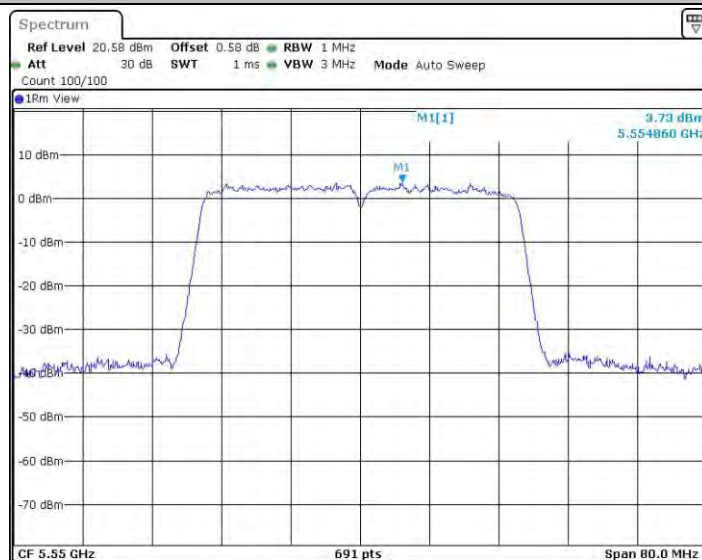
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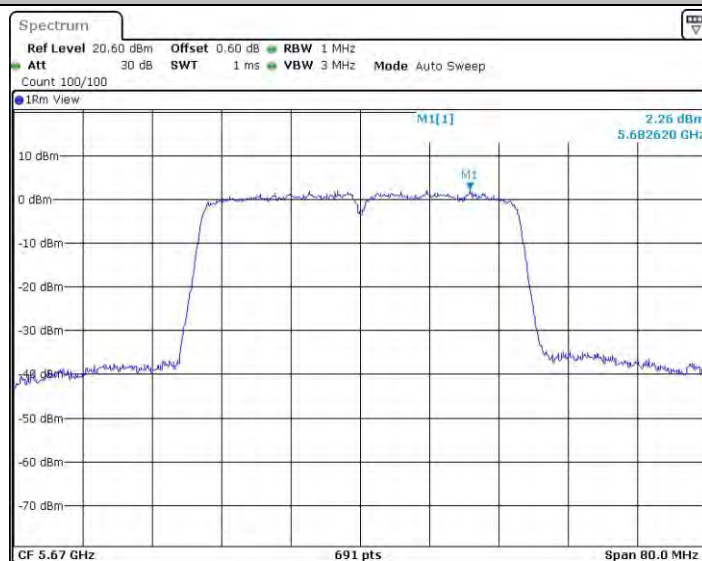
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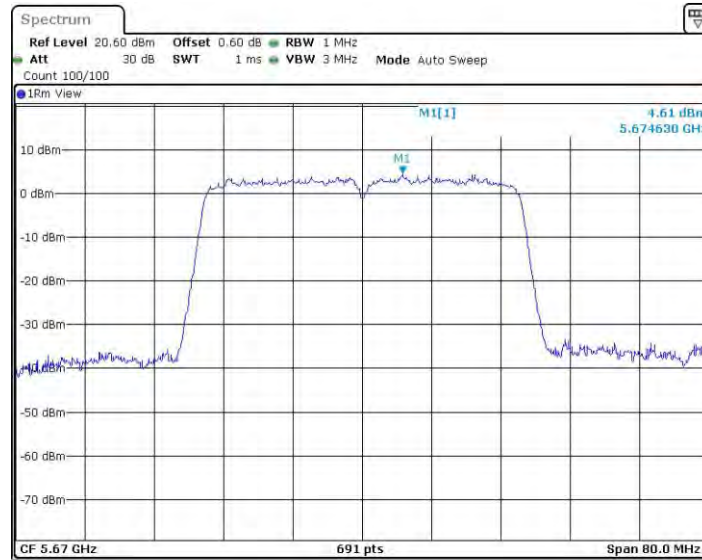
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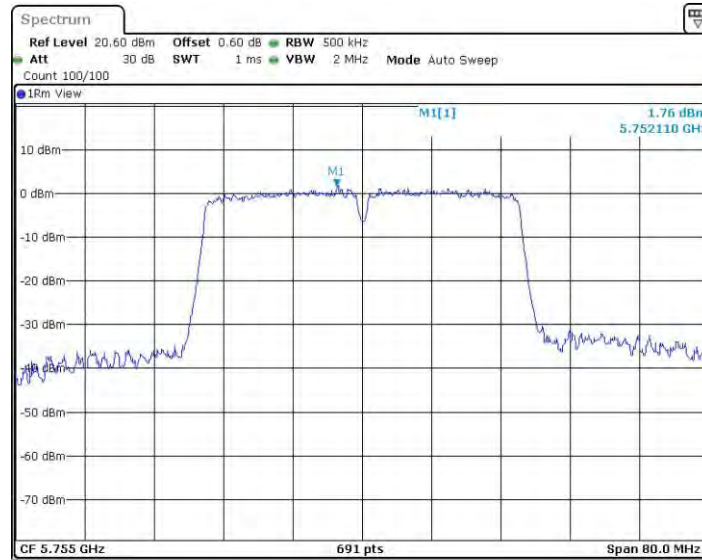
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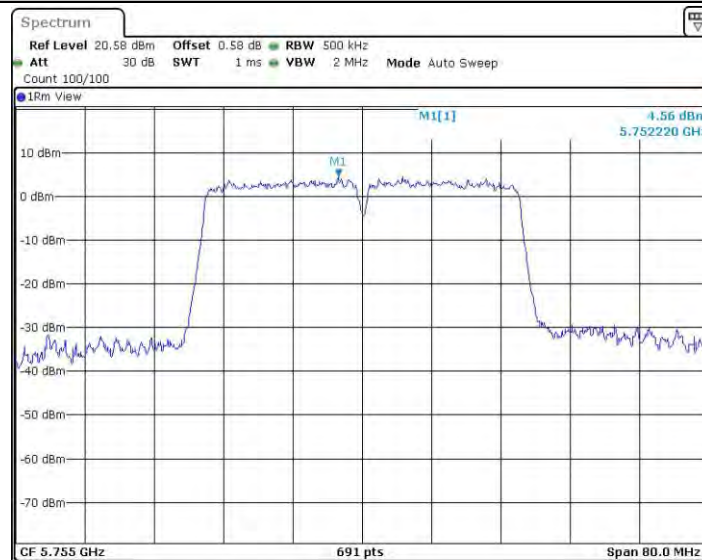
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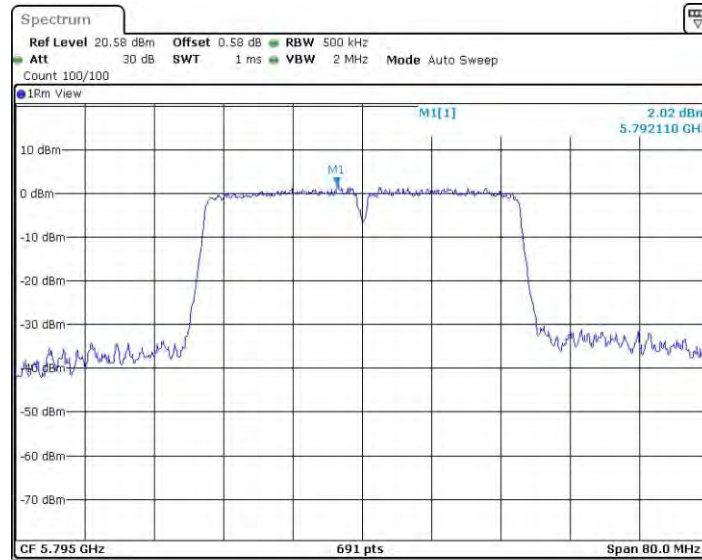
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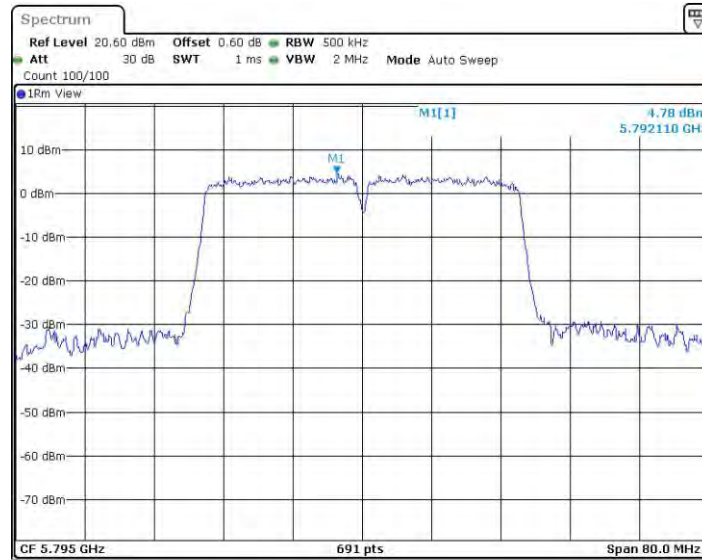
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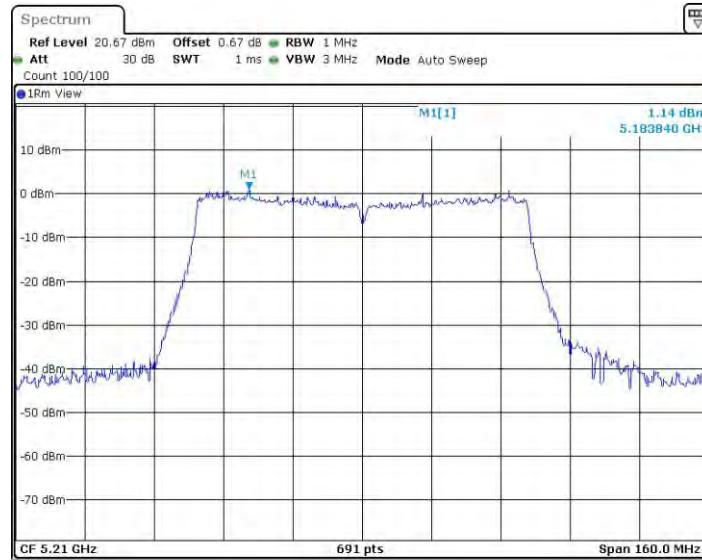
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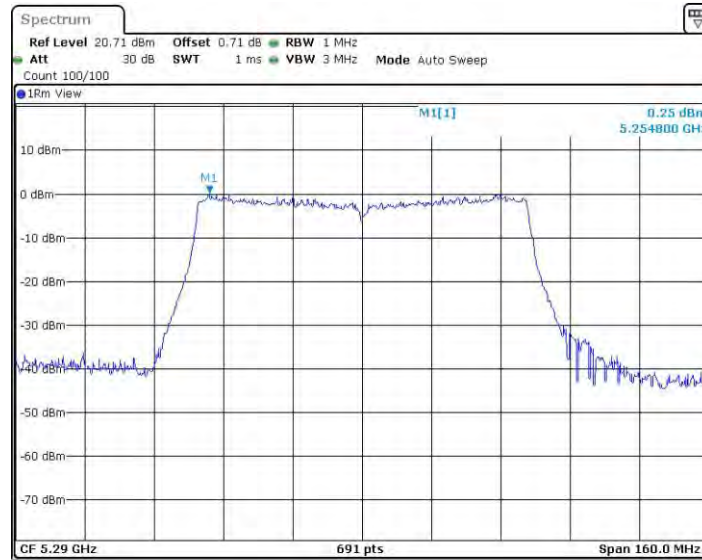
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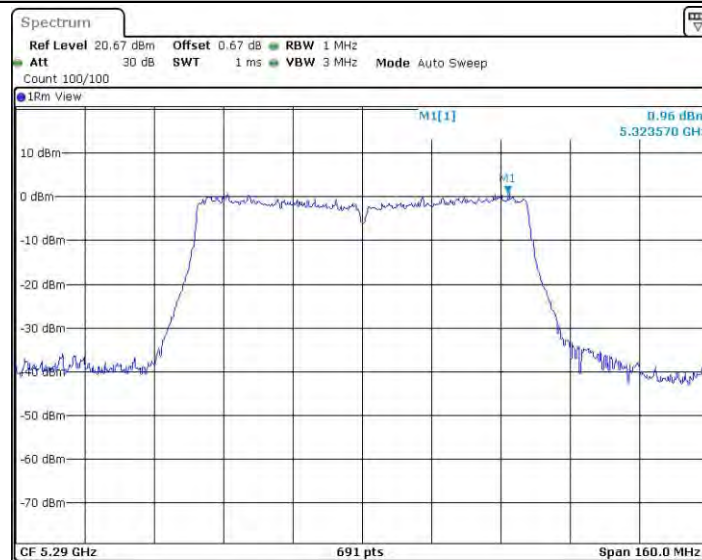
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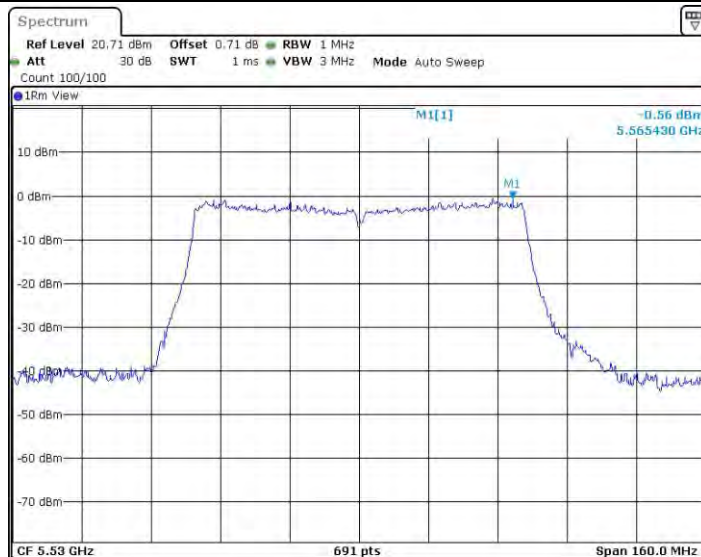
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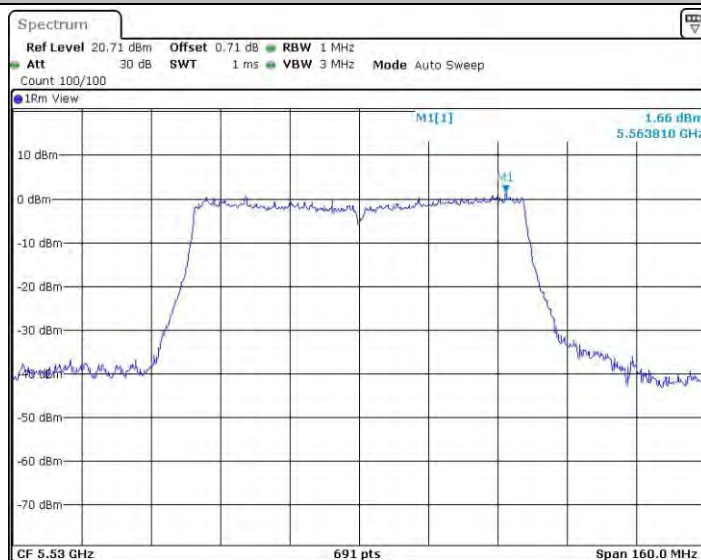
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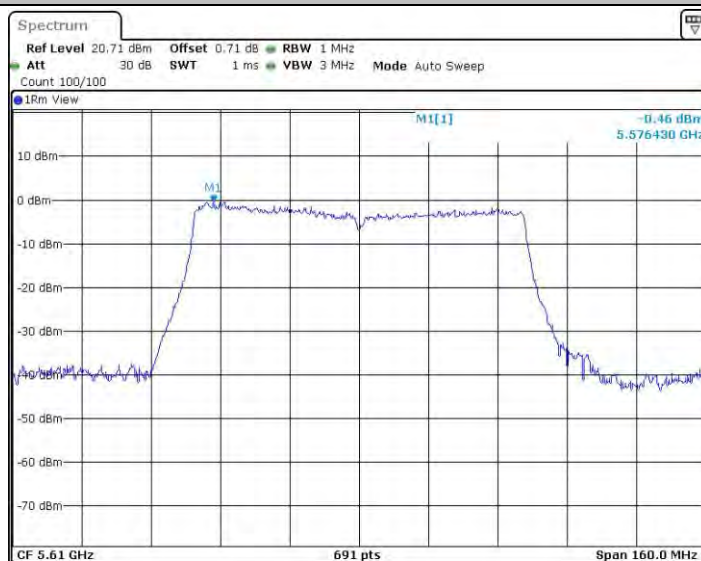
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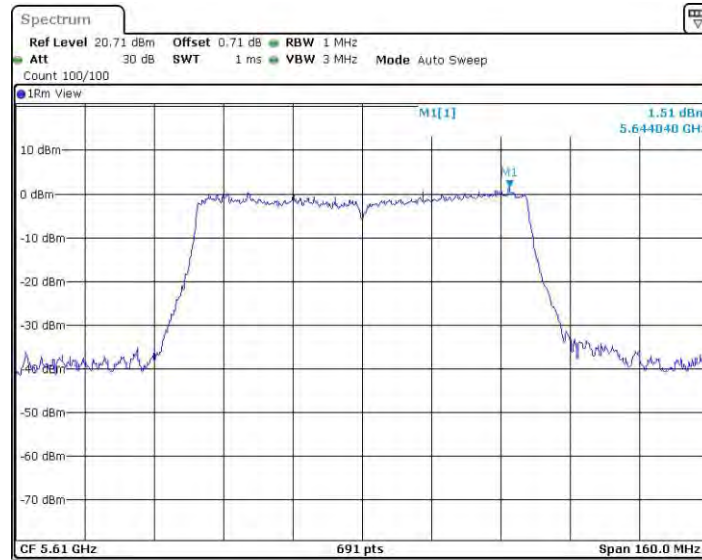
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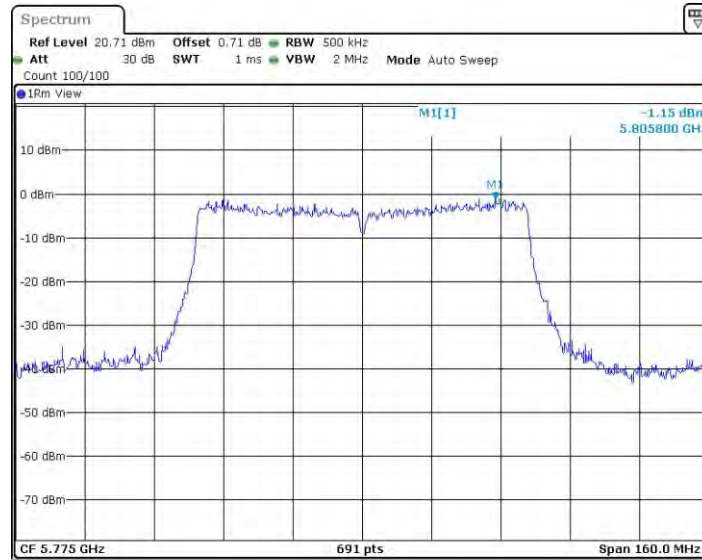
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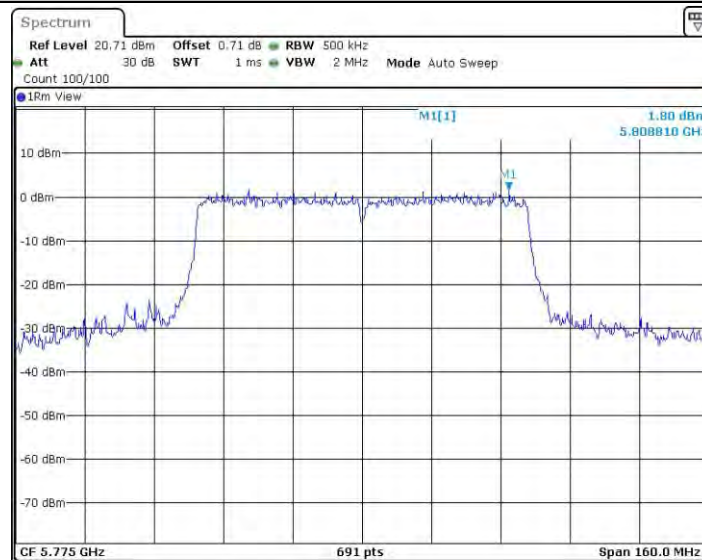
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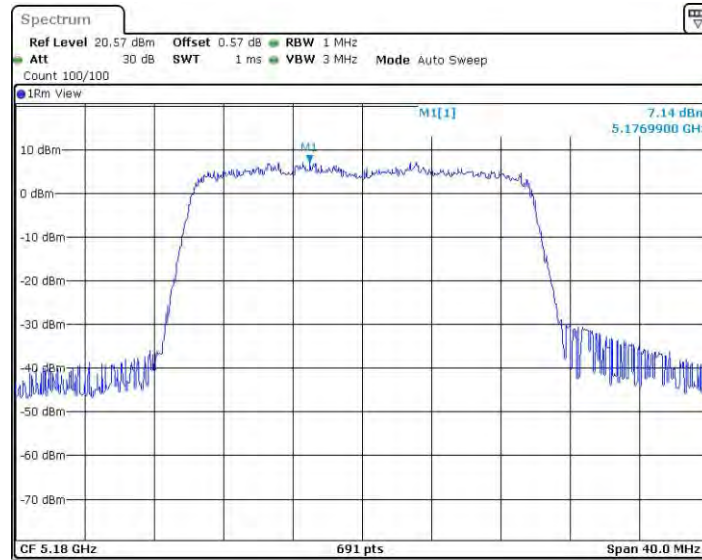
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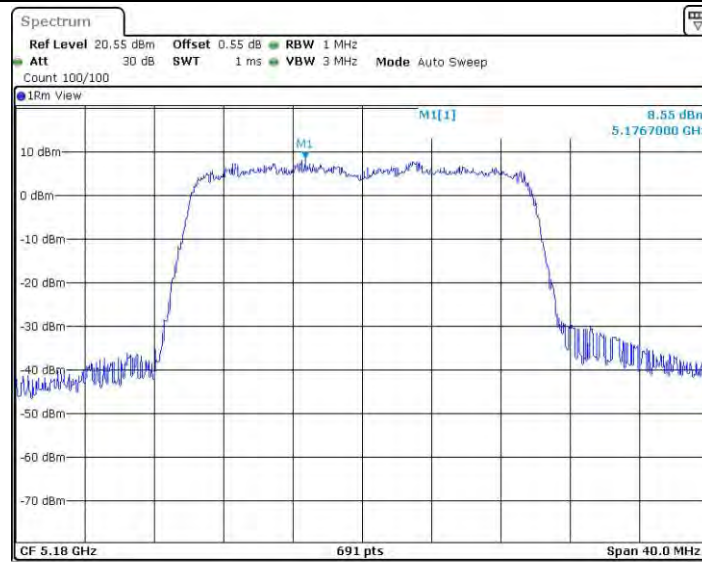
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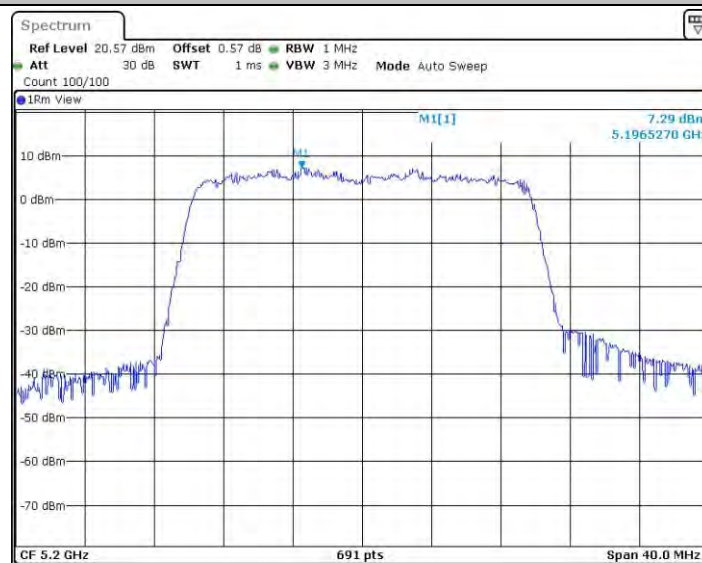
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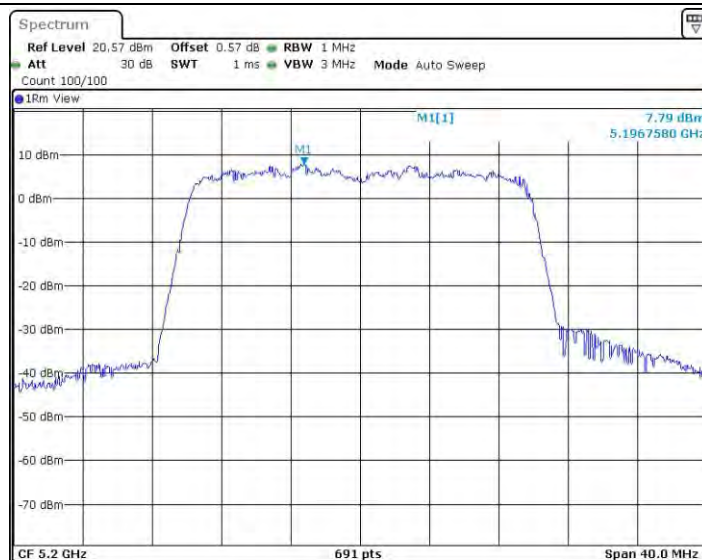
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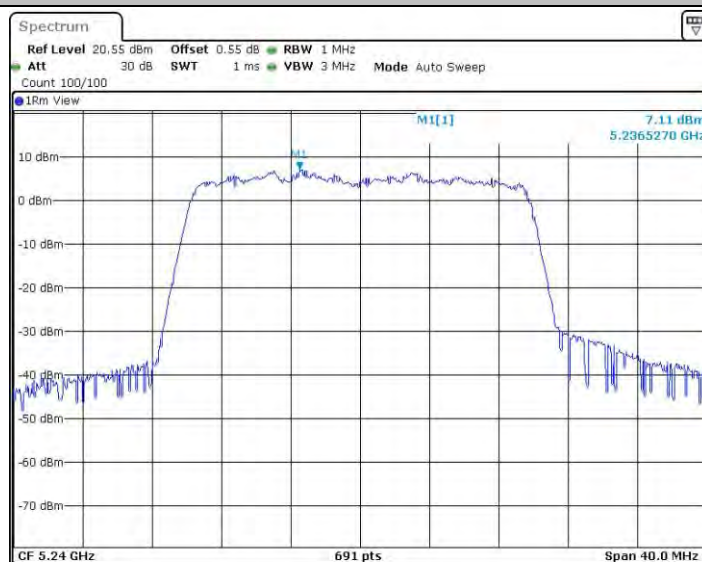
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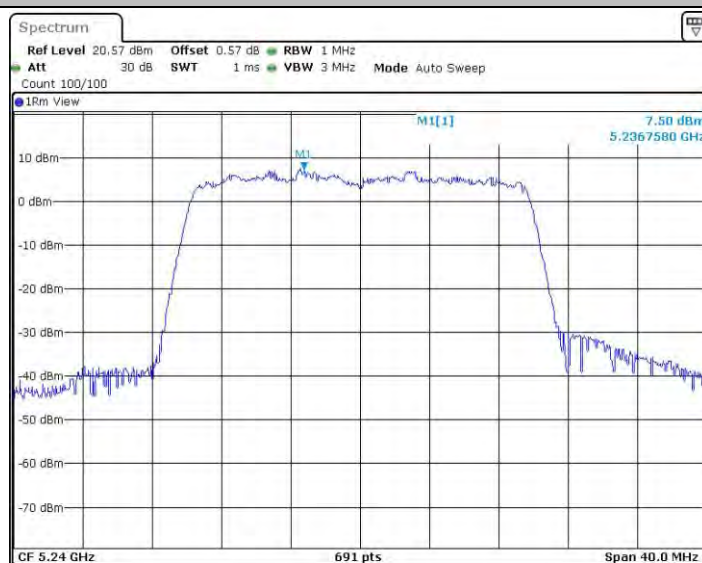
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11AX20MIMO_Ant1_5200



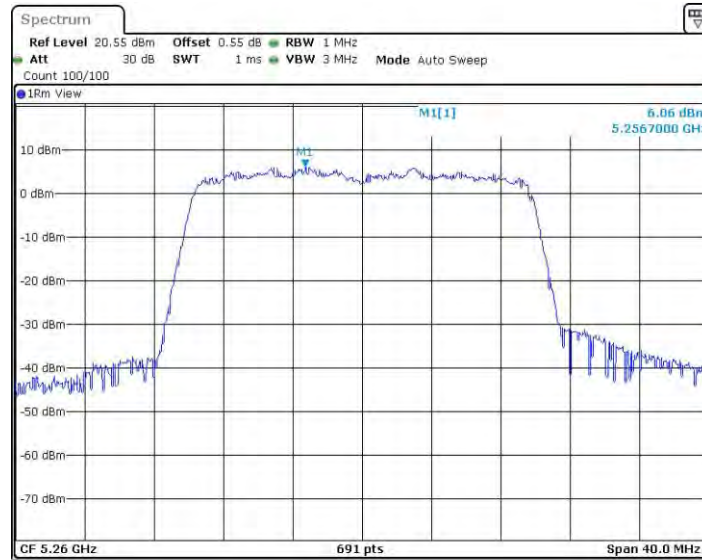
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11AX20MIMO_Ant1_5240



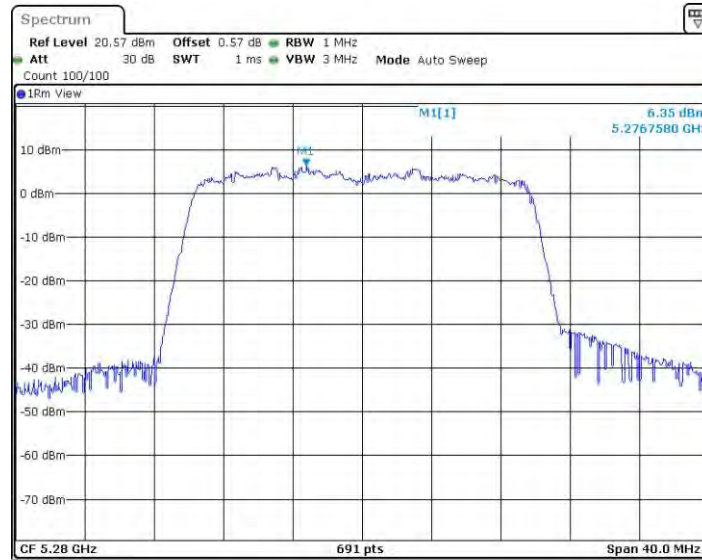
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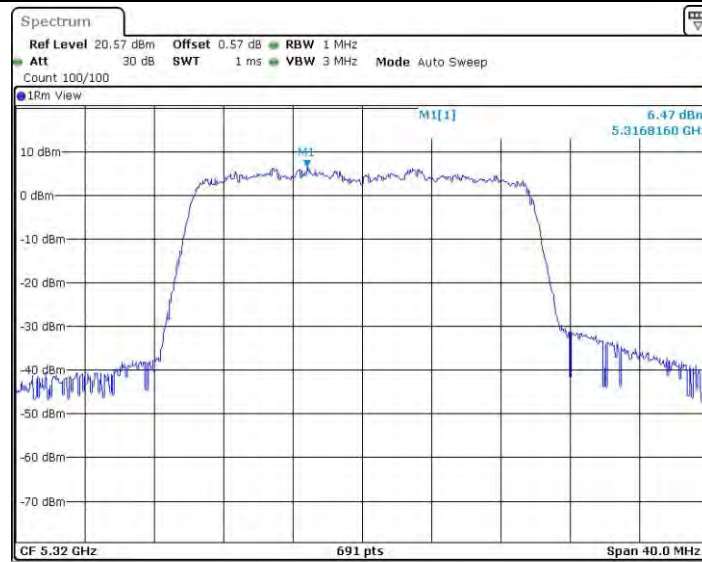
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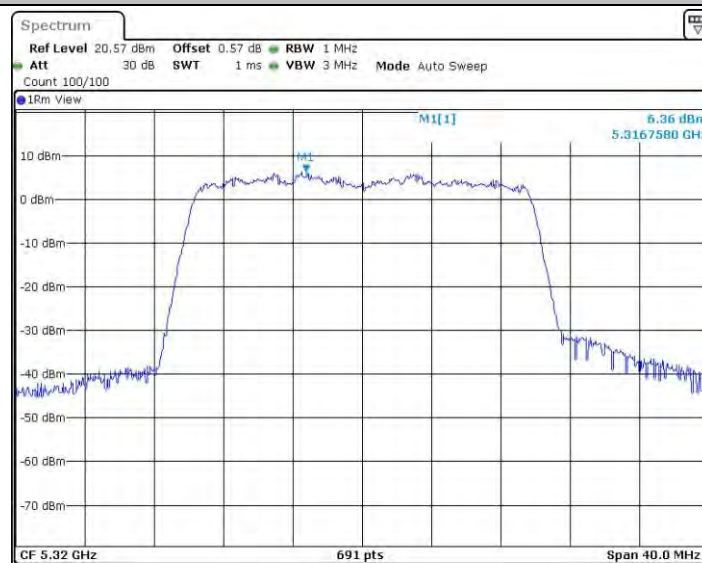
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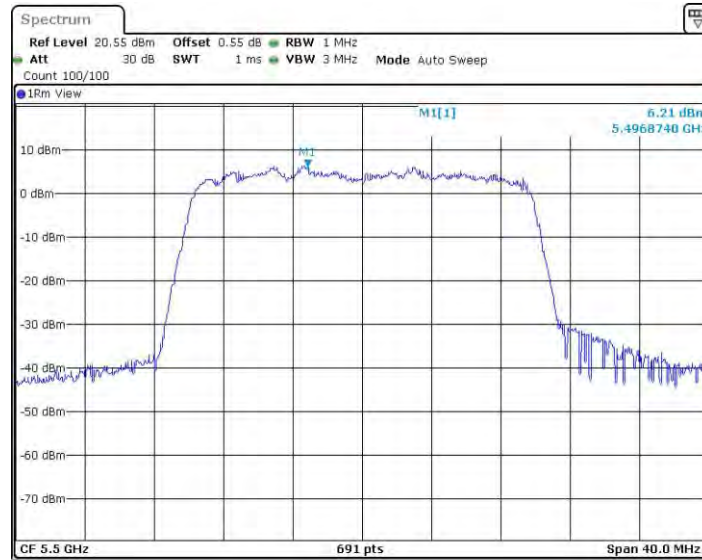
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11AX20MIMO_Ant0_5320



11AX20MIMO_Ant1_5320



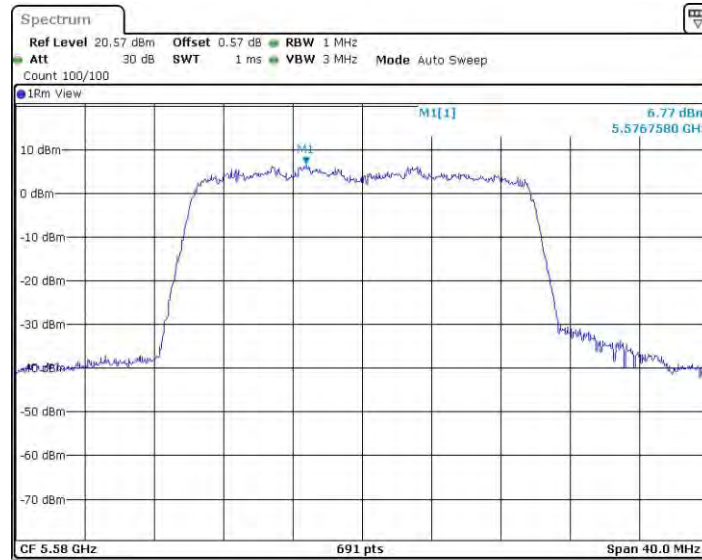
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11AX20MIMO_Ant1_5500



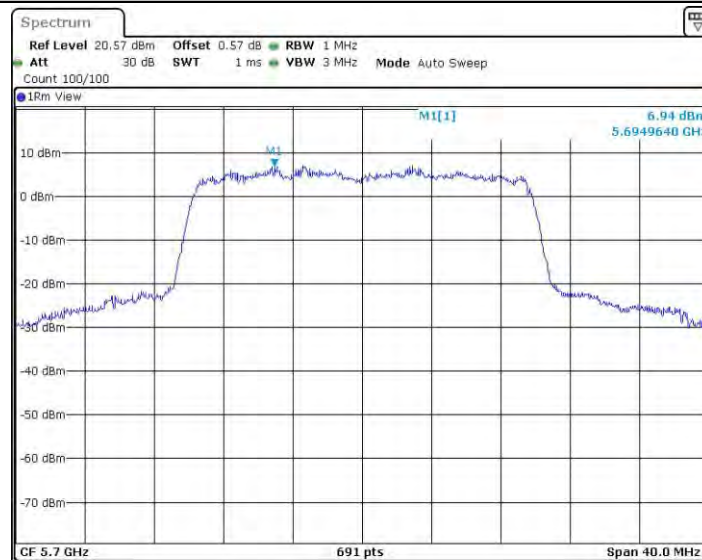
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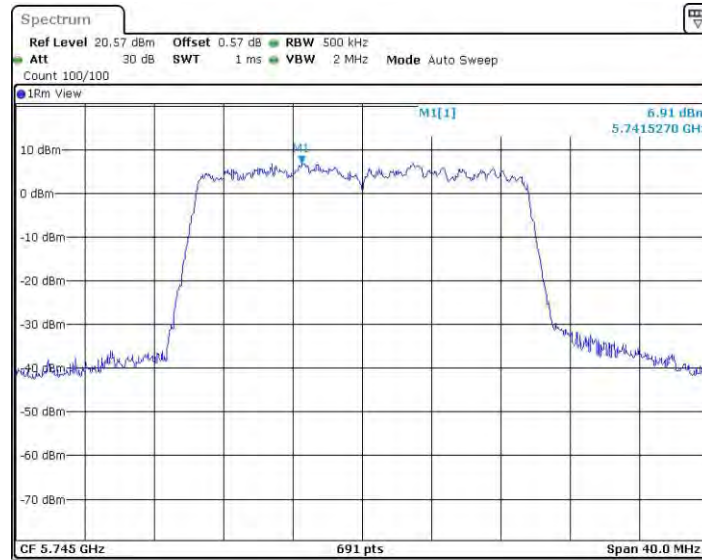
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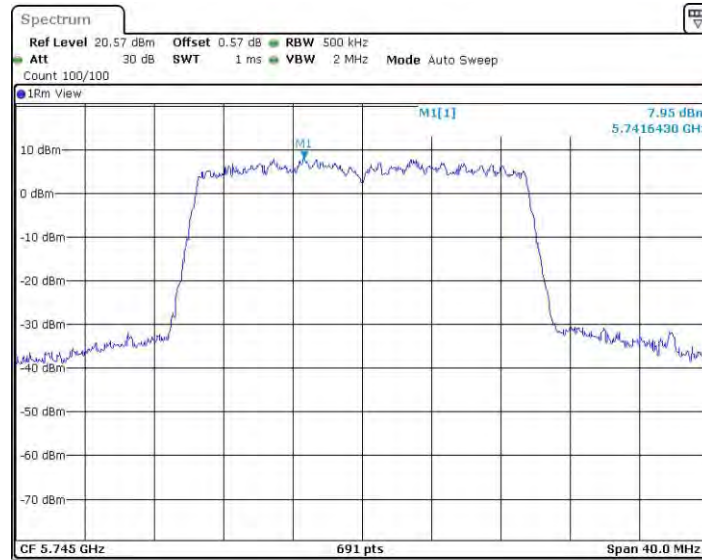
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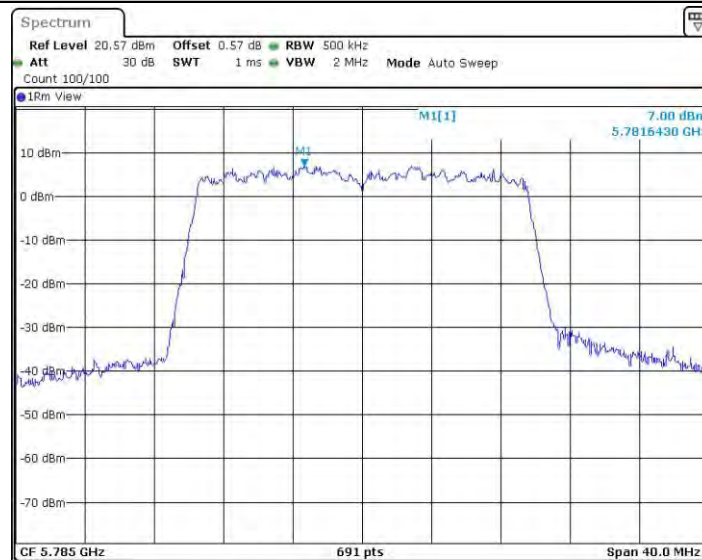
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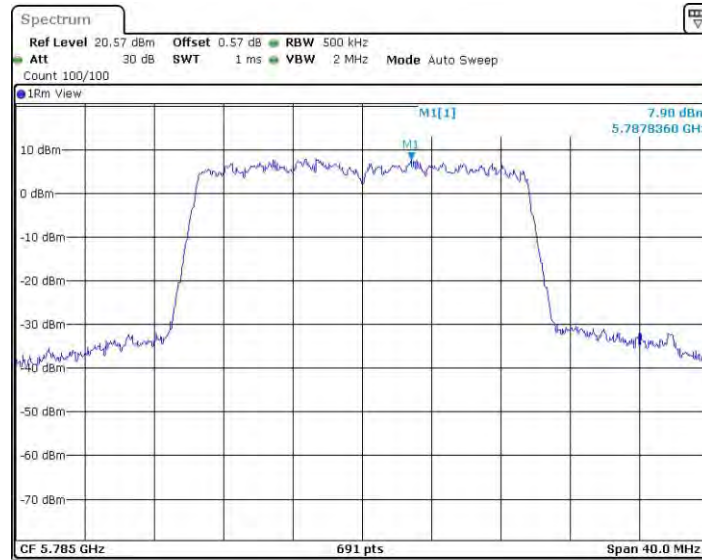
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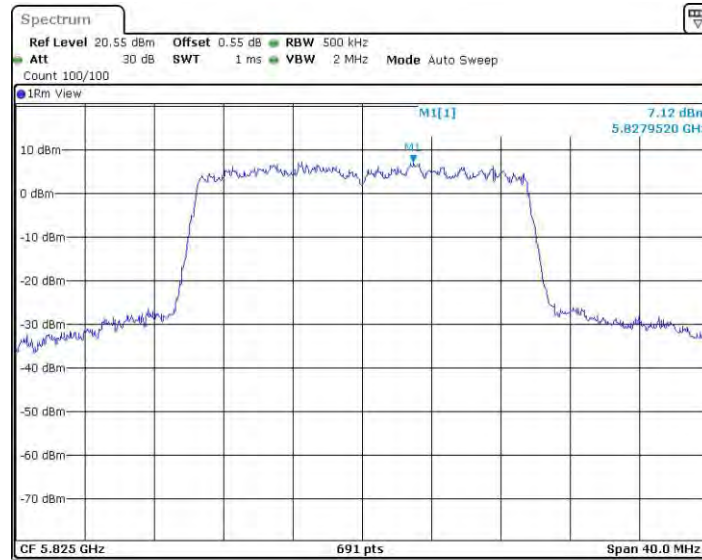
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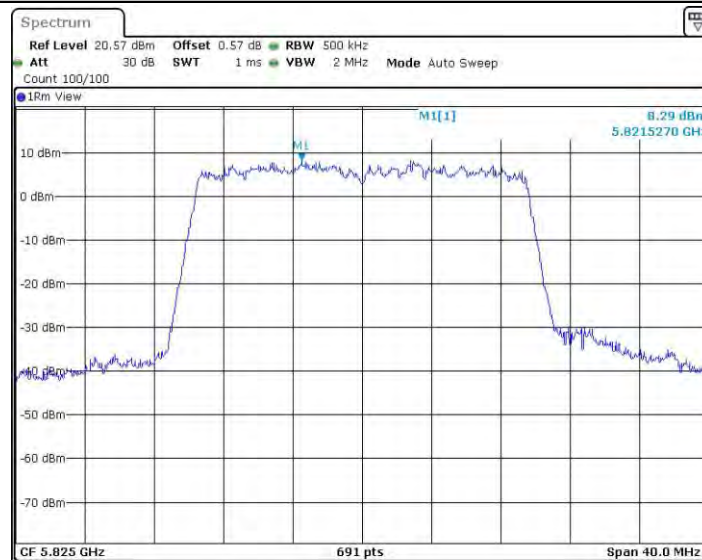
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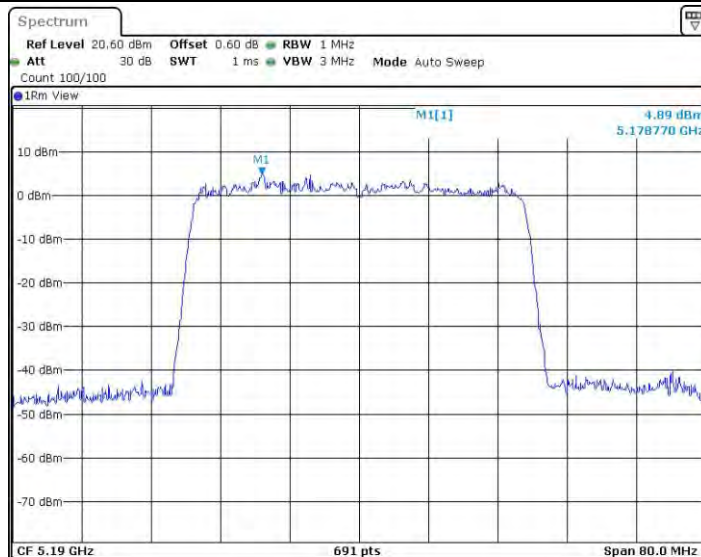
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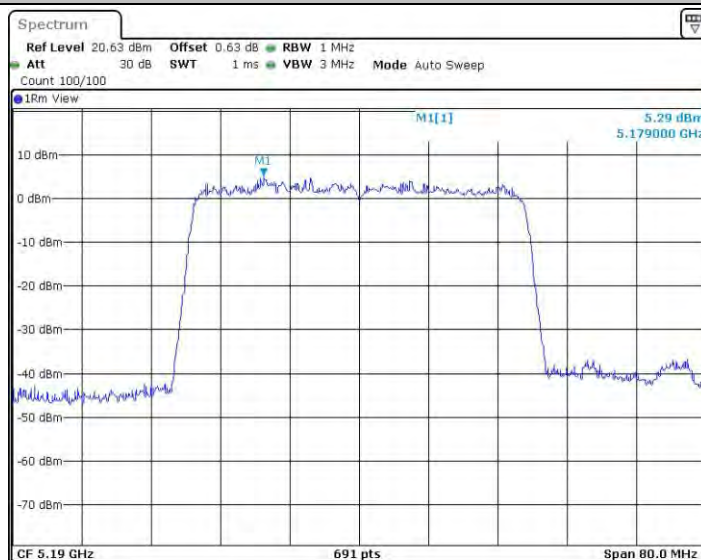
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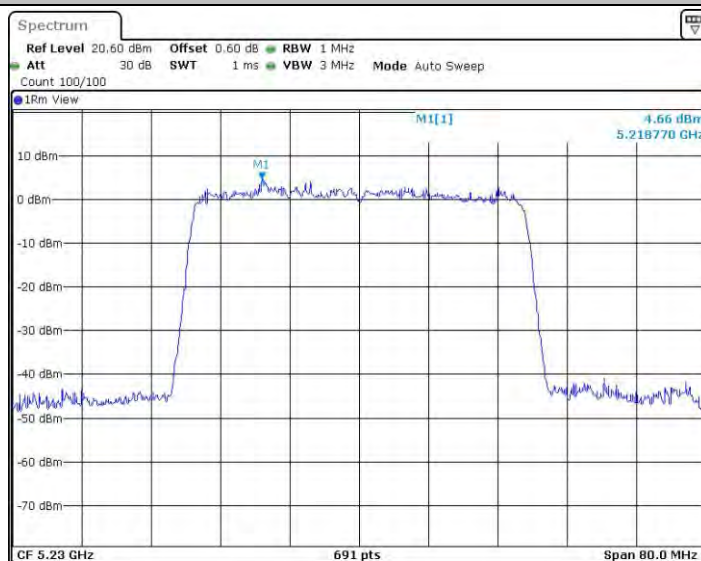
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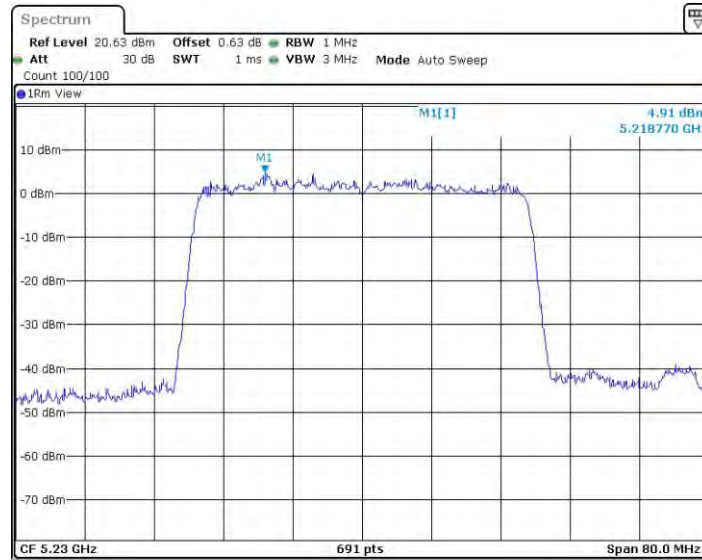
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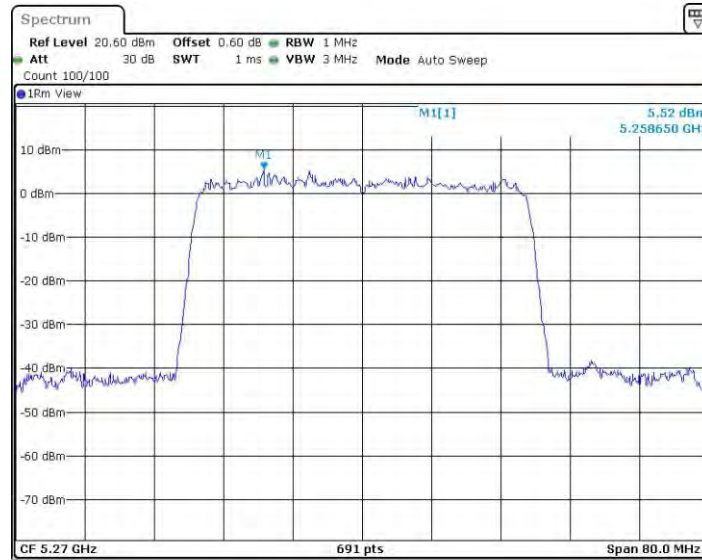
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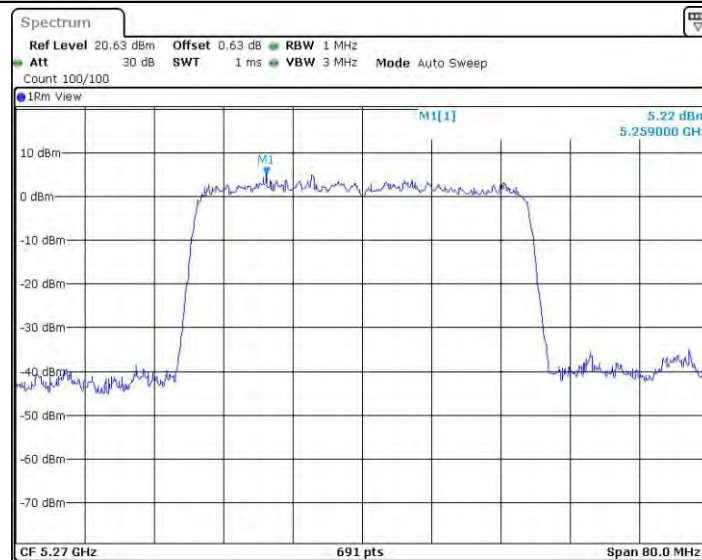
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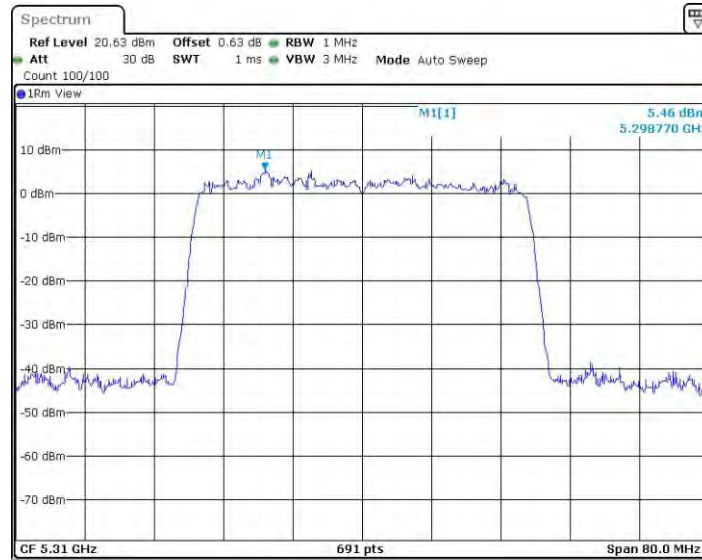
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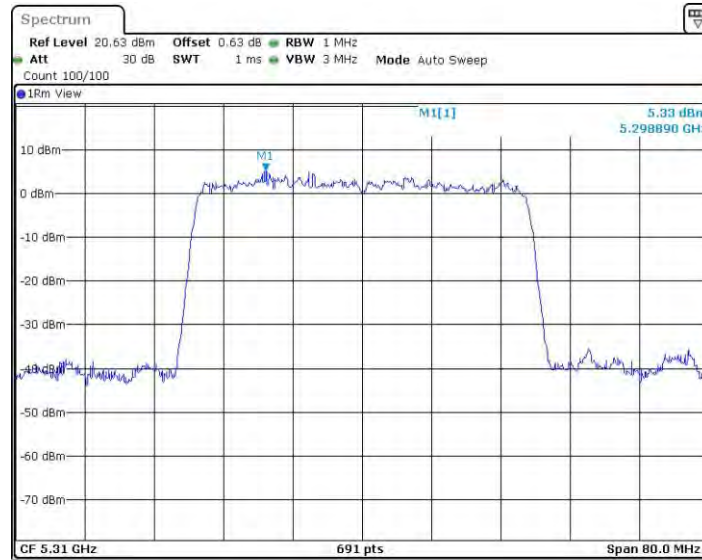
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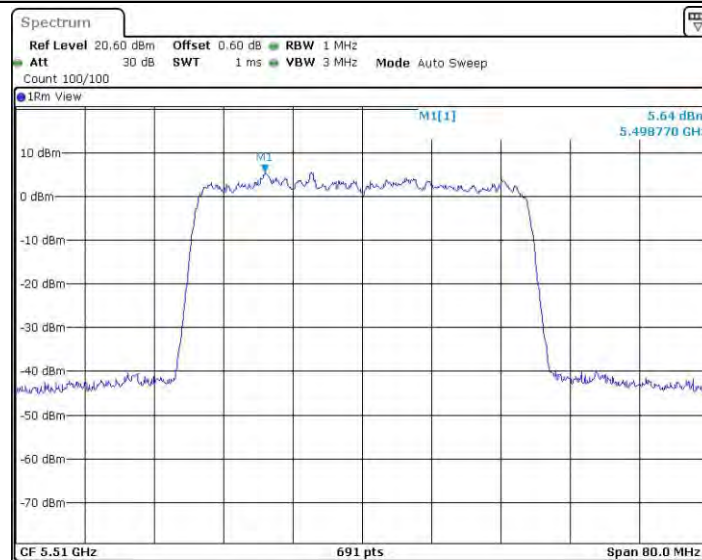
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11AX40MIMO_Ant0_5310



11AX40MIMO_Ant1_5310



11AX40MIMO_Ant0_5510