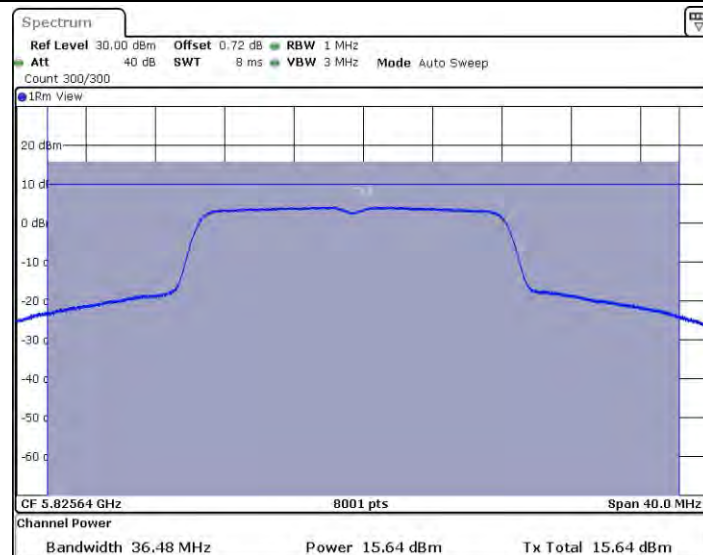
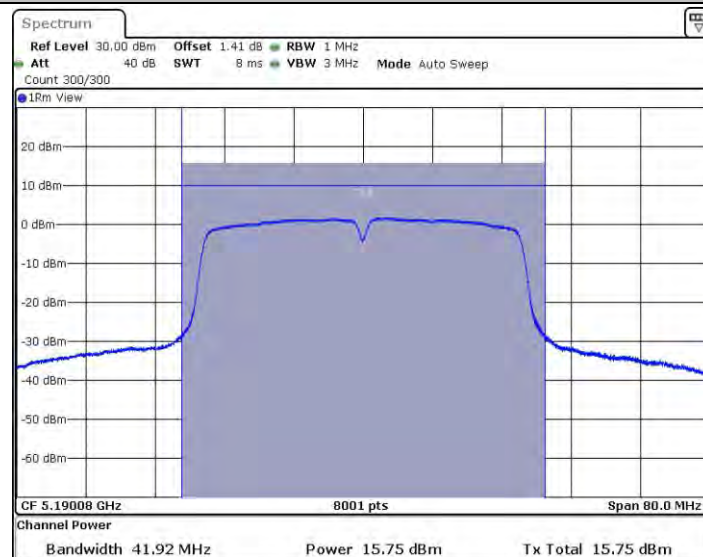
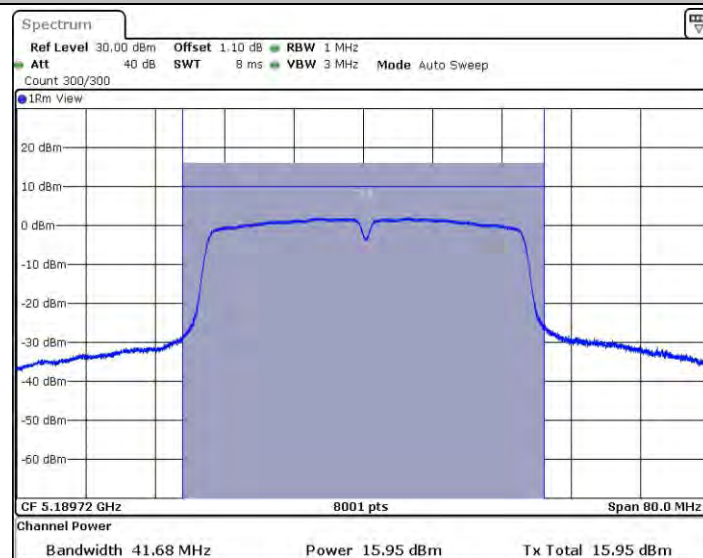




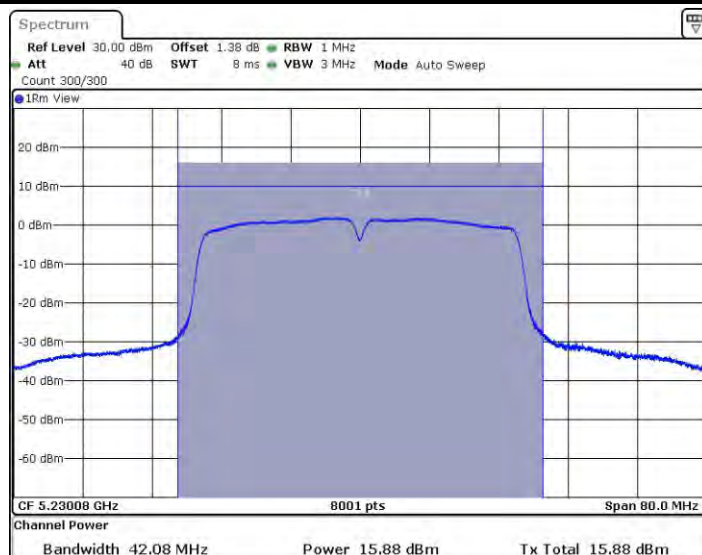
11N20SISO_Ant2_5825



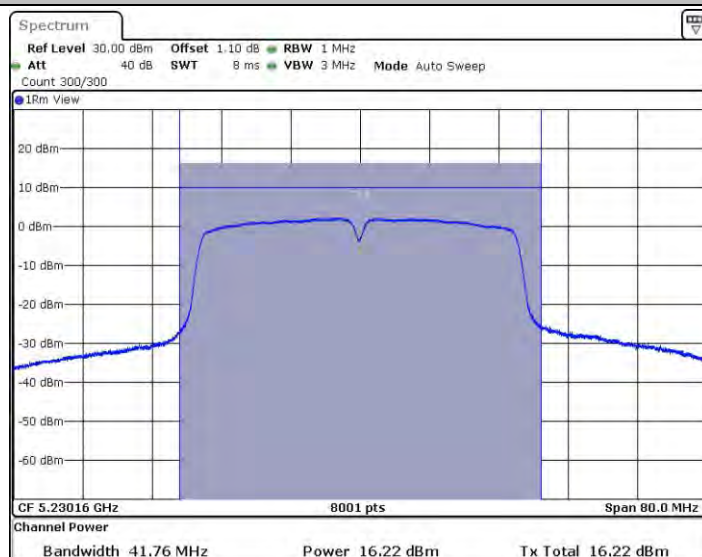
11N40SISO_Ant1_5190



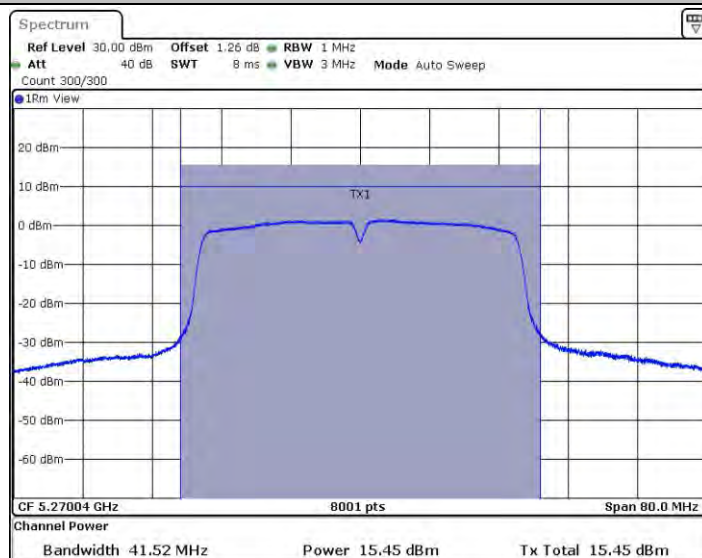
11N40SISO_Ant2_5190



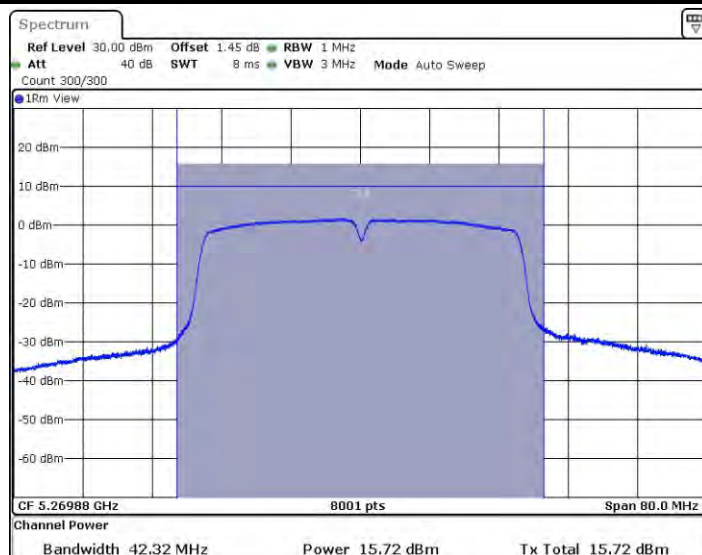
11N40SISO_Ant1_5230



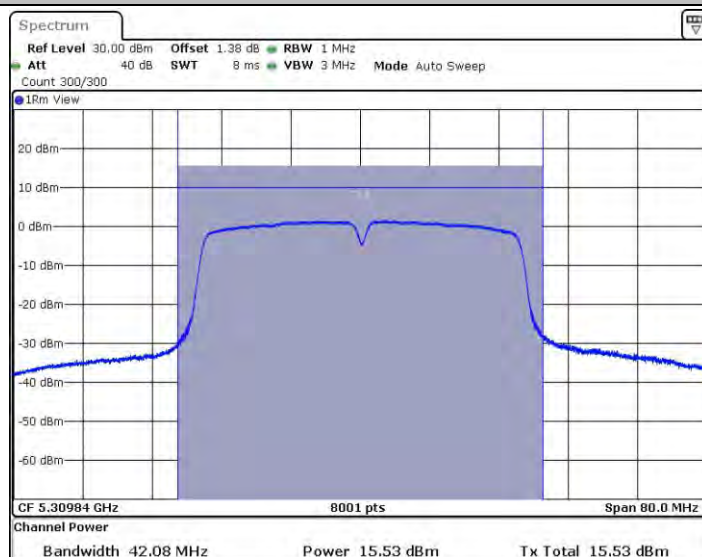
11N40SISO_Ant2_5230



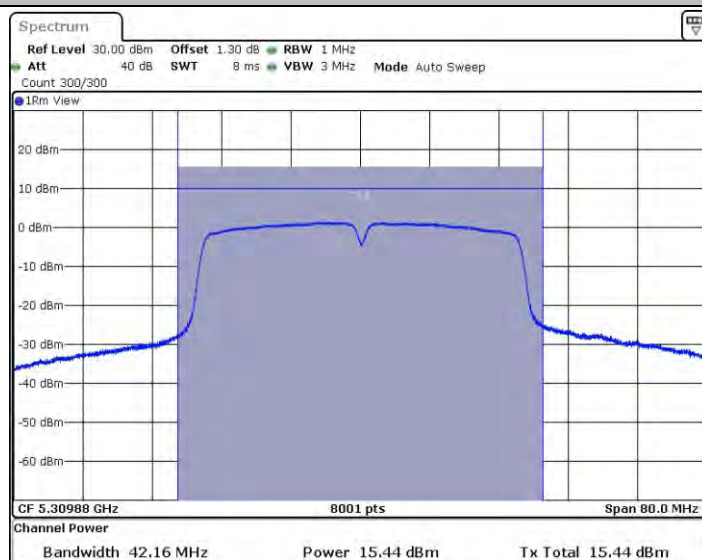
11N40SISO_Ant1_5270



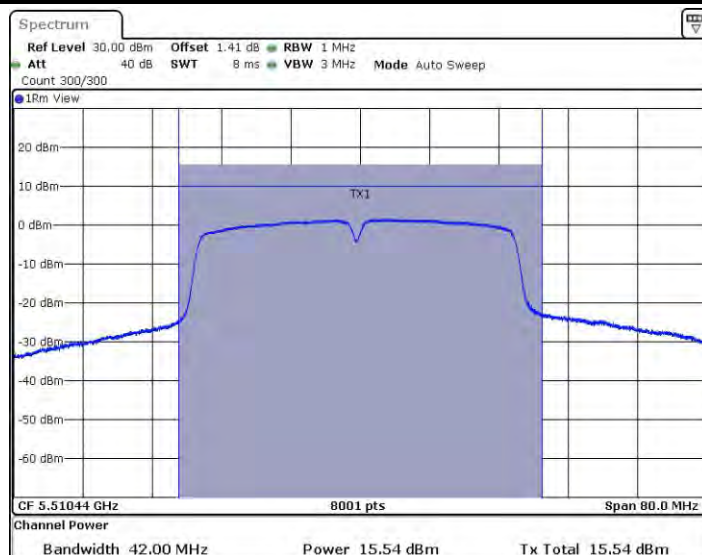
11N40SISO_Ant2_5270



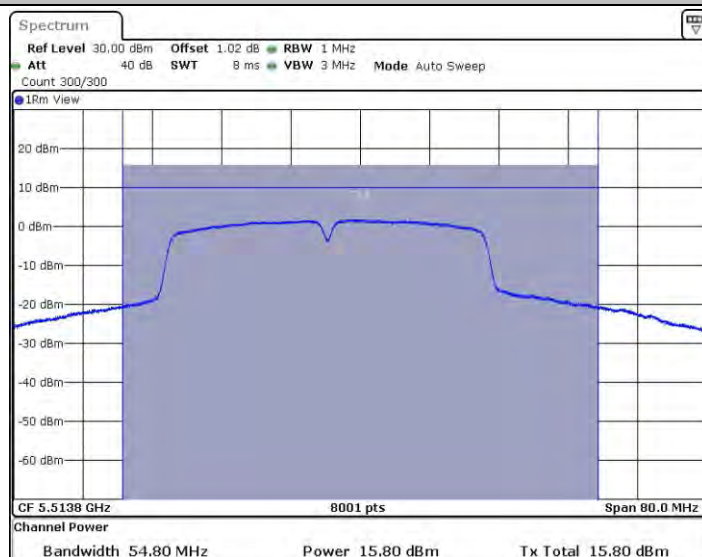
11N40SISO_Ant1_5310



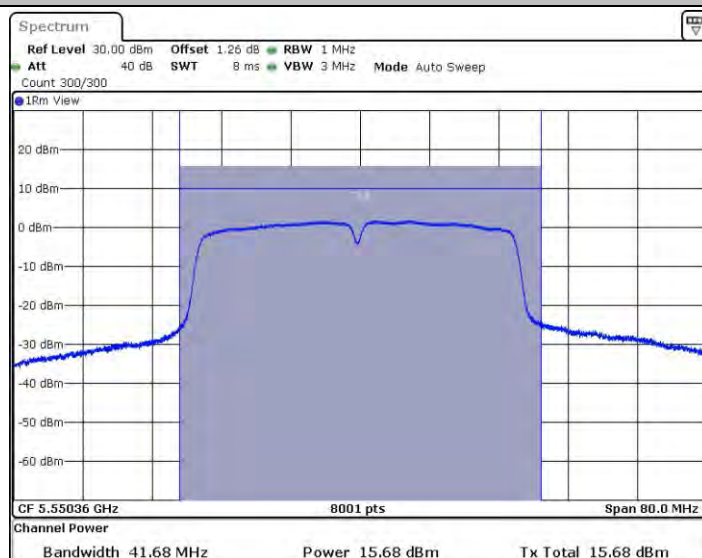
11N40SISO_Ant2_5310



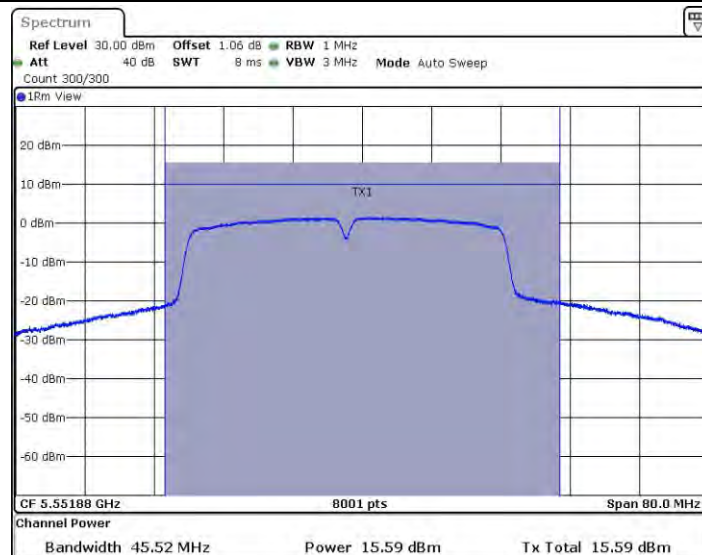
11N40SISO_Ant1_5510



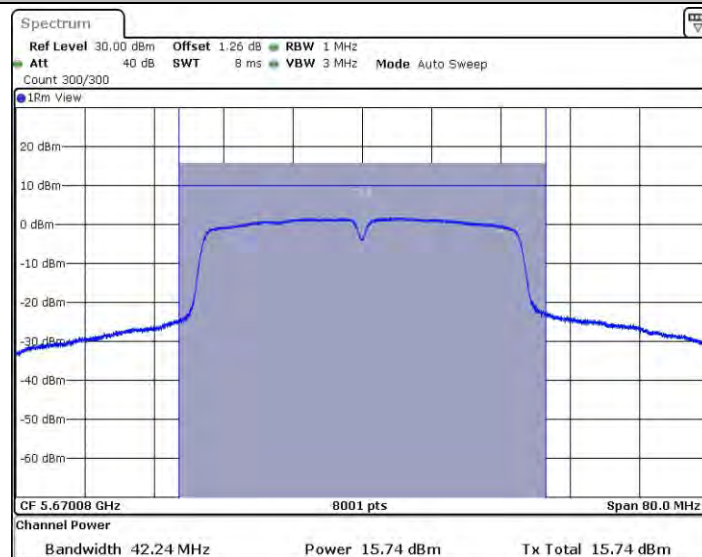
11N40SISO_Ant2_5510



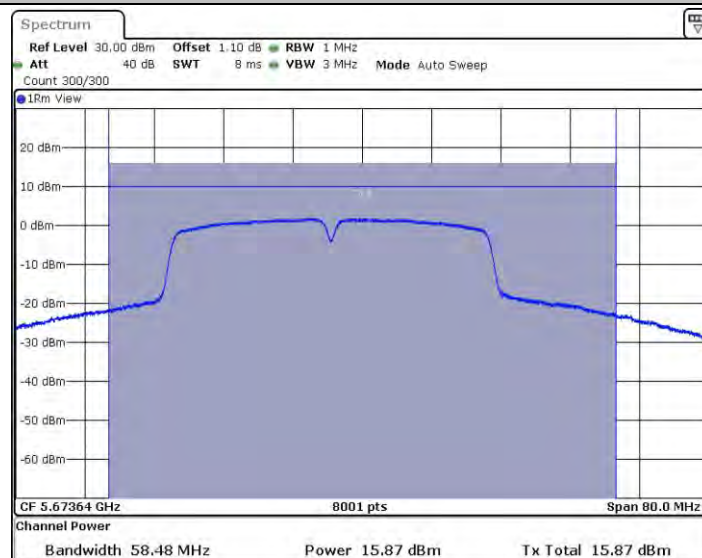
11N40SISO_Ant1_5550



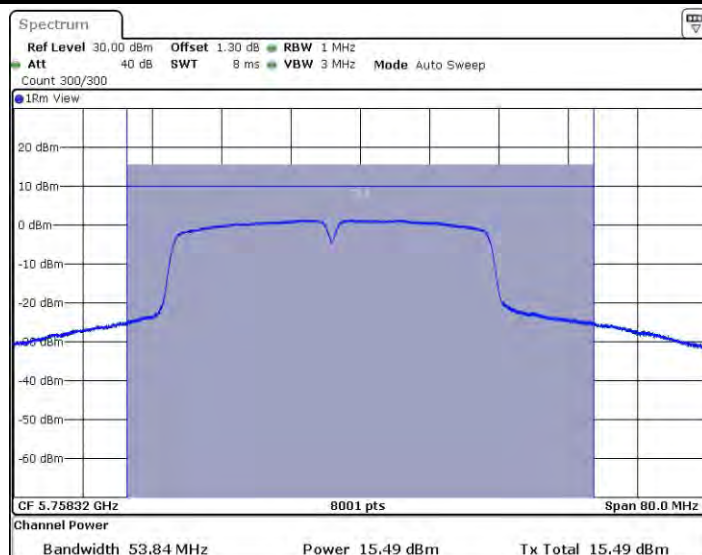
11N40SISO_Ant2_5550



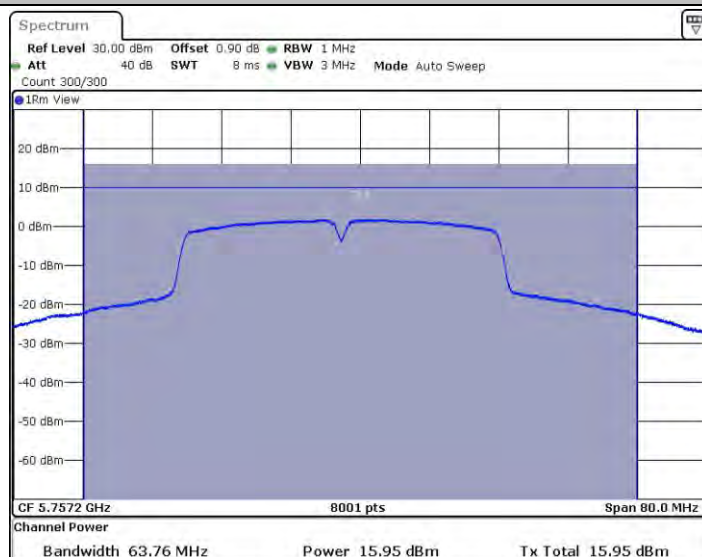
11N40SISO_Ant1_5670



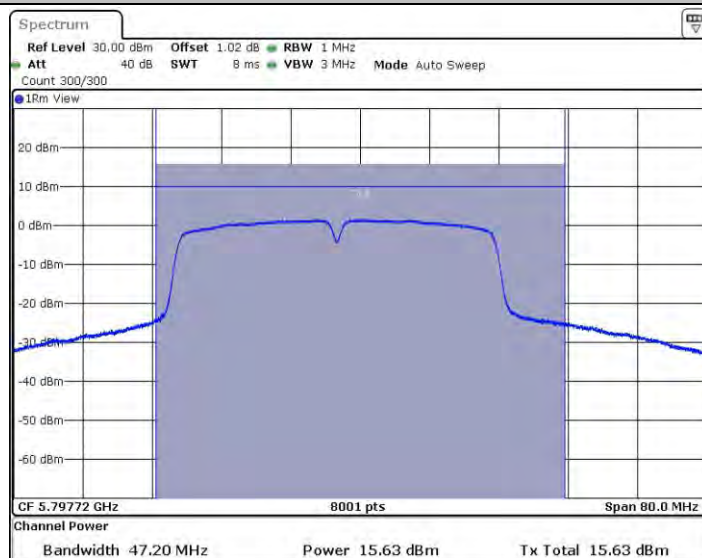
11N40SISO_Ant2_5670



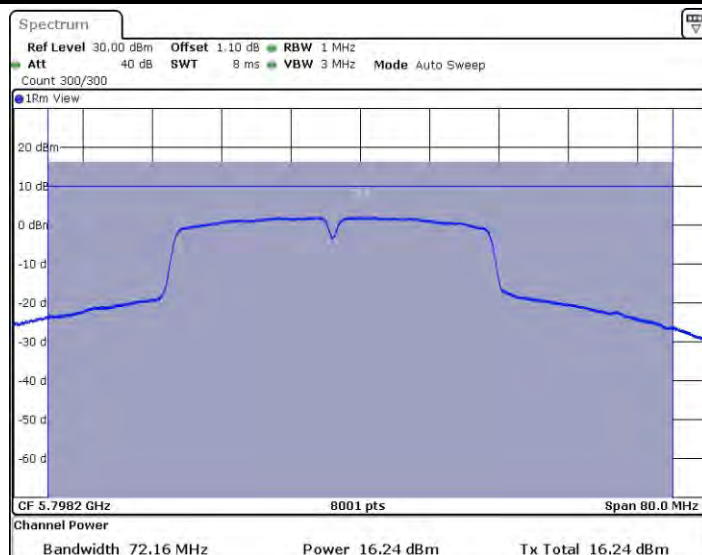
11N40SISO_Ant1_5755



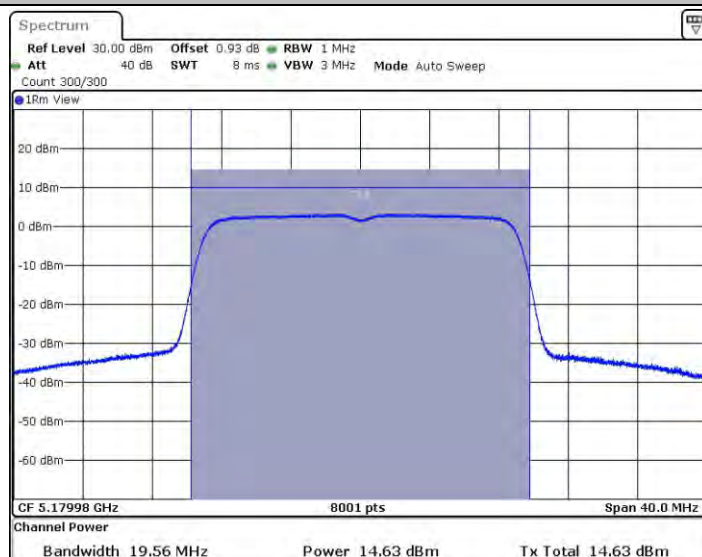
11N40SISO_Ant2_5755



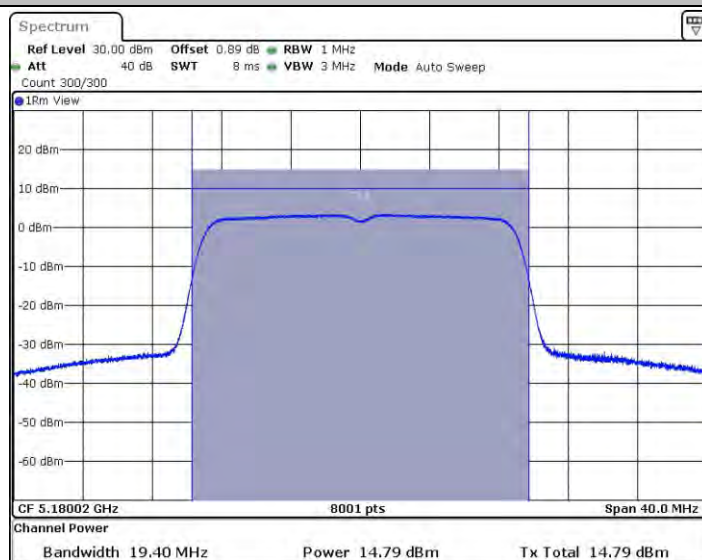
11N40SISO_Ant1_5795



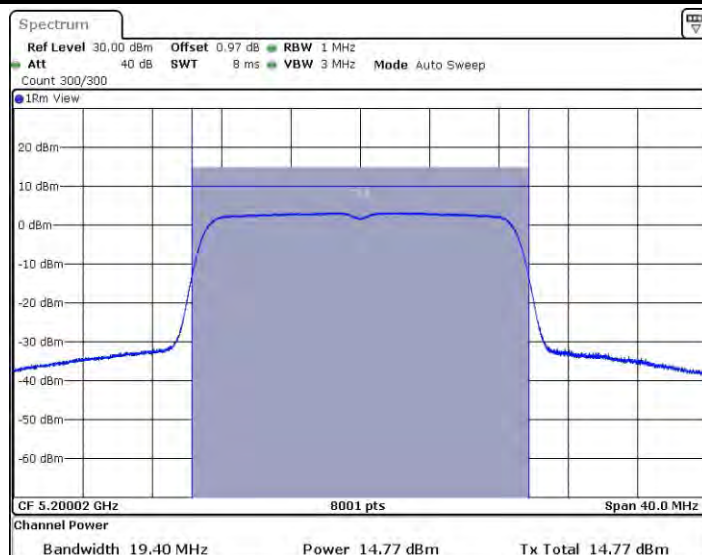
11N40SISO_Ant2_5795



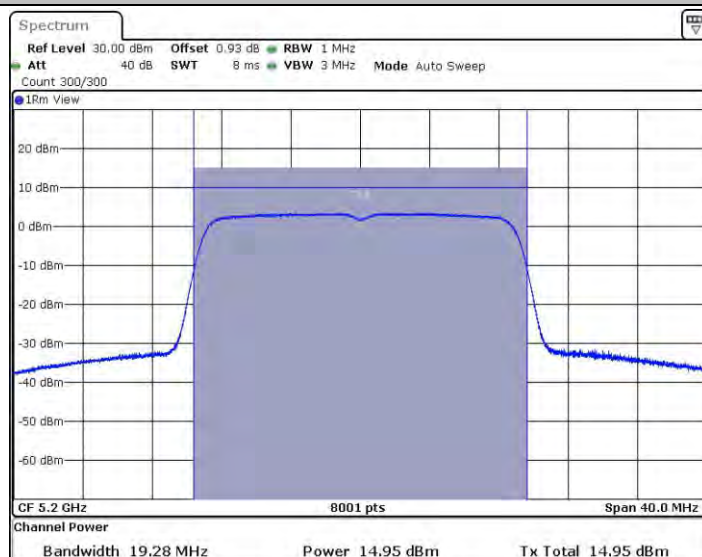
11AC20SISO_Ant1_5180



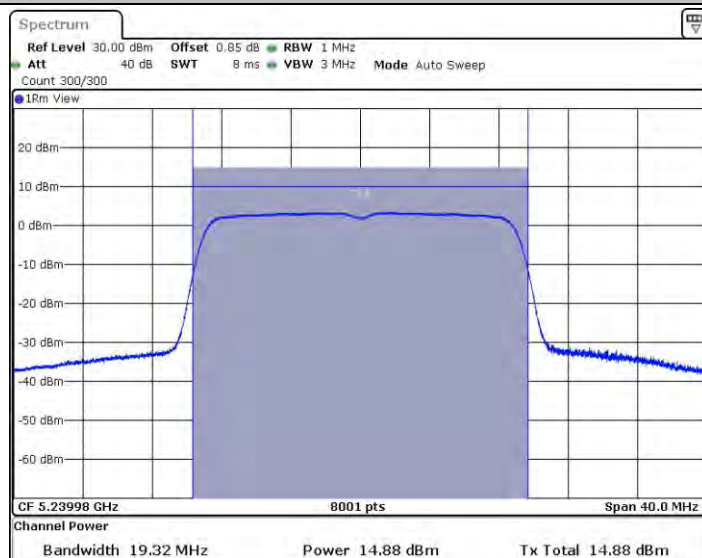
11AC20SISO_Ant2_5180



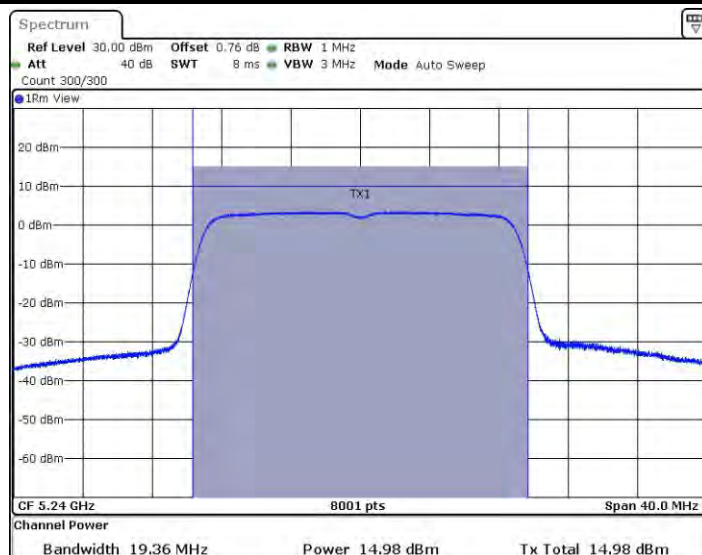
11AC20SISO_Ant1_5200



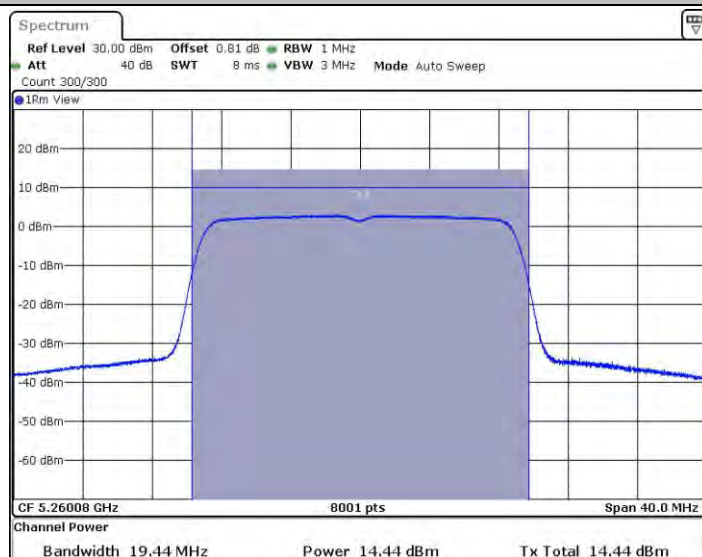
11AC20SISO_Ant2_5200



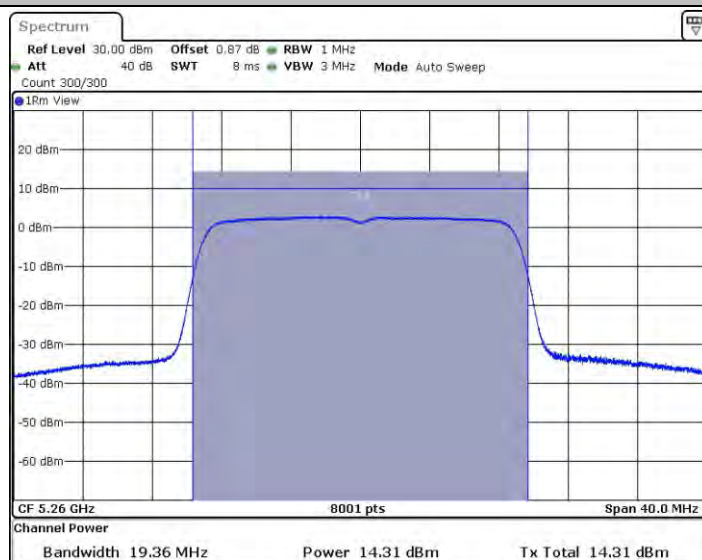
11AC20SISO_Ant1_5240



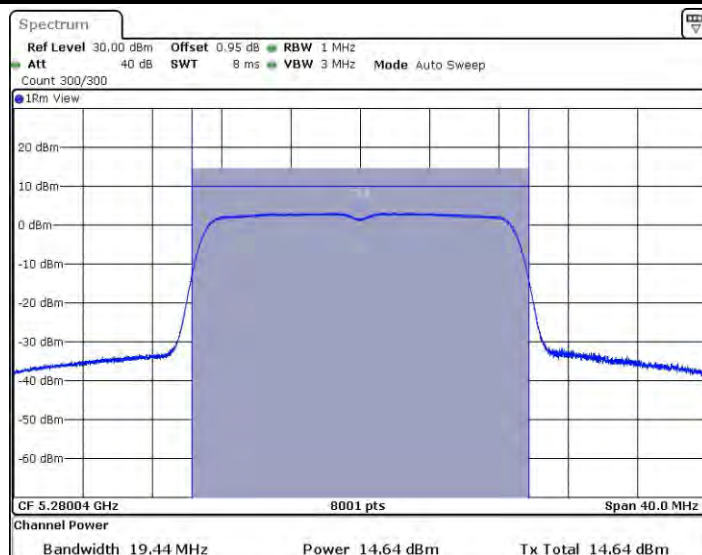
11AC20SISO_Ant2_5240



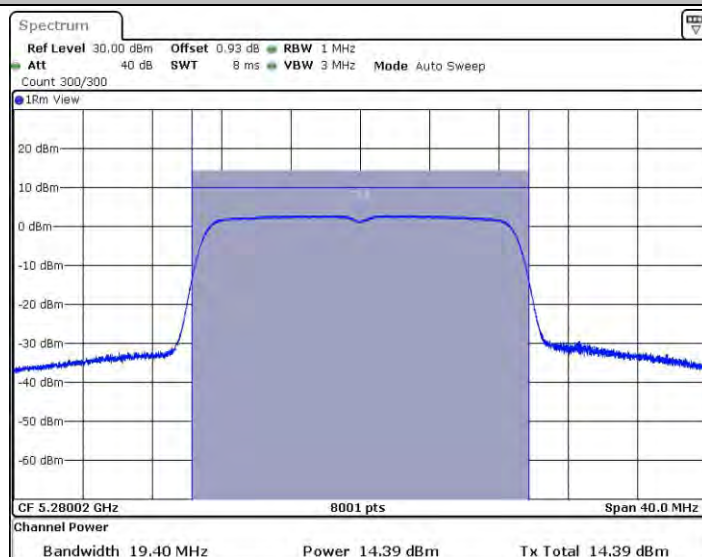
11AC20SISO_Ant1_5260



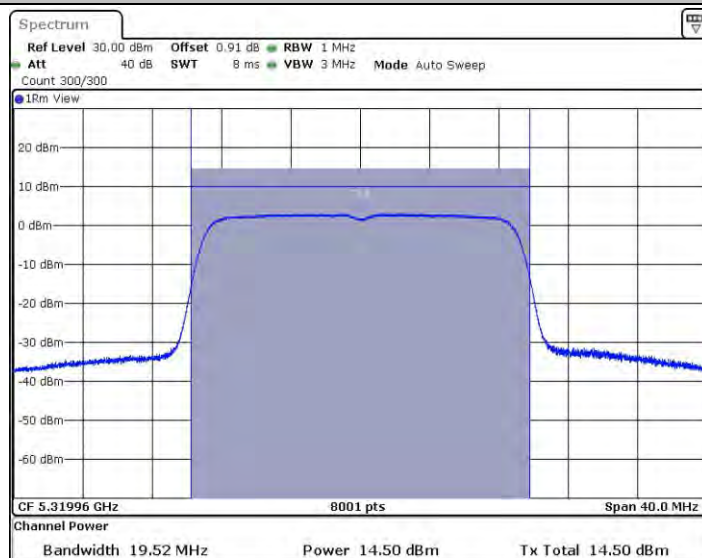
11AC20SISO_Ant2_5260



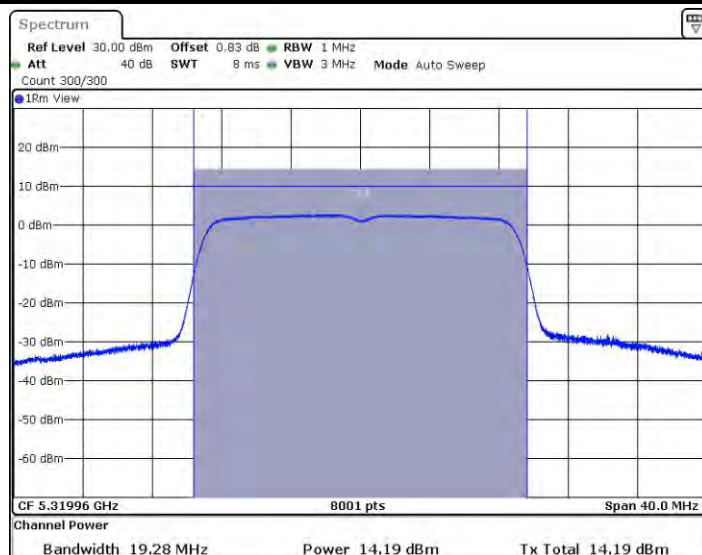
11AC20SISO_Ant1_5280



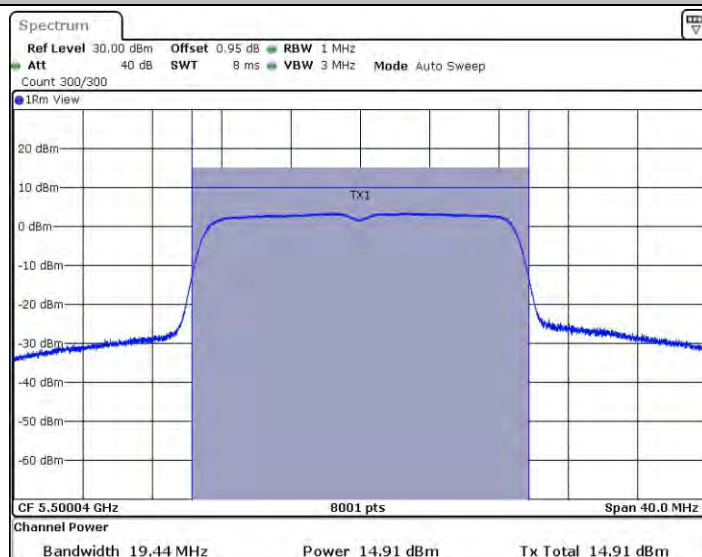
11AC20SISO_Ant2_5280



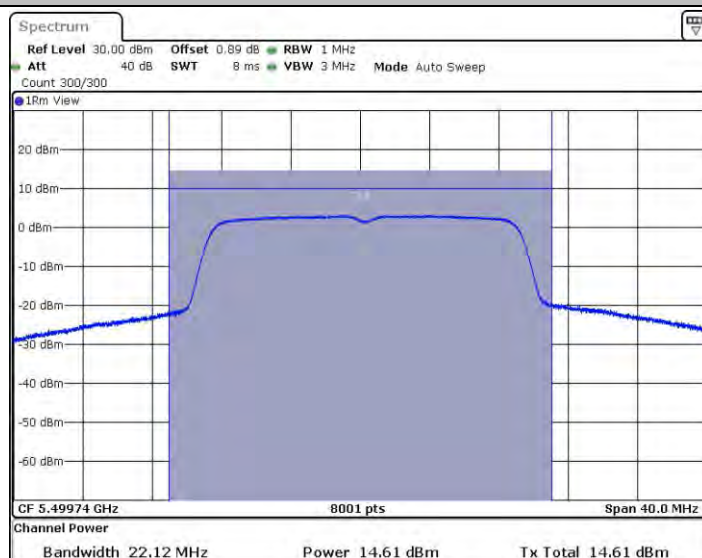
11AC20SISO_Ant1_5320



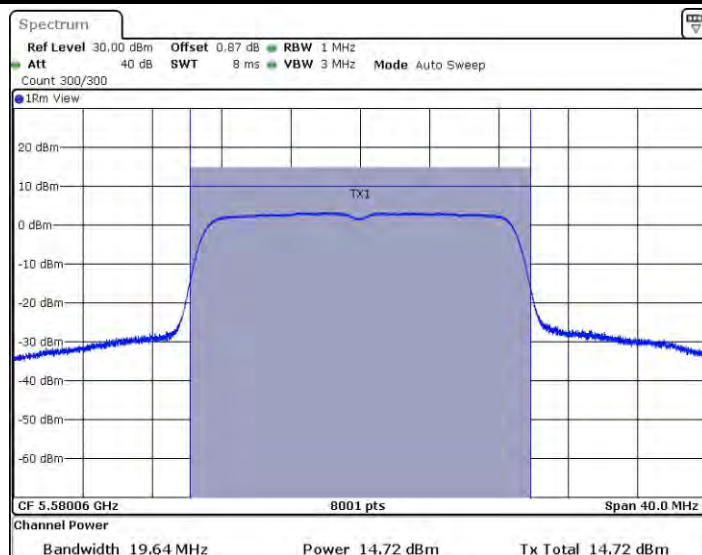
11AC20SISO_Ant2_5320



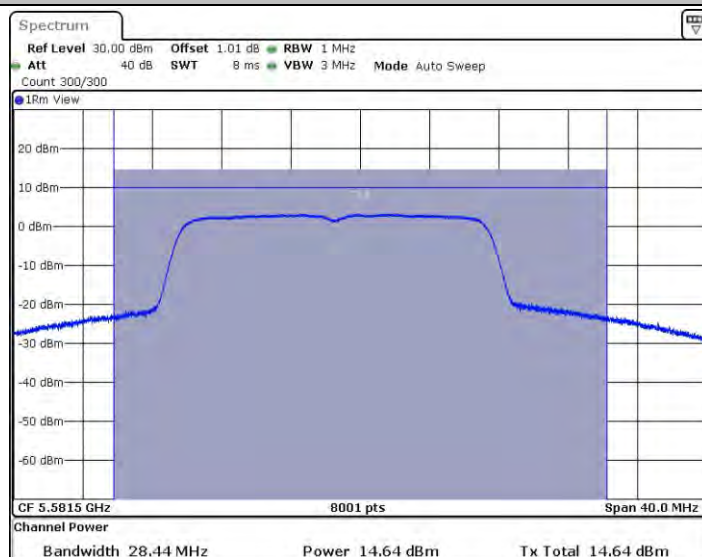
11AC20SISO_Ant1_5500



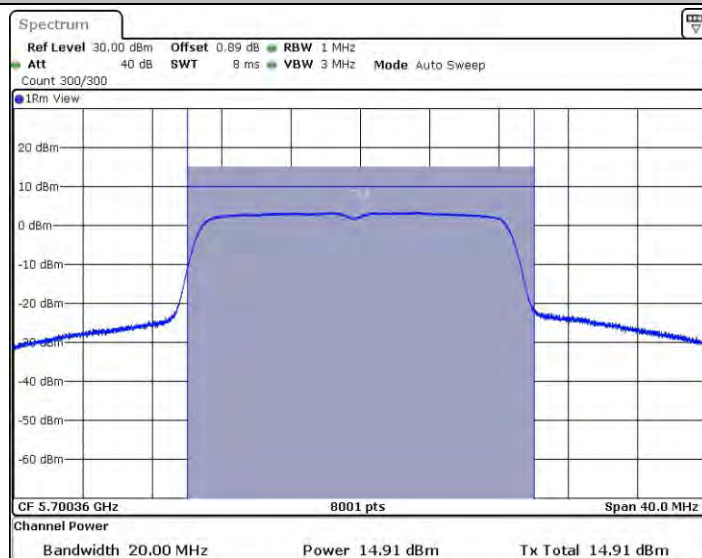
11AC20SISO_Ant2_5500



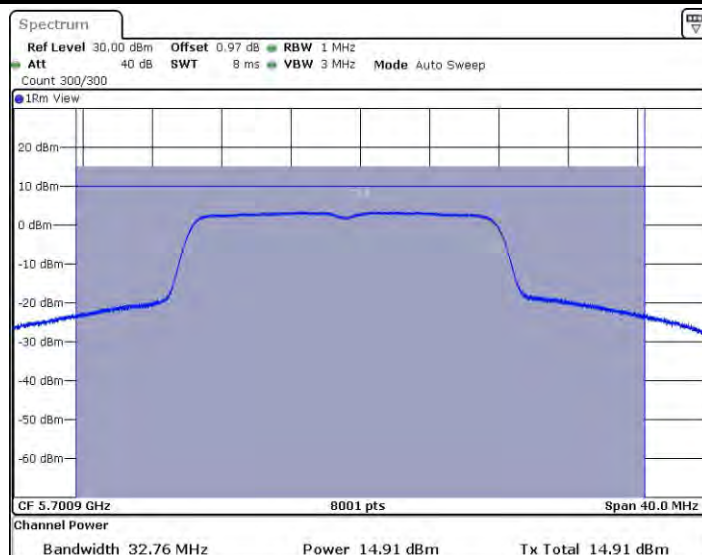
11AC20SISO_Ant1_5580



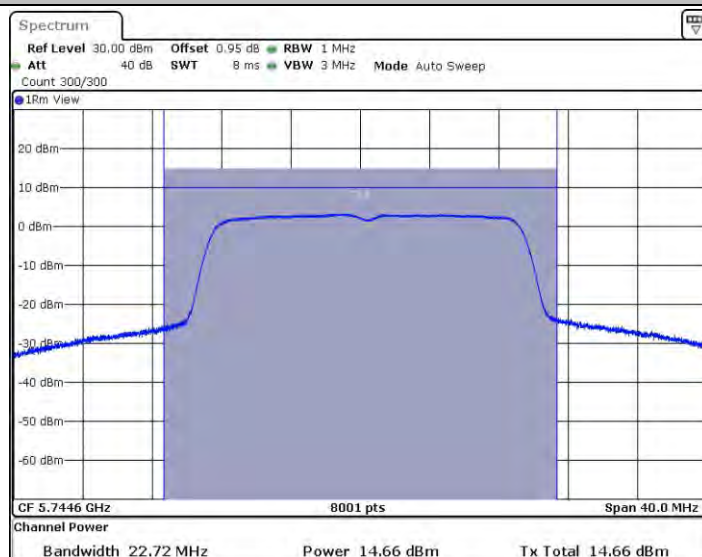
11AC20SISO_Ant2_5580



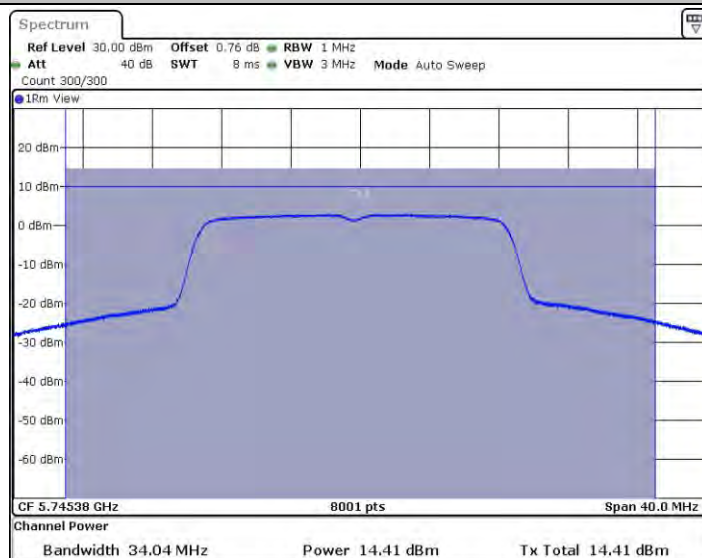
11AC20SISO_Ant1_5700



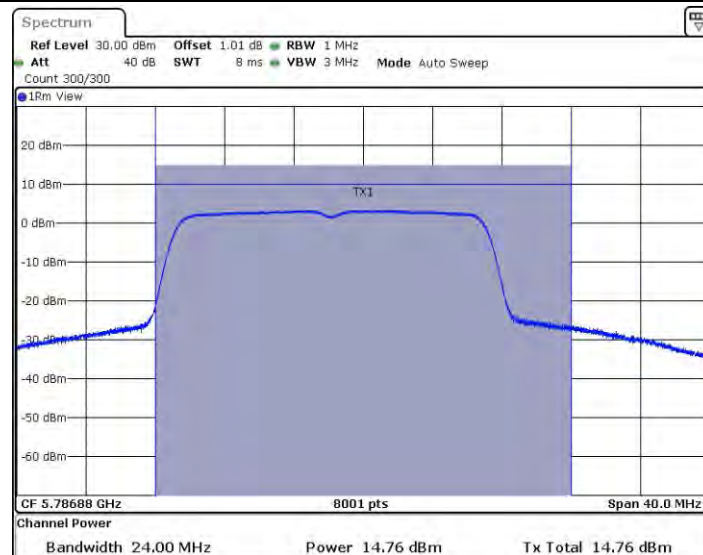
11AC20SISO_Ant2_5700



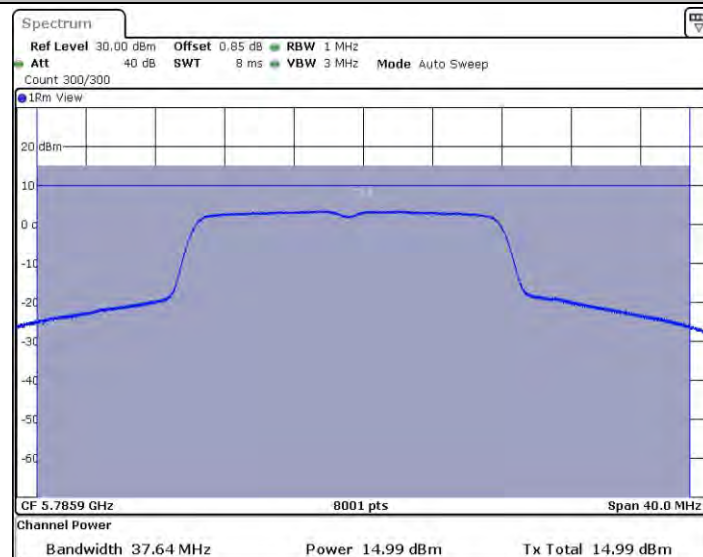
11AC20SISO_Ant1_5745



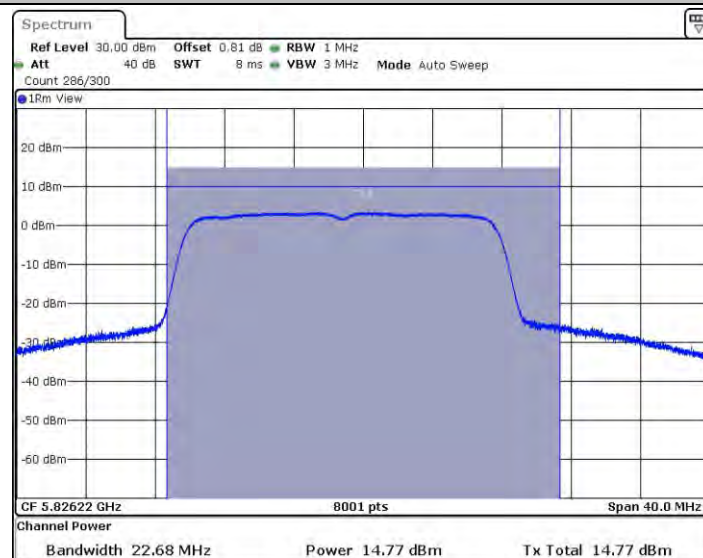
11AC20SISO_Ant2_5745



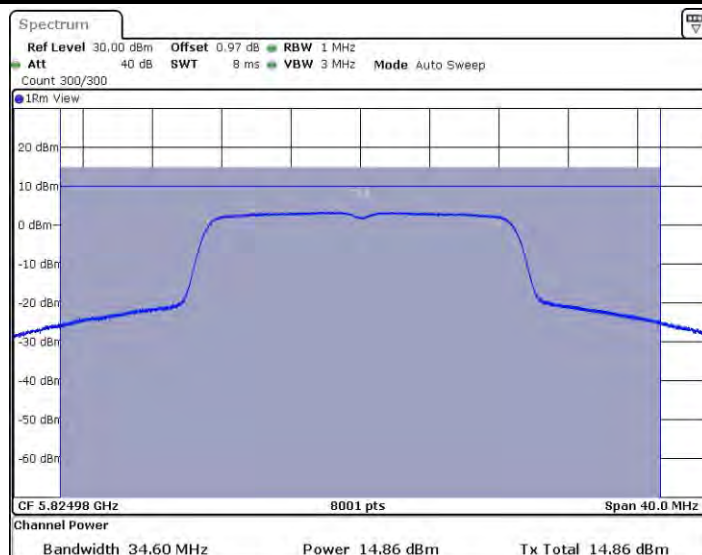
11AC20SISO_Ant1_5785



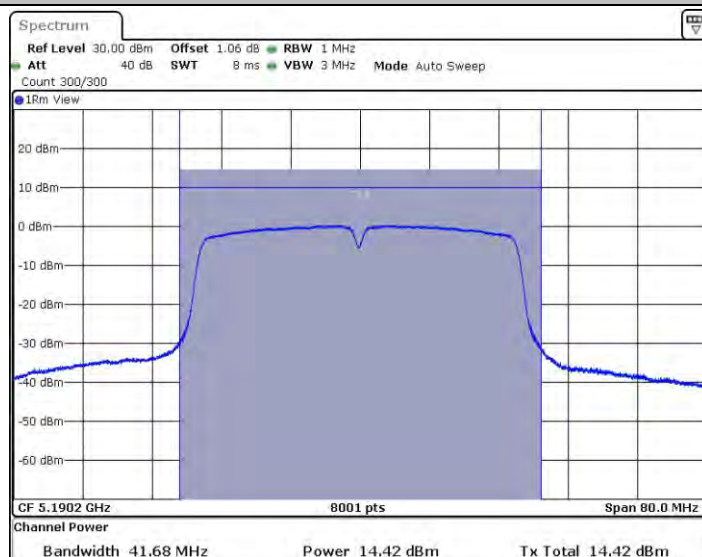
11AC20SISO_Ant2_5785



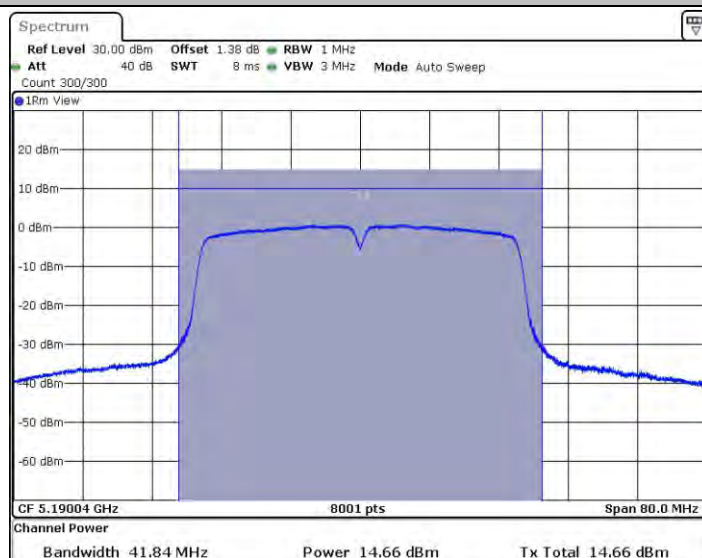
11AC20SISO_Ant1_5825



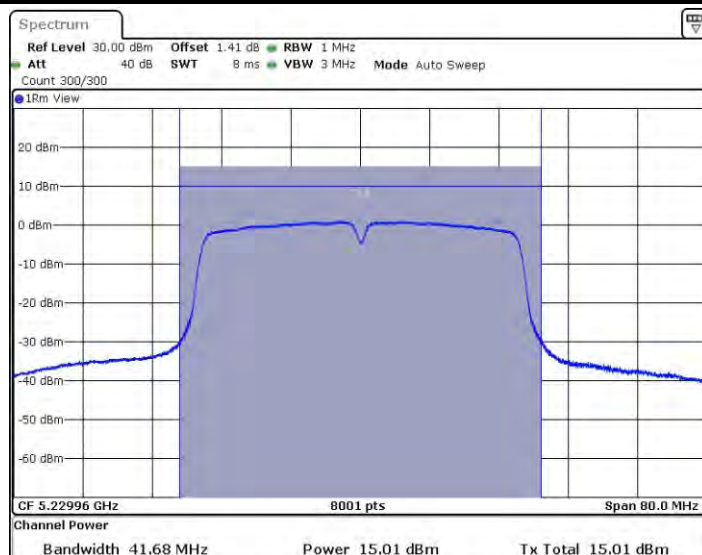
11AC20SISO_Ant2_5825



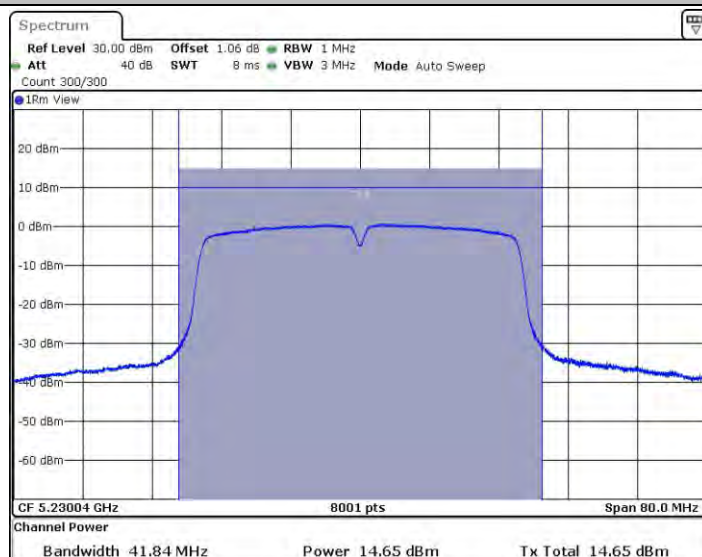
11AC40SISO_Ant1_5190



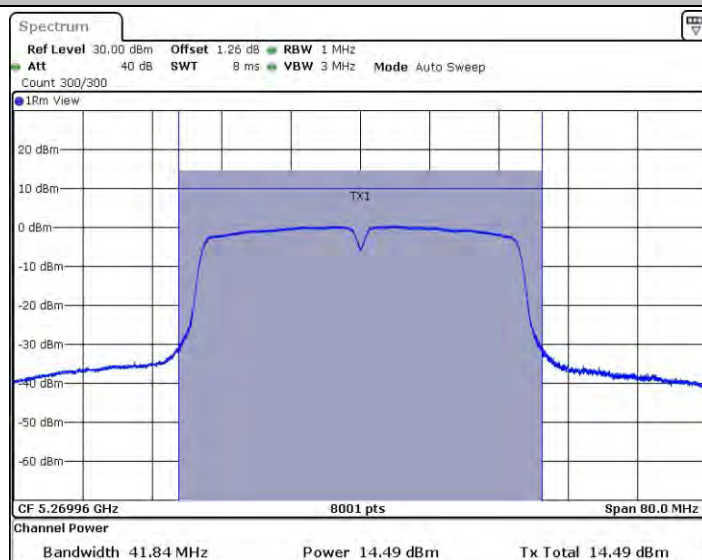
11AC40SISO_Ant2_5190



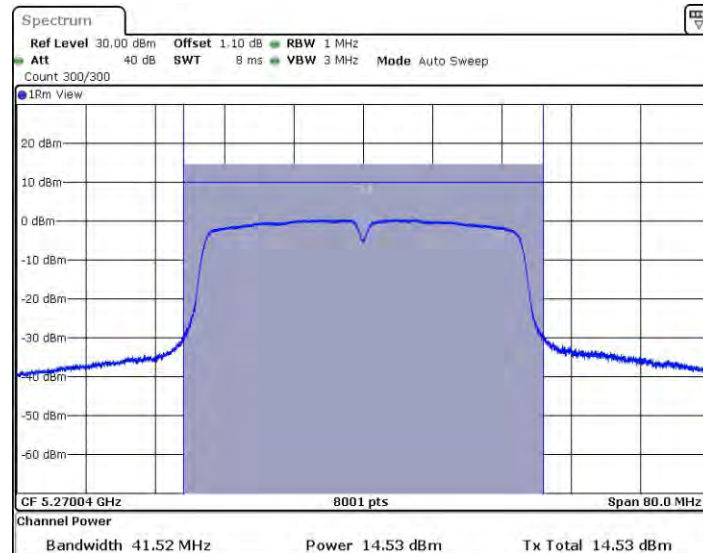
11AC40SISO_Ant1_5230



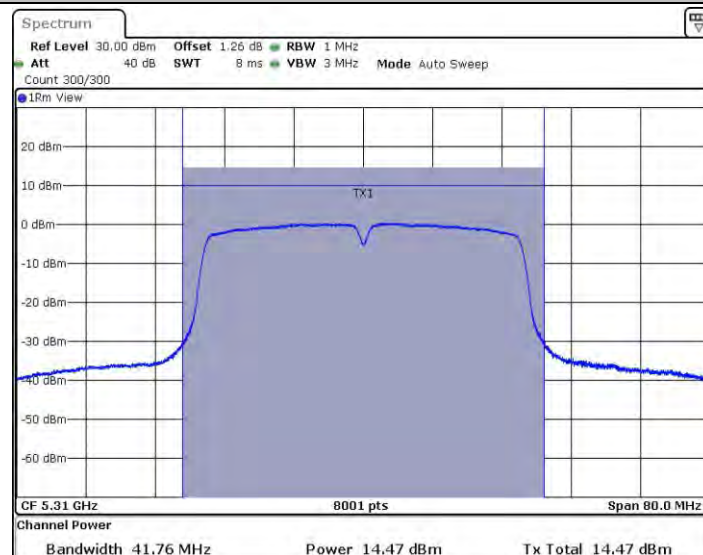
11AC40SISO_Ant2_5230



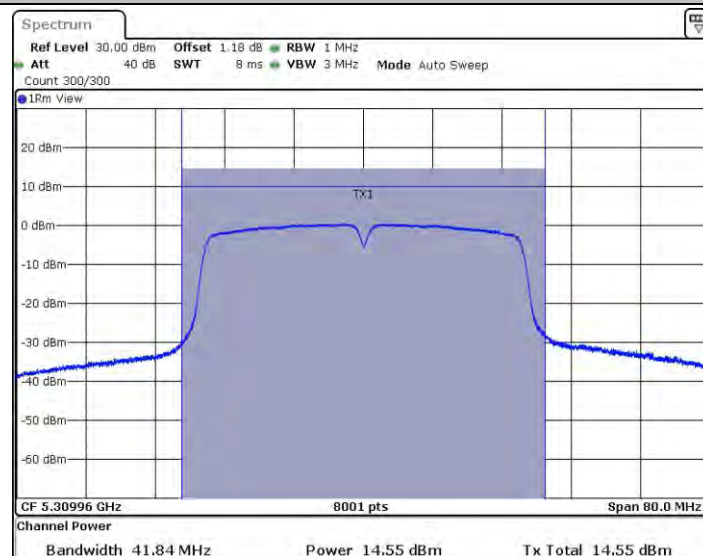
11AC40SISO_Ant1_5270



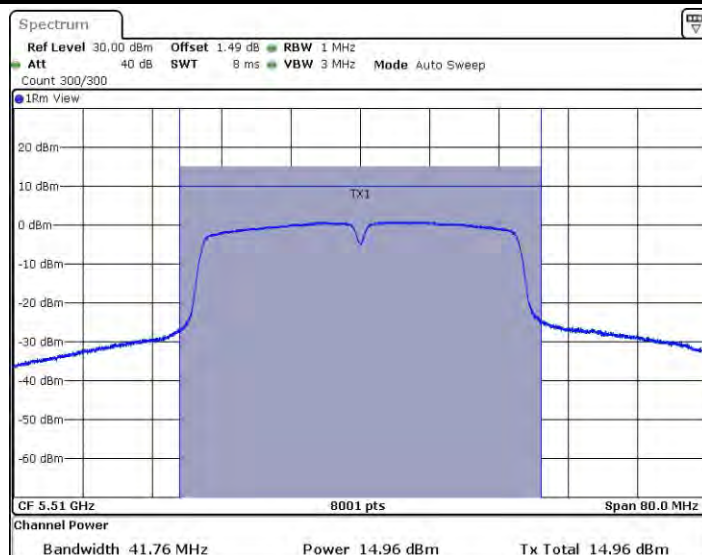
11AC40SISO_Ant2_5270



11AC40SISO_Ant1_5310



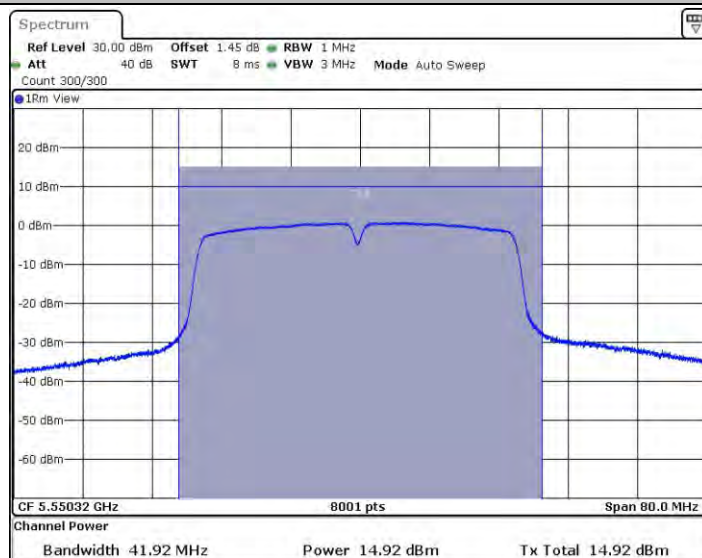
11AC40SISO_Ant2_5310



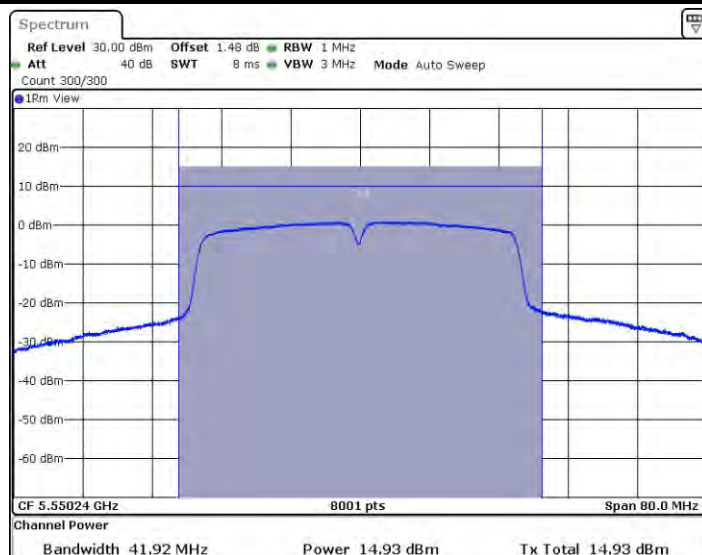
11AC40SISO_Ant1_5510



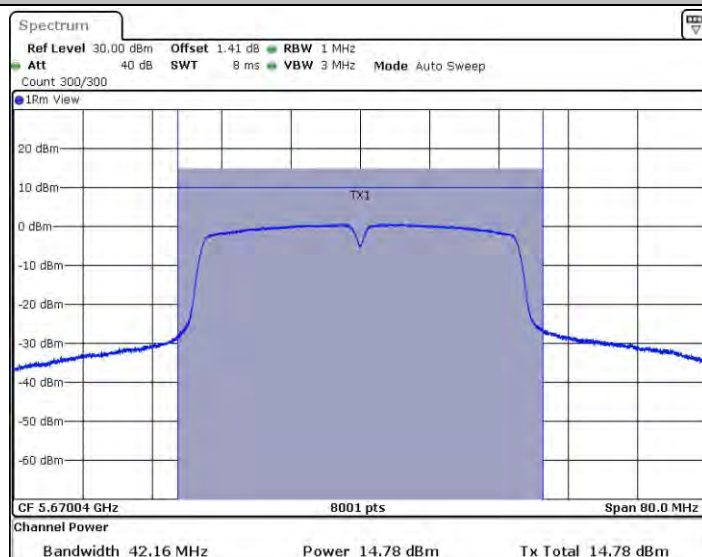
11AC40SISO_Ant2_5510



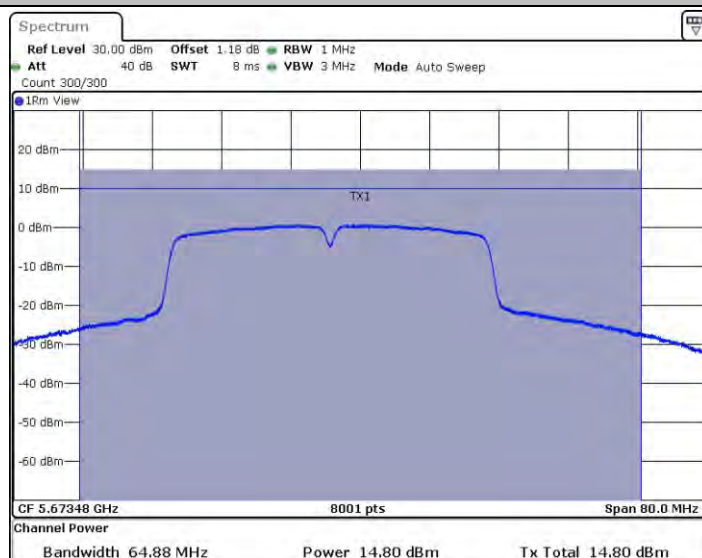
11AC40SISO_Ant1_5550



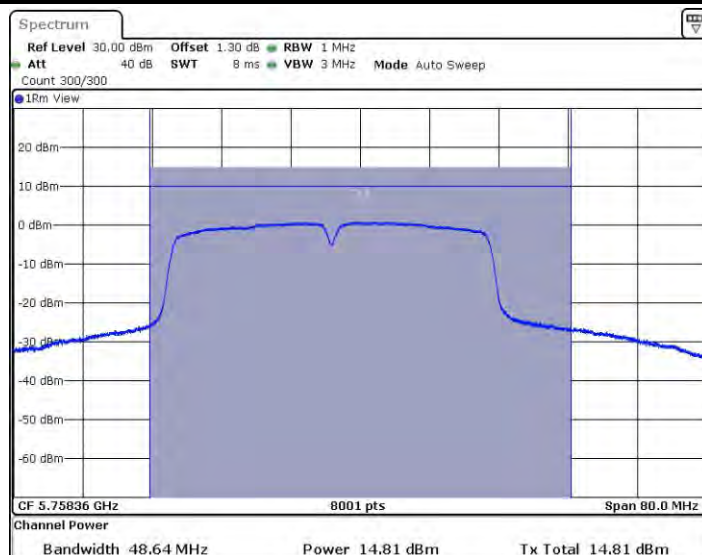
11AC40SISO_Ant2_5550



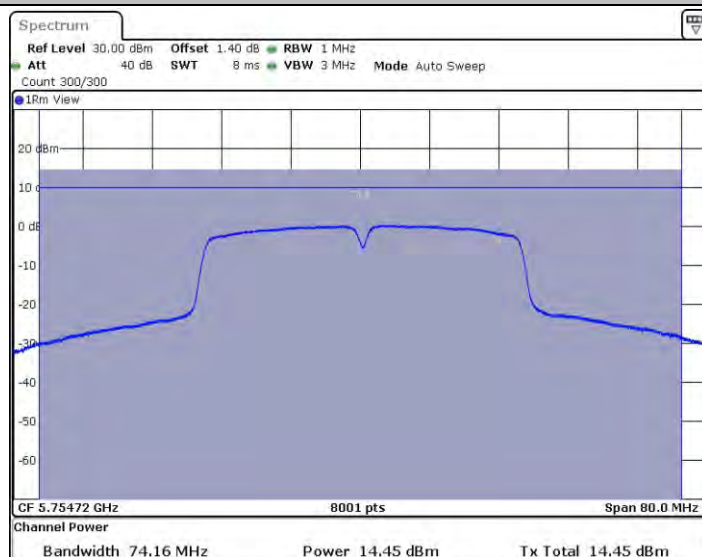
11AC40SISO_Ant1_5670



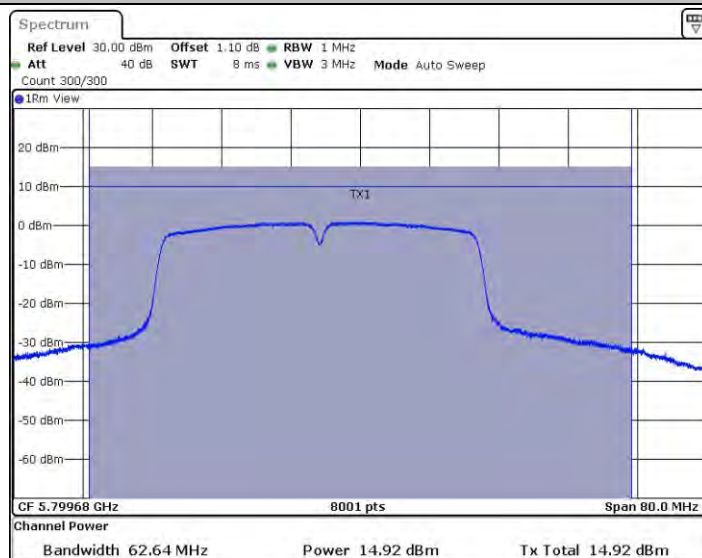
11AC40SISO_Ant2_5670



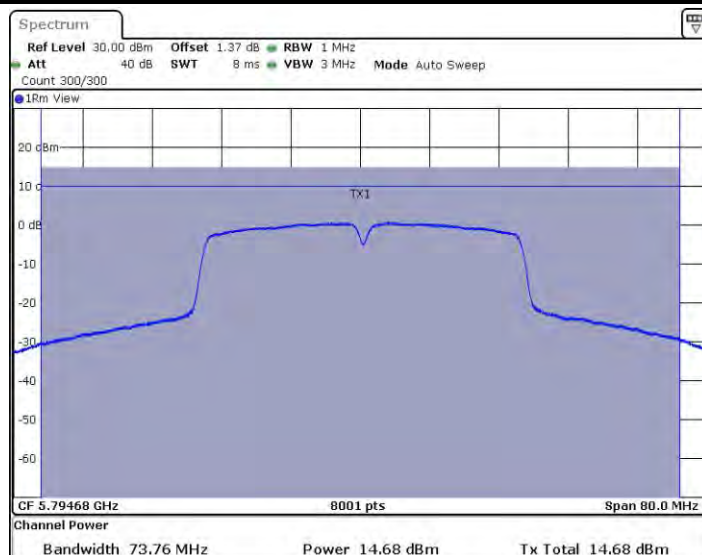
11AC40SISO_Ant1_5755



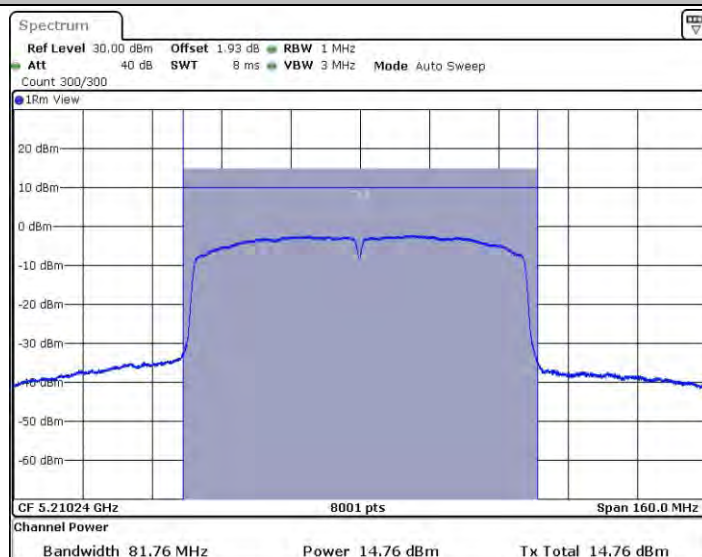
11AC40SISO_Ant2_5755



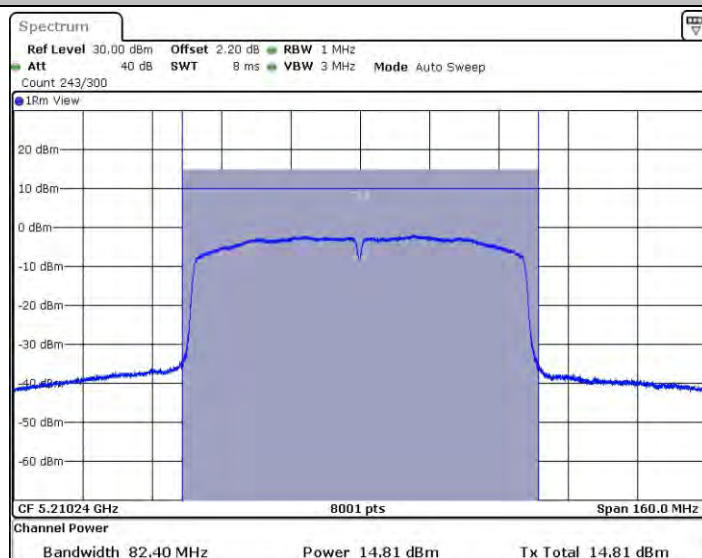
11AC40SISO_Ant1_5795



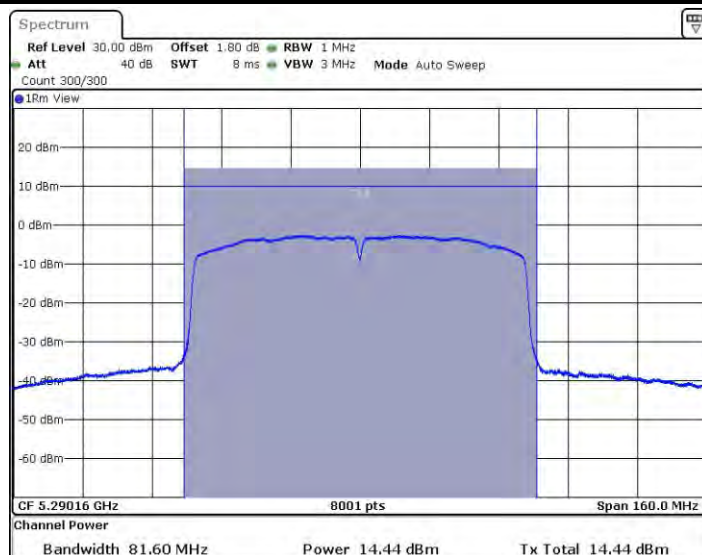
11AC40SISO_Ant2_5795



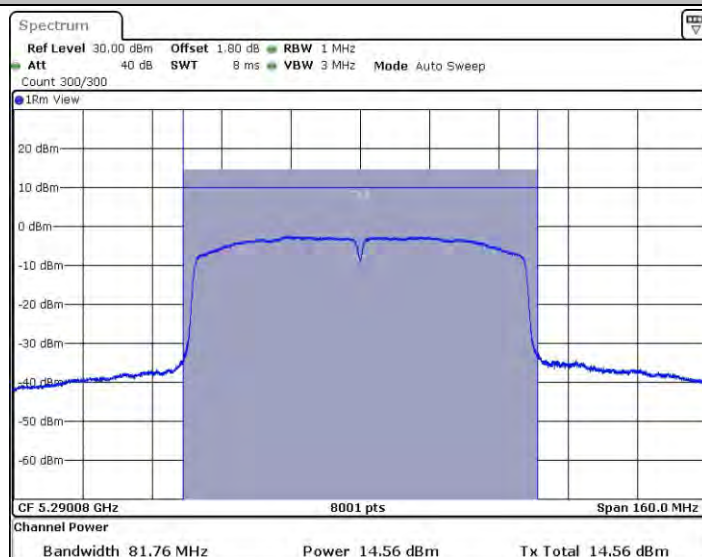
11AC80SISO_Ant1_5210



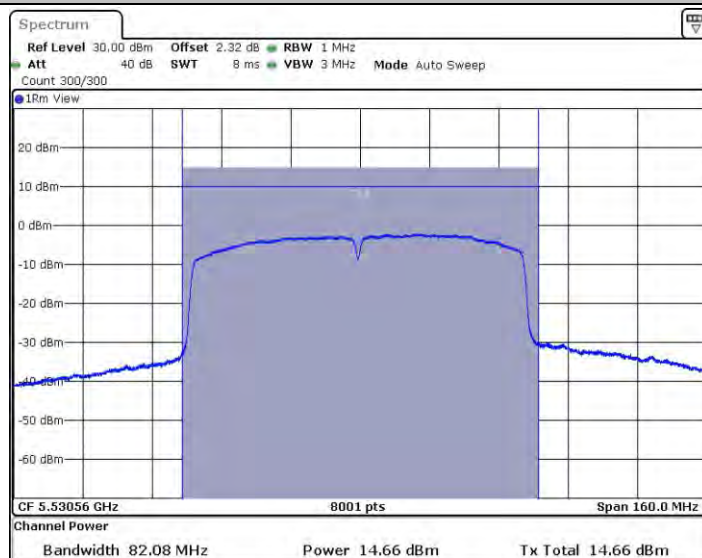
11AC80SISO_Ant2_5210



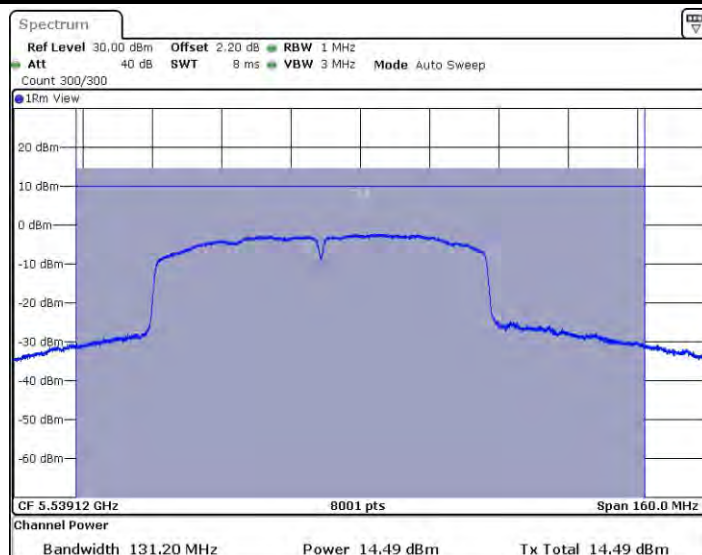
11AC80SISO_Ant1_5290



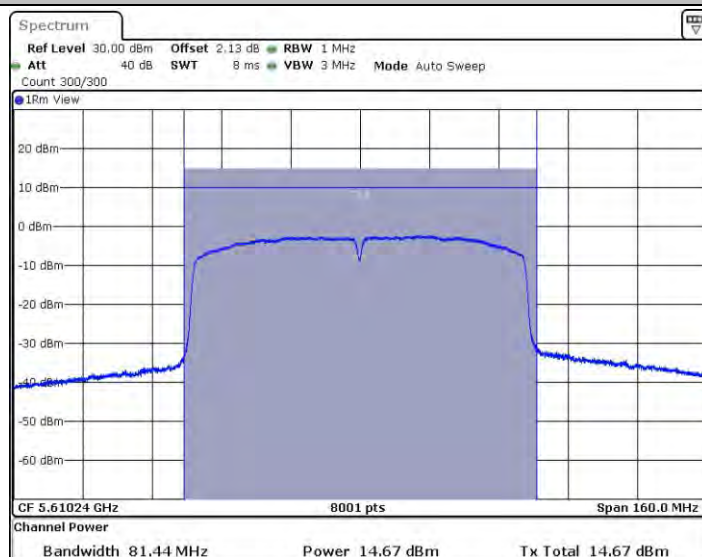
11AC80SISO_Ant2_5290



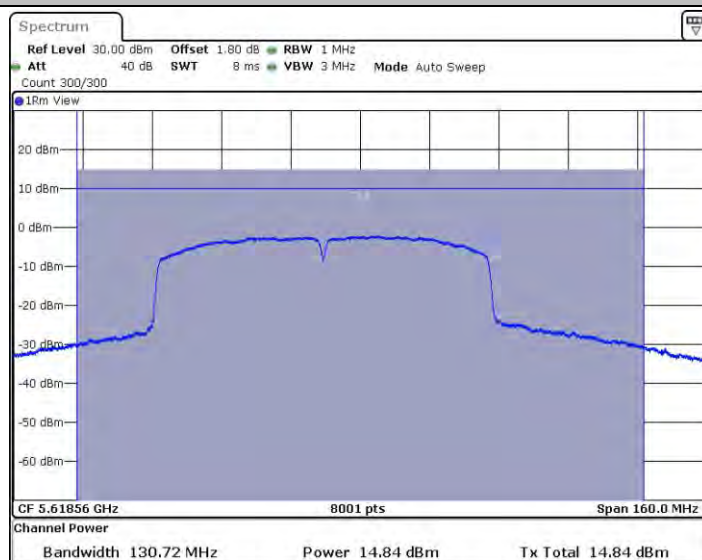
11AC80SISO_Ant1_5530



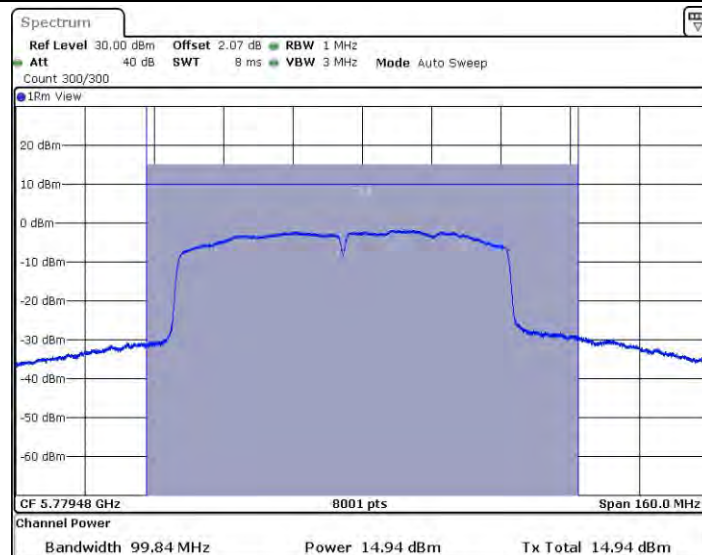
11AC80SISO_Ant2_5530



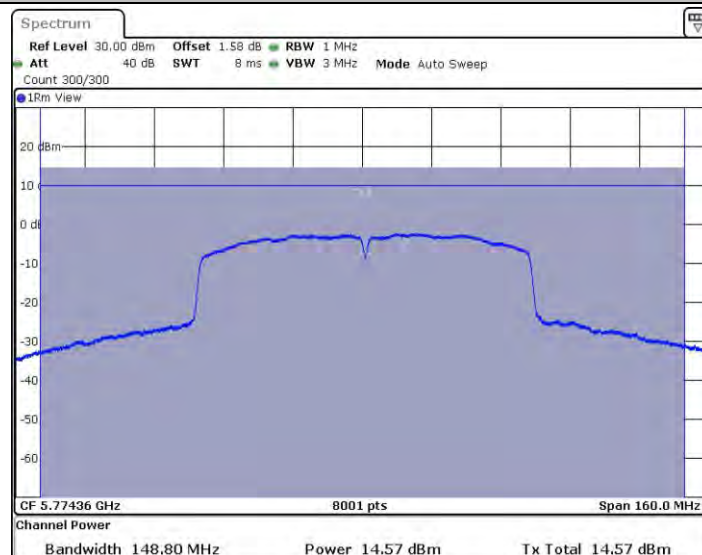
11AC80SISO_Ant1_5610



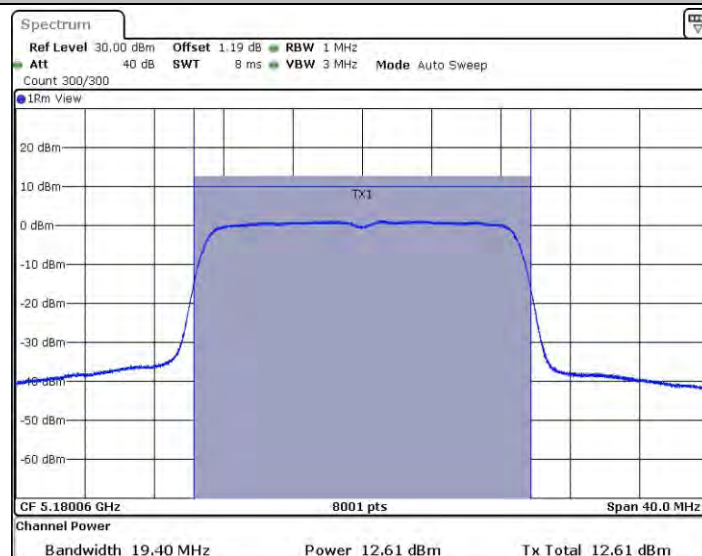
11AC80SISO_Ant2_5610



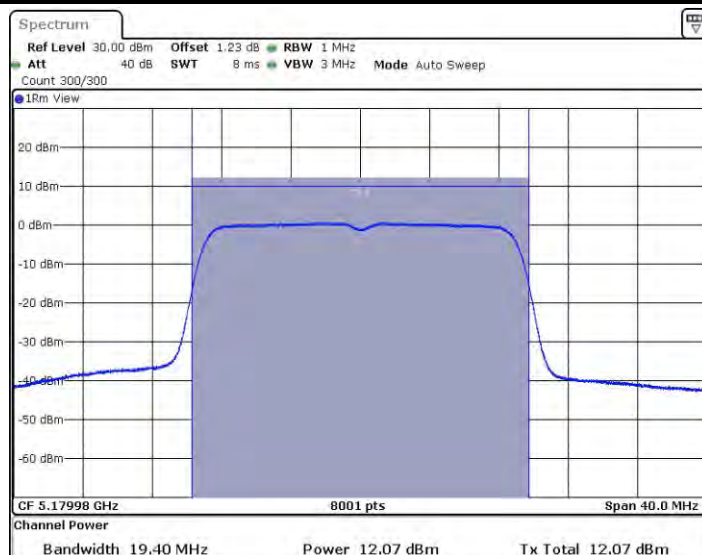
11AC80SISO_Ant1_5775



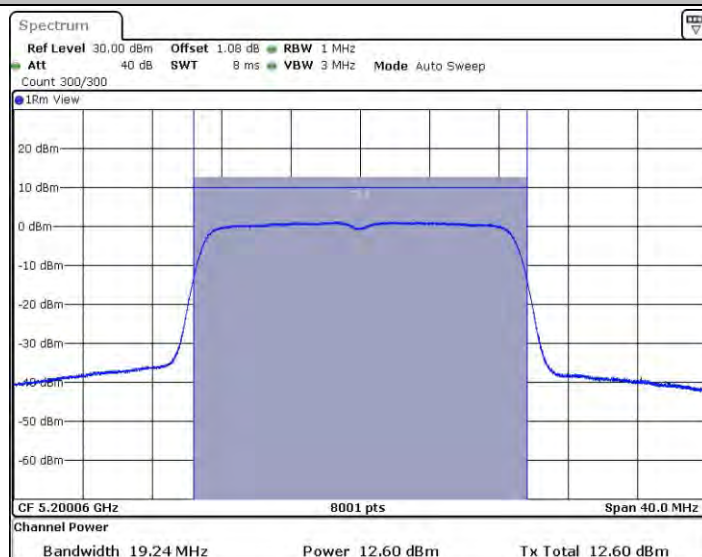
11AC80SISO_Ant2_5775



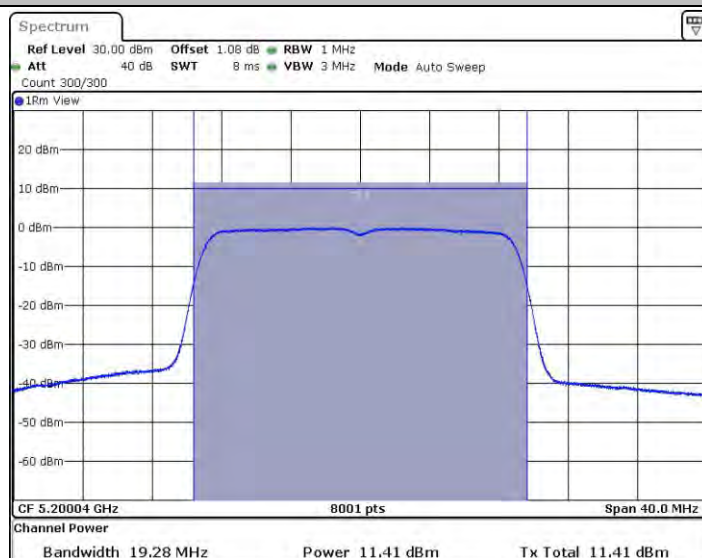
11N20MIMO_Ant1_5180



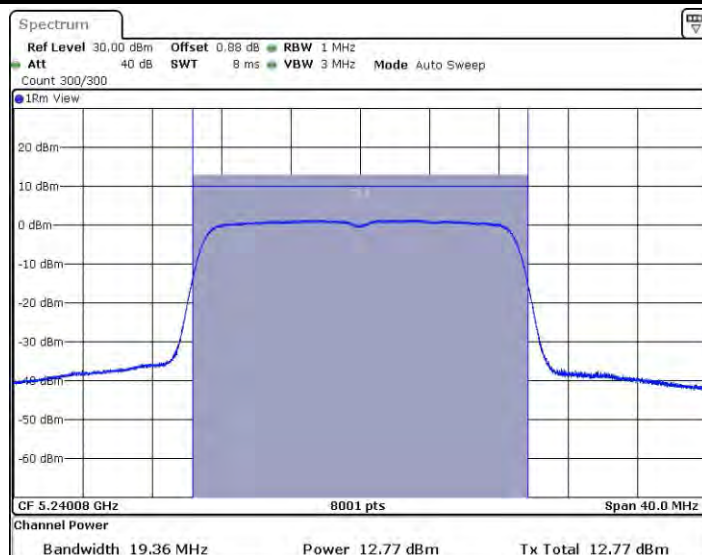
11N20MIMO_Ant2_5180



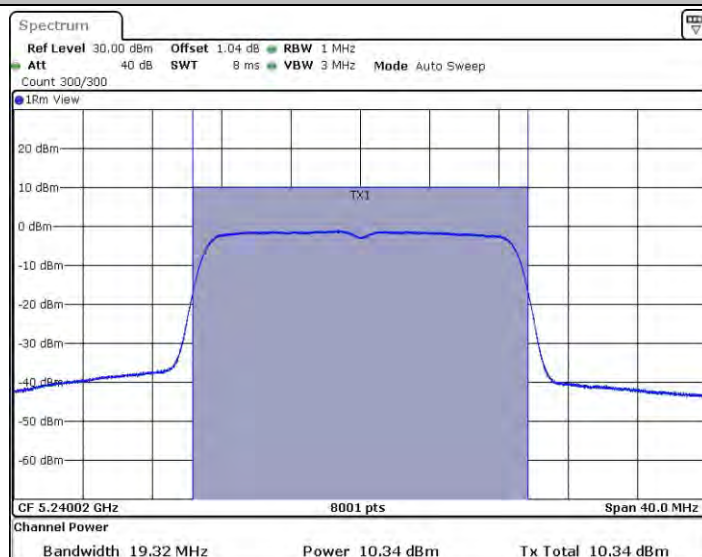
11N20MIMO_Ant1_5200



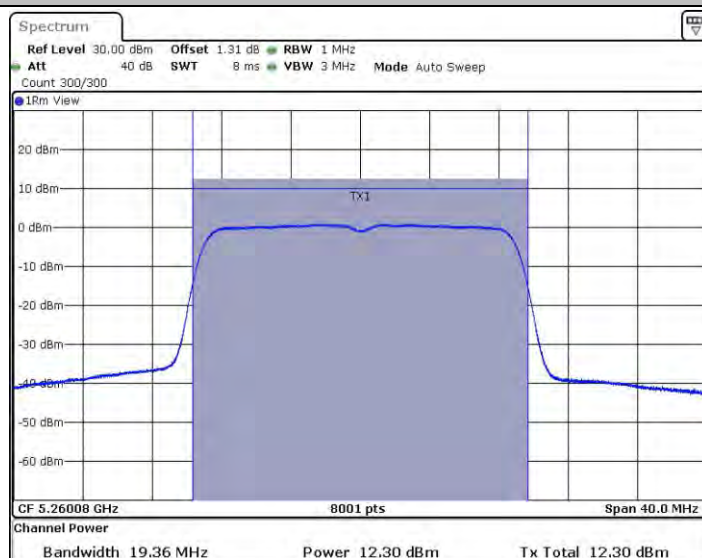
11N20MIMO_Ant2_5200



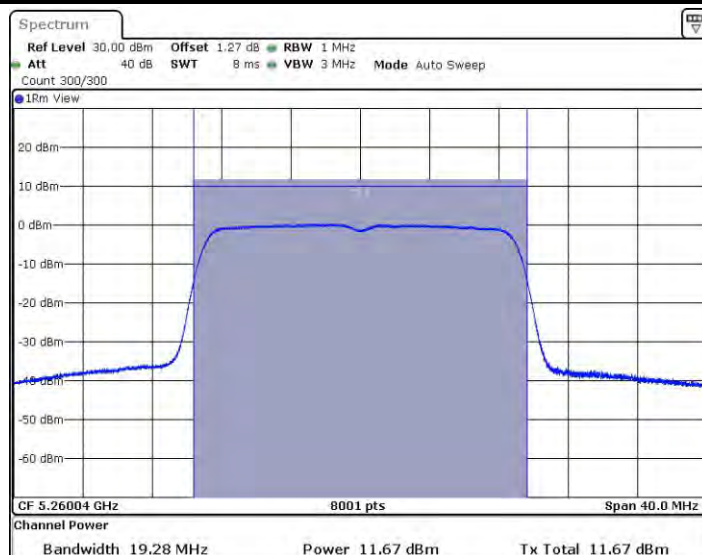
11N20MIMO_Ant1_5240



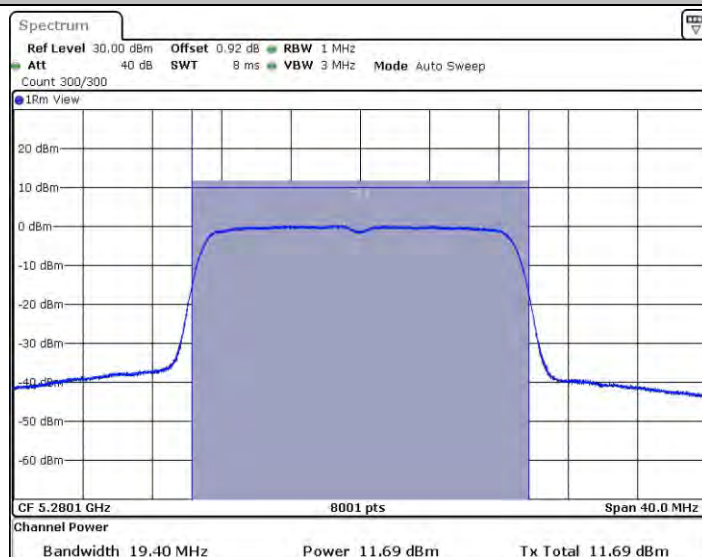
11N20MIMO_Ant2_5240



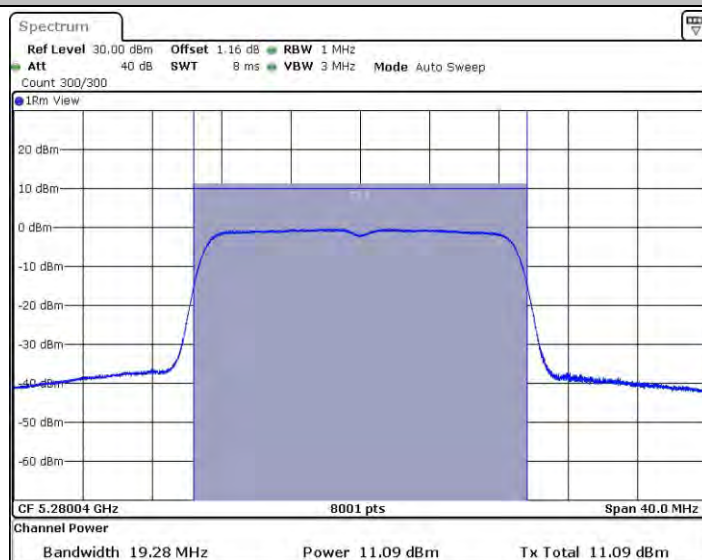
11N20MIMO_Ant1_5260



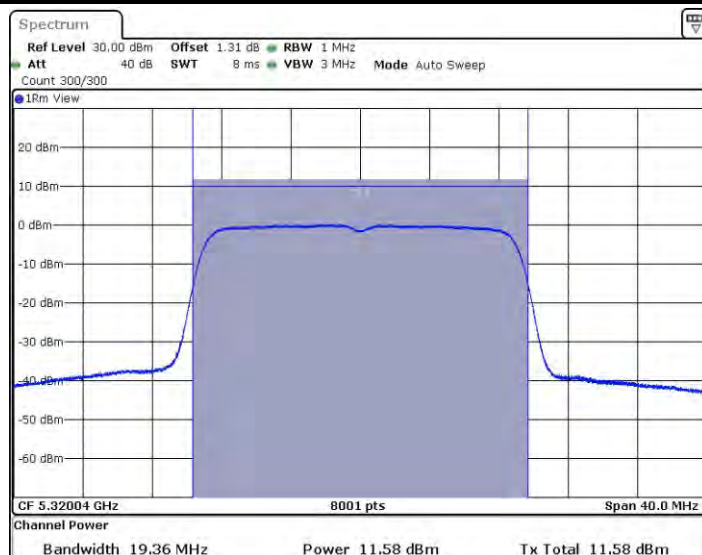
11N20MIMO_Ant2_5260



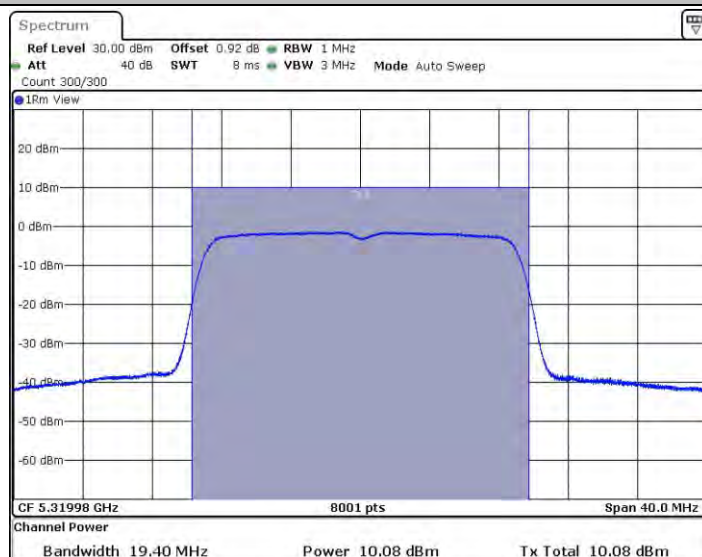
11N20MIMO_Ant1_5280



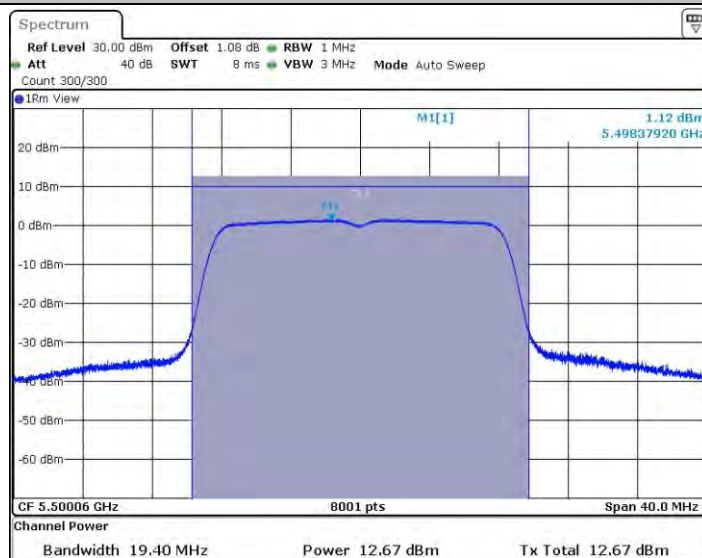
11N20MIMO_Ant2_5280



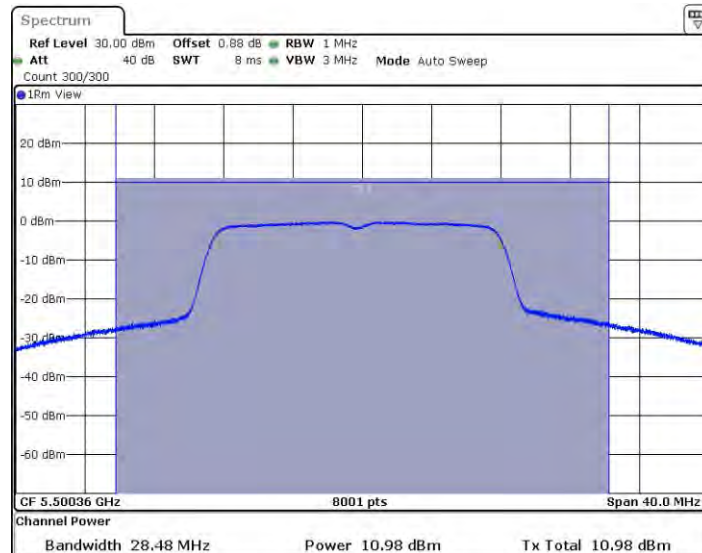
11N20MIMO_Ant1_5320



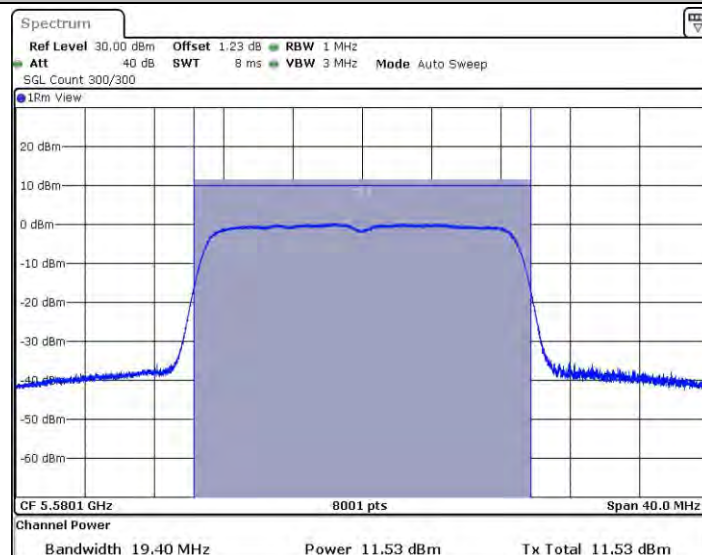
11N20MIMO_Ant2_5320



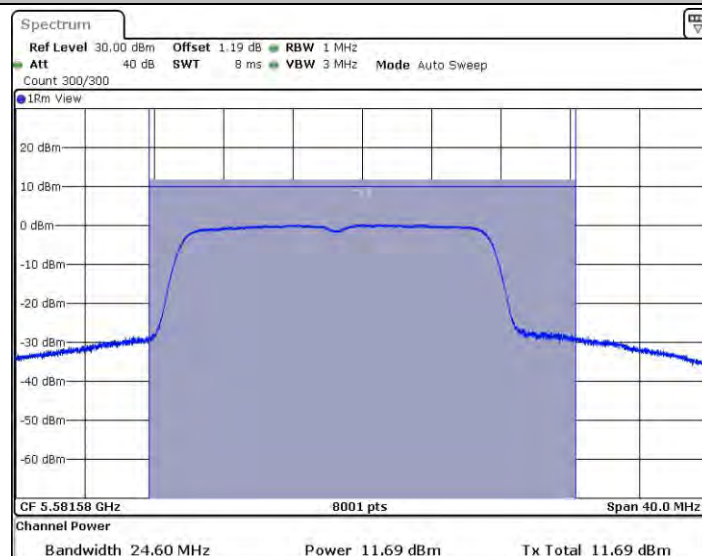
11N20MIMO_Ant1_5500



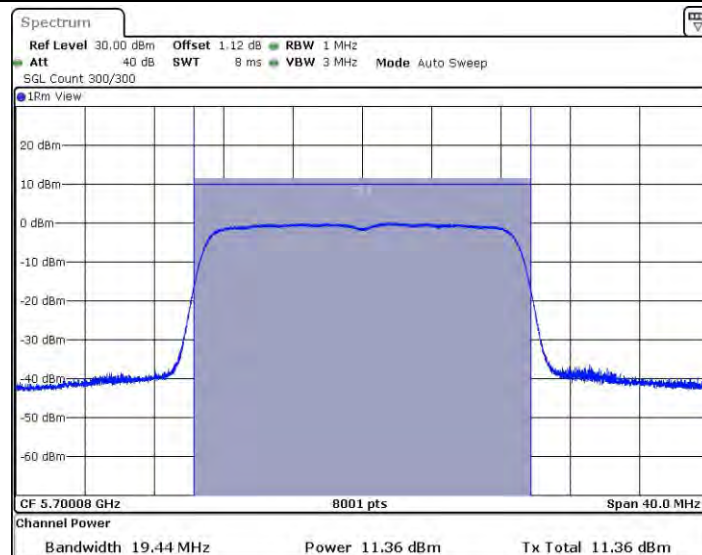
11N20MIMO_Ant2_5500



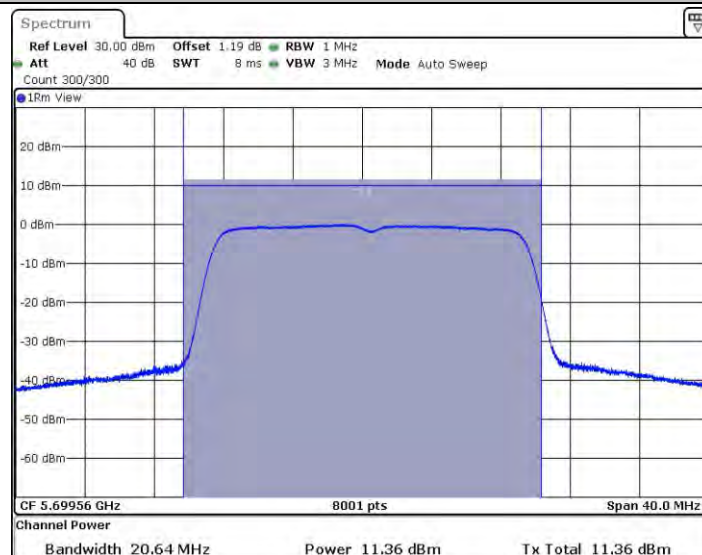
11N20MIMO_Ant1_5580



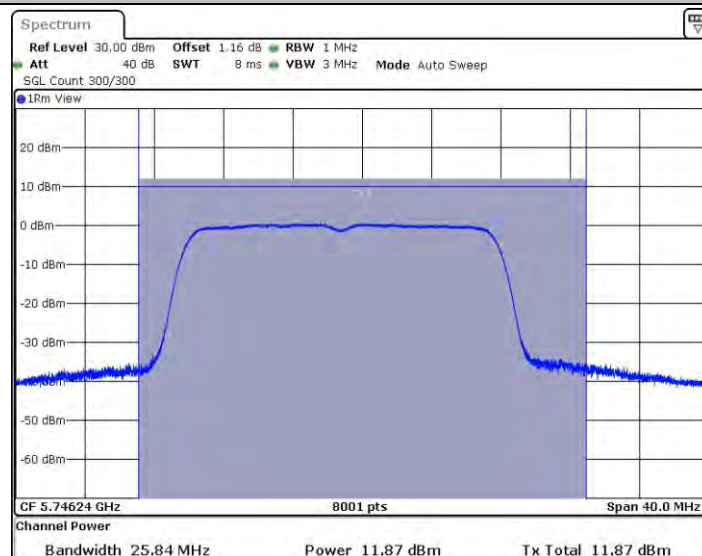
11N20MIMO_Ant2_5580



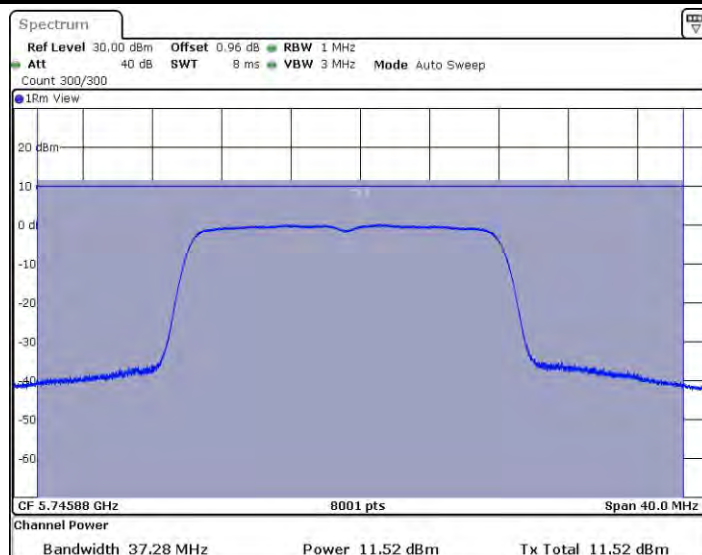
11N20MIMO_Ant1_5700



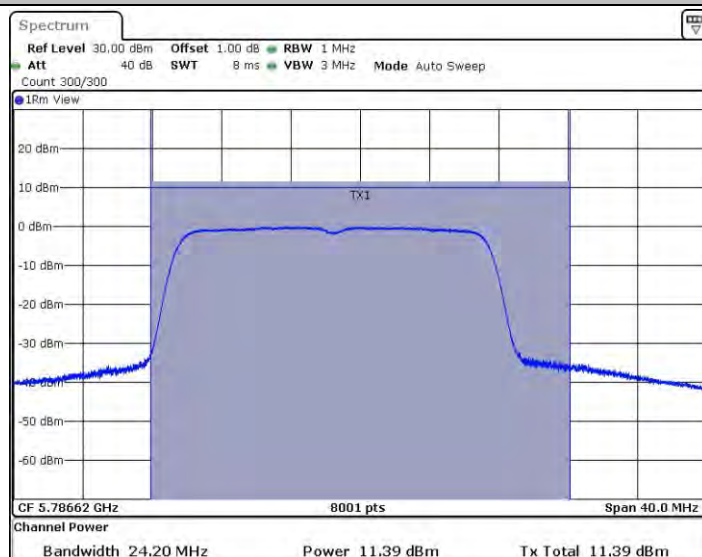
11N20MIMO_Ant2_5700



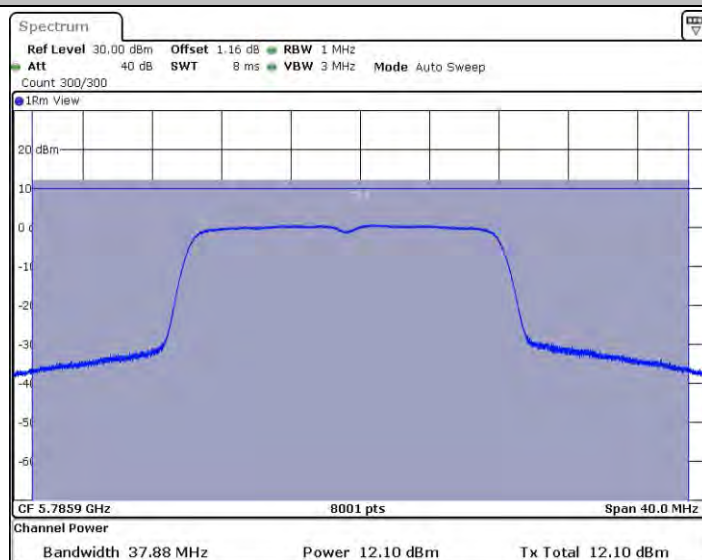
11N20MIMO_Ant1_5745



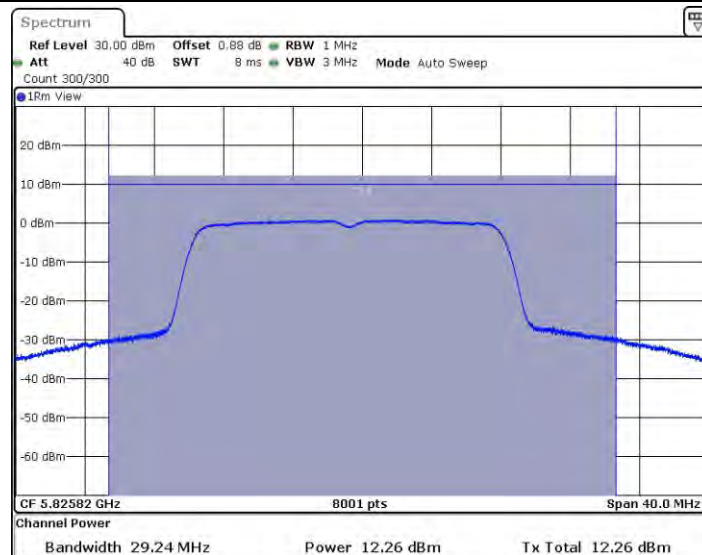
11N20MIMO_Ant2_5745



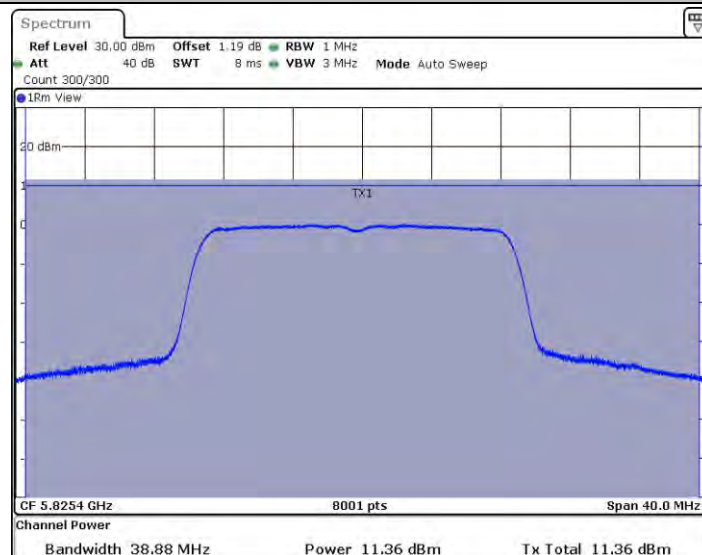
11N20MIMO_Ant1_5785



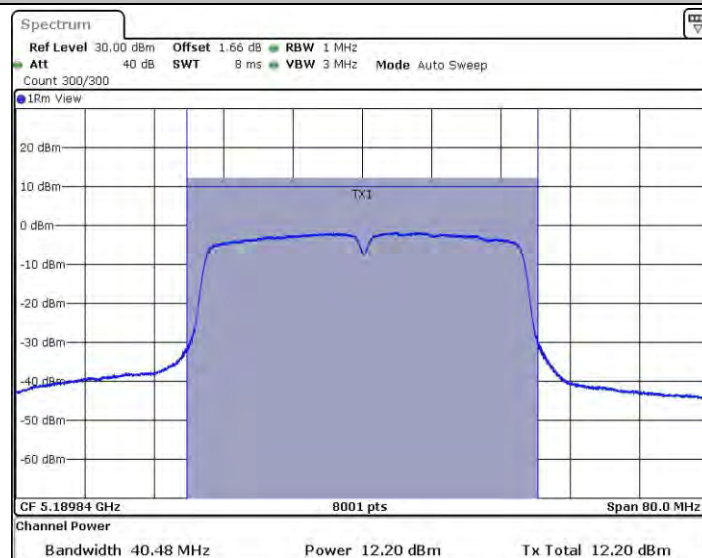
11N20MIMO_Ant2_5785



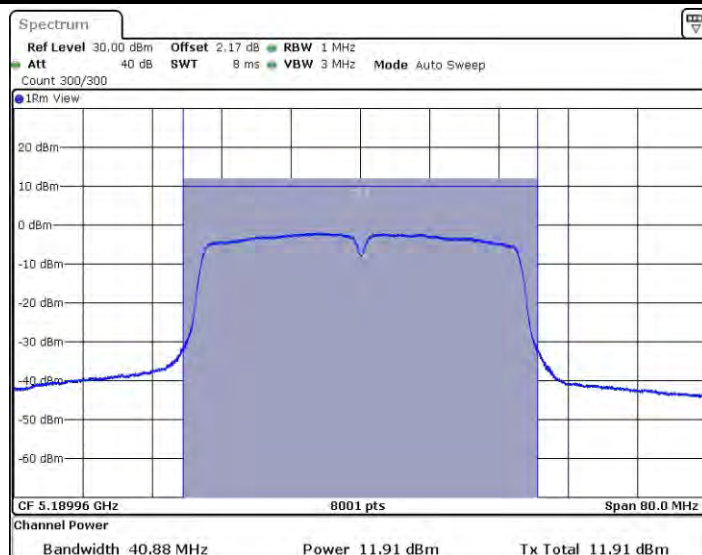
11N20MIMO_Ant1_5825



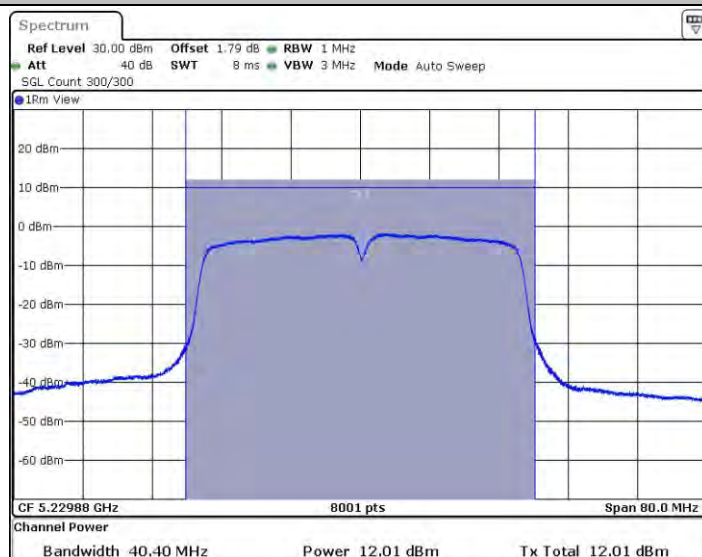
11N20MIMO_Ant2_5825



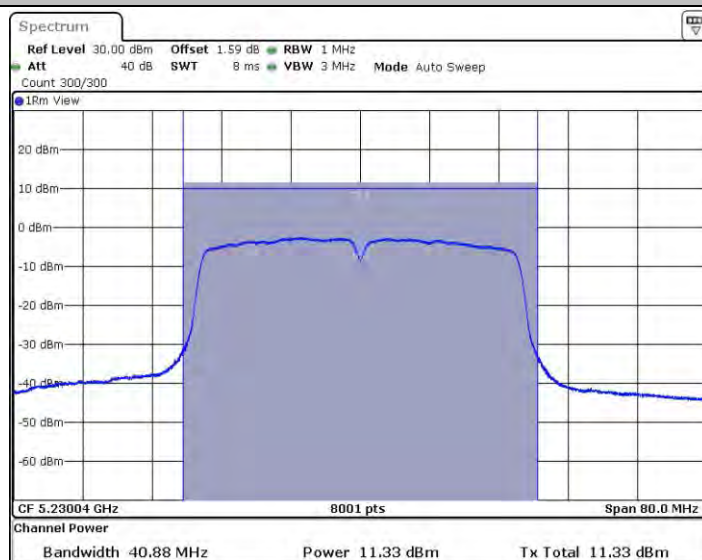
11N40MIMO_Ant1_5190



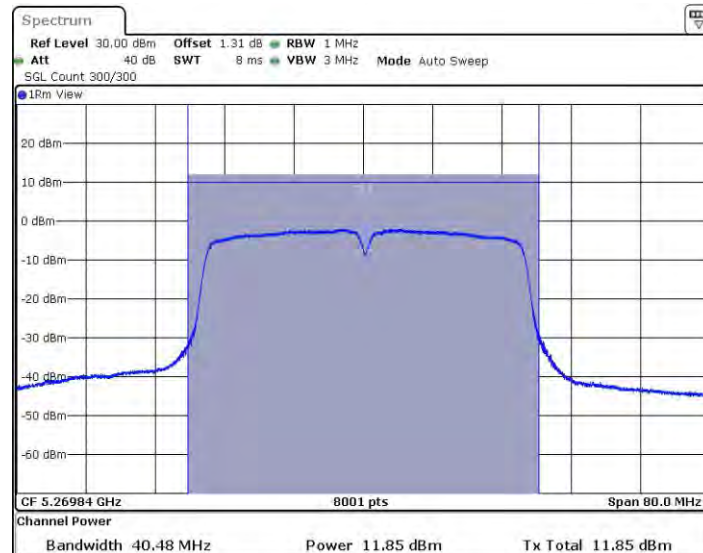
11N40MIMO_Ant2_5190



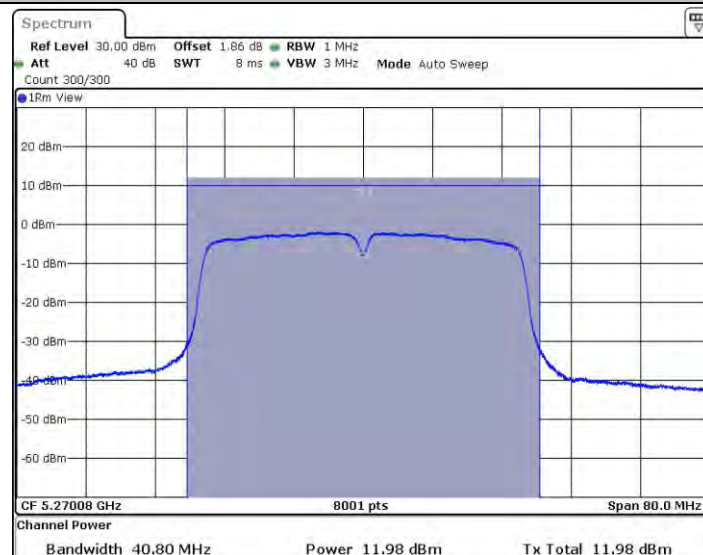
11N40MIMO_Ant1_5230



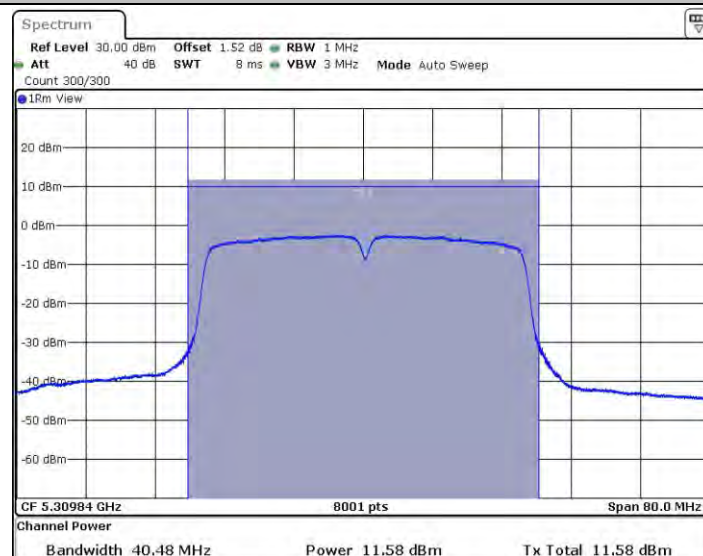
11N40MIMO_Ant2_5230



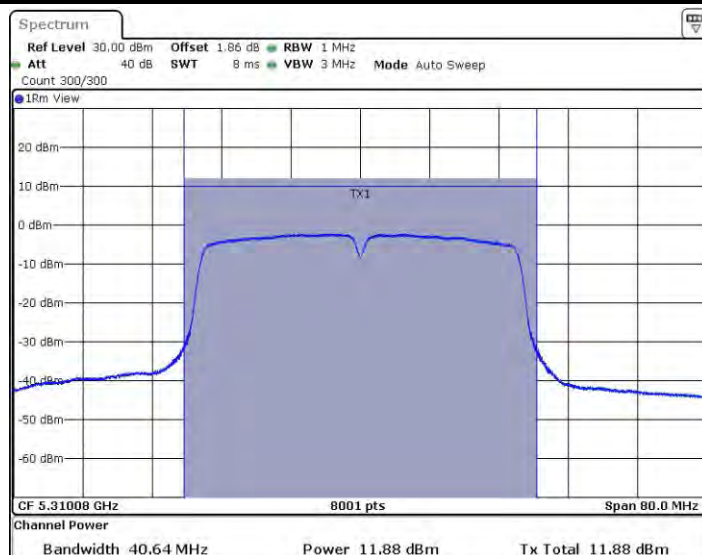
11N40MIMO_Ant1_5270



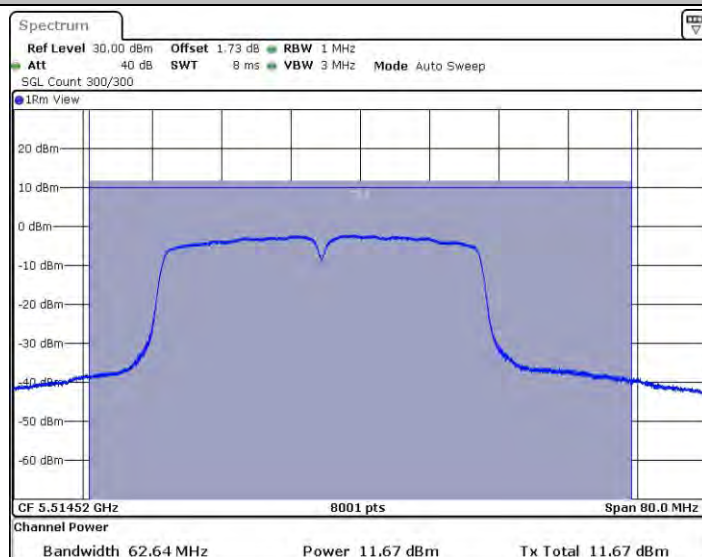
11N40MIMO_Ant2_5270



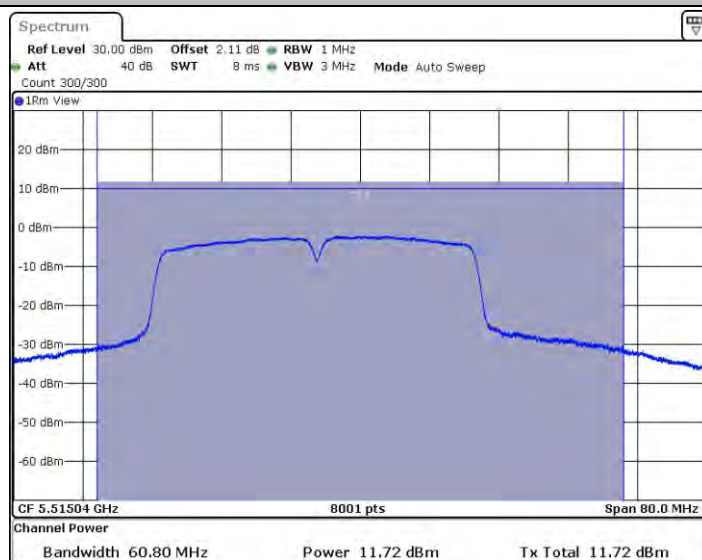
11N40MIMO_Ant1_5310



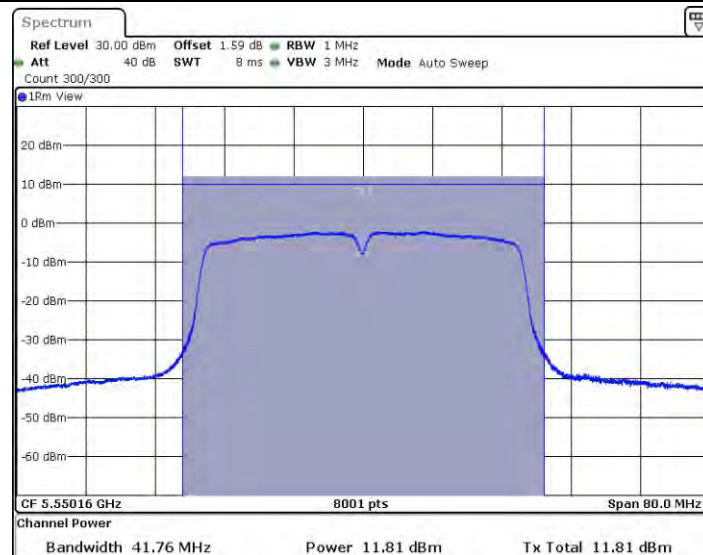
11N40MIMO_Ant2_5310



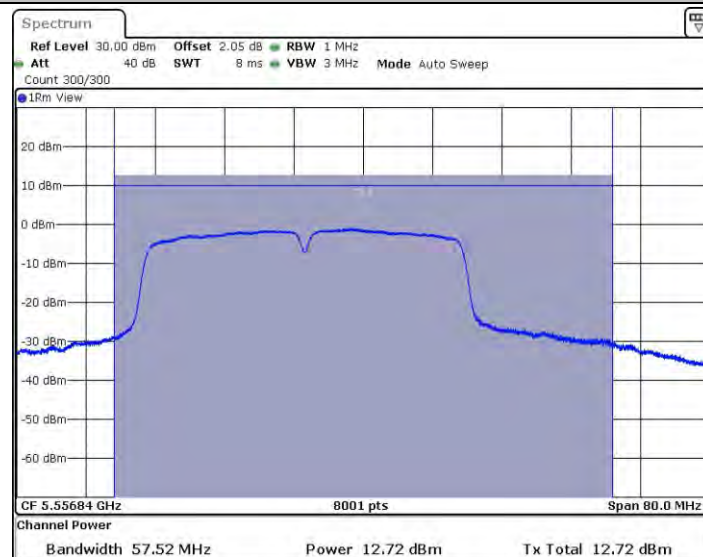
11N40MIMO_Ant1_5510



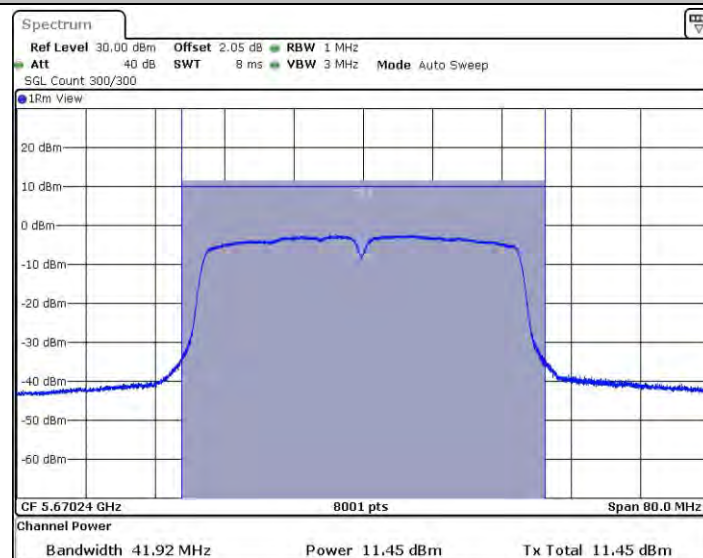
11N40MIMO_Ant2_5510



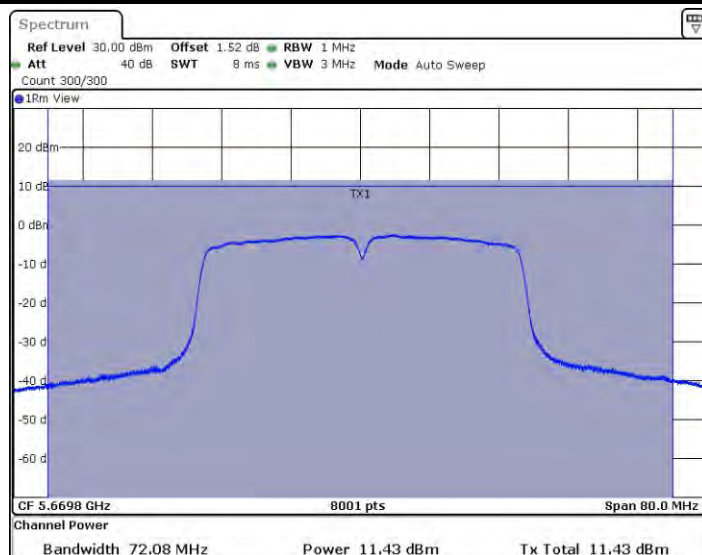
11N40MIMO_Ant1_5550



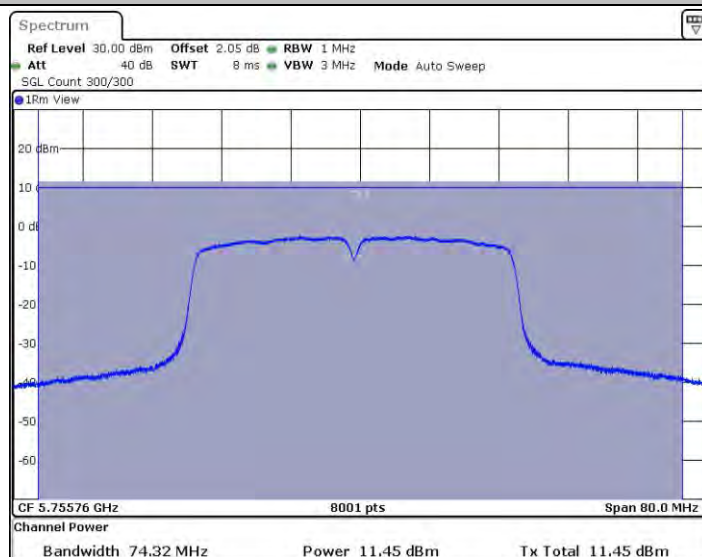
11N40MIMO_Ant2_5550



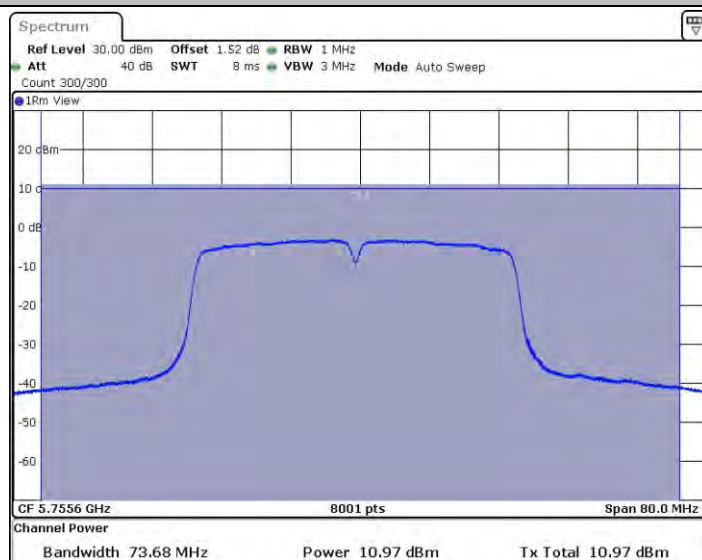
11N40MIMO_Ant1_5670



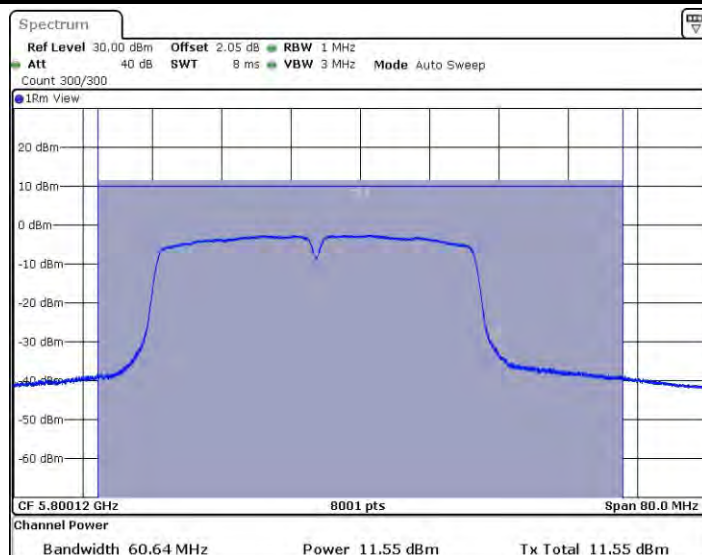
11N40MIMO_Ant2_5670



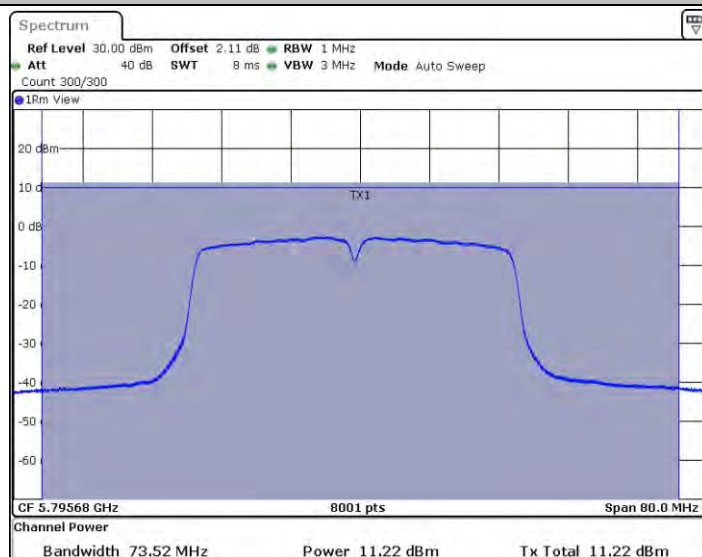
11N40MIMO_Ant1_5755



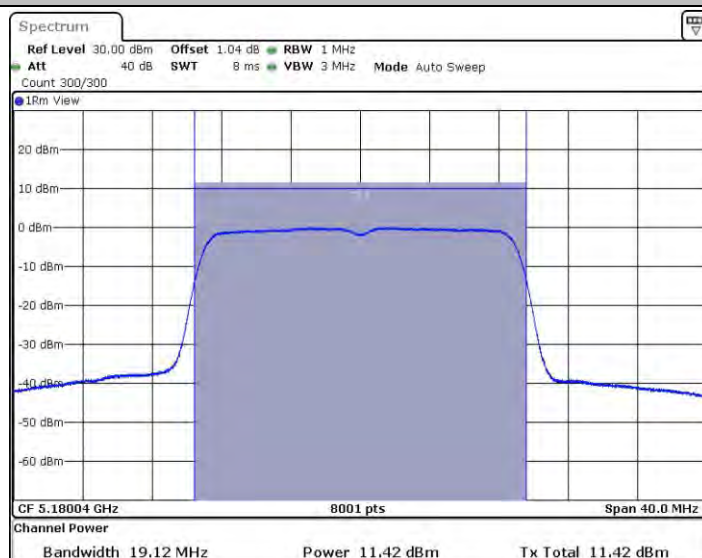
11N40MIMO_Ant2_5755



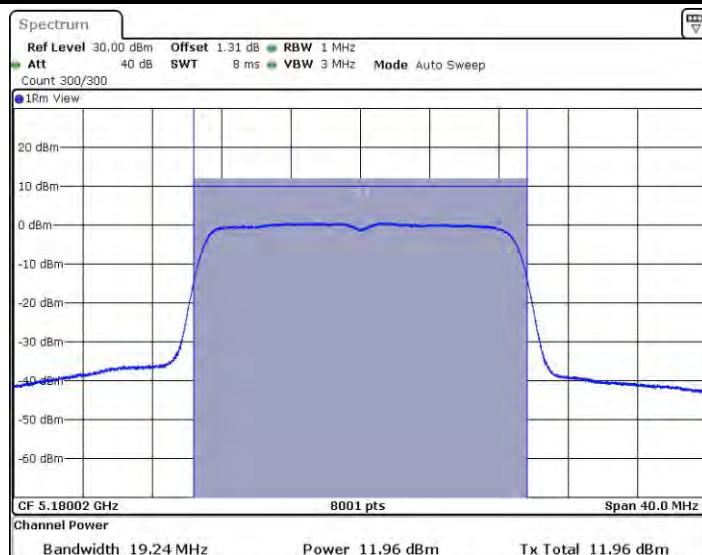
11N40MIMO_Ant1_5795



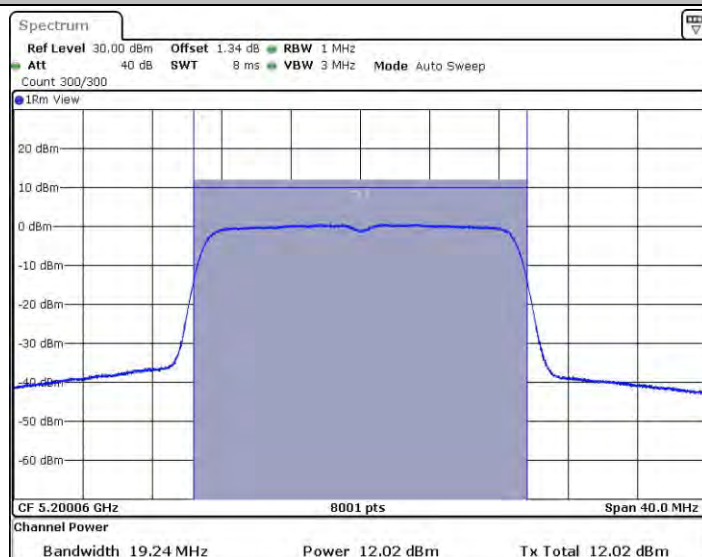
11N40MIMO_Ant2_5795



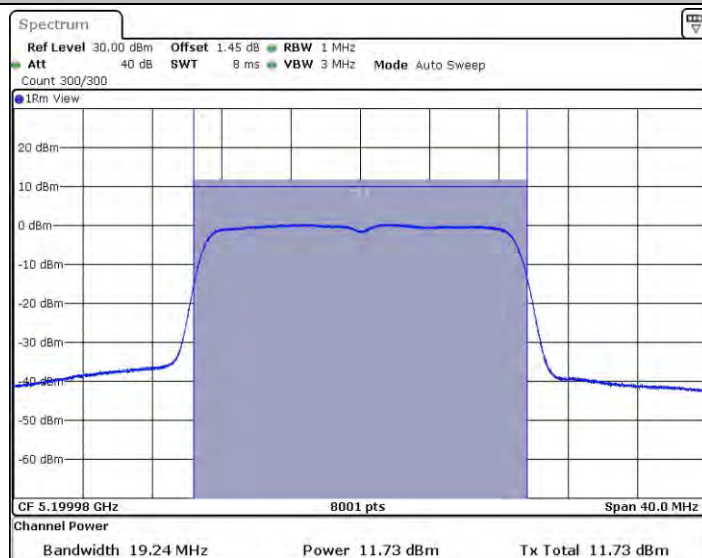
11AC20MIMO_Ant1_5180



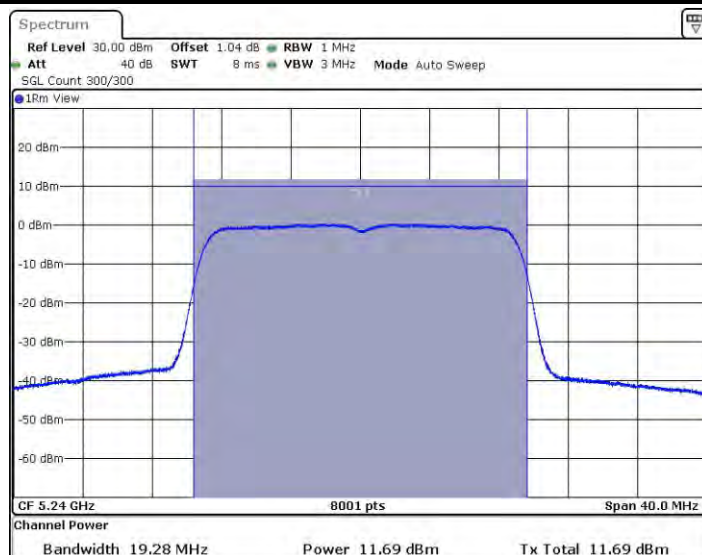
11AC20MIMO_Ant2_5180



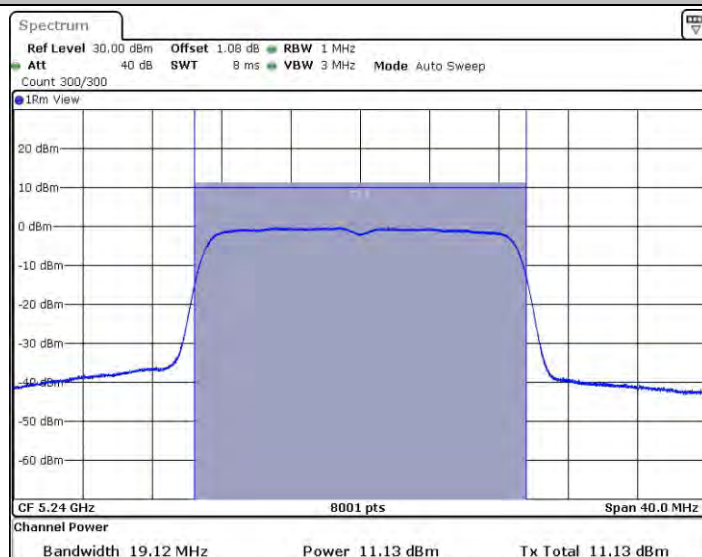
11AC20MIMO_Ant1_5200



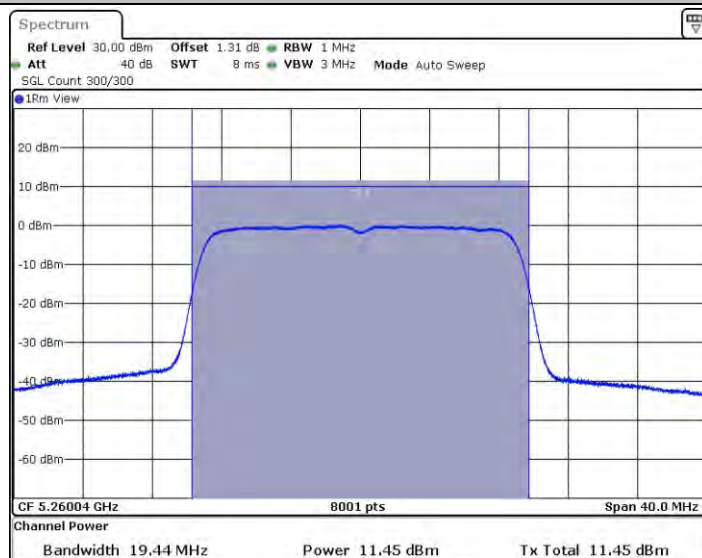
11AC20MIMO_Ant2_5200



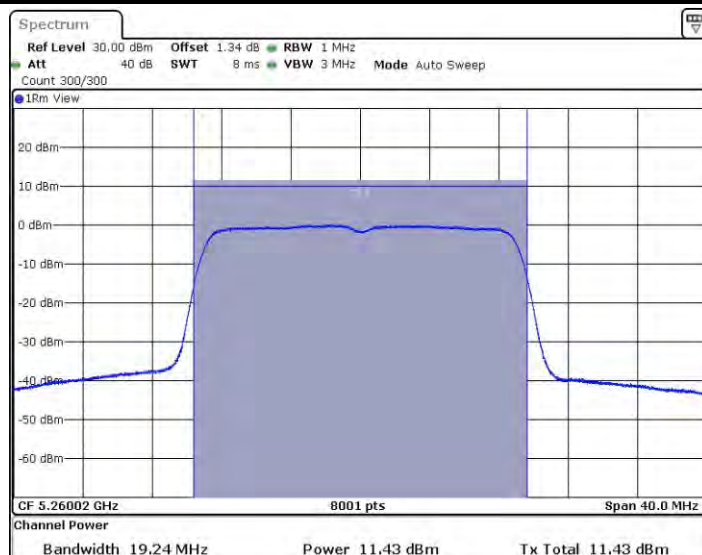
11AC20MIMO_Ant1_5240



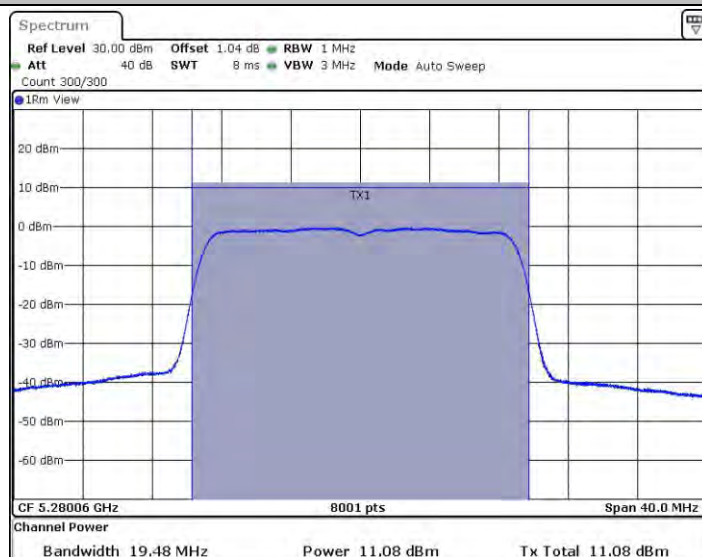
11AC20MIMO_Ant2_5240



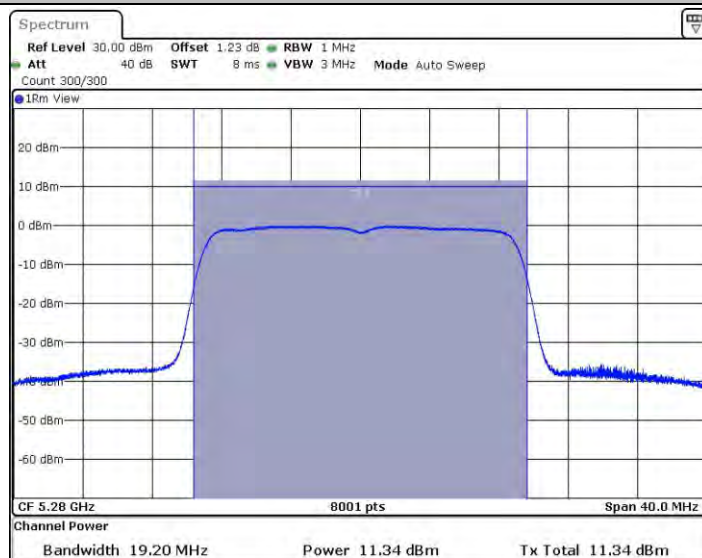
11AC20MIMO_Ant1_5260



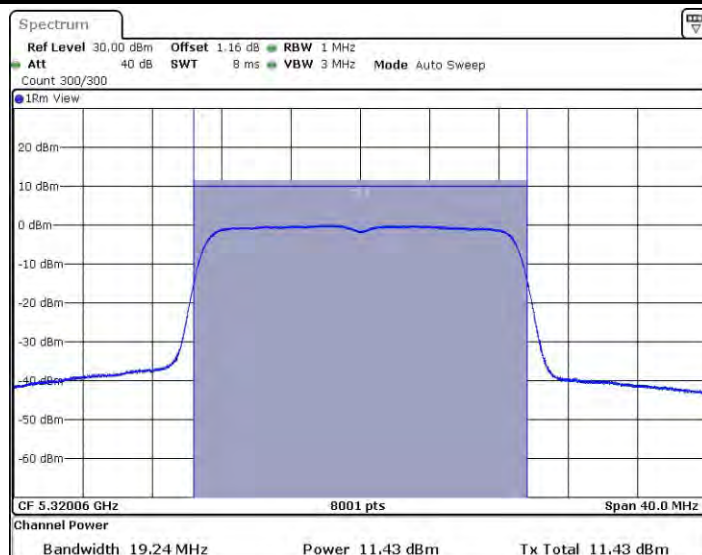
11AC20MIMO_Ant2_5260



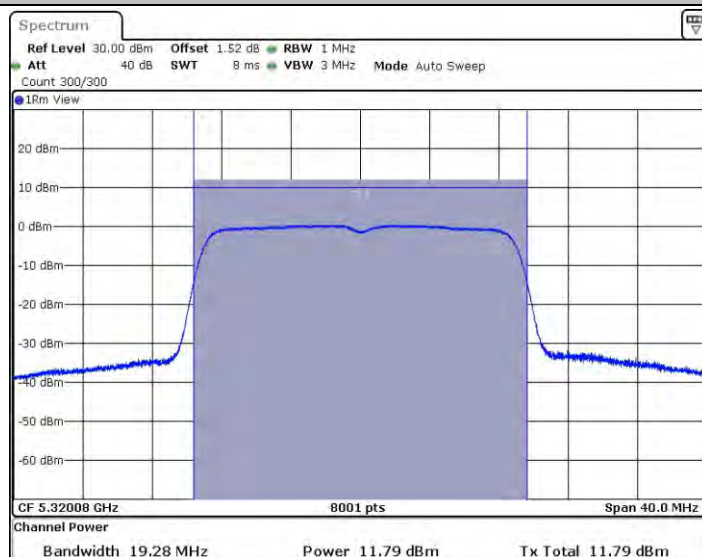
11AC20MIMO_Ant1_5280



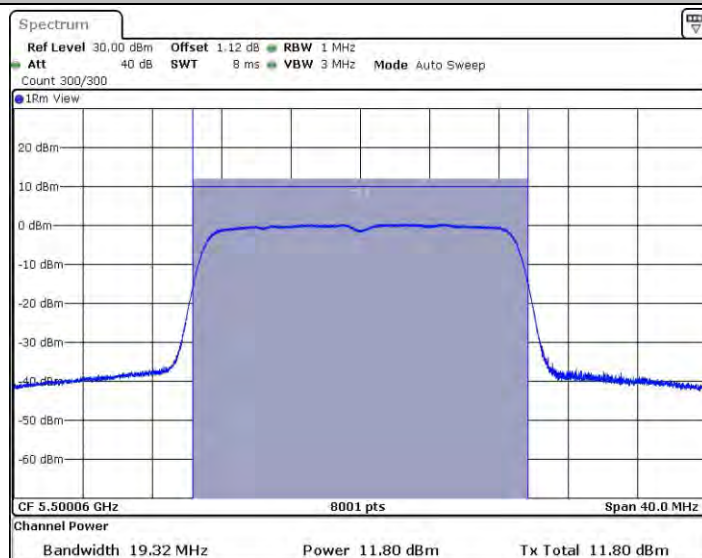
11AC20MIMO_Ant2_5280



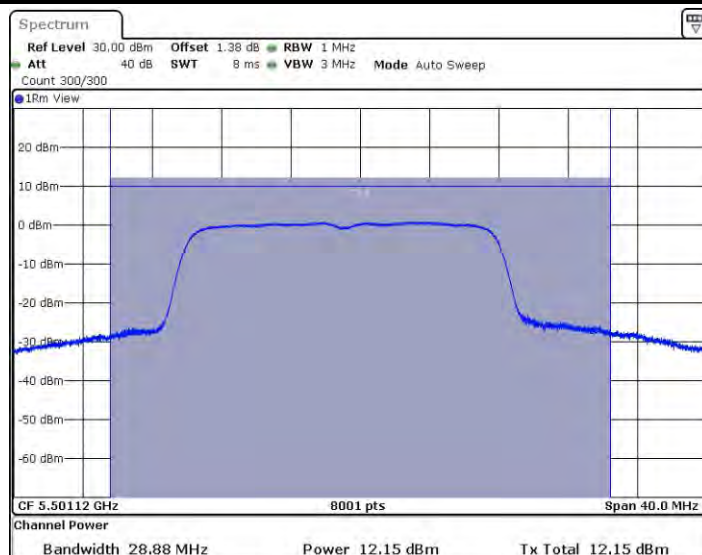
11AC20MIMO_Ant1_5320



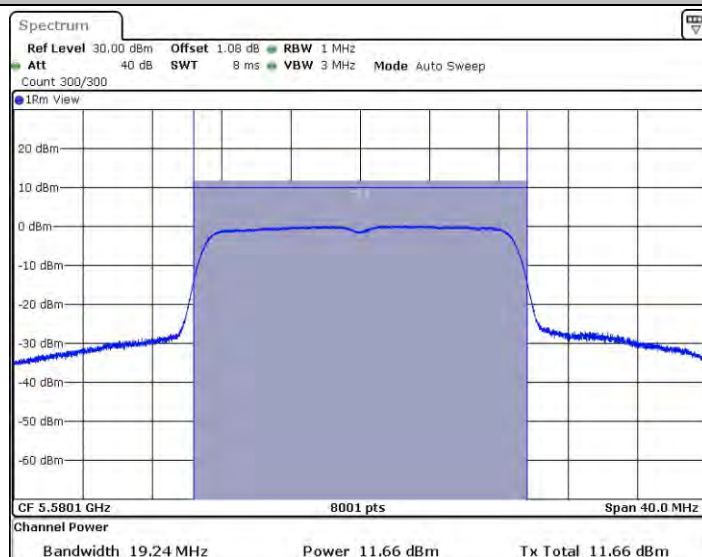
11AC20MIMO_Ant2_5320



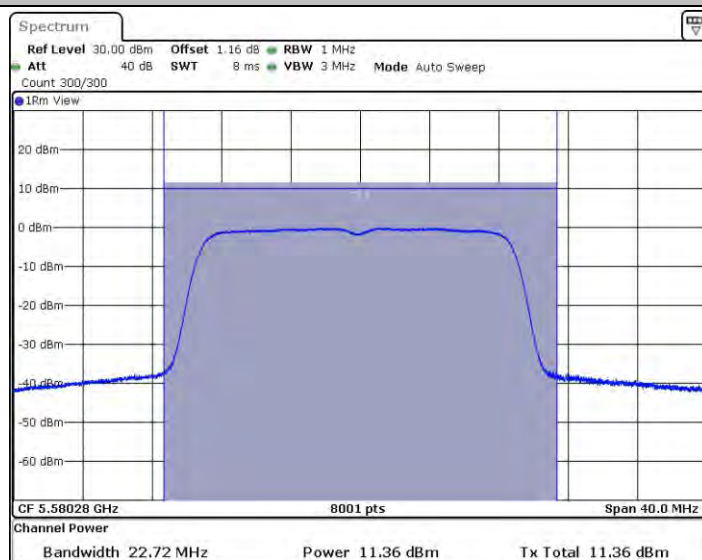
11AC20MIMO_Ant1_5500



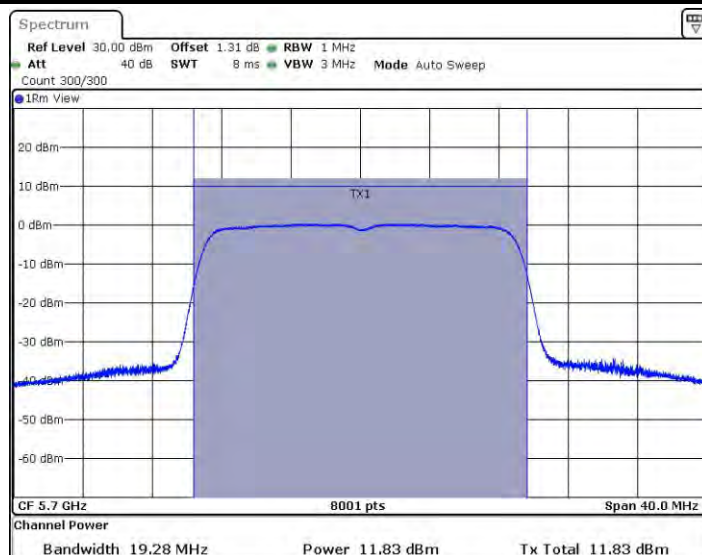
11AC20MIMO_Ant2_5500



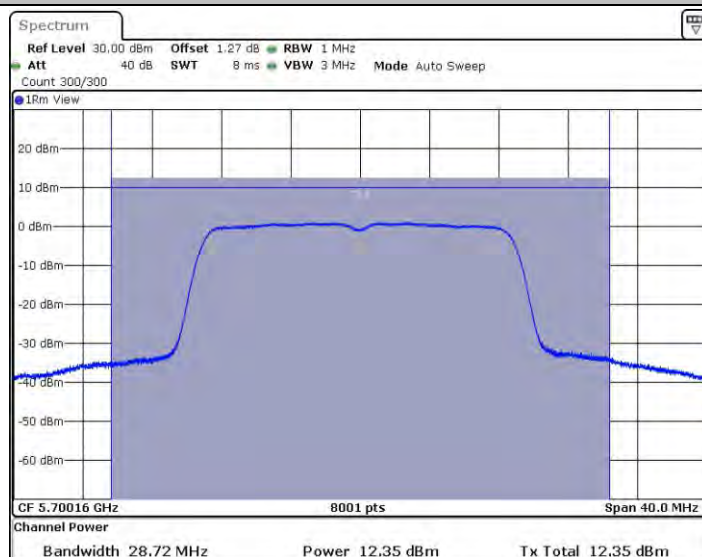
11AC20MIMO_Ant1_5580



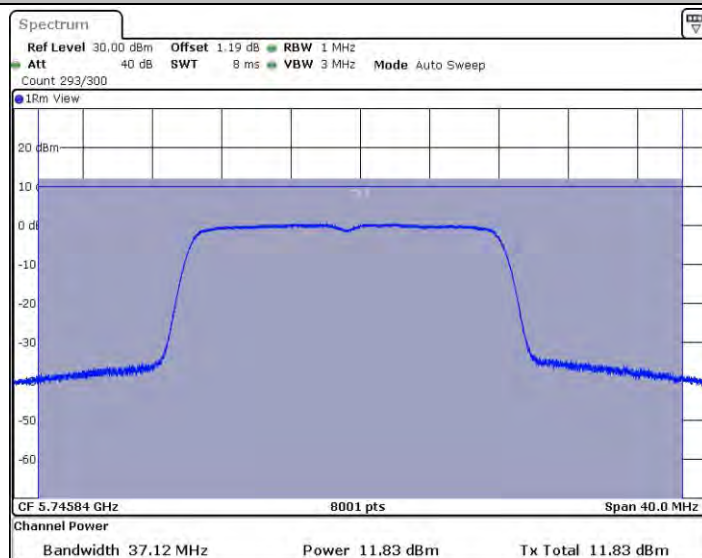
11AC20MIMO_Ant2_5580



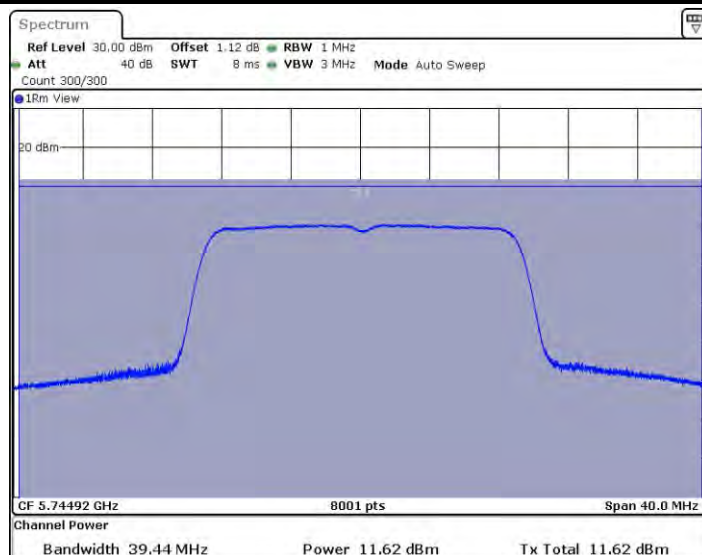
11AC20MIMO_Ant1_5700



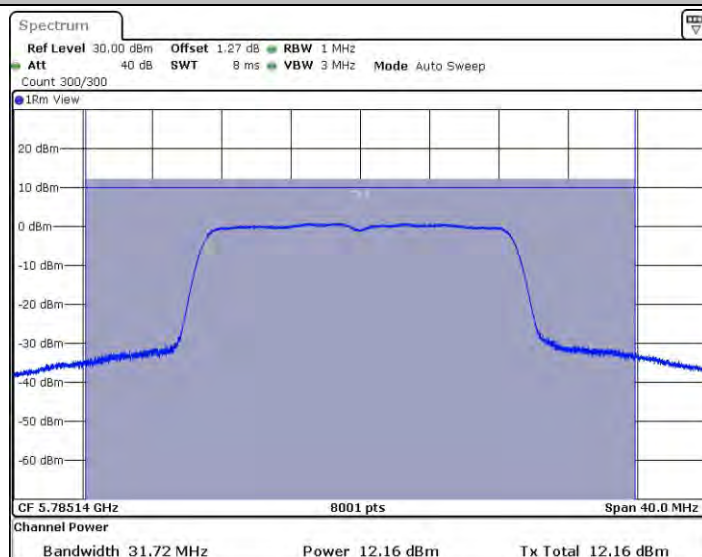
11AC20MIMO_Ant2_5700



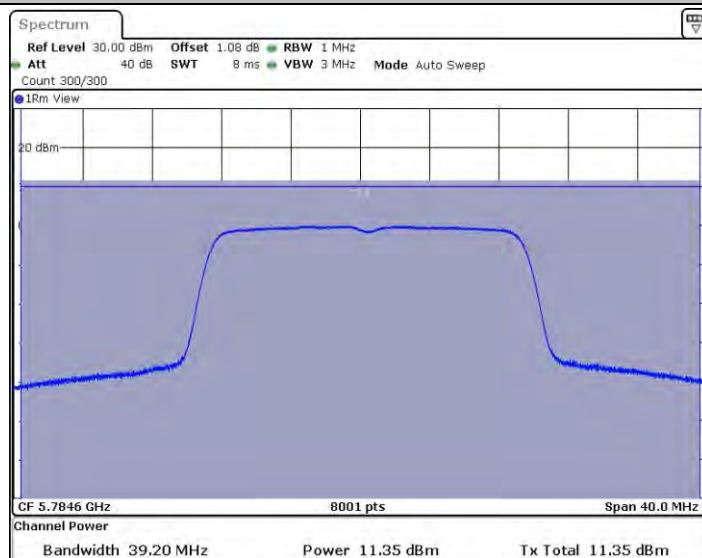
11AC20MIMO_Ant1_5745



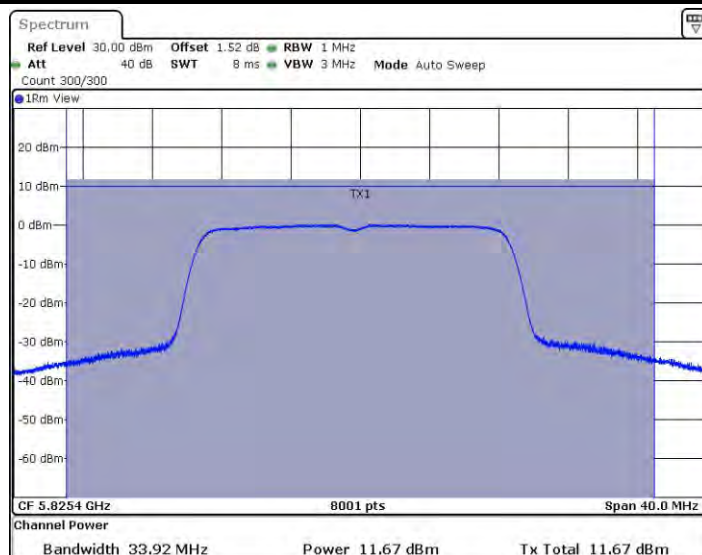
11AC20MIMO_Ant2_5745



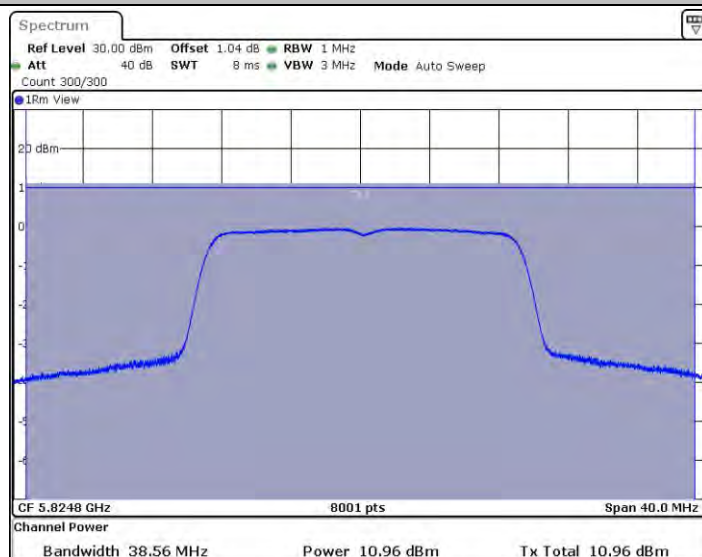
11AC20MIMO_Ant1_5785



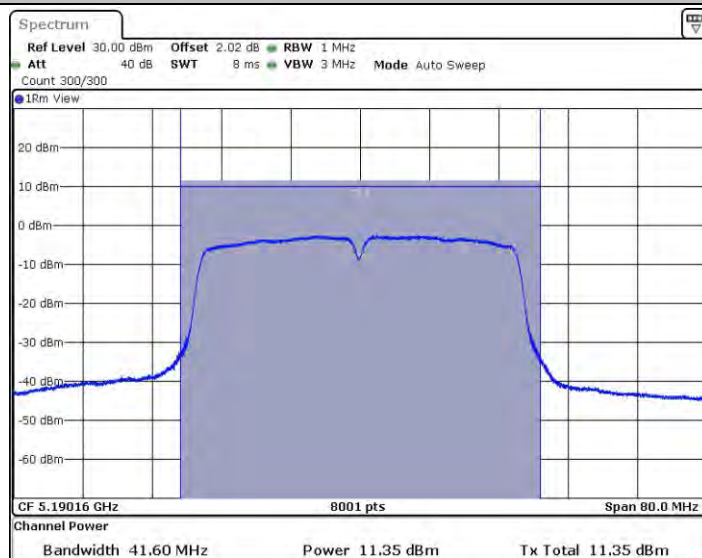
11AC20MIMO_Ant2_5785



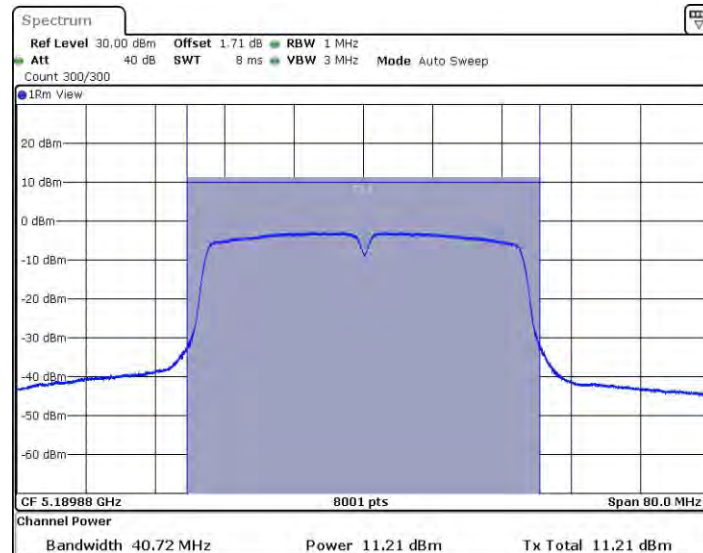
11AC20MIMO_Ant1_5825



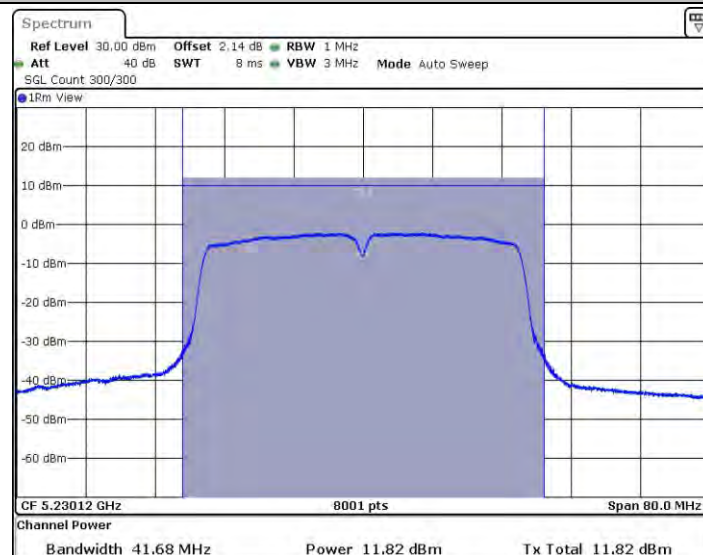
11AC20MIMO_Ant2_5825



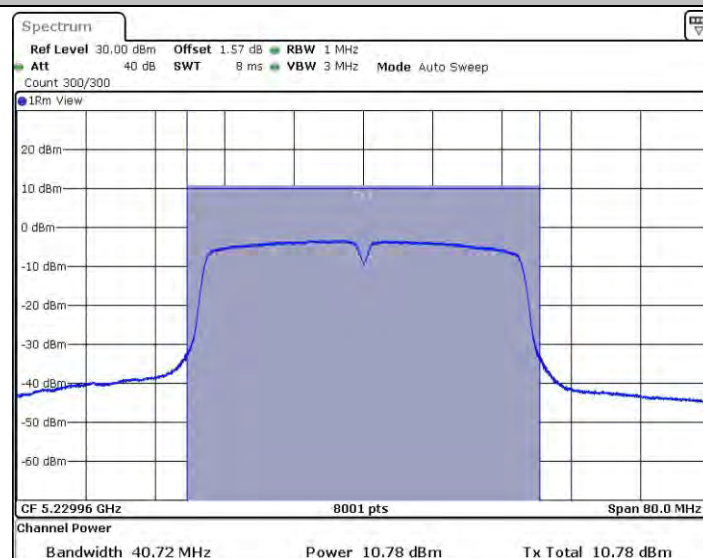
11AC40MIMO_Ant1_5190



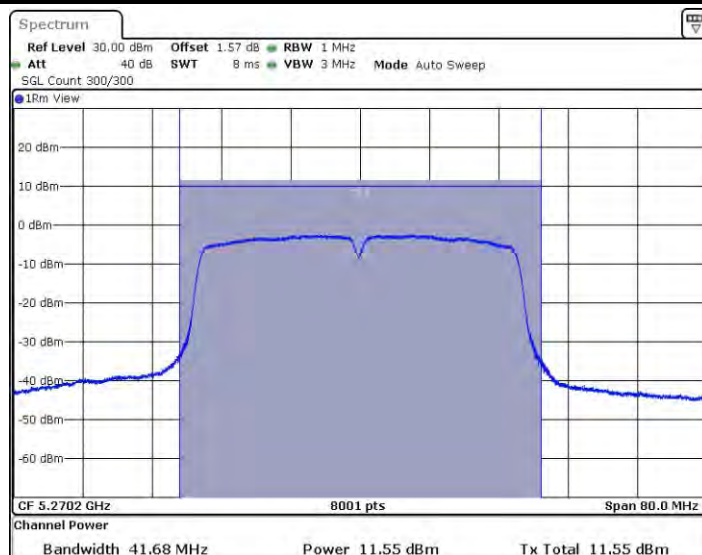
11AC40MIMO_Ant2_5190



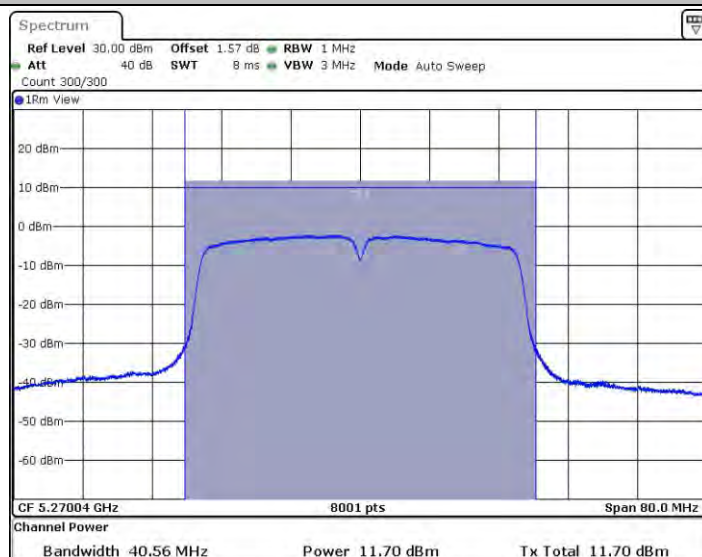
11AC40MIMO_Ant1_5230



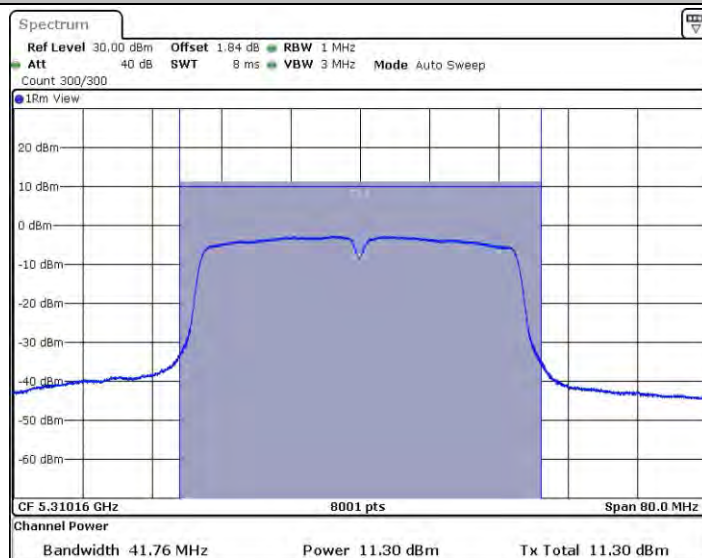
11AC40MIMO_Ant2_5230



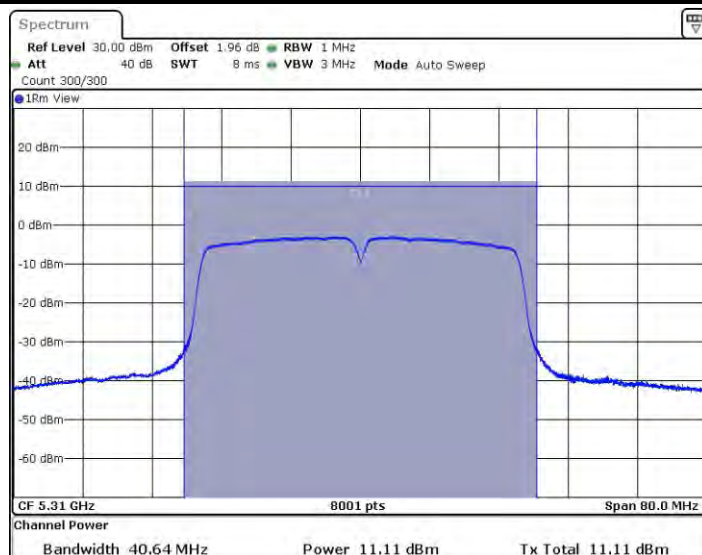
11AC40MIMO_Ant1_5270



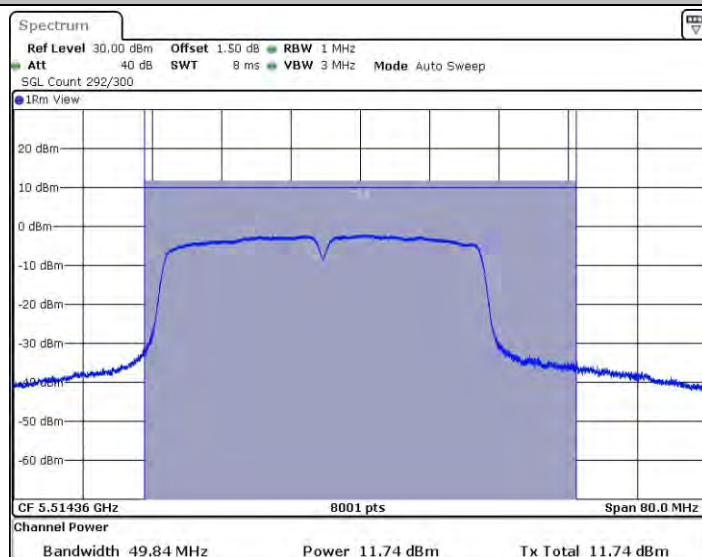
11AC40MIMO_Ant2_5270



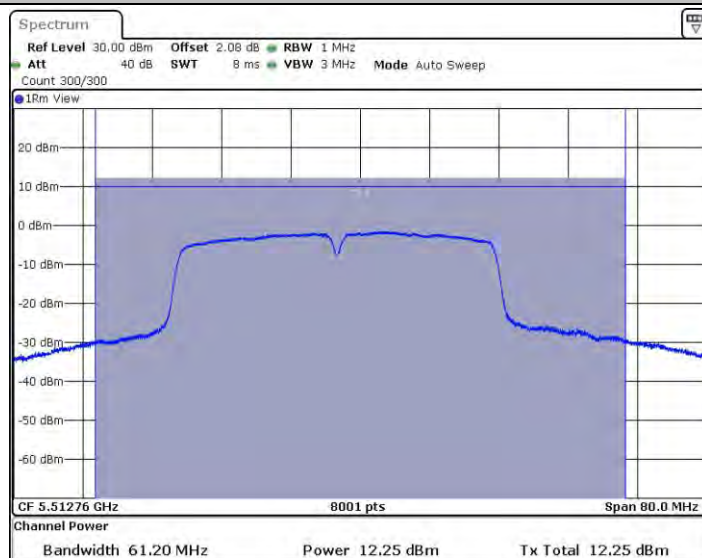
11AC40MIMO_Ant1_5310



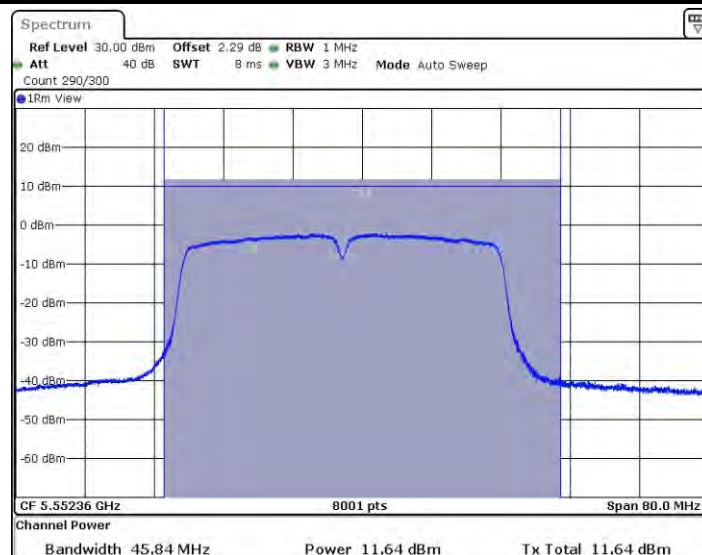
11AC40MIMO_Ant2_5310



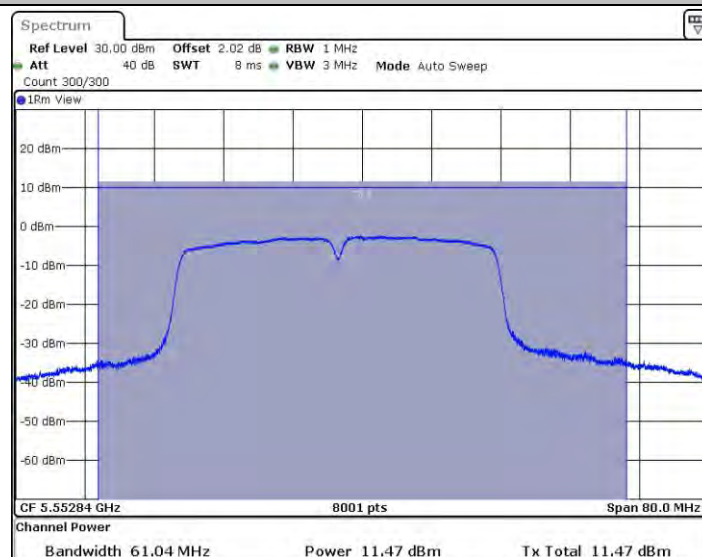
11AC40MIMO_Ant1_5510



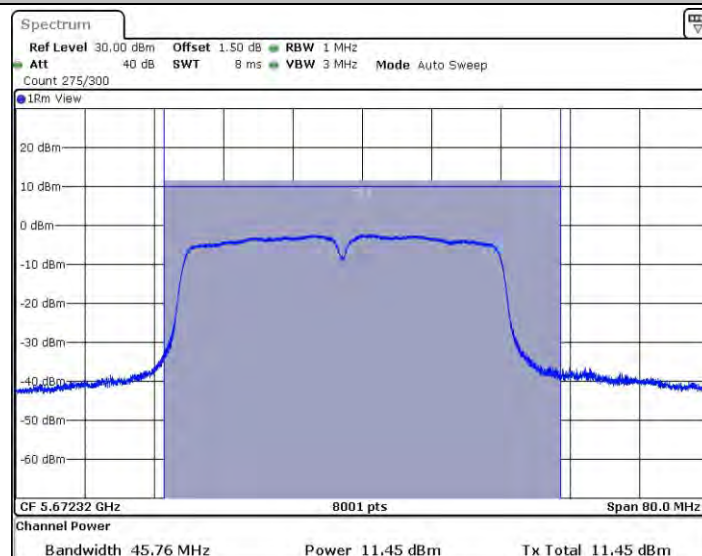
11AC40MIMO_Ant2_5510



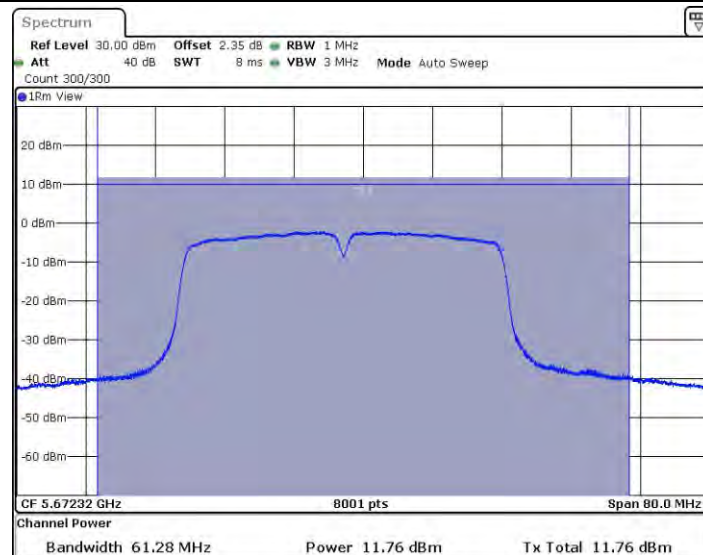
11AC40MIMO_Ant1_5550



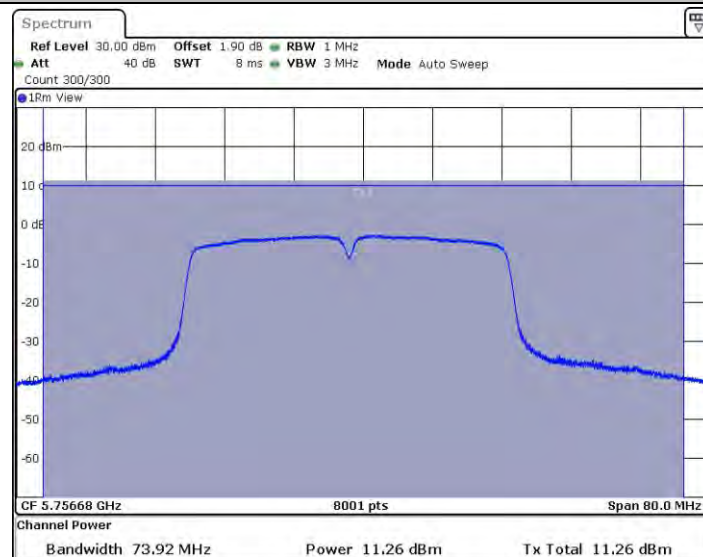
11AC40MIMO_Ant2_5550



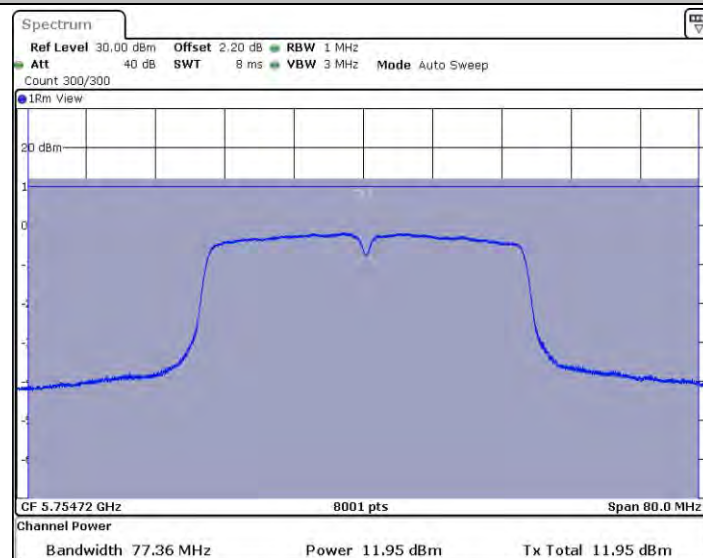
11AC40MIMO_Ant1_5670



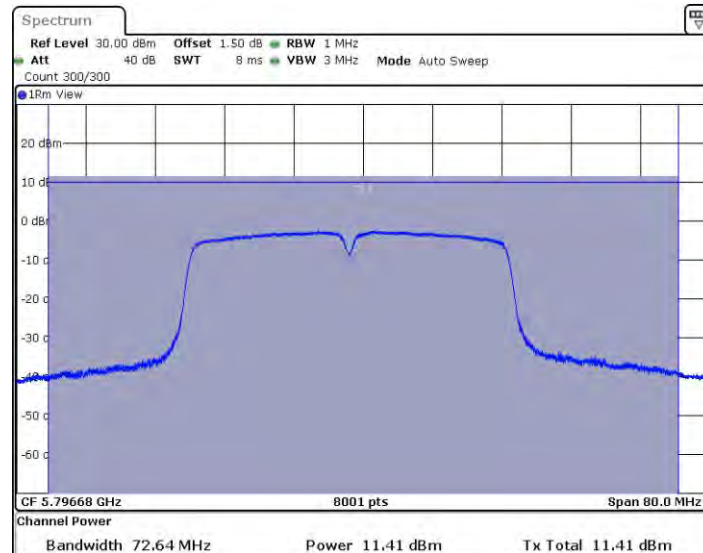
11AC40MIMO_Ant2_5670



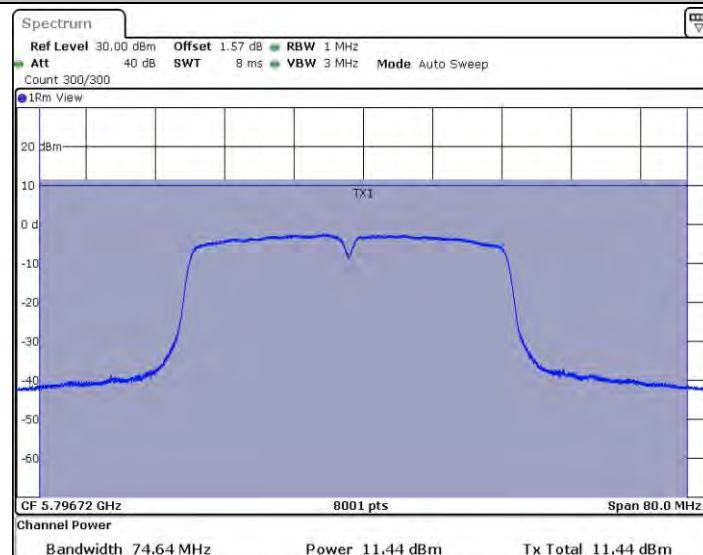
11AC40MIMO_Ant1_5755



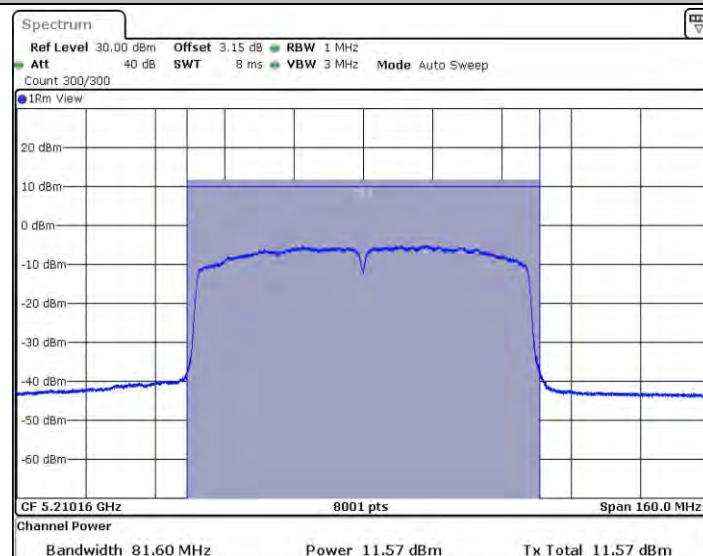
11AC40MIMO_Ant2_5755



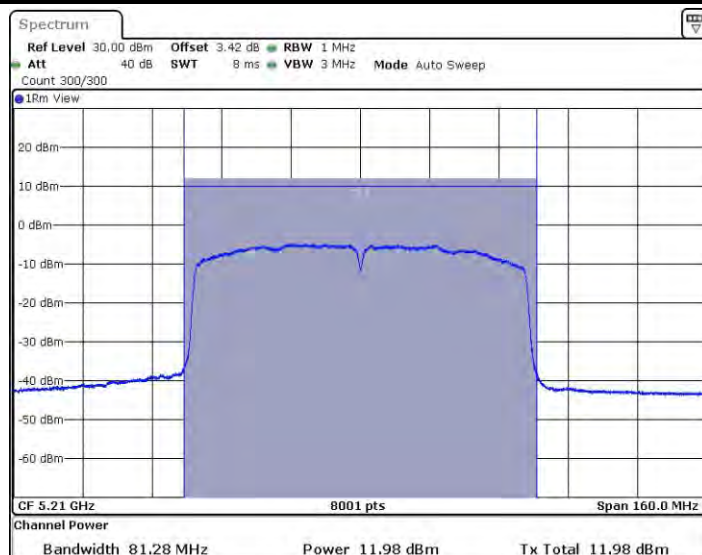
11AC40MIMO_Ant1_5795



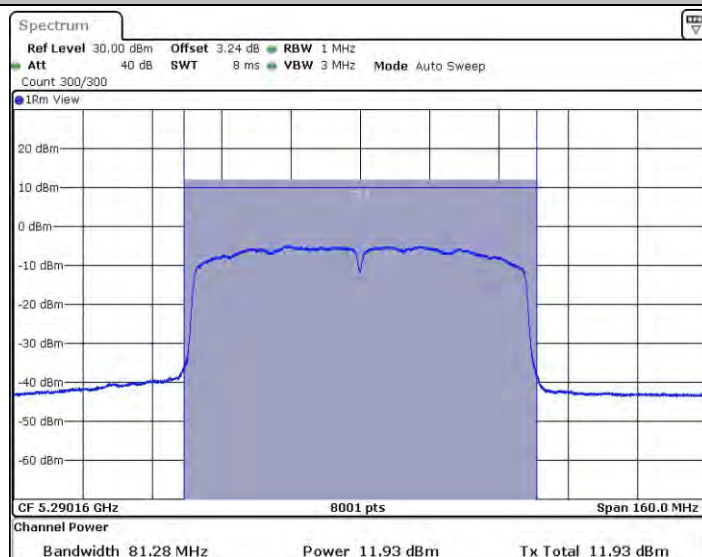
11AC40MIMO_Ant2_5795



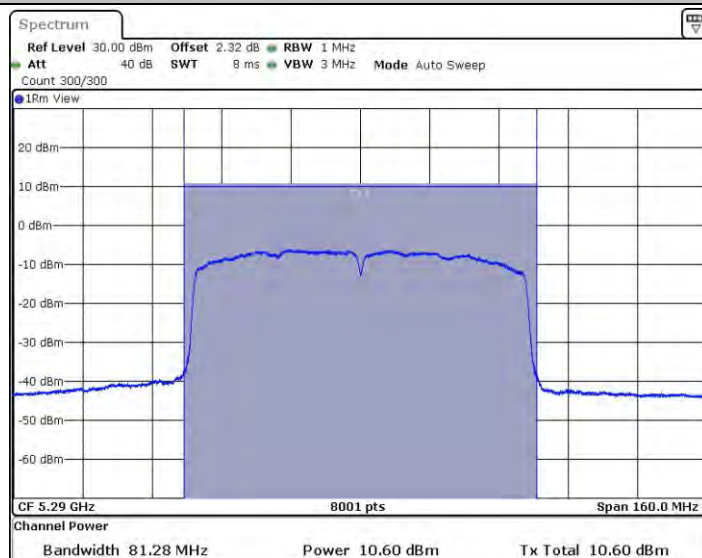
11AC80MIMO_Ant1_5210



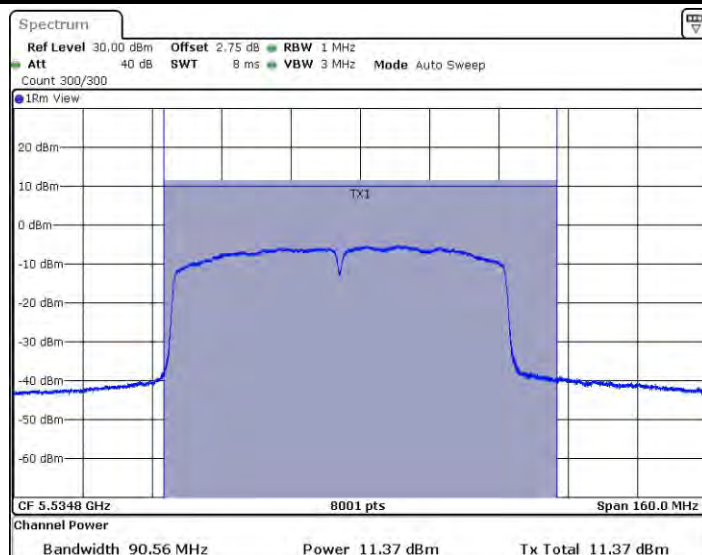
11AC80MIMO_Ant2_5210



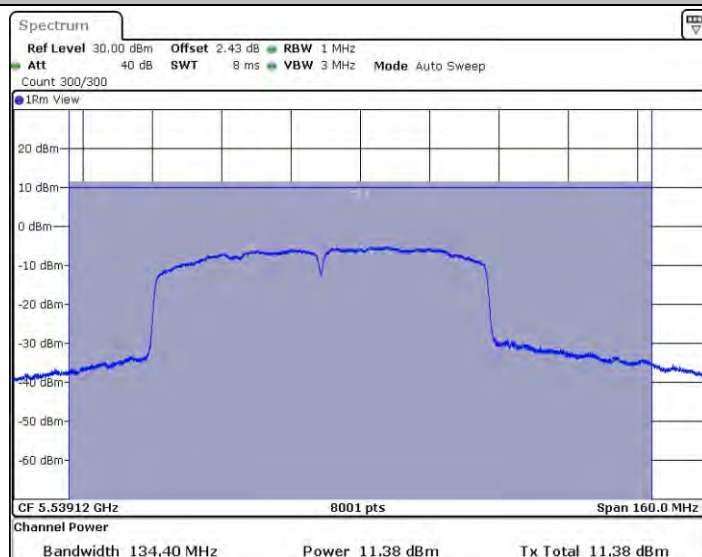
11AC80MIMO_Ant1_5290



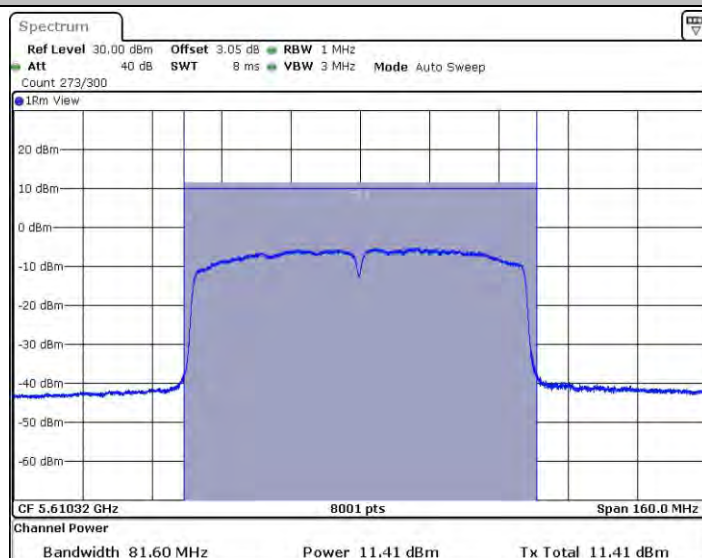
11AC80MIMO_Ant2_5290



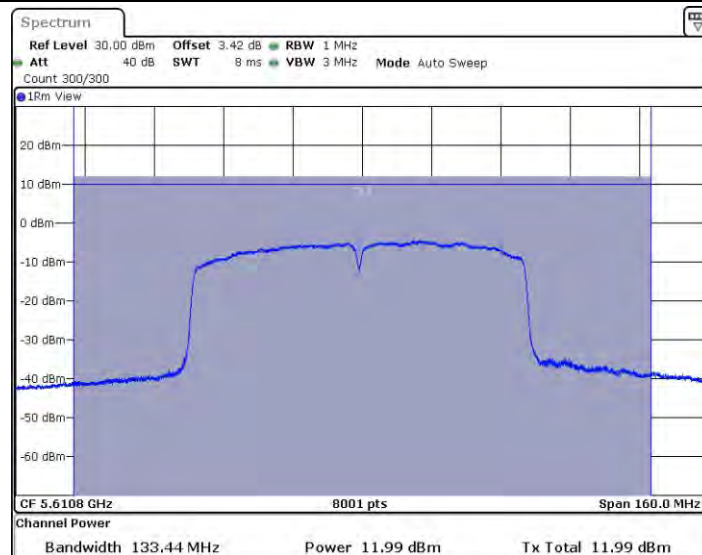
11AC80MIMO_Ant1_5530



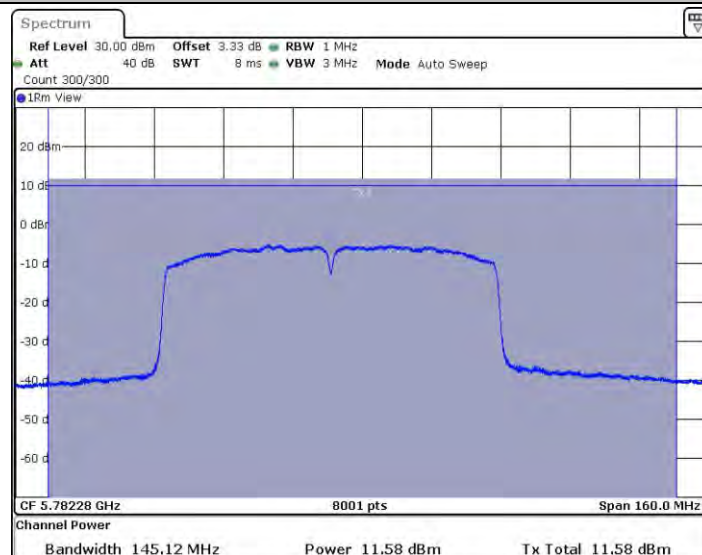
11AC80MIMO_Ant2_5530



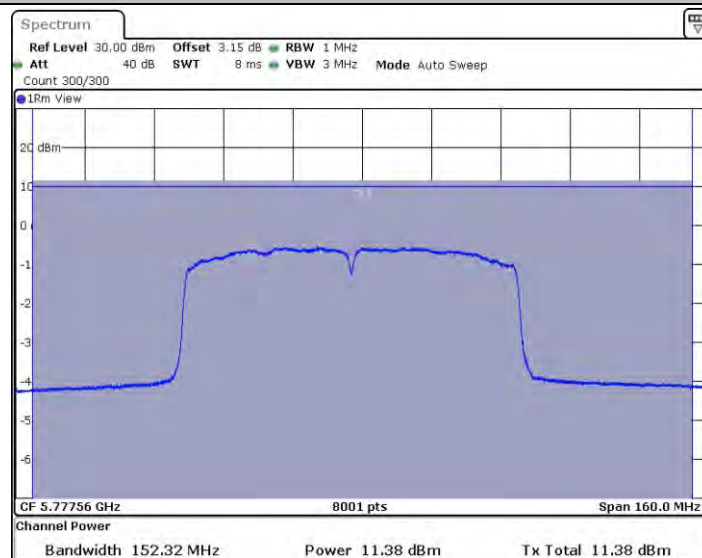
11AC80MIMO_Ant1_5610



11AC80MIMO_Ant2_5610



11AC80MIMO_Ant1_5775



11AC80MIMO_Ant2_5775

Appendix C: Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant1	5180	9.61	<=11	PASS
	Ant2	5180	9.67	<=11	PASS
	Ant1	5200	9.24	<=11	PASS
	Ant2	5200	9.84	<=11	PASS
	Ant1	5240	10.15	<=11	PASS
	Ant2	5240	10.02	<=11	PASS
	Ant1	5260	9.36	<=11	PASS
	Ant2	5260	9.18	<=11	PASS
	Ant1	5280	9.62	<=11	PASS
	Ant2	5280	9.16	<=11	PASS
	Ant1	5320	9.6	<=11	PASS
	Ant2	5320	9.26	<=11	PASS
	Ant1	5500	10	<=11	PASS
	Ant2	5500	10.16	<=11	PASS
	Ant1	5580	10.47	<=11	PASS
	Ant2	5580	9.6	<=11	PASS
	Ant1	5700	9.7	<=11	PASS
	Ant2	5700	10.51	<=11	PASS
	Ant1	5745	7.85	<=30	PASS
	Ant2	5745	8.35	<=30	PASS
	Ant1	5785	9.07	<=30	PASS
	Ant2	5785	9.6	<=30	PASS
	Ant1	5825	11.17	<=30	PASS
	Ant2	5825	8.84	<=30	PASS
11N20SISO	Ant1	5180	9.31	<=11	PASS
	Ant2	5180	9.76	<=11	PASS
	Ant1	5200	8.87	<=11	PASS
	Ant2	5200	9.97	<=11	PASS
	Ant1	5240	9.46	<=11	PASS
	Ant2	5240	9.89	<=11	PASS
	Ant1	5260	8.7	<=11	PASS
	Ant2	5260	9.72	<=11	PASS
	Ant1	5280	9.35	<=11	PASS
	Ant2	5280	9.39	<=11	PASS
	Ant1	5320	9	<=11	PASS
	Ant2	5320	9.14	<=11	PASS

	Ant1	5500	9.28	<=11	PASS
	Ant2	5500	10.31	<=11	PASS
	Ant1	5580	9.47	<=11	PASS
	Ant2	5580	9.69	<=11	PASS
	Ant1	5700	9.74	<=11	PASS
	Ant2	5700	10.15	<=11	PASS
	Ant1	5745	8.43	<=30	PASS
	Ant2	5745	8.17	<=30	PASS
	Ant1	5785	8.11	<=30	PASS
	Ant2	5785	8.66	<=30	PASS
	Ant1	5825	8.98	<=30	PASS
	Ant2	5825	8.43	<=30	PASS
11N40SISO	Ant1	5190	6.52	<=11	PASS
	Ant2	5190	6.92	<=11	PASS
	Ant1	5230	6.93	<=11	PASS
	Ant2	5230	7.46	<=11	PASS
	Ant1	5270	6.83	<=11	PASS
	Ant2	5270	7.17	<=11	PASS
	Ant1	5310	6.95	<=11	PASS
	Ant2	5310	7.06	<=11	PASS
	Ant1	5510	6.99	<=11	PASS
	Ant2	5510	7.64	<=11	PASS
	Ant1	5550	6.85	<=11	PASS
	Ant2	5550	6.95	<=11	PASS
	Ant1	5670	7.23	<=11	PASS
	Ant2	5670	7.78	<=11	PASS
	Ant1	5755	6.18	<=30	PASS
	Ant2	5755	5.99	<=30	PASS
	Ant1	5795	5.97	<=30	PASS
	Ant2	5795	6.39	<=30	PASS
11AC20SISO	Ant1	5180	8.73	<=11	PASS
	Ant2	5180	8.71	<=11	PASS
	Ant1	5200	8.57	<=11	PASS
	Ant2	5200	8.65	<=11	PASS
	Ant1	5240	9.12	<=11	PASS
	Ant2	5240	9.33	<=11	PASS
	Ant1	5260	8.35	<=11	PASS
	Ant2	5260	8.72	<=11	PASS
	Ant1	5280	8.67	<=11	PASS
	Ant2	5280	8.73	<=11	PASS
	Ant1	5320	8.39	<=11	PASS
	Ant2	5320	8.35	<=11	PASS
	Ant1	5500	9.24	<=11	PASS

	Ant2	5500	8.37	<=11	PASS
	Ant1	5580	9.01	<=11	PASS
	Ant2	5580	8.5	<=11	PASS
	Ant1	5700	9.21	<=11	PASS
	Ant2	5700	9.9	<=11	PASS
	Ant1	5745	7.59	<=30	PASS
	Ant2	5745	6.85	<=30	PASS
	Ant1	5785	7.21	<=30	PASS
	Ant2	5785	7.57	<=30	PASS
	Ant1	5825	7.57	<=30	PASS
	Ant2	5825	7.8	<=30	PASS
11AC40SISO	Ant1	5190	5.63	<=11	PASS
	Ant2	5190	5.75	<=11	PASS
	Ant1	5230	6.09	<=11	PASS
	Ant2	5230	6.17	<=11	PASS
	Ant1	5270	5.82	<=11	PASS
	Ant2	5270	6.26	<=11	PASS
	Ant1	5310	5.53	<=11	PASS
	Ant2	5310	6.12	<=11	PASS
	Ant1	5510	6.9	<=11	PASS
	Ant2	5510	6.29	<=11	PASS
	Ant1	5550	6.84	<=11	PASS
	Ant2	5550	6.33	<=11	PASS
	Ant1	5670	6.38	<=11	PASS
	Ant2	5670	6.7	<=11	PASS
	Ant1	5755	5.02	<=30	PASS
	Ant2	5755	4.88	<=30	PASS
	Ant1	5795	5.38	<=30	PASS
	Ant2	5795	4.64	<=30	PASS
11AC80SISO	Ant1	5210	3.46	<=11	PASS
	Ant2	5210	3.68	<=11	PASS
	Ant1	5290	3.49	<=11	PASS
	Ant2	5290	3.14	<=11	PASS
	Ant1	5530	3.92	<=11	PASS
	Ant2	5530	4.13	<=11	PASS
	Ant1	5610	3.66	<=11	PASS
	Ant2	5610	3.96	<=11	PASS
	Ant1	5775	3.35	<=30	PASS
	Ant2	5775	2.12	<=30	PASS
11N20MIMO	Ant1	5180	7.11	<=10.81	PASS
	Ant2	5180	8.03	<=10.81	PASS
	total	5180	10.60	<=10.81	PASS
	Ant1	5200	7.54	<=10.81	PASS

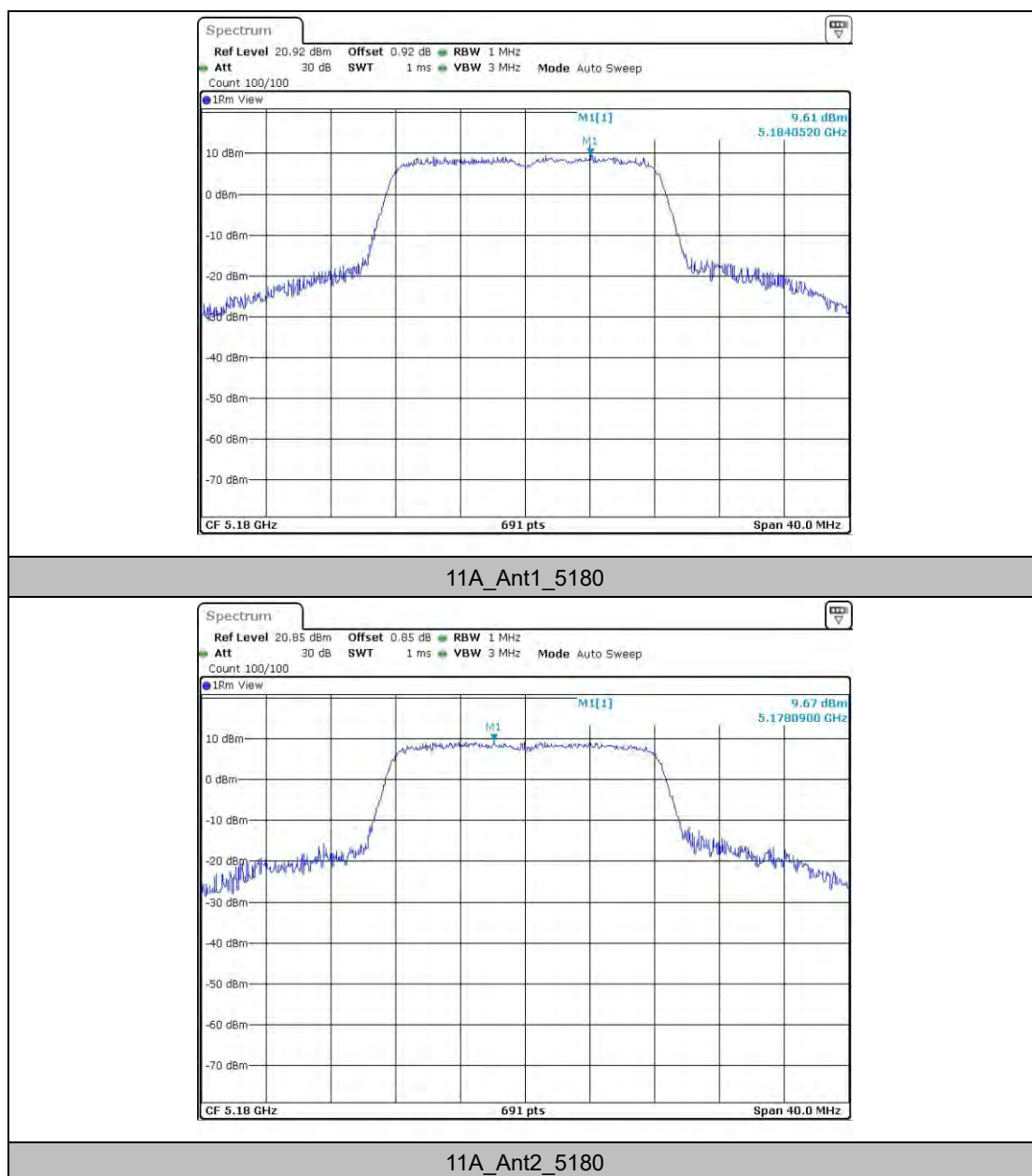
	Ant2	5200	6.92	<=10.81	PASS
	total	5200	10.25	<=10.81	PASS
	Ant1	5240	8.05	<=10.81	PASS
	Ant2	5240	6.35	<=10.81	PASS
	total	5240	10.29	<=10.81	PASS
	Ant1	5260	7.56	<=10.81	PASS
	Ant2	5260	6.96	<=10.81	PASS
	total	5260	10.28	<=10.81	PASS
	Ant1	5280	6.94	<=10.81	PASS
	Ant2	5280	7.11	<=10.81	PASS
	total	5280	10.04	<=10.81	PASS
	Ant1	5320	6.91	<=10.81	PASS
	Ant2	5320	5.95	<=10.81	PASS
	total	5320	9.47	<=10.81	PASS
	Ant1	5500	7.2	<=10.81	PASS
	Ant2	5500	6.5	<=10.81	PASS
	total	5500	9.87	<=10.81	PASS
	Ant1	5580	7.16	<=10.81	PASS
	Ant2	5580	7.08	<=10.81	PASS
	total	5580	10.13	<=10.81	PASS
	Ant1	5700	6.94	<=10.81	PASS
	Ant2	5700	7.92	<=10.81	PASS
	total	5700	10.47	<=10.81	PASS
	Ant1	5745	5.44	<=29.81	PASS
	Ant2	5745	6.08	<=29.81	PASS
	total	5745	8.78	<=29.81	PASS
	Ant1	5785	5.26	<=29.81	PASS
	Ant2	5785	5.69	<=29.81	PASS
	total	5785	8.49	<=29.81	PASS
	Ant1	5825	5.65	<=29.81	PASS
	Ant2	5825	6.35	<=29.81	PASS
	total	5825	9.02	<=29.81	PASS
11N40MIMO	Ant1	5190	4.1	<=10.81	PASS
	Ant2	5190	5.55	<=10.81	PASS
	total	5190	7.90	<=10.81	PASS
	Ant1	5230	4.13	<=10.81	PASS
	Ant2	5230	4.56	<=10.81	PASS
	total	5230	7.36	<=10.81	PASS
	Ant1	5270	3.72	<=10.81	PASS
	Ant2	5270	6.33	<=10.81	PASS
	total	5270	8.23	<=10.81	PASS
	Ant1	5310	3.56	<=10.81	PASS
	Ant2	5310	3.71	<=10.81	PASS

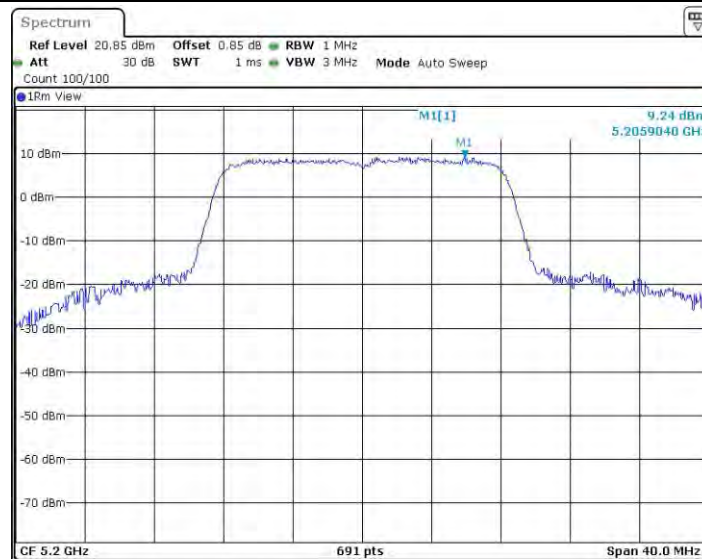
	total	5310	6.65	<=10.81	PASS
	Ant1	5510	3.67	<=10.81	PASS
	Ant2	5510	5.49	<=10.81	PASS
	total	5510	7.68	<=10.81	PASS
	Ant1	5550	4.14	<=10.81	PASS
	Ant2	5550	7.47	<=10.81	PASS
	total	5550	8.96	<=10.81	PASS
	Ant1	5670	3.7	<=10.81	PASS
	Ant2	5670	4.66	<=10.81	PASS
	total	5670	7.22	<=10.81	PASS
	Ant1	5755	1.8	<=29.81	PASS
	Ant2	5755	3.5	<=29.81	PASS
	total	5755	5.74	<=29.81	PASS
	Ant1	5795	2.28	<=29.81	PASS
	Ant2	5795	3.33	<=29.81	PASS
	total	5795	5.85	<=29.81	PASS
11AC20MIMO	Ant1	5180	6.15	<=10.81	PASS
	Ant2	5180	7.7	<=10.81	PASS
	total	5180	10.00	<=10.81	PASS
	Ant1	5200	6.27	<=10.81	PASS
	Ant2	5200	7.29	<=10.81	PASS
	total	5200	9.82	<=10.81	PASS
	Ant1	5240	5.53	<=10.81	PASS
	Ant2	5240	7.32	<=10.81	PASS
	total	5240	9.53	<=10.81	PASS
	Ant1	5260	5.49	<=10.81	PASS
	Ant2	5260	5.57	<=10.81	PASS
	total	5260	8.54	<=10.81	PASS
	Ant1	5280	5.42	<=10.81	PASS
	Ant2	5280	7.25	<=10.81	PASS
	total	5280	9.44	<=10.81	PASS
	Ant1	5320	5.82	<=10.81	PASS
	Ant2	5320	8.22	<=10.81	PASS
	total	5320	10.19	<=10.81	PASS
	Ant1	5500	6.09	<=10.81	PASS
	Ant2	5500	8.86	<=10.81	PASS
	total	5500	10.70	<=10.81	PASS
	Ant1	5580	7.63	<=10.81	PASS
	Ant2	5580	5.7	<=10.81	PASS
	total	5580	9.78	<=10.81	PASS
	Ant1	5700	6.38	<=10.81	PASS
	Ant2	5700	8.65	<=10.81	PASS
	total	5700	10.67	<=10.81	PASS

	Ant1	5745	4.58	<=29.81	PASS
	Ant2	5745	6.17	<=29.81	PASS
	total	5745	8.46	<=29.81	PASS
	Ant1	5785	5.07	<=29.81	PASS
	Ant2	5785	5.15	<=29.81	PASS
	total	5785	8.12	<=29.81	PASS
	Ant1	5825	5.5	<=29.81	PASS
	Ant2	5825	5.87	<=29.81	PASS
	total	5825	8.70	<=29.81	PASS
11AC40MIMO	Ant1	5190	2.82	<=10.81	PASS
	Ant2	5190	4.94	<=10.81	PASS
	total	5190	7.02	<=10.81	PASS
	Ant1	5230	3.62	<=10.81	PASS
	Ant2	5230	4.75	<=10.81	PASS
	total	5230	7.23	<=10.81	PASS
	Ant1	5270	3.78	<=10.81	PASS
	Ant2	5270	6.08	<=10.81	PASS
	total	5270	8.09	<=10.81	PASS
	Ant1	5310	3.14	<=10.81	PASS
	Ant2	5310	4.68	<=10.81	PASS
	total	5310	6.99	<=10.81	PASS
	Ant1	5510	4.23	<=10.81	PASS
	Ant2	5510	7.04	<=10.81	PASS
	total	5510	8.87	<=10.81	PASS
	Ant1	5550	3.41	<=10.81	PASS
	Ant2	5550	7.16	<=10.81	PASS
	total	5550	9.19	<=10.81	PASS
	Ant1	5670	3.57	<=10.81	PASS
	Ant2	5670	5.78	<=10.81	PASS
	total	5670	7.82	<=10.81	PASS
	Ant1	5755	1.93	<=29.81	PASS
	Ant2	5755	4.25	<=29.81	PASS
	total	5755	6.25	<=29.81	PASS
	Ant1	5795	2.2	<=29.81	PASS
	Ant2	5795	3.97	<=29.81	PASS
	total	5795	6.18	<=29.81	PASS
11AC80MIMO	Ant1	5210	2.08	<=10.81	PASS
	Ant2	5210	3.07	<=10.81	PASS
	total	5210	5.61	<=10.81	PASS
	Ant1	5290	2.3	<=10.81	PASS
	Ant2	5290	2.3	<=10.81	PASS
	total	5290	5.31	<=10.81	PASS
	Ant1	5530	1.36	<=10.81	PASS

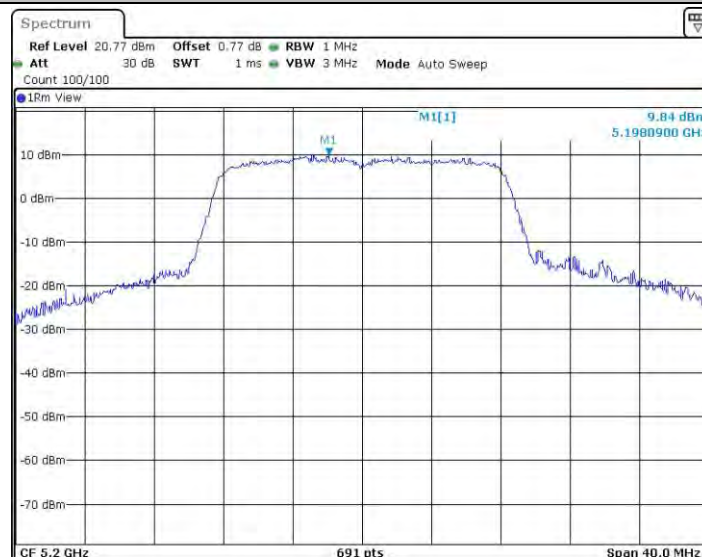
	Ant2	5530	3.35	<=10.81	PASS
	total	5530	5.48	<=10.81	PASS
	Ant1	5610	1.98	<=10.81	PASS
	Ant2	5610	3.7	<=10.81	PASS
	total	5610	5.93	<=10.81	PASS
	Ant1	5775	-0.31	<=29.81	PASS
	Ant2	5775	1.2	<=29.81	PASS
	total	5775	3.52	<=29.81	PASS

Test Graphs





11A_Ant1_5200



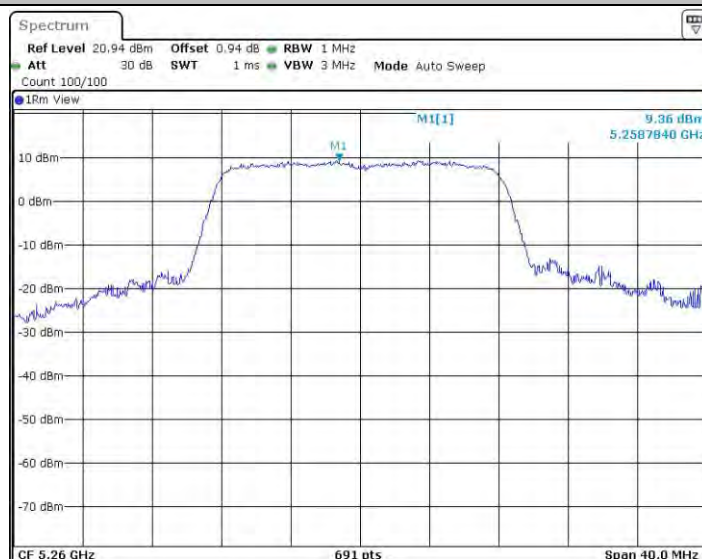
11A_Ant2_5200



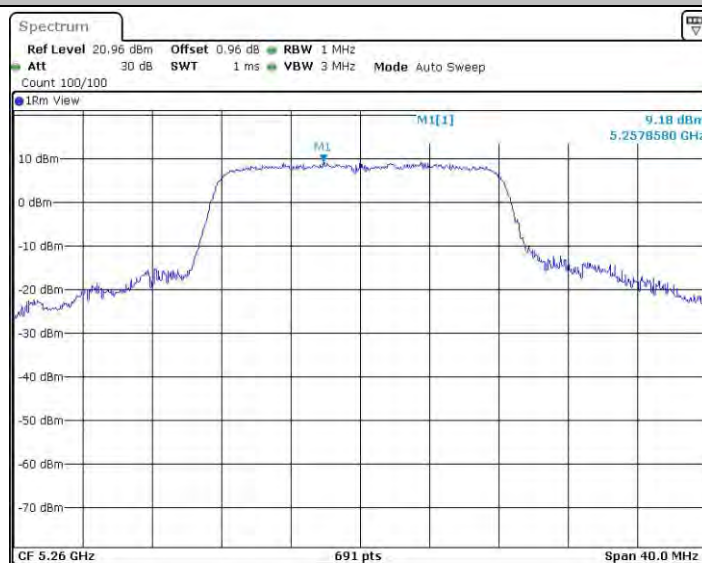
11A_Ant1_5240



11A_Ant2_5240



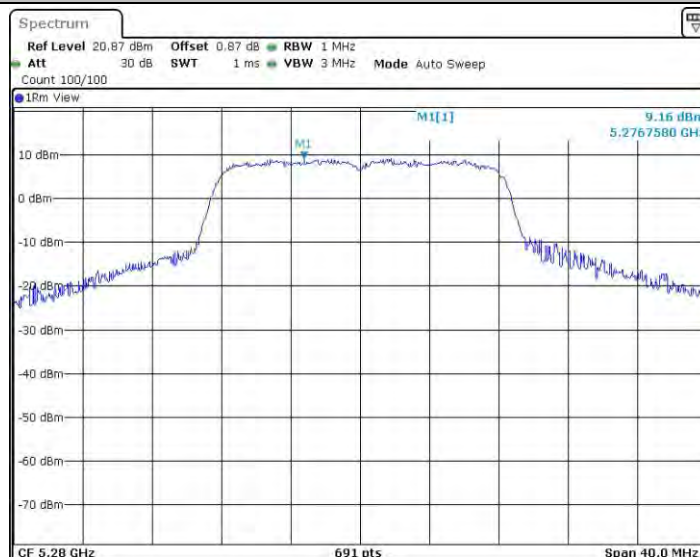
11A_Ant1_5260



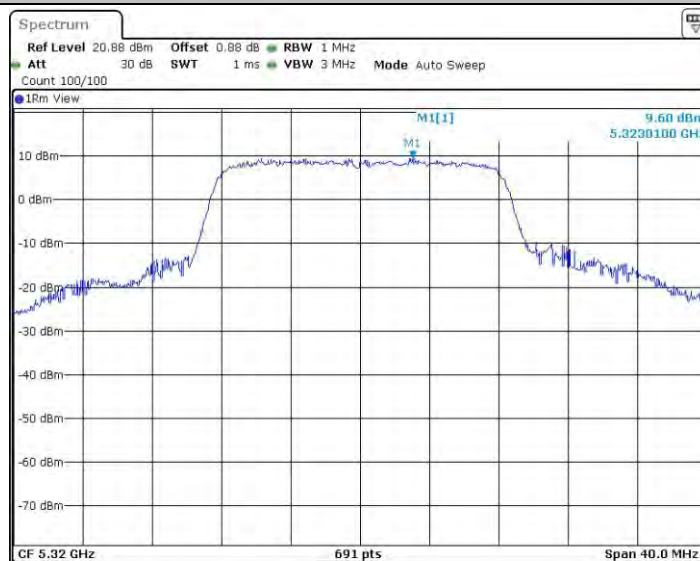
11A_Ant2_5260



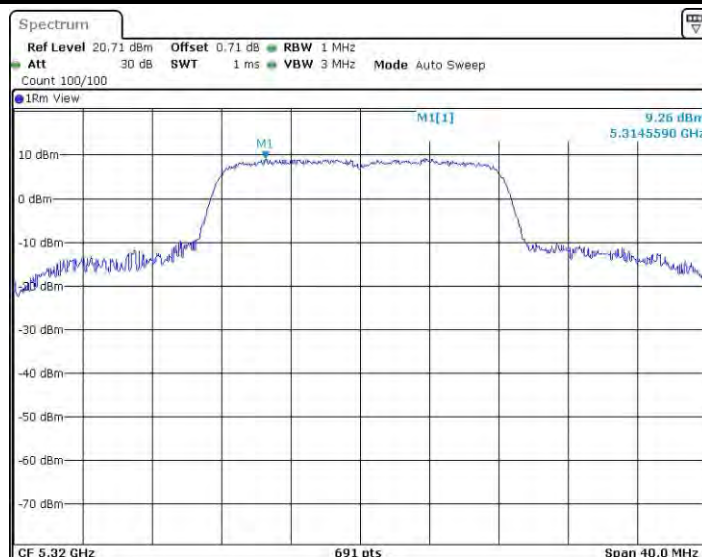
11A_Ant1_5280



11A_Ant2_5280



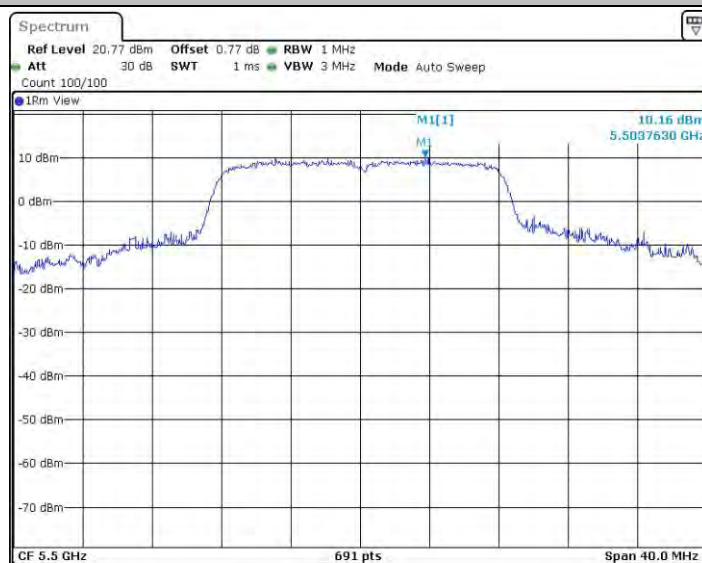
11A_Ant1_5320



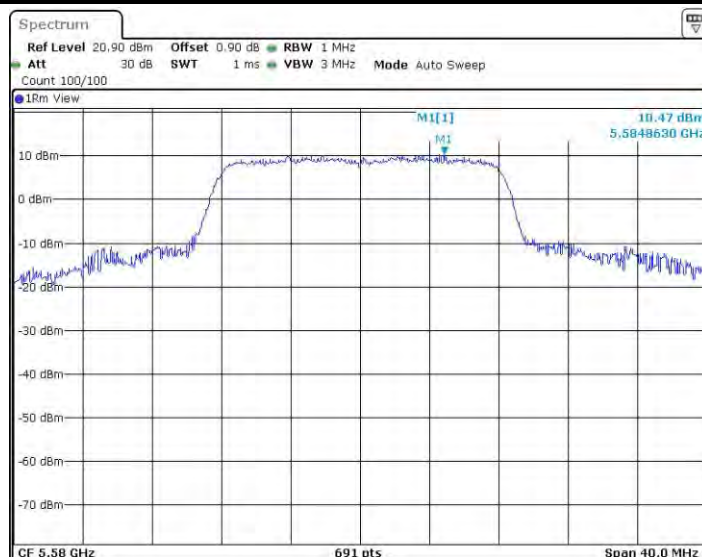
11A_Ant2_5320



11A_Ant1_5500



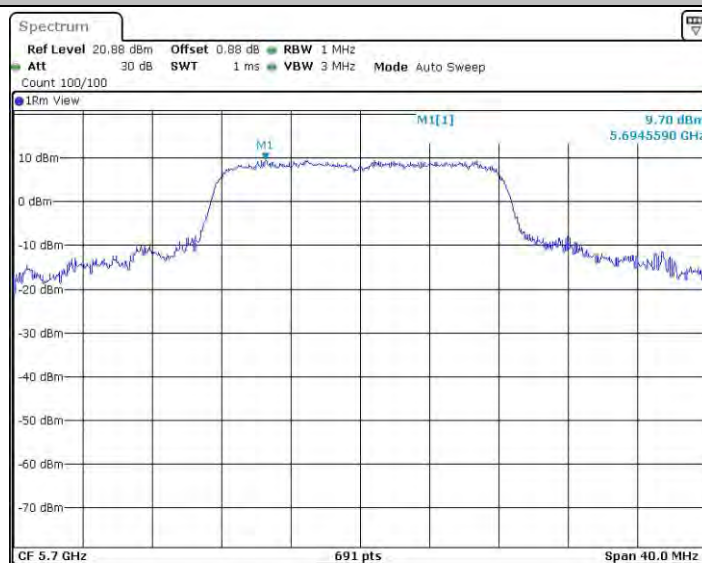
11A_Ant2_5500



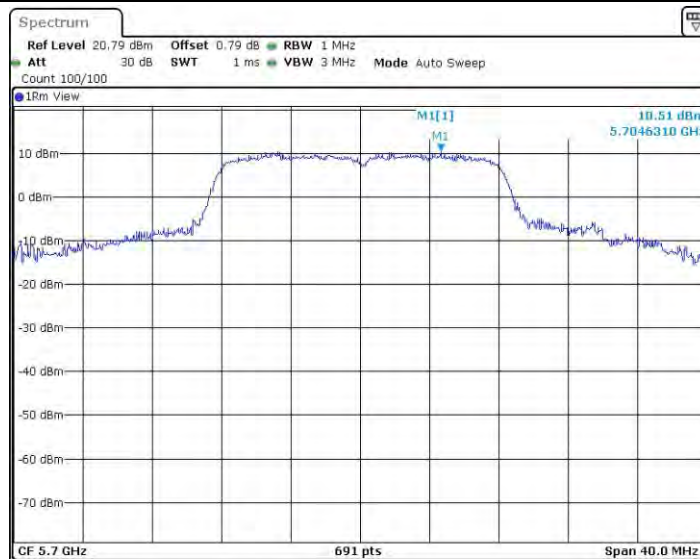
11A_Ant1_5580



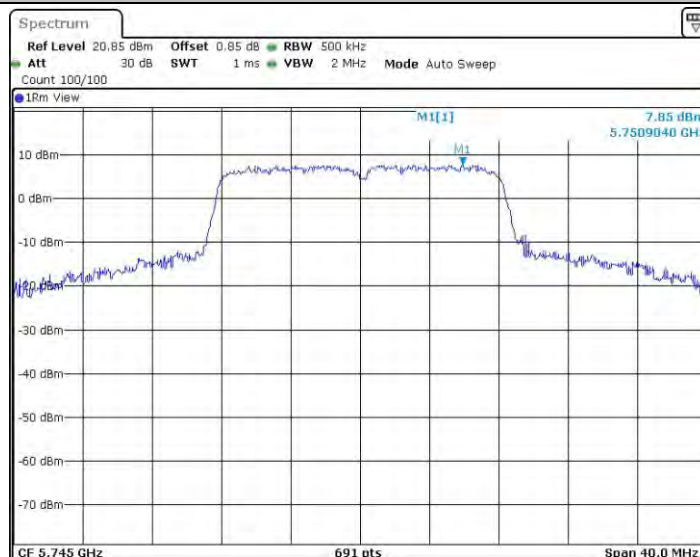
11A_Ant2_5580



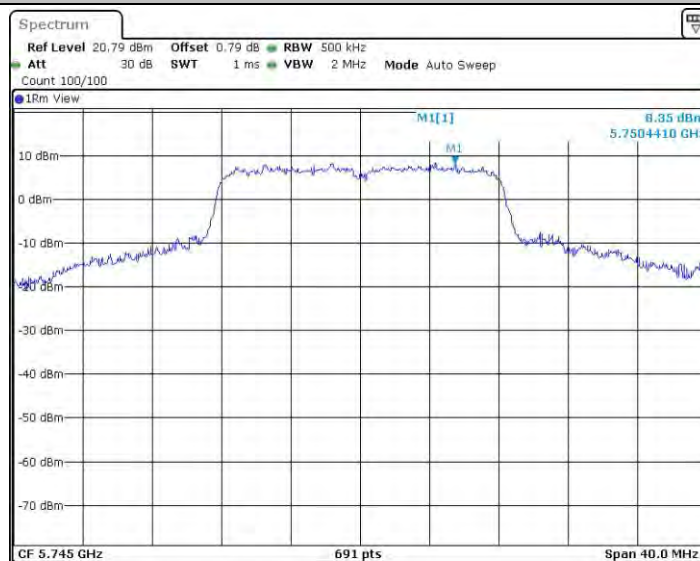
11A_Ant1_5700



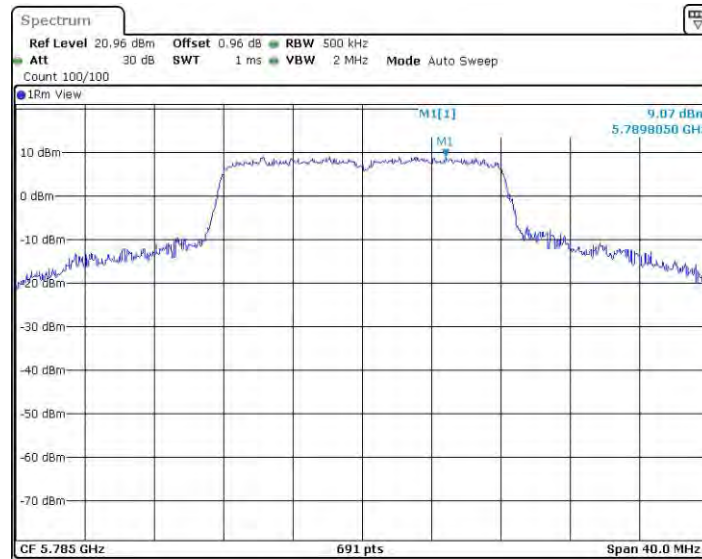
11A_Ant2_5700



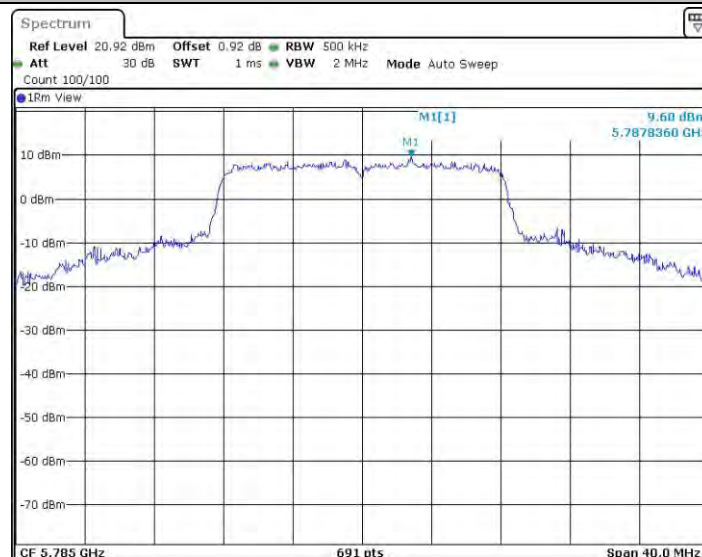
11A_Ant1_5745



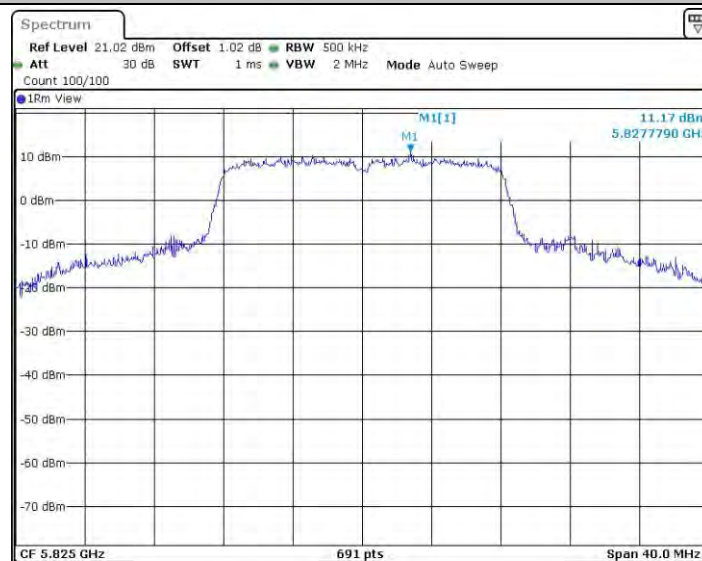
11A_Ant2_5745



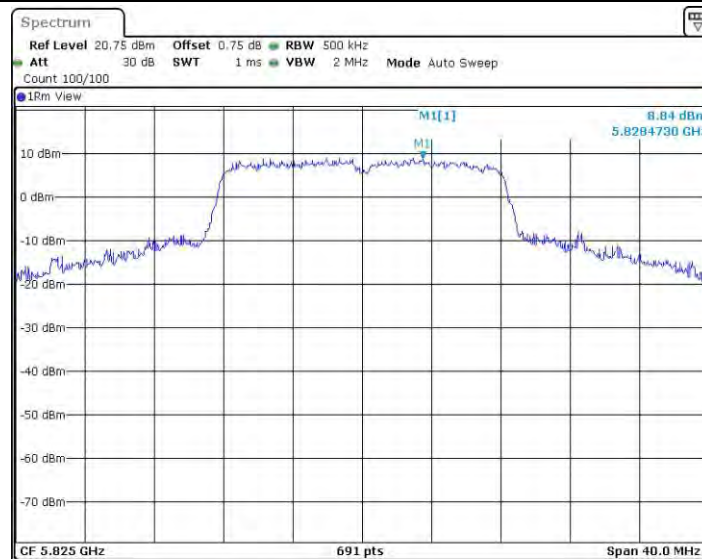
11A_Ant1_5785



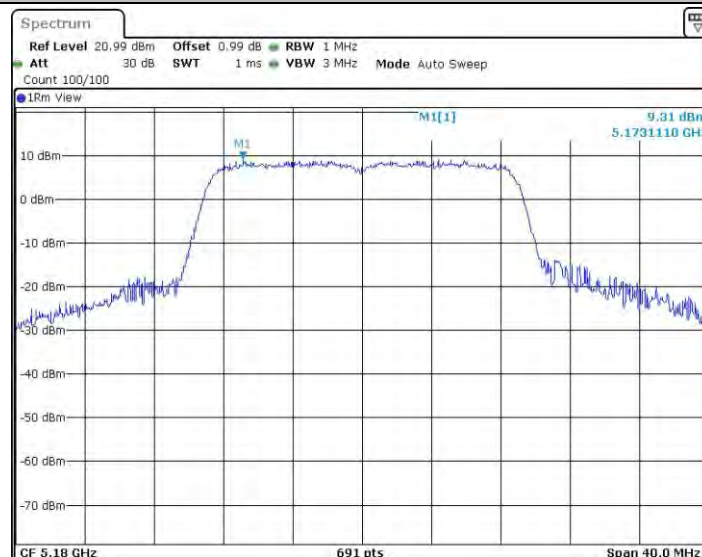
11A_Ant2_5785



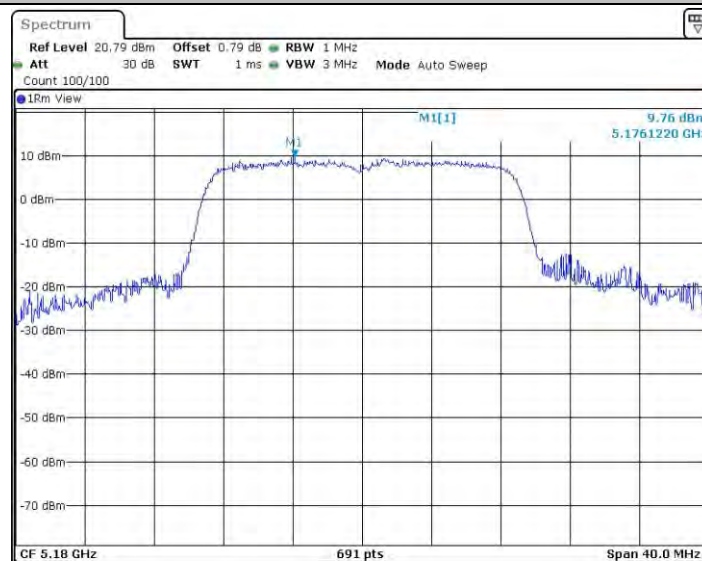
11A_Ant1_5825



11A_Ant2_5825



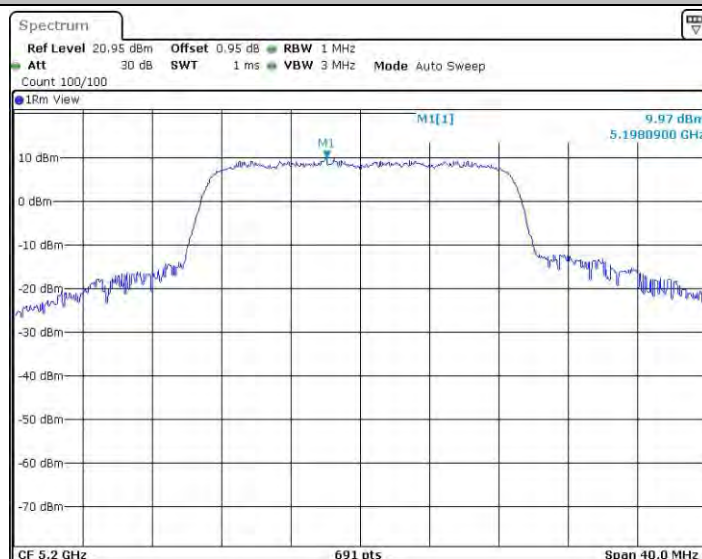
11N20SISO_Ant1_5180



11N20SISO_Ant2_5180



11N20SISO_Ant1_5200



11N20SISO_Ant2_5200



11N20SISO_Ant1_5240



11N20SISO_Ant2_5240



11N20SISO_Ant1_5260



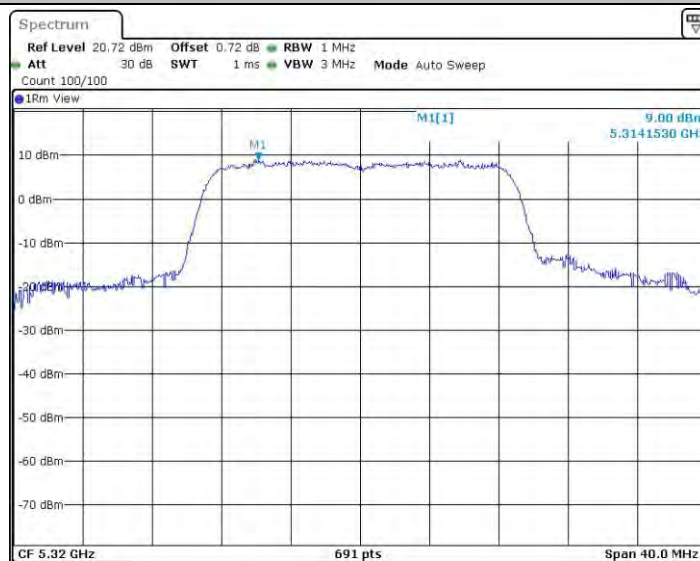
11N20SISO_Ant2_5260



11N20SISO_Ant1_5280



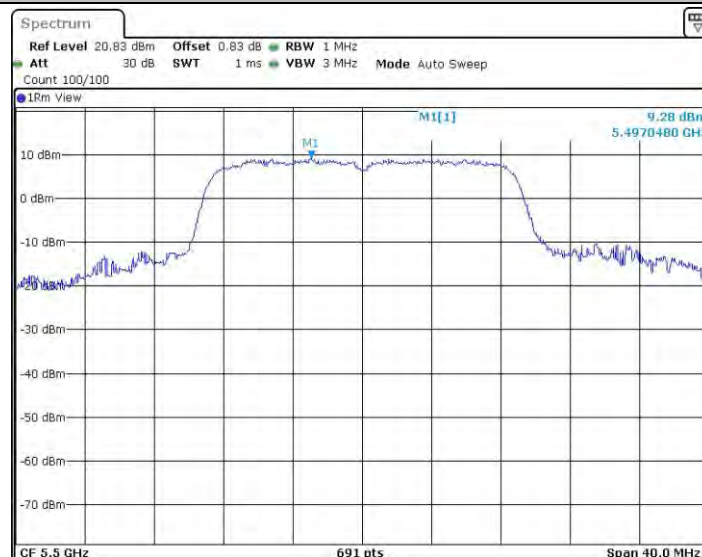
11N20SISO_Ant2_5280



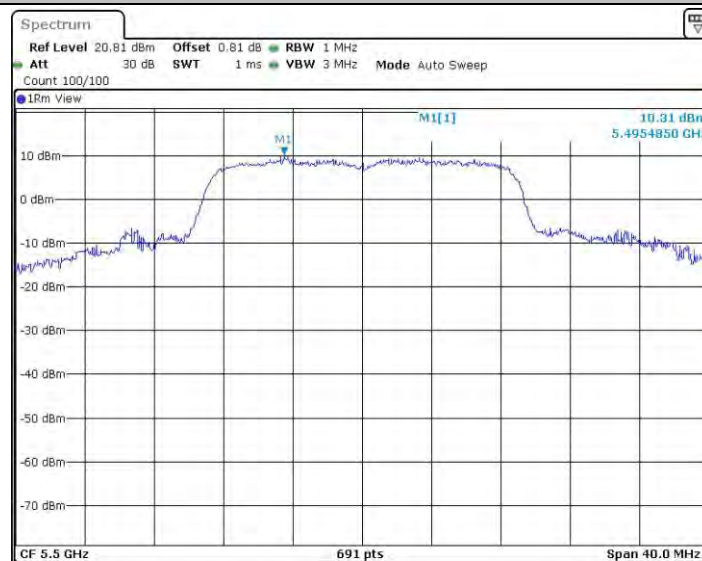
11N20SISO_Ant1_5320



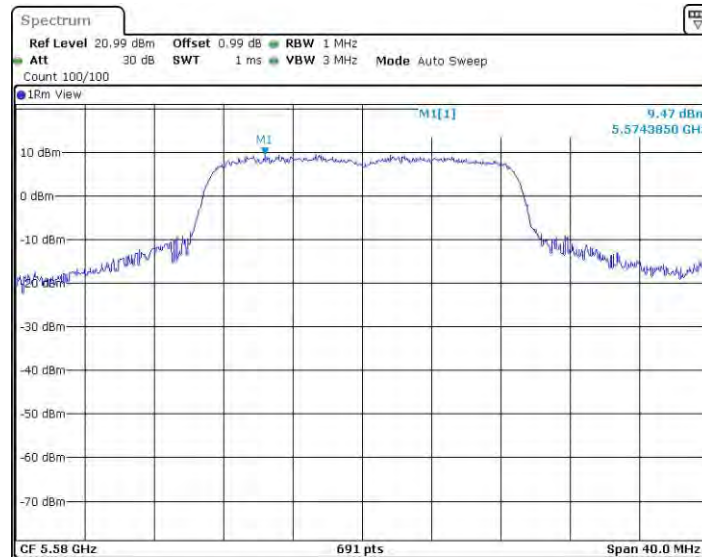
11N20SISO_Ant2_5320



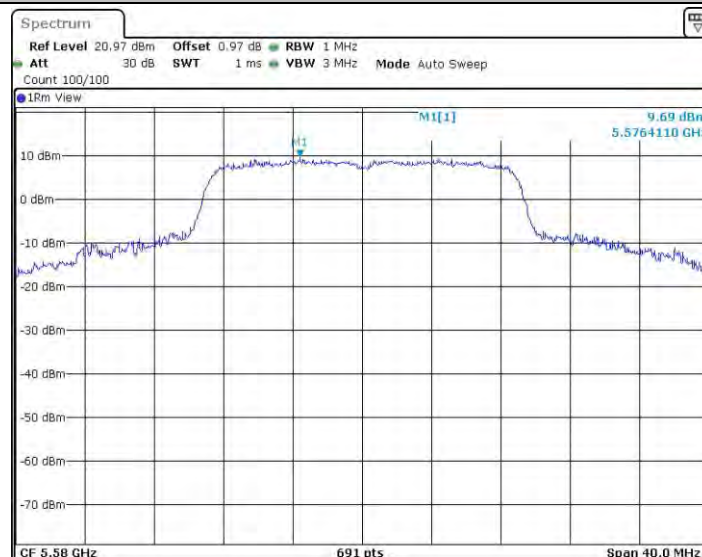
11N20SISO_Ant1_5500



11N20SISO_Ant2_5500



11N20SISO_Ant1_5580



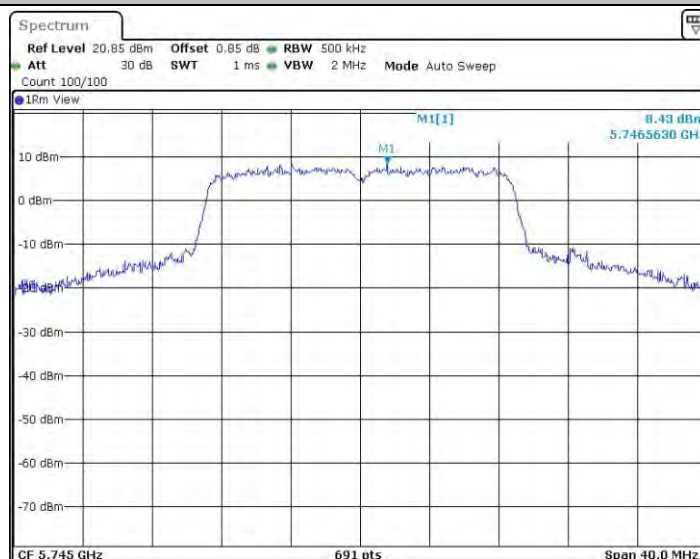
11N20SISO_Ant2_5580



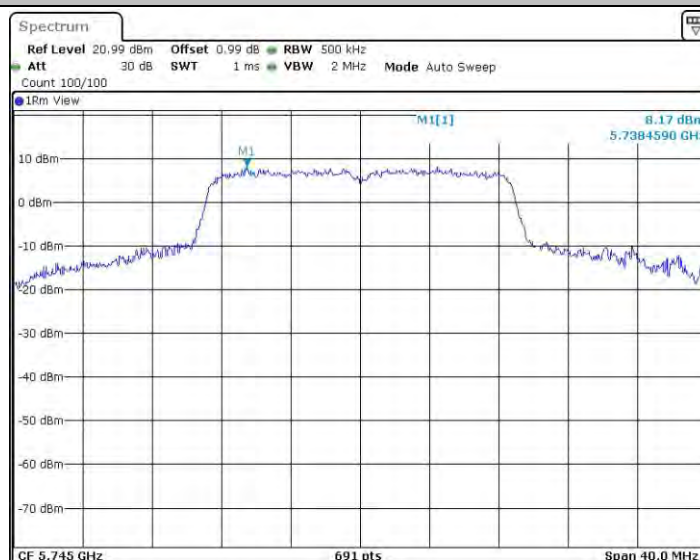
11N20SISO_Ant1_5700



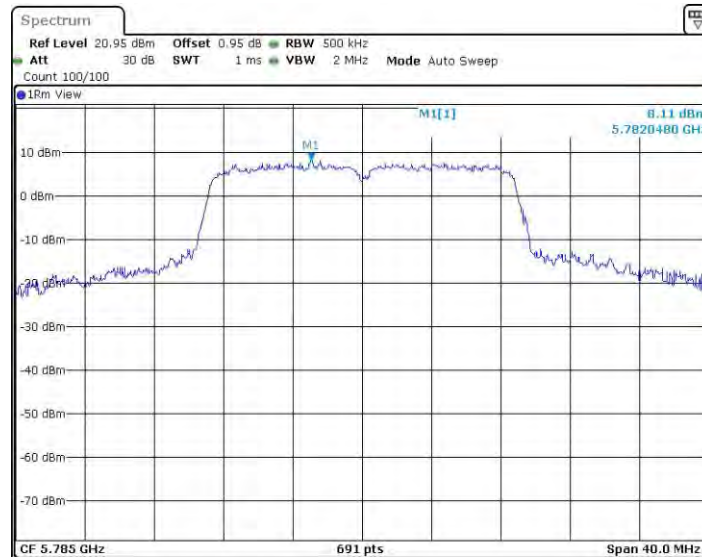
11N20SISO_Ant2_5700



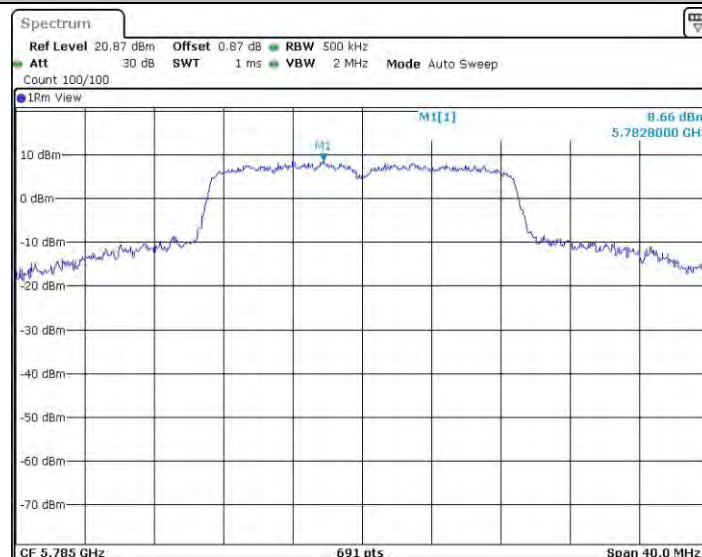
11N20SISO_Ant1_5745



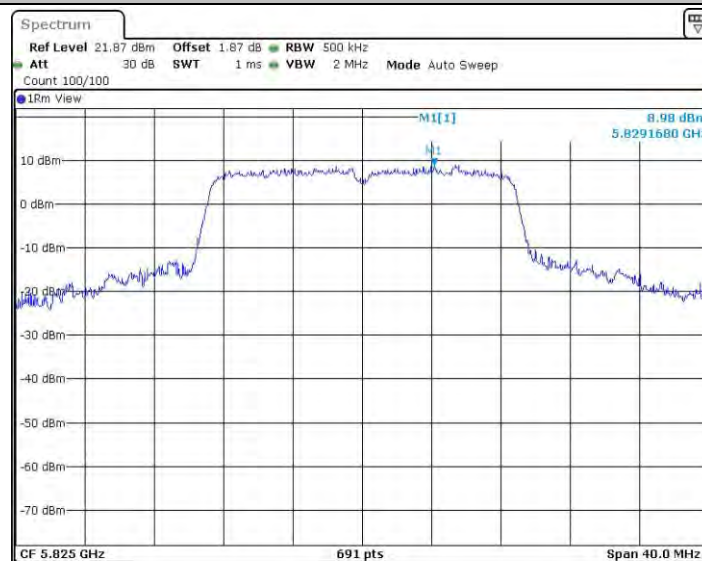
11N20SISO_Ant2_5745



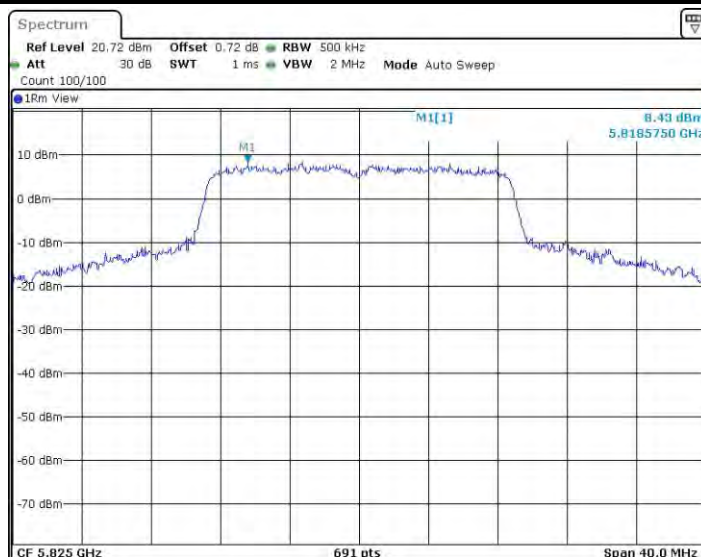
11N20SISO_Ant1_5785



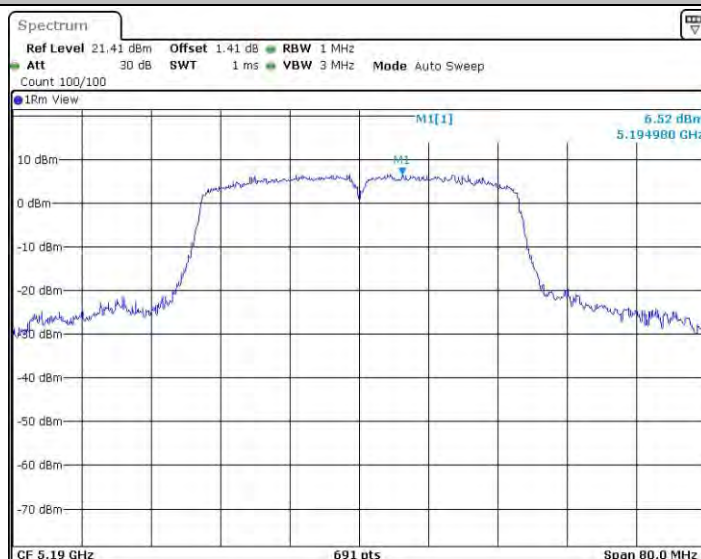
11N20SISO_Ant2_5785



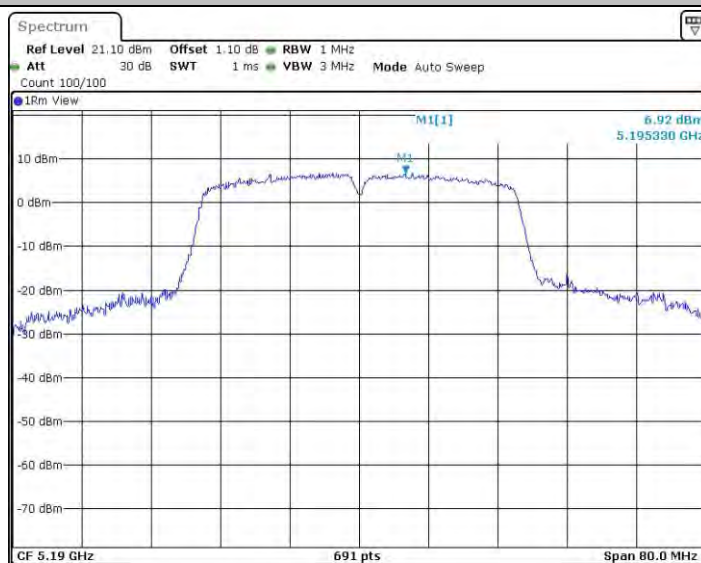
11N20SISO_Ant1_5825



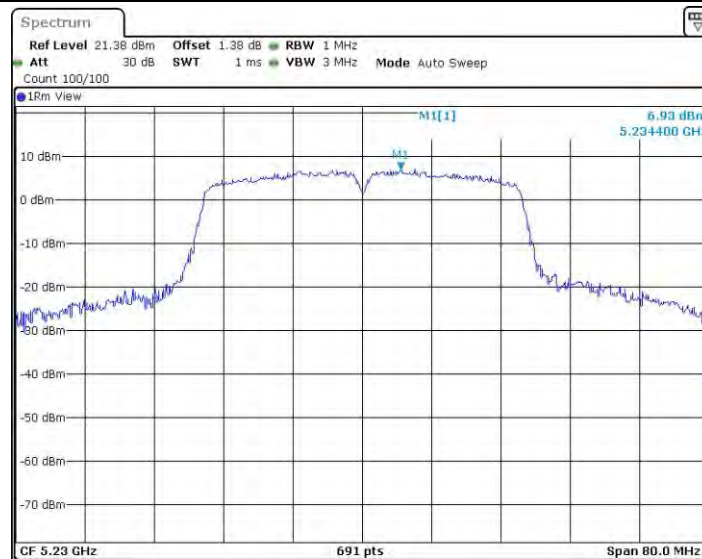
11N20SISO_Ant2_5825



11N40SISO_Ant1_5190



11N40SISO_Ant2_5190



11N40SISO_Ant1_5230



11N40SISO_Ant2_5230



11N40SISO_Ant1_5270



11N40SISO_Ant2_5270



11N40SISO_Ant1_5310



11N40SISO_Ant2_5310



11N40SISO_Ant1_5510



11N40SISO_Ant2_5510



11N40SISO_Ant1_5550



11N40SISO_Ant2_5550



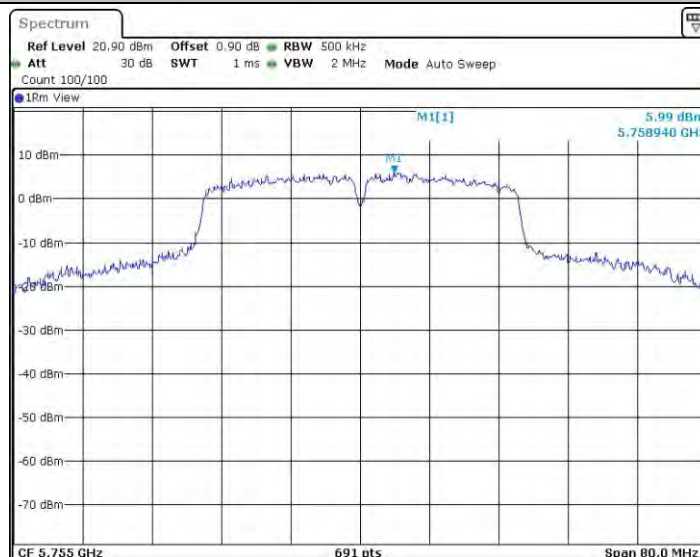
11N40SISO_Ant1_5670



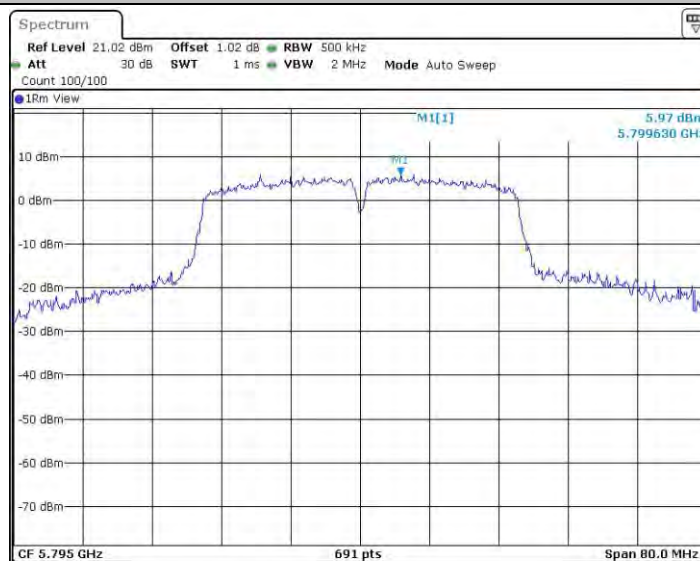
11N40SISO_Ant2_5670



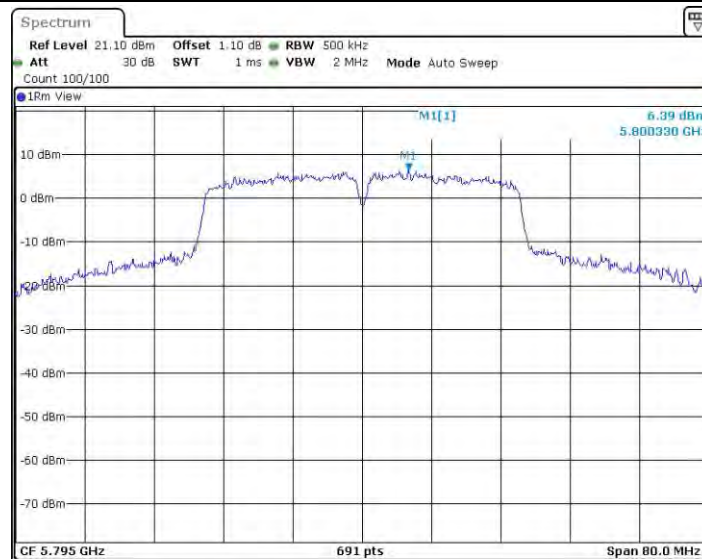
11N40SISO_Ant1_5755



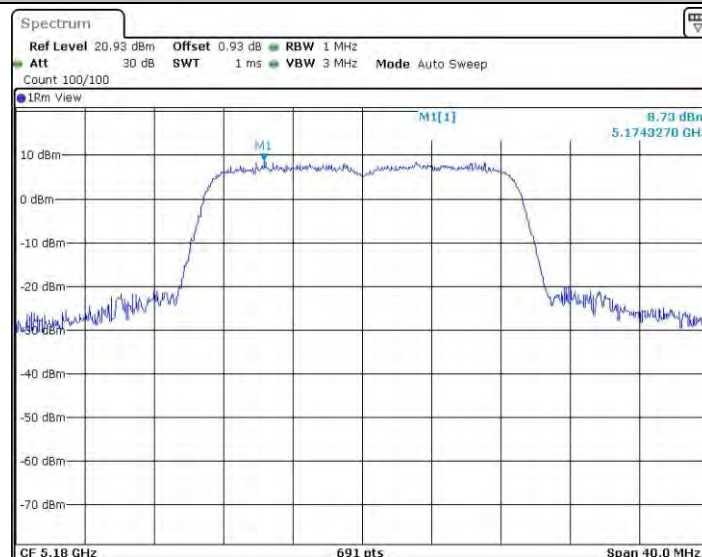
11N40SISO_Ant2_5755



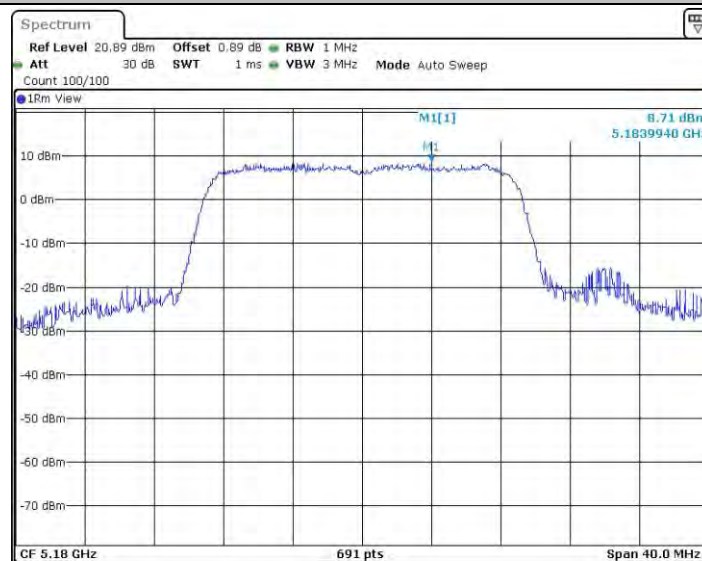
11N40SISO_Ant1_5795



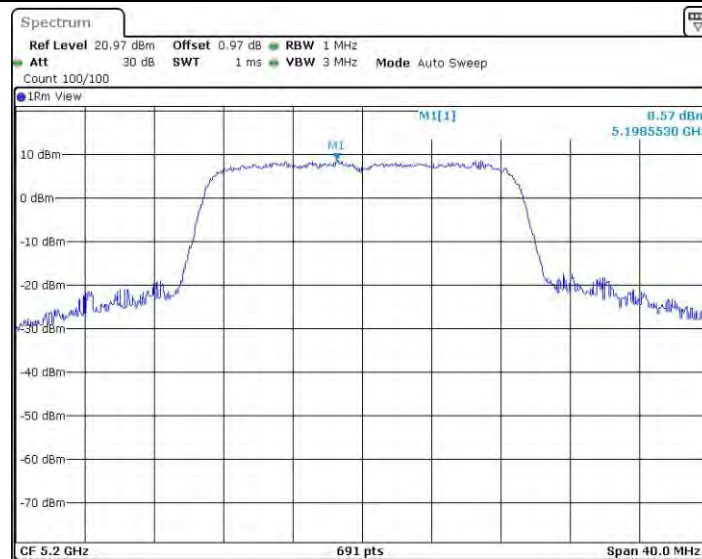
11N40SISO_Ant2_5795



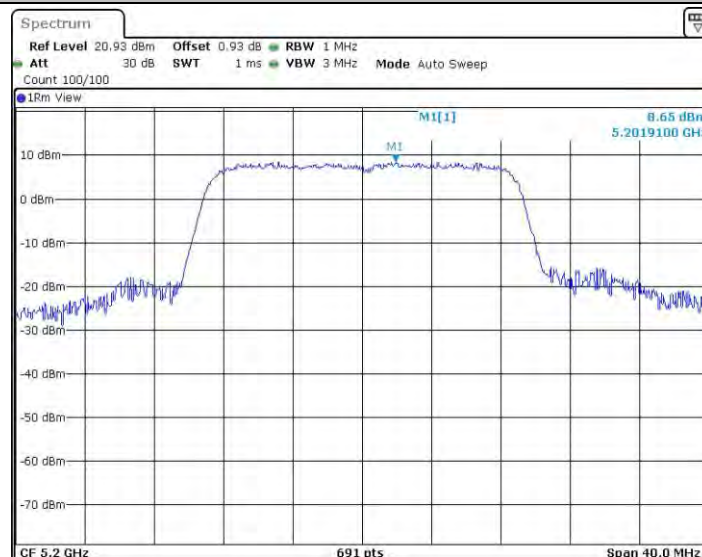
11AC20SISO_Ant1_5180



11AC20SISO_Ant2_5180



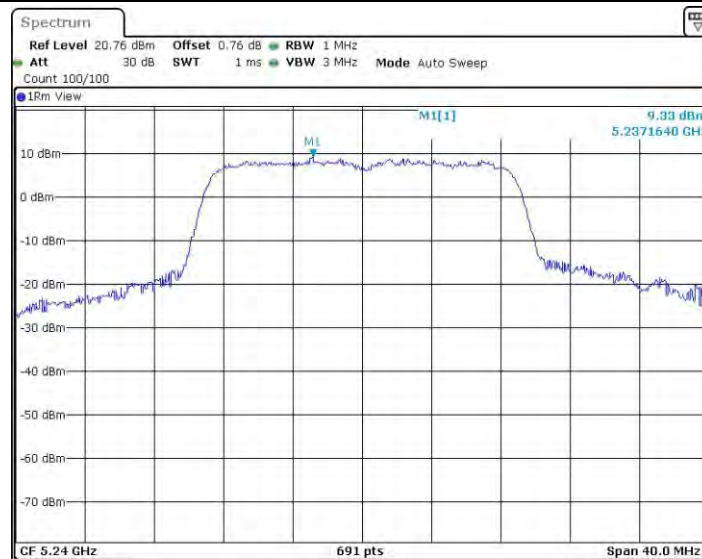
11AC20SISO_Ant1_5200



11AC20SISO_Ant2_5200



11AC20SISO_Ant1_5240



11AC20SISO_Ant2_5240



11AC20SISO_Ant1_5260



11AC20SISO_Ant2_5260



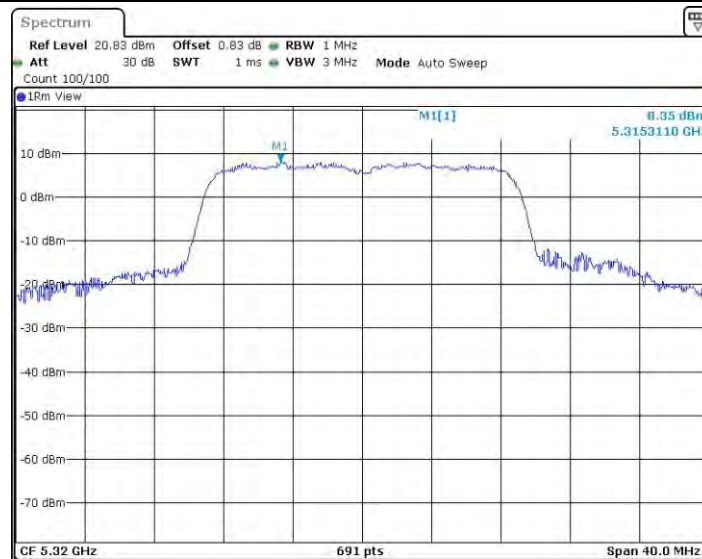
11AC20SISO_Ant1_5280



11AC20SISO_Ant2_5280



11AC20SISO_Ant1_5320



11AC20SISO_Ant2_5320



11AC20SISO_Ant1_5500



11AC20SISO_Ant2_5500