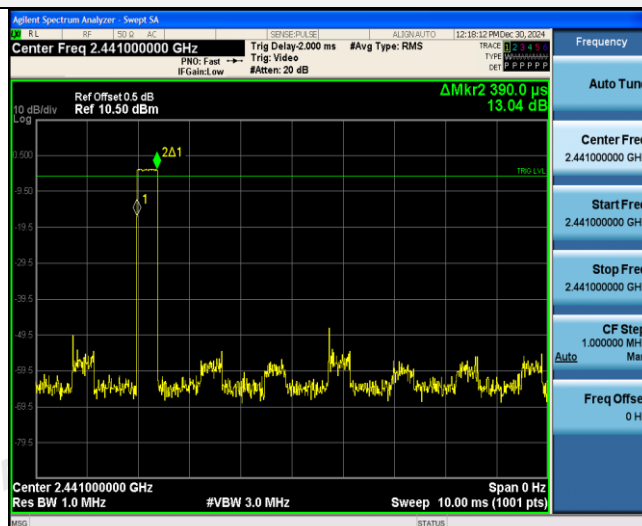


$\pi/4$ DQPSK Modulation

2-DH1 burst time

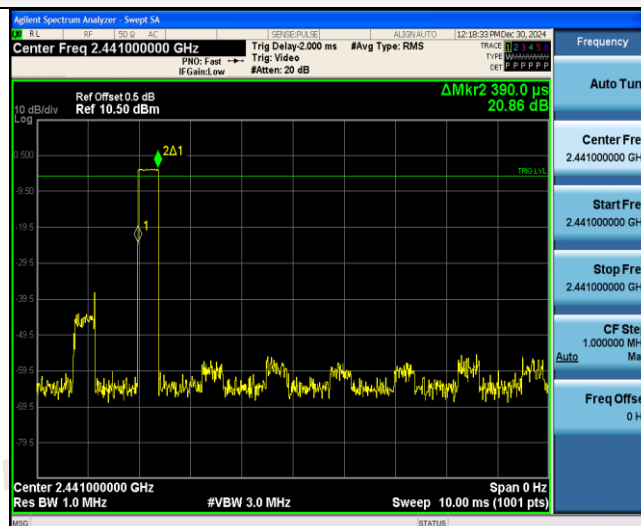


2-DH3 burst time

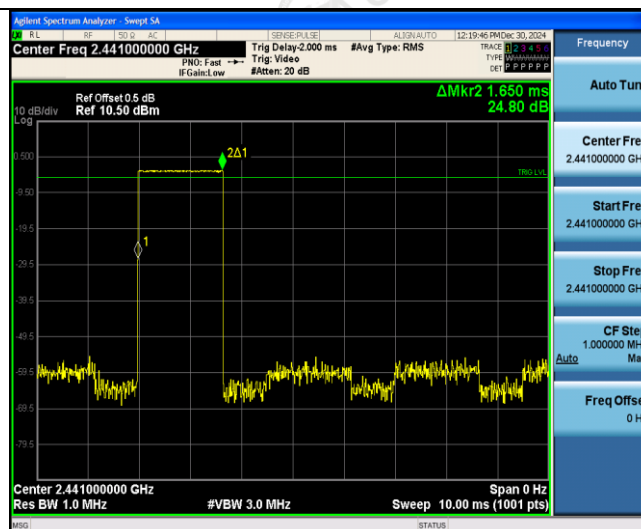


2-DH5 burst time

8DPSK Modulation



3-DH1 burst time



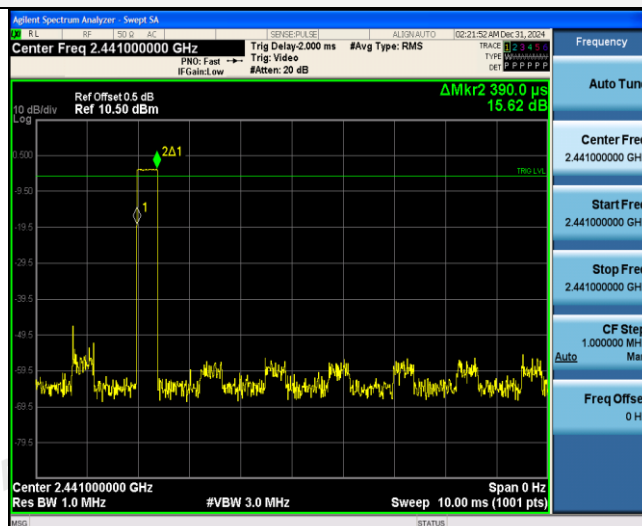
3-DH3 burst time



3-DH5 burst time

Right:

GFSK Modulation



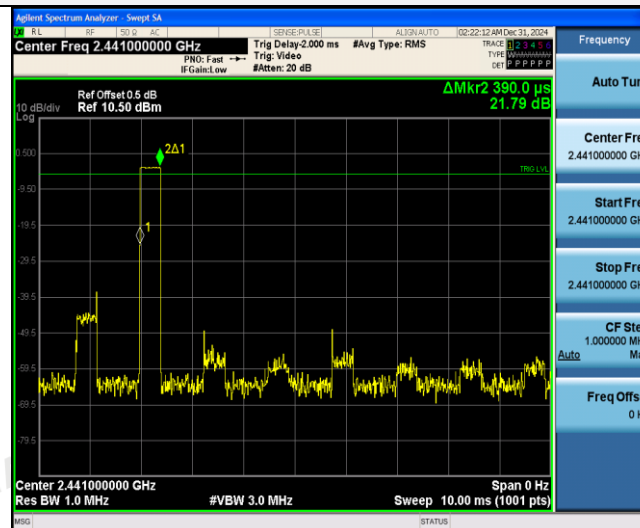
DH1 burst time



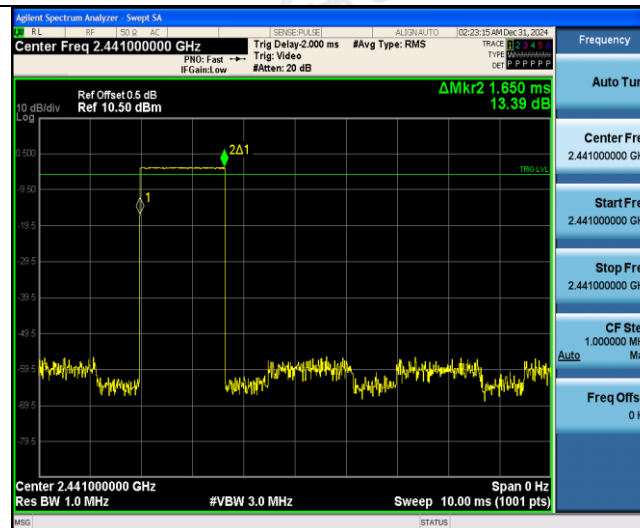
DH3 burst time



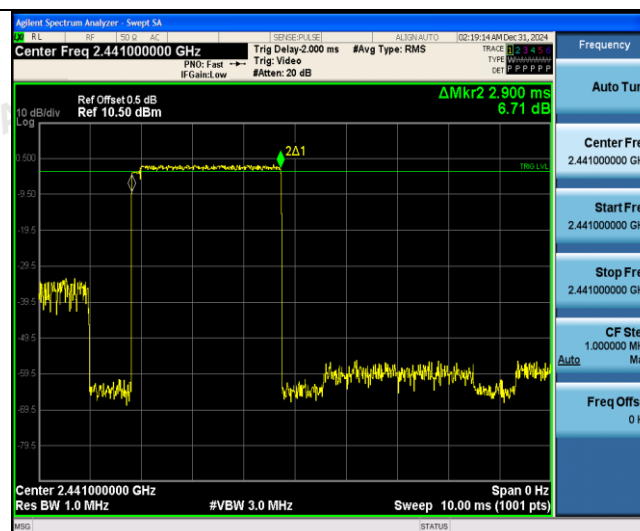
DH5 burst time

$\pi/4$ DQPSK Modulation

2-DH1 burst time

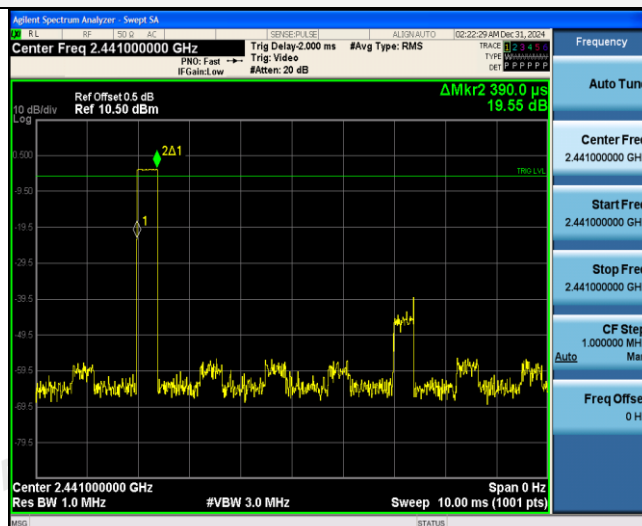


2-DH3 burst time



2-DH5 burst time

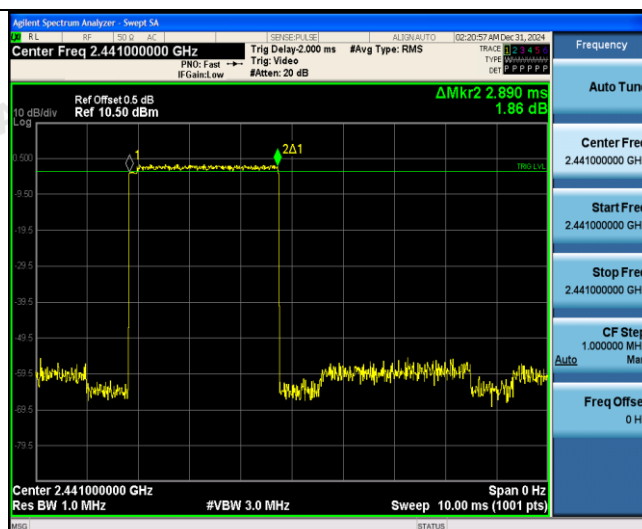
8DPSK Modulation



3-DH1 burst time



3-DH3 burst time



3-DH5 burst time

4.8 Out-of-band Emissions

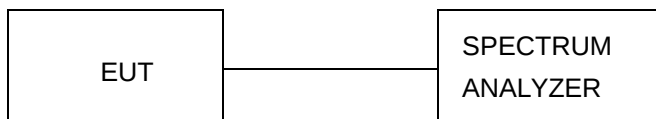
Limit

In any 100 kHz bandwidth outside the frequency band in which the spread spectrum or digitally modulated intentional radiator is operating, the radio frequency power that is produced by the intentional radiator shall be at least 20 dB below that in the 100 kHz bandwidth within the band that contains the highest level of the desired power, based on either an RF conducted or a radiated measurement, provided the transmitter demonstrates compliance with the peak conducted power limits. If the transmitter complies with the conducted power limits based on the use of RMS averaging over a time interval, as permitted under paragraph (b)(3) of this section, the attenuation required under this paragraph shall be 30 dB instead of 20 dB. Attenuation below the general limits specified in §15.209(a) is not required.

Test Procedure

Connect the transmitter output to spectrum analyzer using a low loss RF cable, and set the spectrum analyzer to RBW=100 kHz, VBW= 300 kHz, peak detector, and max hold. Measurements utilizing these settings are made of the in-band reference level, band edge and out-of-band emissions.

Test Configuration



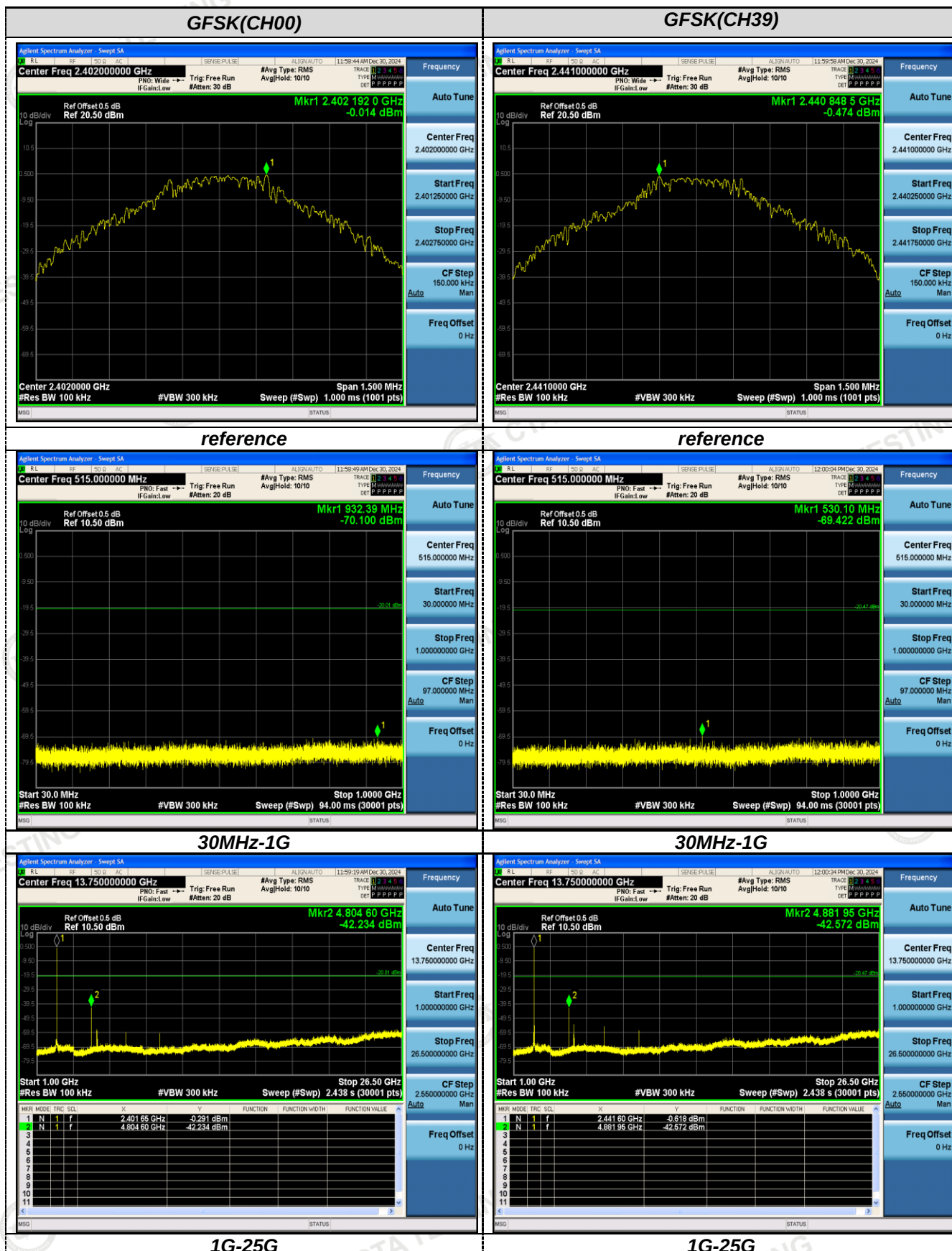
Test Results

Remark: The measurement frequency range is from 30MHz to the 10th harmonic of the fundamental frequency. The lowest, middle and highest channels are tested to verify the spurious emissions and band edge measurement data.

We measured all conditions (DH1, DH3, DH5) and recorded worst case at DH5

Test plot as follows:

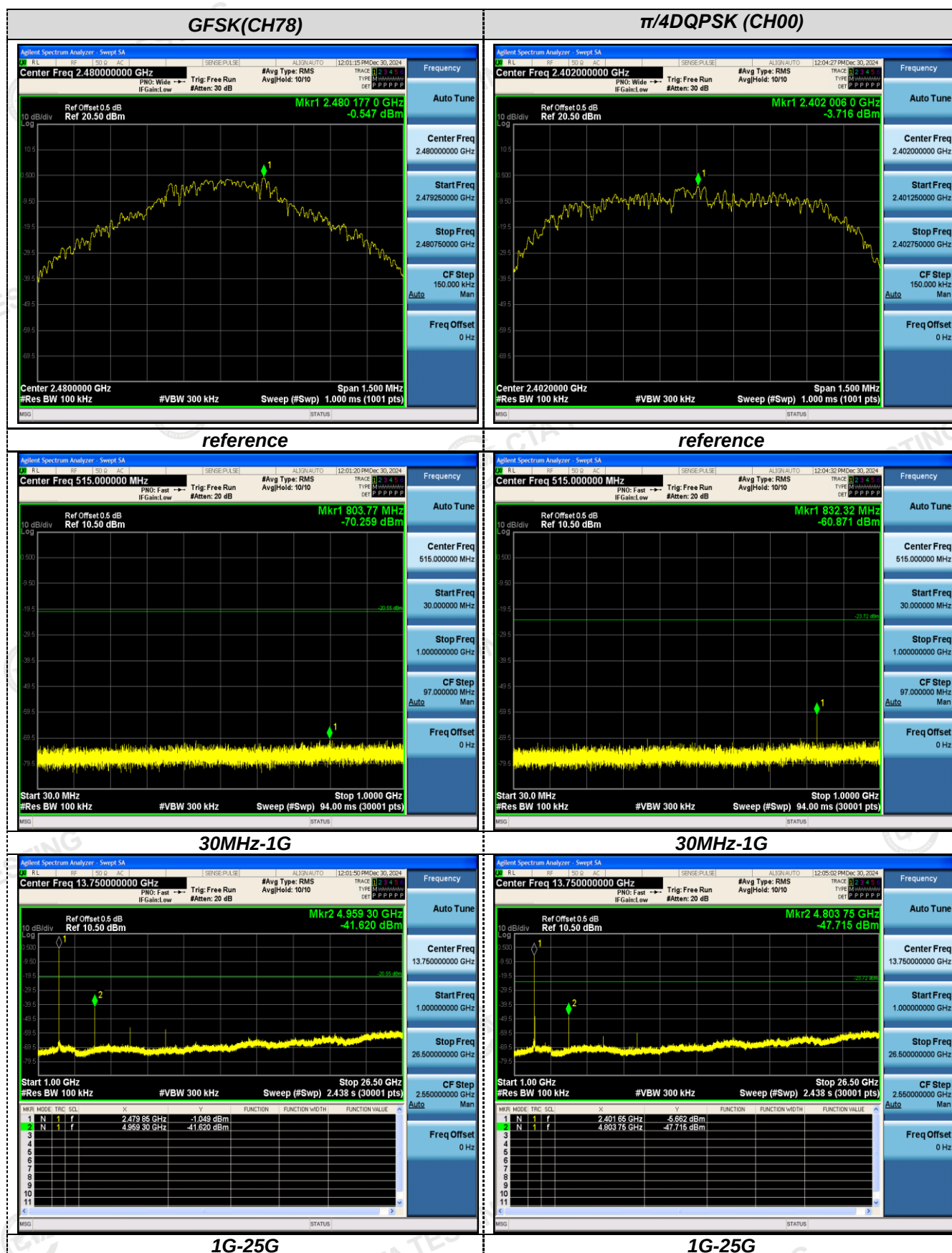
Left:

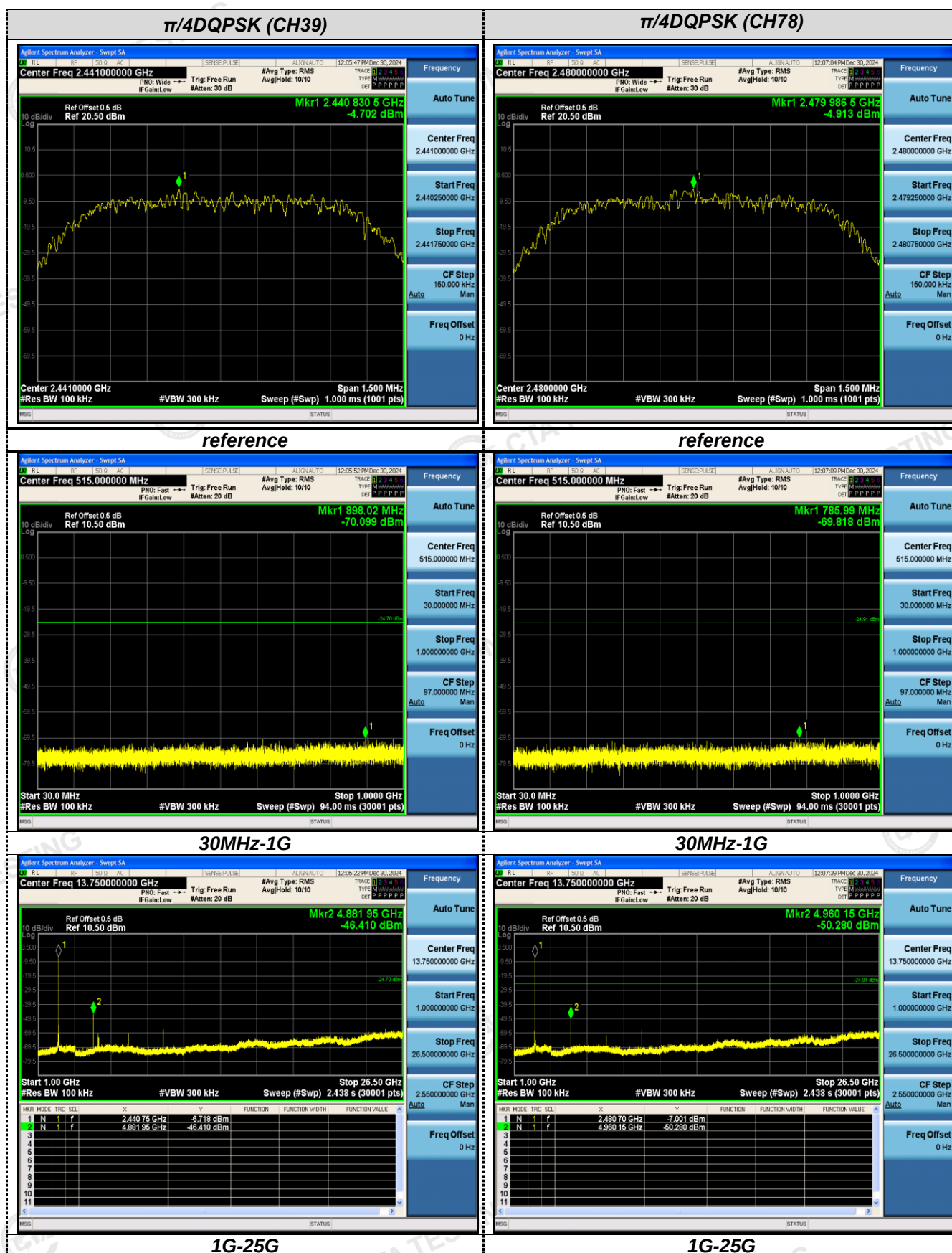


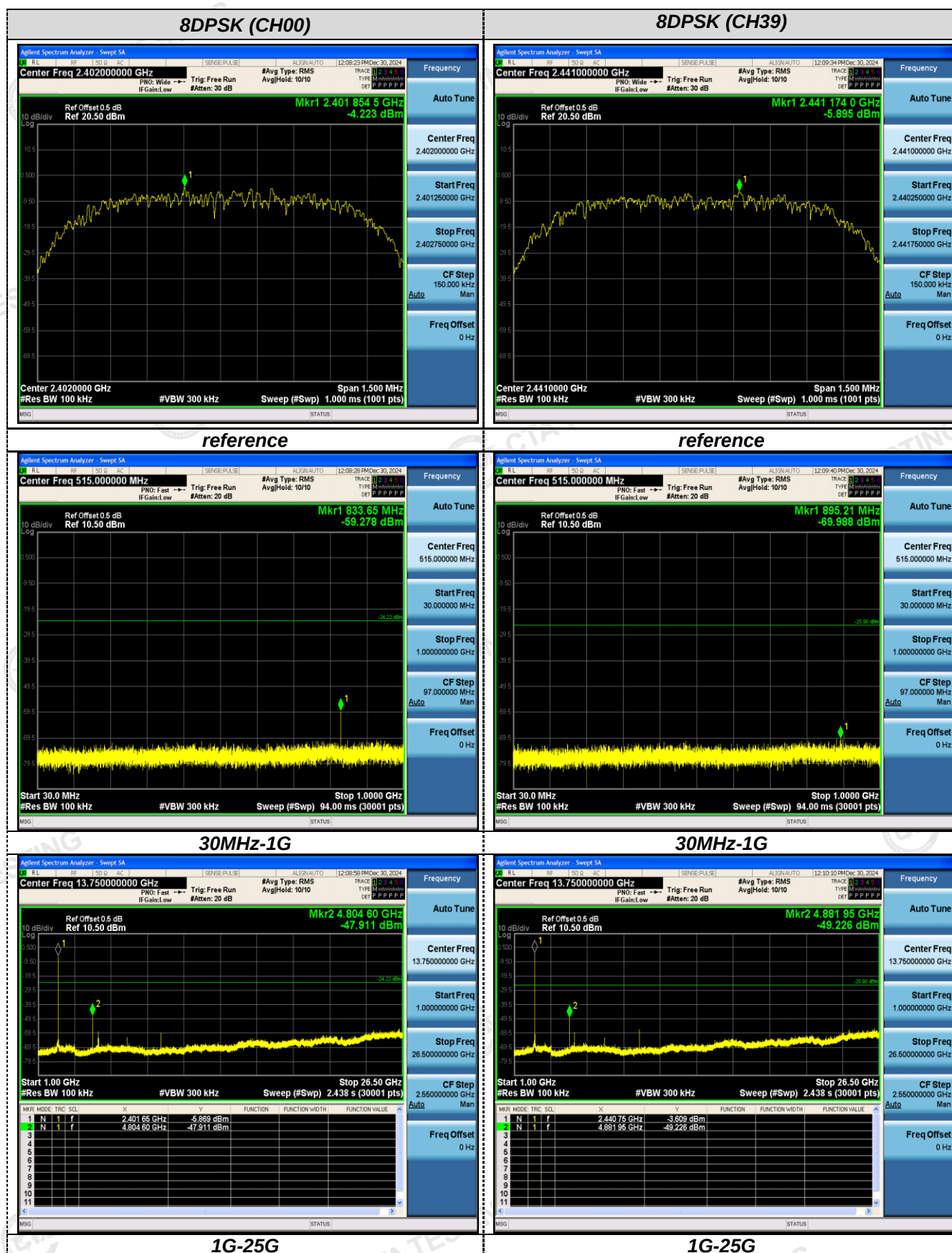
Shenzhen CTA Testing Technology Co., Ltd.

Room 106, Building 1, Yibaolai Industrial Park, Qiaotou Community, Fuhai Street, Bao'an District, Shenzhen, China

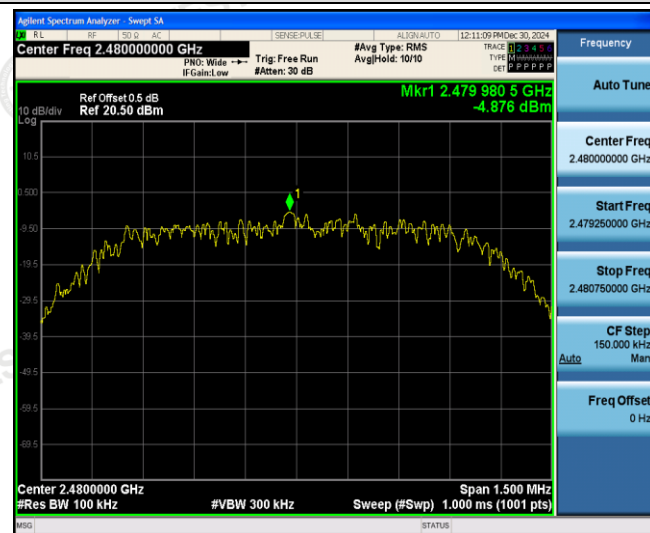
Tel: +86-755 2322 5875 E-mail: cta@cta-test.cn Web: http://www.cta-test.cn



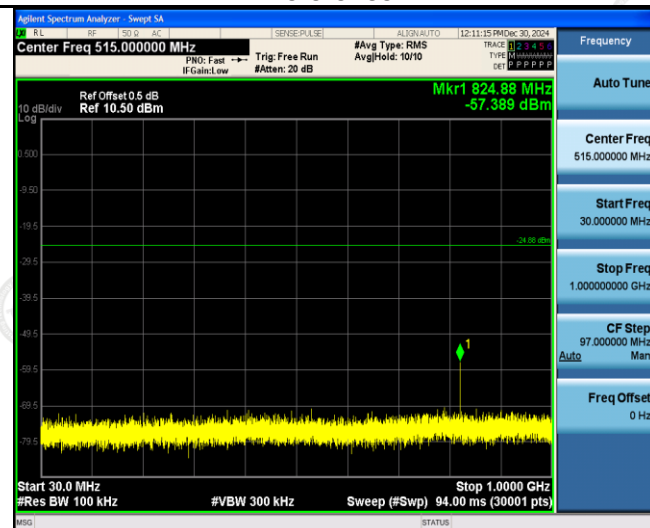




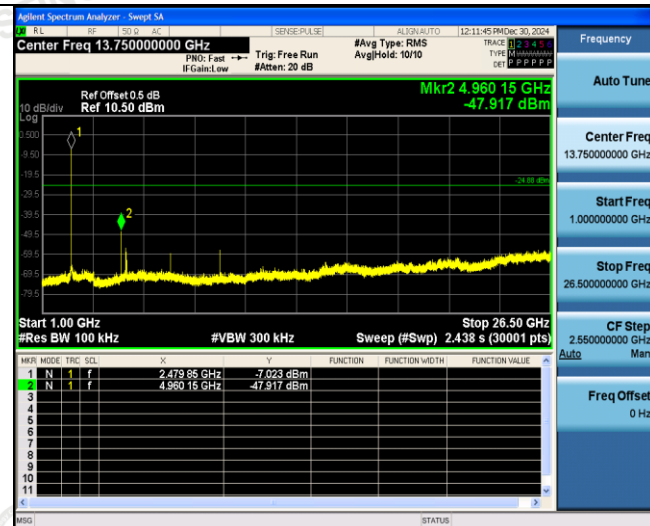
8DPSK (CH78)



reference



30MHz-1G



1G-25G

Right:

