

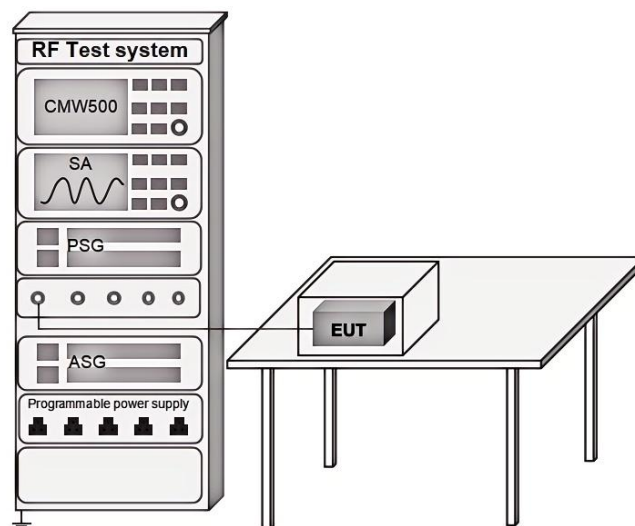
6.15 DFS: Non-occupancy period

Test Standard	47 CFR Part 15, Subpart E 15.407(h)(2)
Test Method	KDB 905462 D02 Section 7.8.3
Test Mode (Pre-Scan)	TX
Test Mode (Final Test)	TX

6.15.1 Limit

Minimum 30 minutes

6.15.2 Test setup



6.15.3 Procedure

- 1) The radar pulse generator is setup to provide a pulse at frequency that the master and client are operating. A type 0 radar pulse with a 1us pulse width and a 1428us PRI is used for the testing.
- 2) The vector signal generator is adjusted to provide the radar burst (18 pulses) at the level of approximately -61dBm at the antenna port of the master device.
- 3) A trigger is provided from the pulse generator to the DFS monitoring system in order to capture the traffic and the occurrence of the radar pulse.
- 4) EUT will associate with the master at channel. The file `iperf.exe` specified by the FCC is streamed from the PC 2 through the master and the client device to the PC 1 and played in full motion video using Media Player Classic Ver. 6.4.8.6 in order to properly load the network for the entire period of the test.

- 5) When radar burst with a level equal to the DFS Detection Threshold +1dB is generated on the operating channel of the U-NII device. At time T0 the radar waveform generator sends a burst of pulse of the radar waveform at Detection Threshold +1dB.
- 6) Observe the transmissions of the EUT at the end of the radar Burst on the Operating Channel. Measure and record the transmissions from the UUT during the observation time (Channel Move Time). One 15 seconds plot is reported for the Short Pulse Radar Type 0. The plot for the Short Pulse Radar Types start at the end of the radar burst. The Channel Move Time will be calculated based on the zoom in 600ms plot of the Short Pulse Radar Type.
- 7) Measurement of the aggregate duration of the Channel Closed Transmission Time method. With the spectrum analyzer set to zero span tuned to the center frequency of the EUT operating channel at the radar simulated frequency, peak detection, and max hold, the dwell time per bin is given by: $Dwell (0.3ms) = S (12000ms) / B (4000)$; where Dwell is the dwell time per spectrum analyzer sampling bin, S is sweep time and B is the number of spectrum analyzer sampling bins. An upper bound of the aggregate duration of the intermittent control signals of Channel Closing Transmission Time is calculated by: $C (ms) = N \times Dwell (0.3ms)$; where C is the Closing Time, N is the number of spectrum analyzer sampling bins (intermittent control signals) showing a U-NII transmission and Dwell is the dwell time per bin.
- 8) Measurement the EUT for more than 30 minutes following the channel move time to verify that no transmission or beacons occur on this channel.

6.15.4 Test data

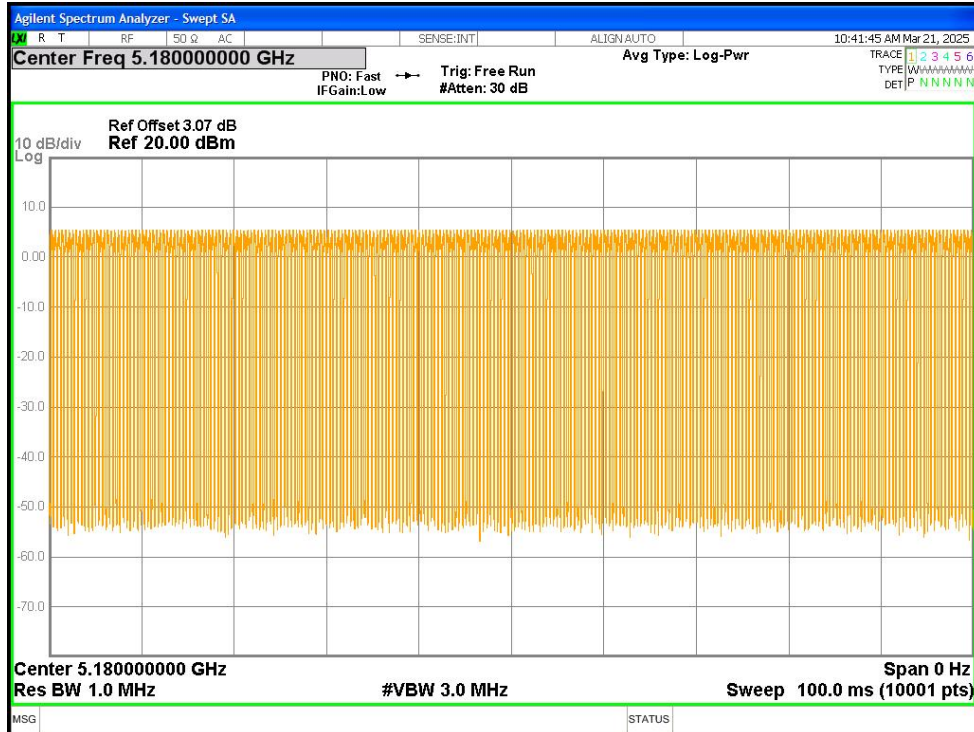
Pass: Please refer to DFS report BLA-EMC-202503-A2704.

7 Appendix A

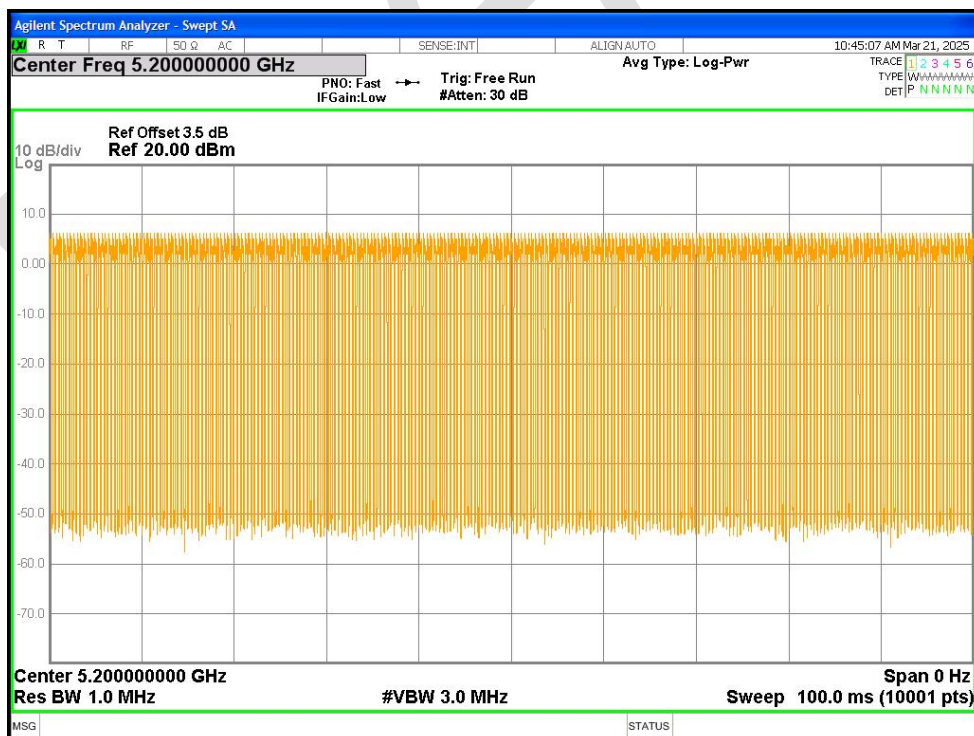
7.1 Duty Cycle

Condition	Mode	Frequency (MHz)	Antenna	Duty Cycle (%)	Correction Factor (dB)
NVNT	a	5180	Ant1	68.73	1.63
NVNT	a	5200	Ant1	68.85	1.62
NVNT	a	5240	Ant1	68.41	1.65
NVNT	a	5260	Ant1	68.84	1.62
NVNT	a	5280	Ant1	68.28	1.66
NVNT	a	5320	Ant1	68.8	1.62
NVNT	a	5500	Ant1	68.85	1.62
NVNT	a	5600	Ant1	68.2	1.66
NVNT	a	5700	Ant1	68.2	1.66
NVNT	a	5745	Ant1	68.36	1.65
NVNT	a	5785	Ant1	68.21	1.66
NVNT	a	5825	Ant1	68.21	1.66
NVNT	n20	5180	Ant1	67	1.74
NVNT	n20	5200	Ant1	66.56	1.77
NVNT	n20	5240	Ant1	67.08	1.73
NVNT	n20	5260	Ant1	67.03	1.74
NVNT	n20	5280	Ant1	67.06	1.74
NVNT	n20	5320	Ant1	67.08	1.73
NVNT	n20	5500	Ant1	67.04	1.74
NVNT	n20	5600	Ant1	67.07	1.73
NVNT	n20	5700	Ant1	66.57	1.77
NVNT	n20	5745	Ant1	66.62	1.76
NVNT	n20	5785	Ant1	66.52	1.77
NVNT	n20	5825	Ant1	66.91	1.74
NVNT	n40	5190	Ant1	53.89	2.68
NVNT	n40	5230	Ant1	54.31	2.65
NVNT	n40	5270	Ant1	53.91	2.68
NVNT	n40	5310	Ant1	54.25	2.66
NVNT	n40	5510	Ant1	54.23	2.66
NVNT	n40	5590	Ant1	54.26	2.65
NVNT	n40	5670	Ant1	53.91	2.68
NVNT	n40	5755	Ant1	54.22	2.66
NVNT	n40	5795	Ant1	54.22	2.66

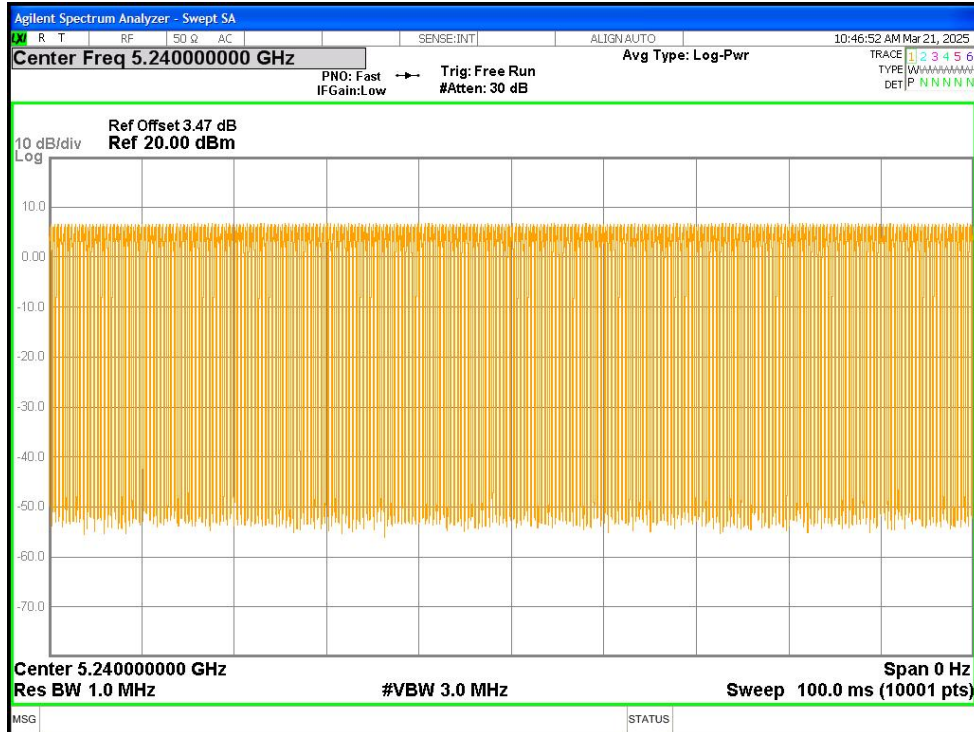
Duty Cycle NVNT a 5180MHz Ant1



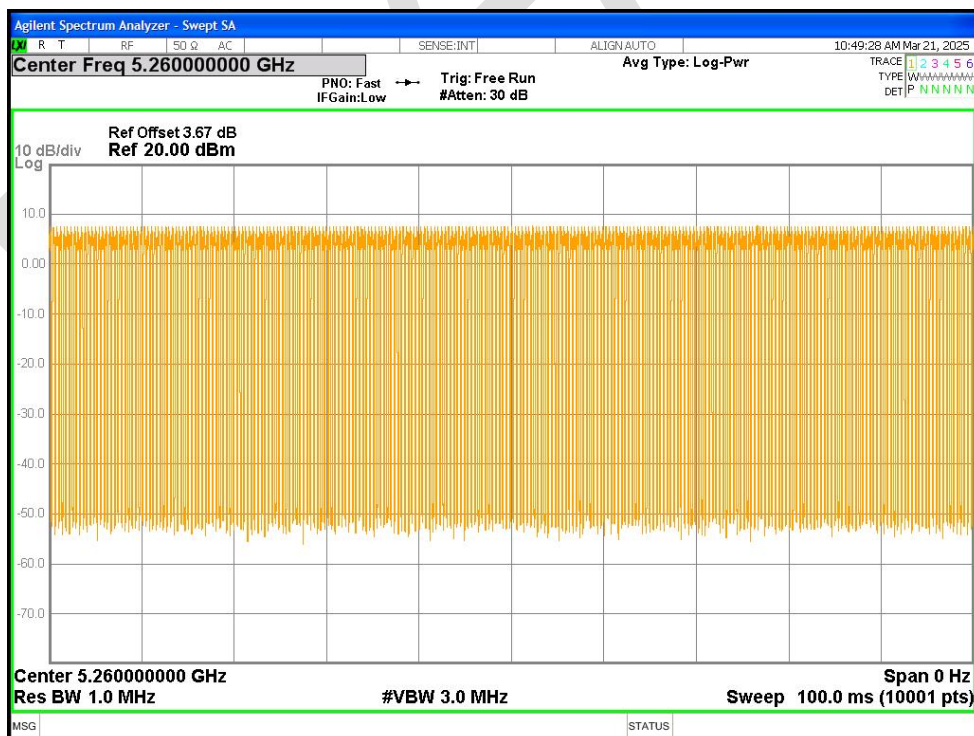
Duty Cycle NVNT a 5200MHz Ant1



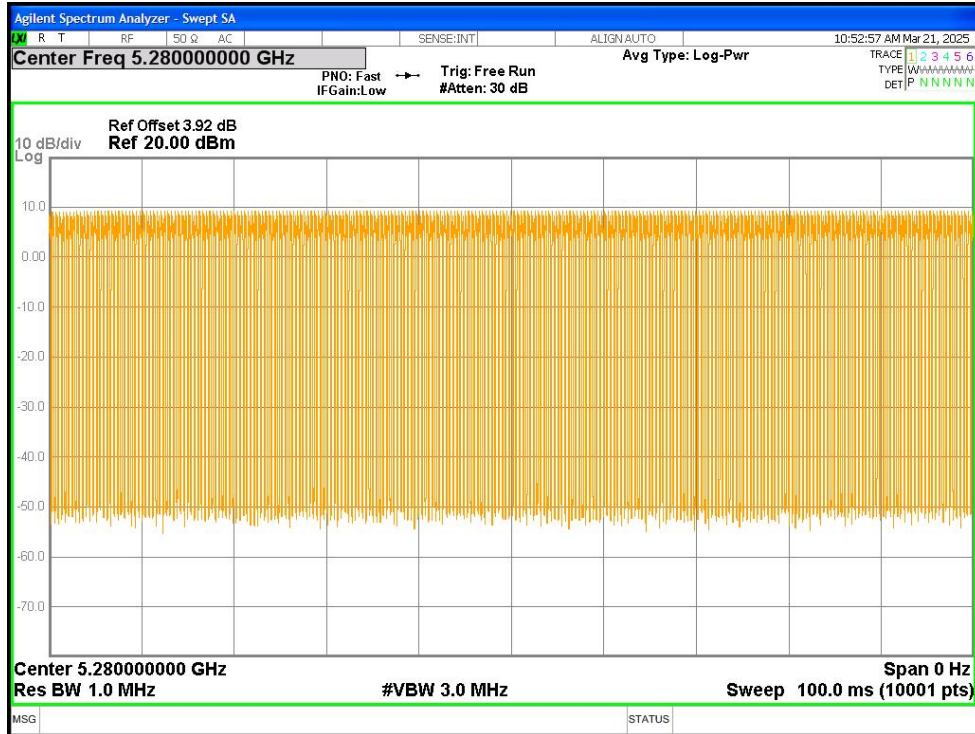
Duty Cycle NVNT a 5240MHz Ant1



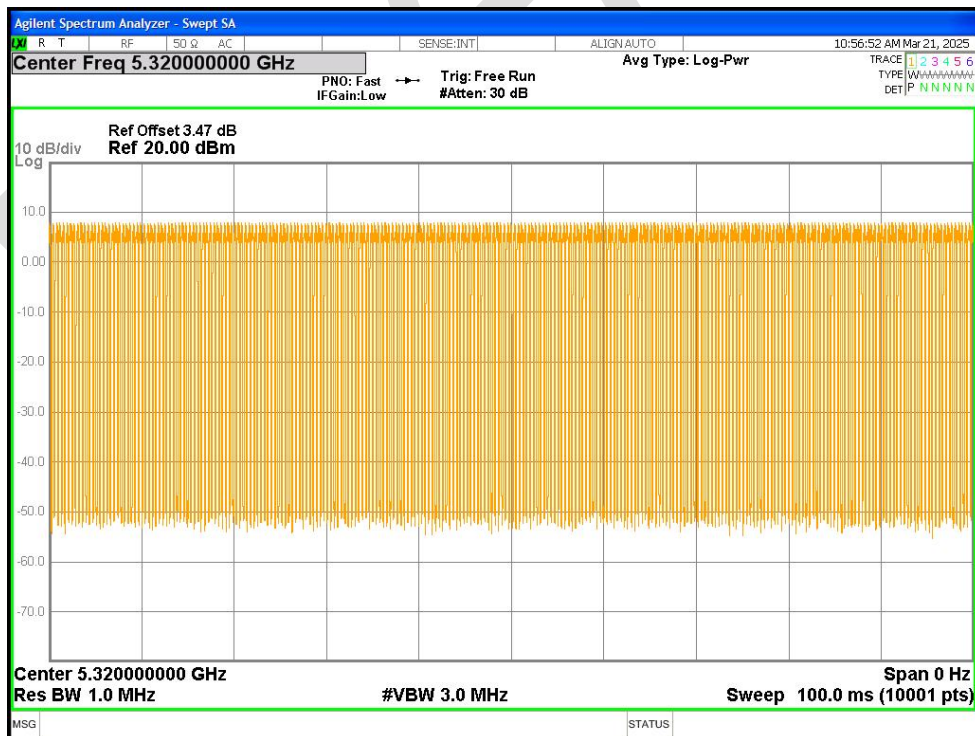
Duty Cycle NVNT a 5260MHz Ant1



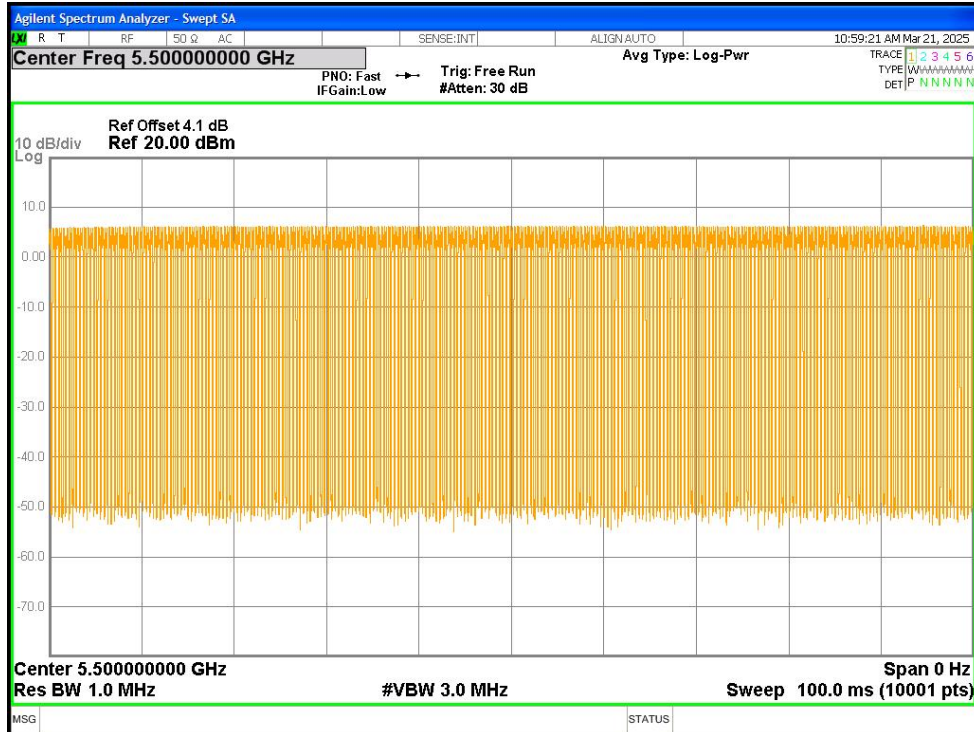
Duty Cycle NVNT a 5280MHz Ant1



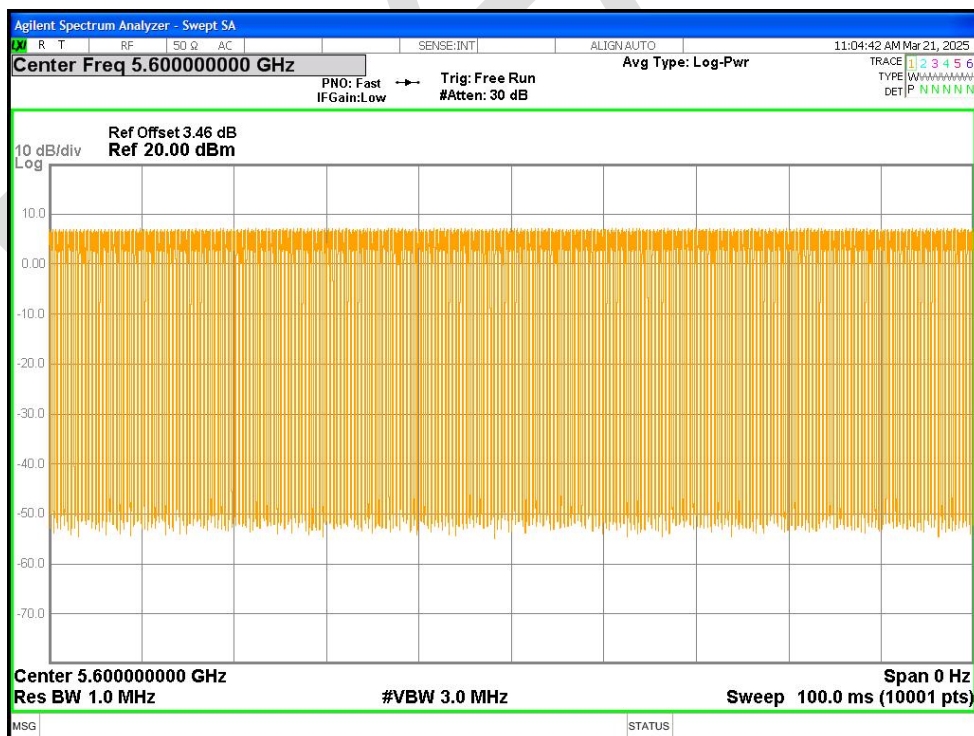
Duty Cycle NVNT a 5320MHz Ant1



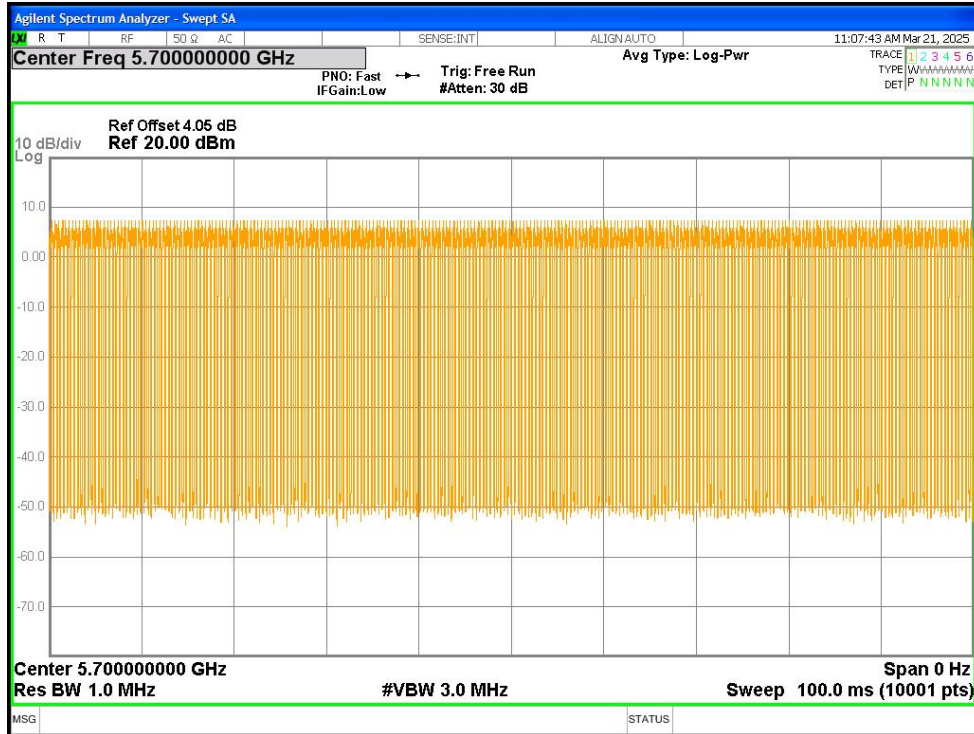
Duty Cycle NVNT a 5500MHz Ant1



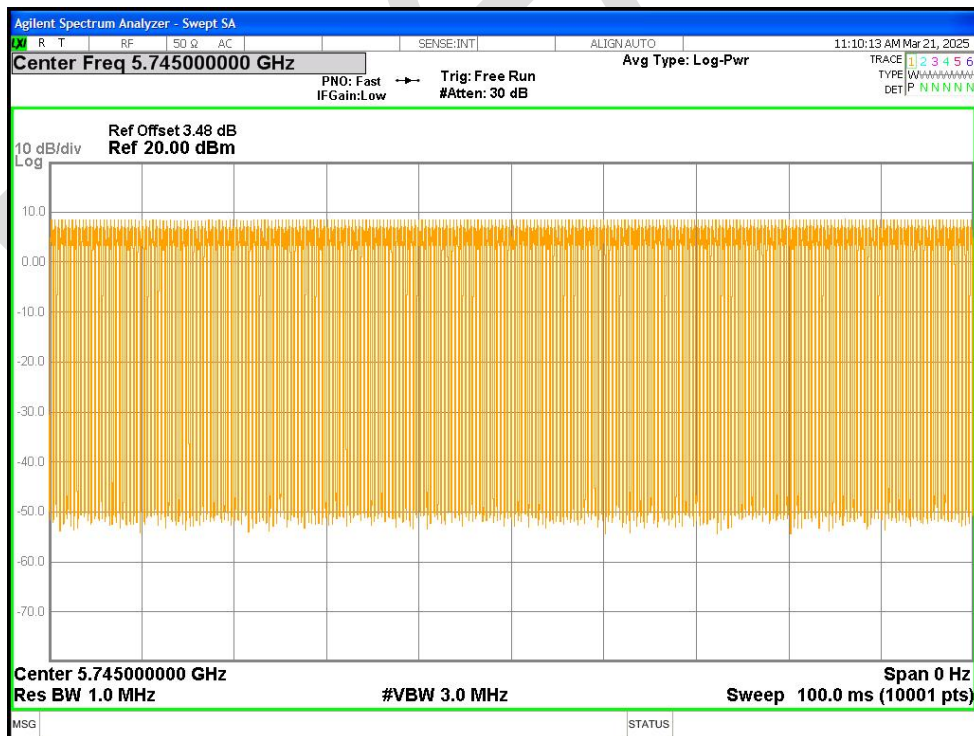
Duty Cycle NVNT a 5600MHz Ant1



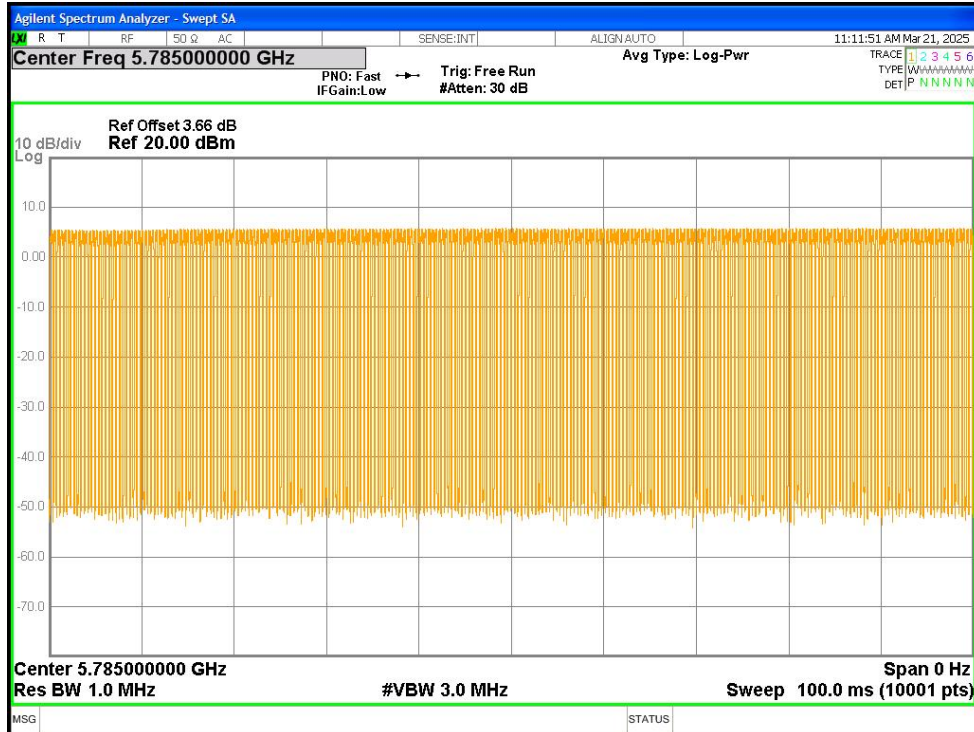
Duty Cycle NVNT a 5700MHz Ant1



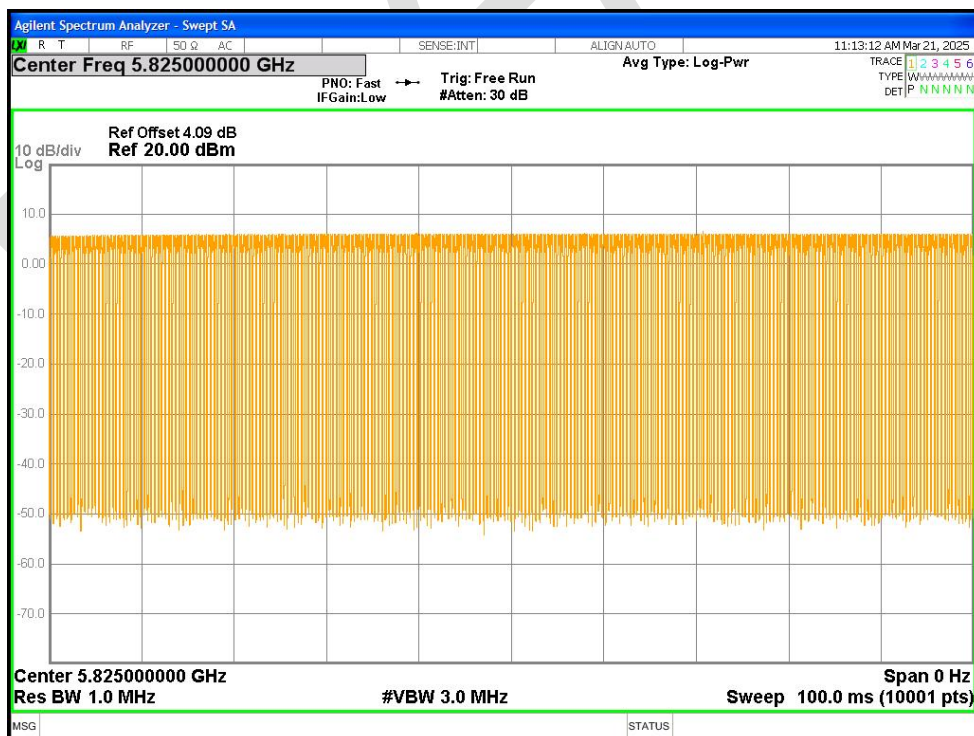
Duty Cycle NVNT a 5745MHz Ant1



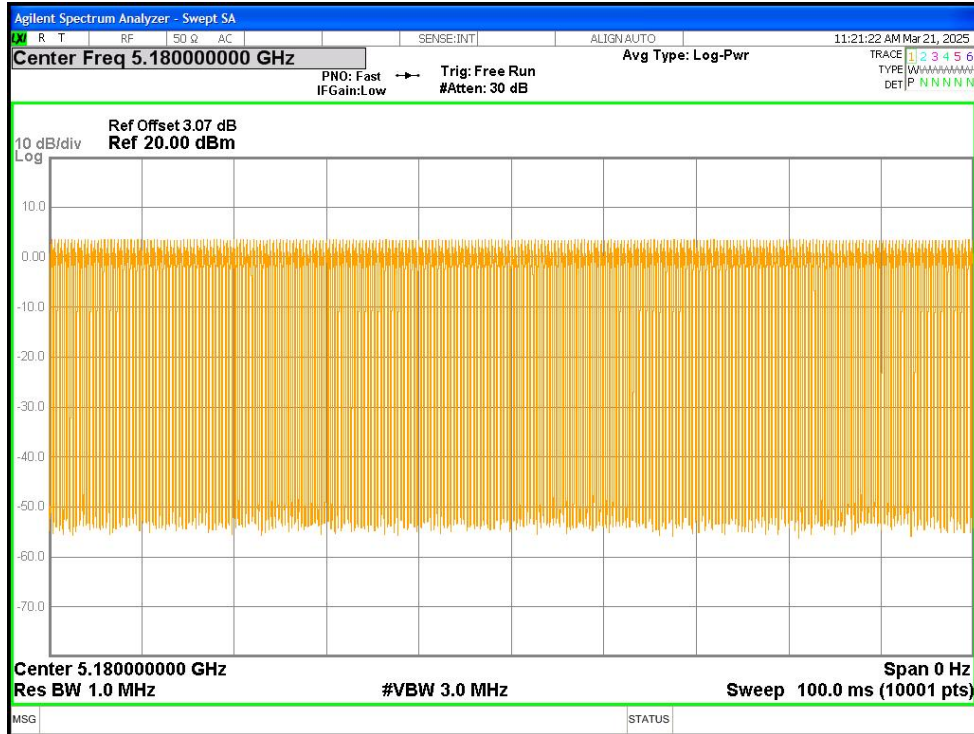
Duty Cycle NVNT a 5785MHz Ant1



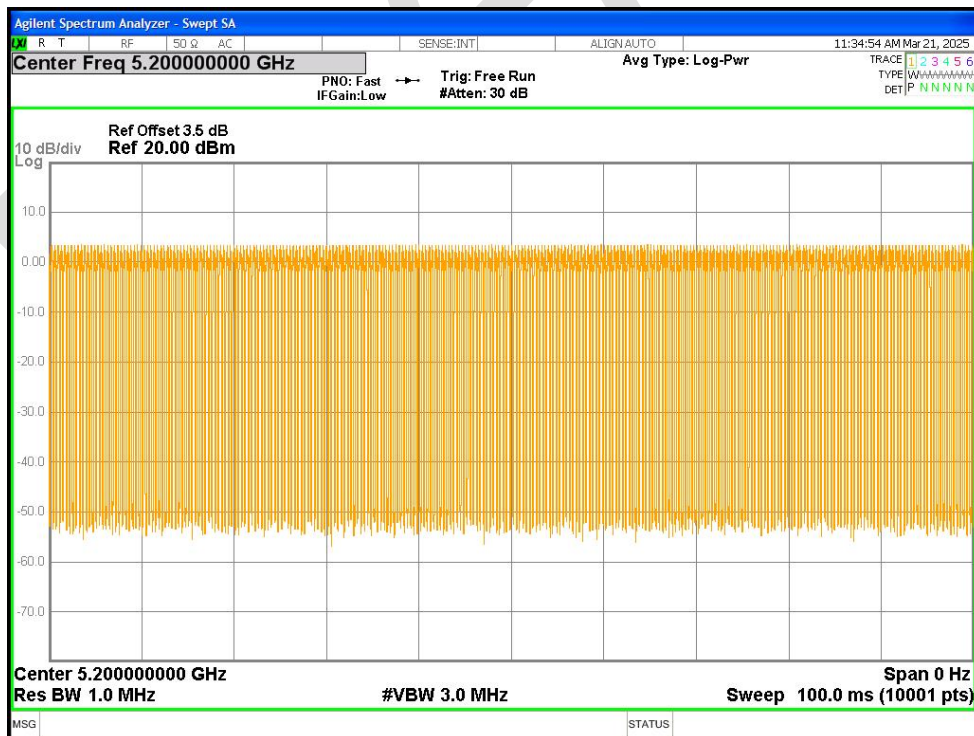
Duty Cycle NVNT a 5825MHz Ant1



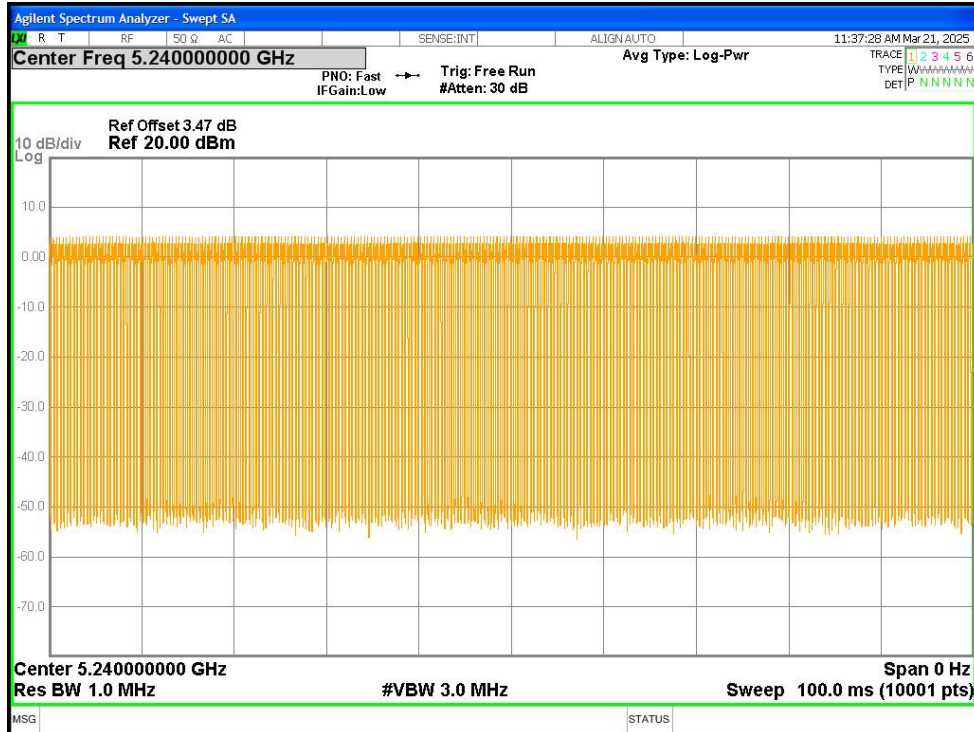
Duty Cycle NVNT n20 5180MHz Ant1



Duty Cycle NVNT n20 5200MHz Ant1



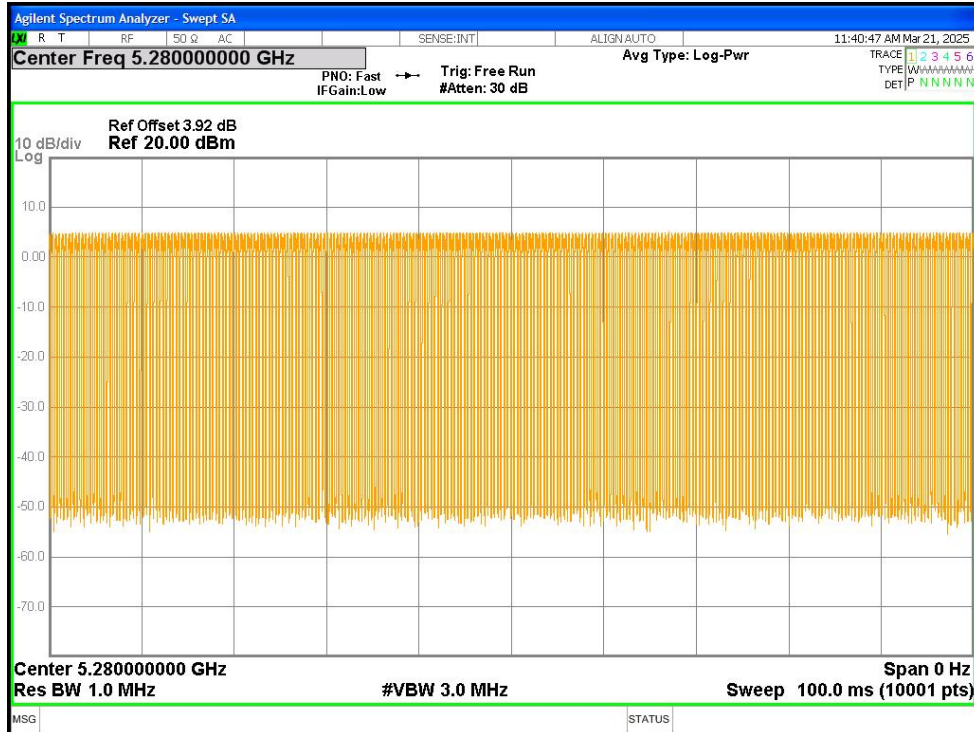
Duty Cycle NVNT n20 5240MHz Ant1



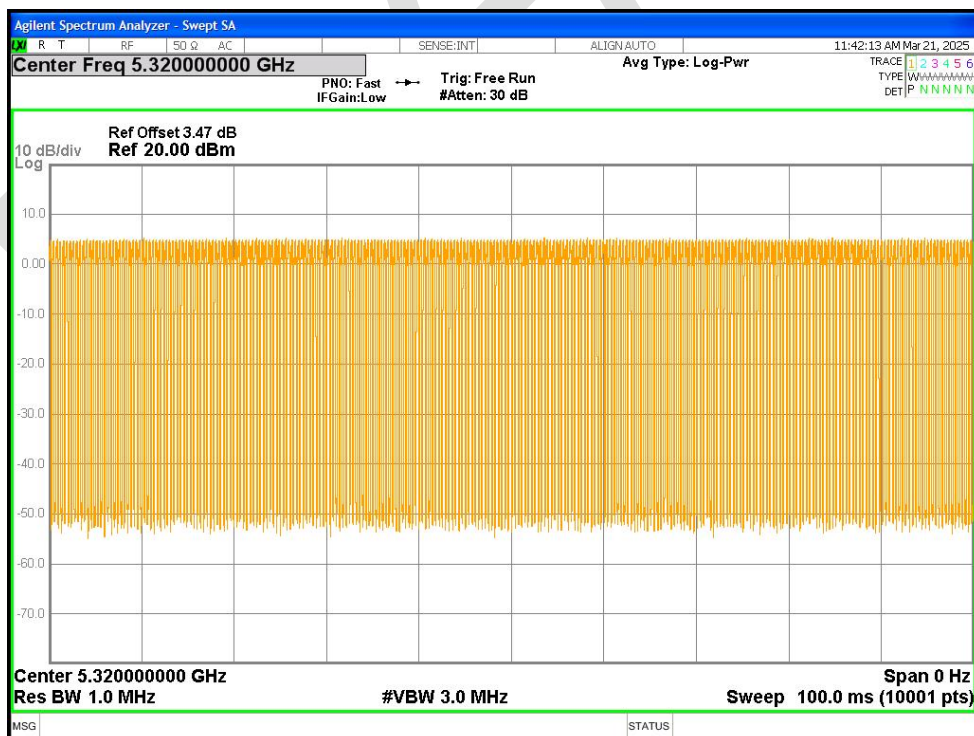
Duty Cycle NVNT n20 5260MHz Ant1



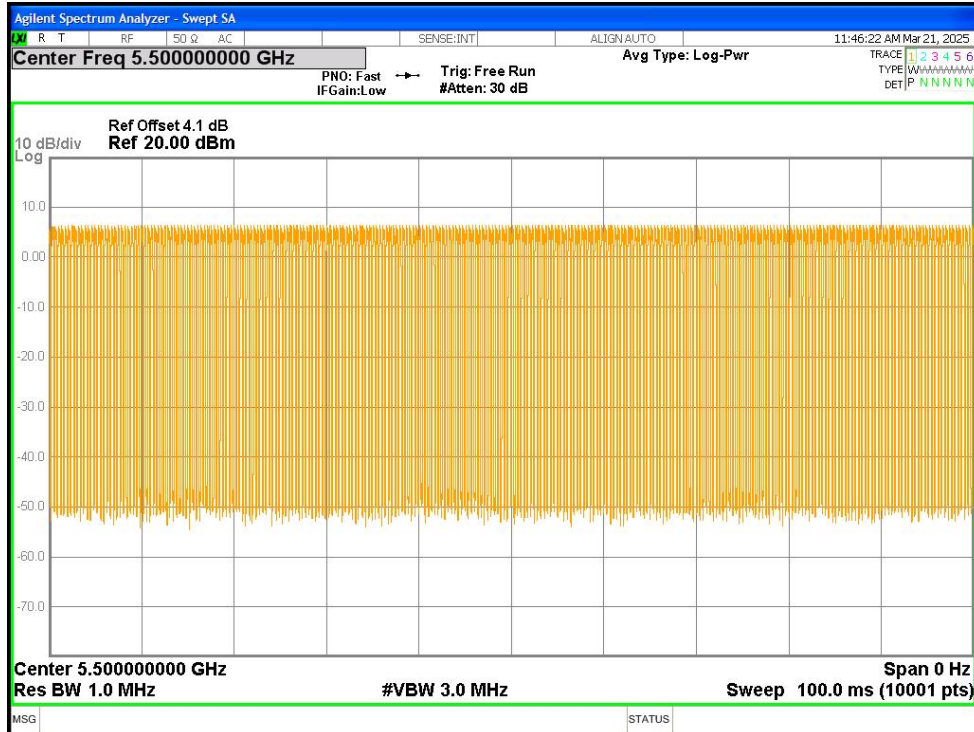
Duty Cycle NVNT n20 5280MHz Ant1



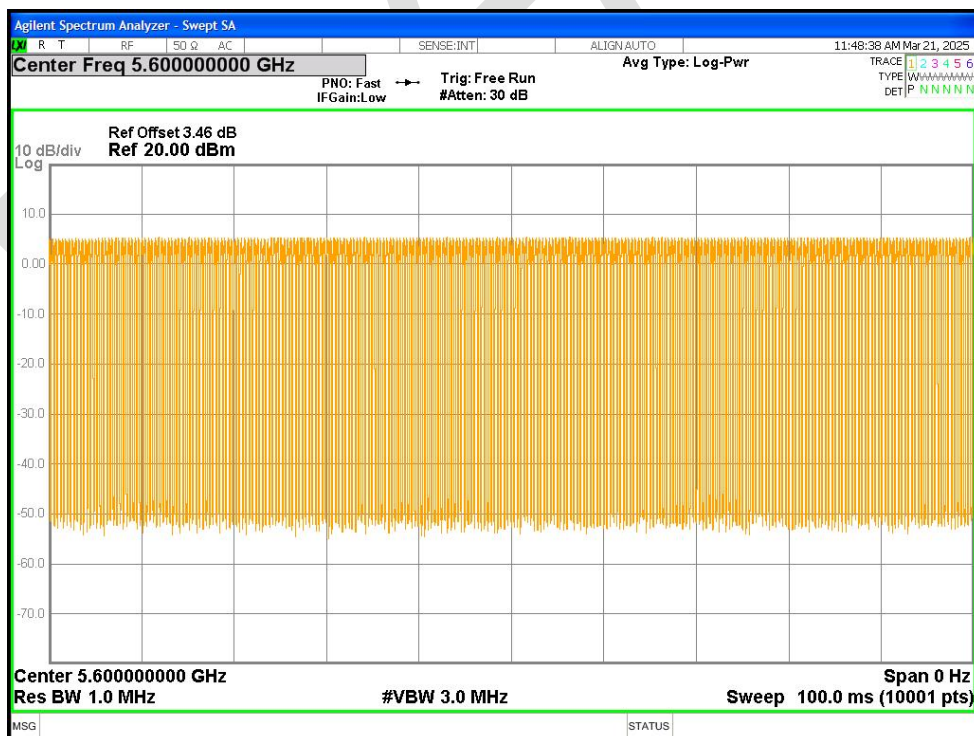
Duty Cycle NVNT n20 5320MHz Ant1



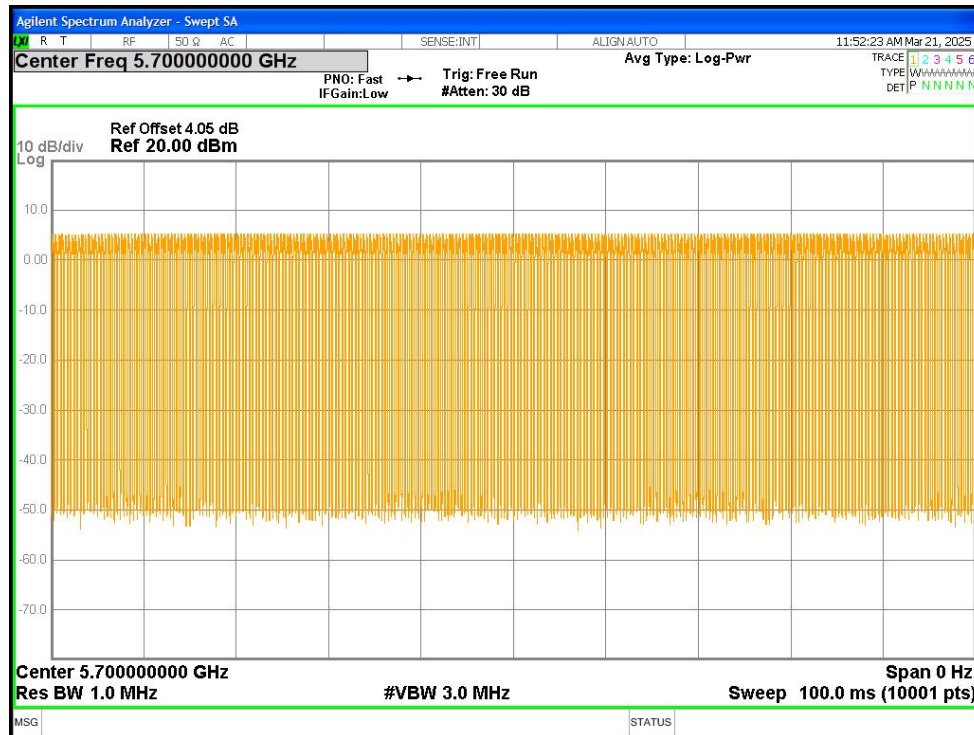
Duty Cycle NVNT n20 5500MHz Ant1



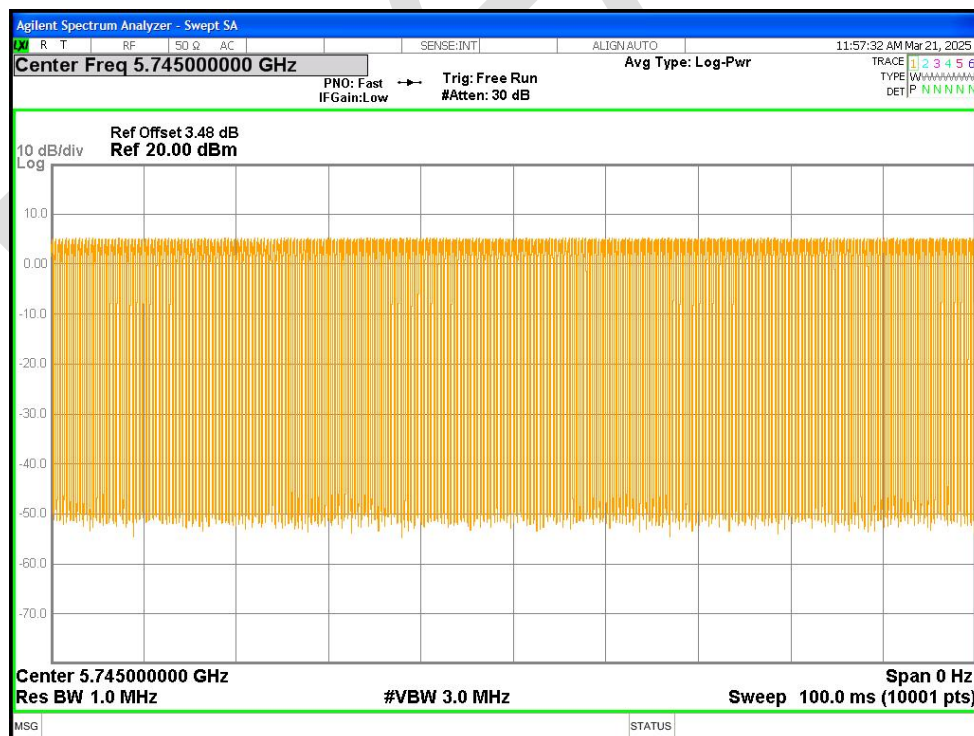
Duty Cycle NVNT n20 5600MHz Ant1



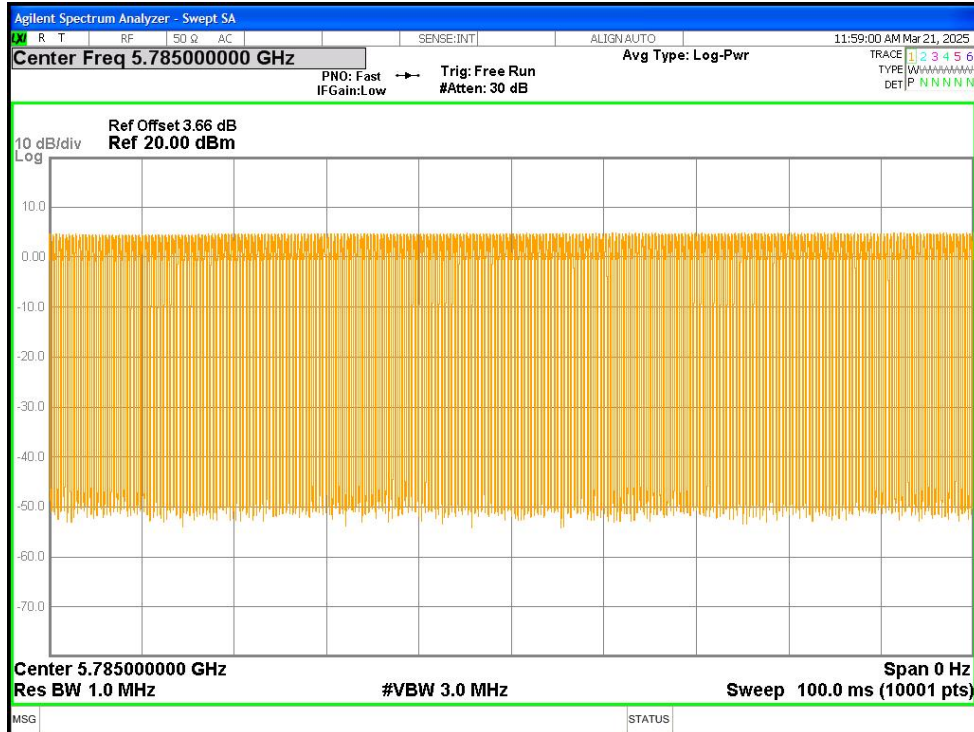
Duty Cycle NVNT n20 5700MHz Ant1



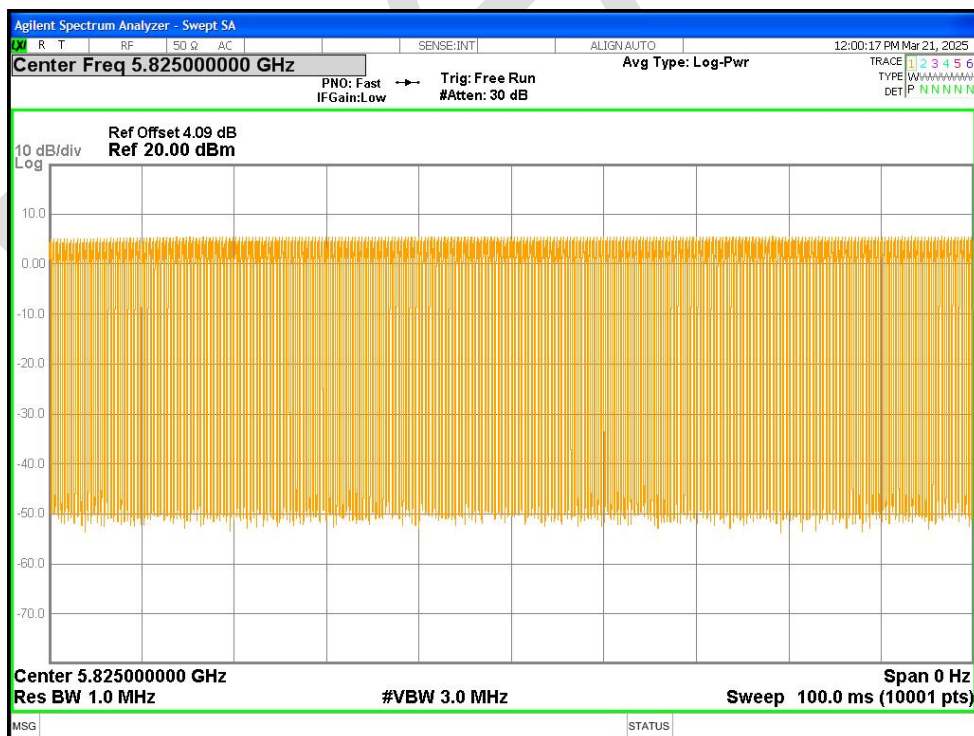
Duty Cycle NVNT n20 5745MHz Ant1



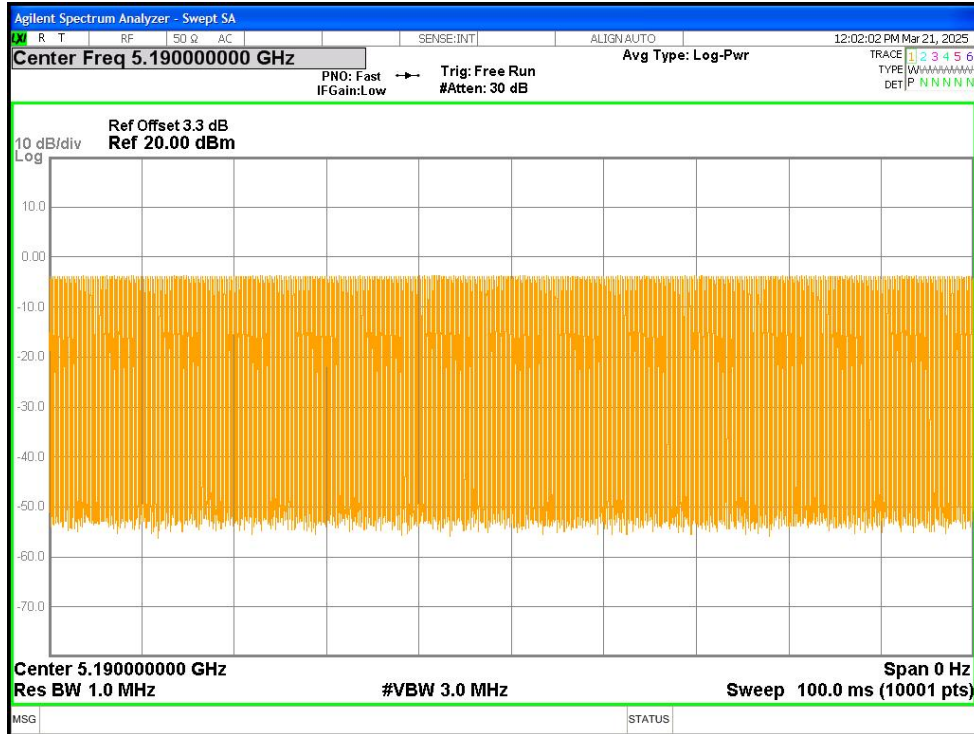
Duty Cycle NVNT n20 5785MHz Ant1



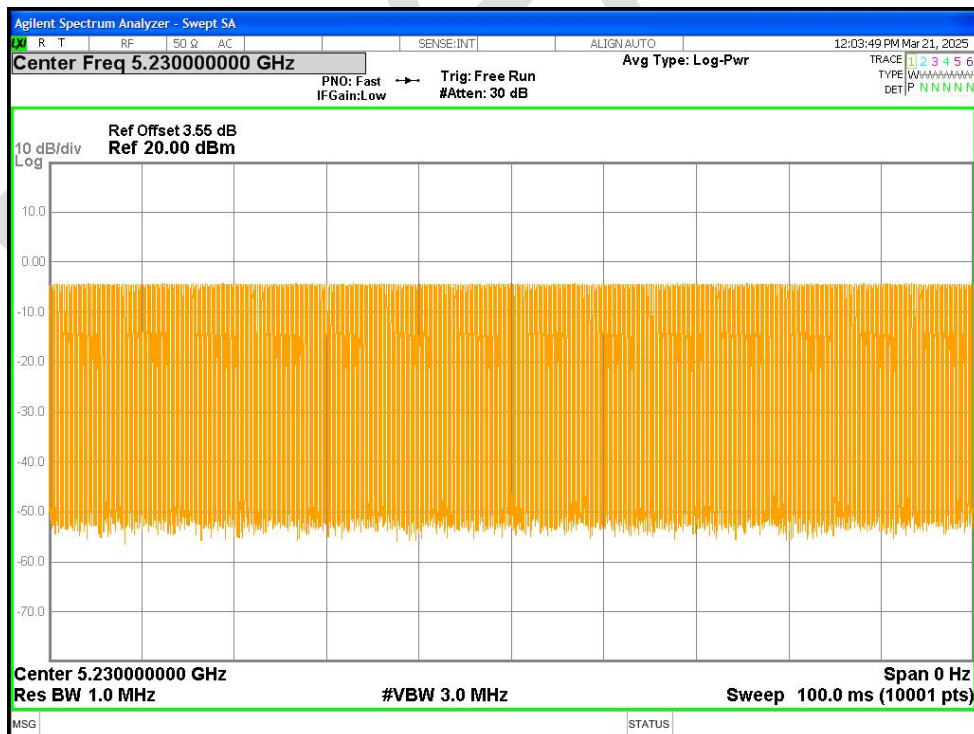
Duty Cycle NVNT n20 5825MHz Ant1



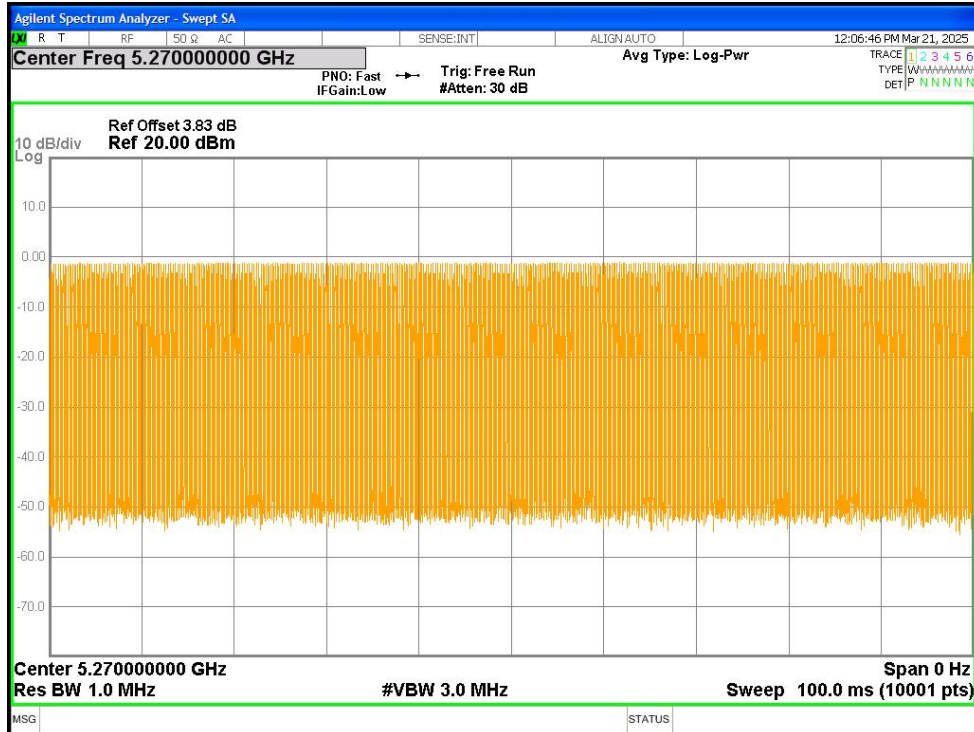
Duty Cycle NVNT n40 5190MHz Ant1



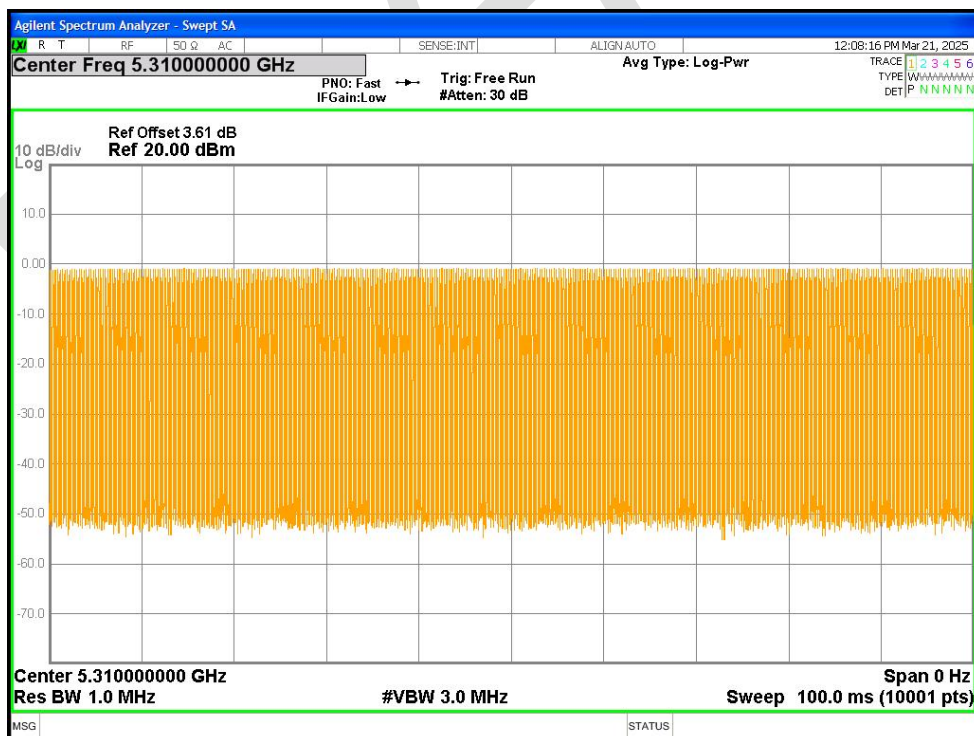
Duty Cycle NVNT n40 5230MHz Ant1



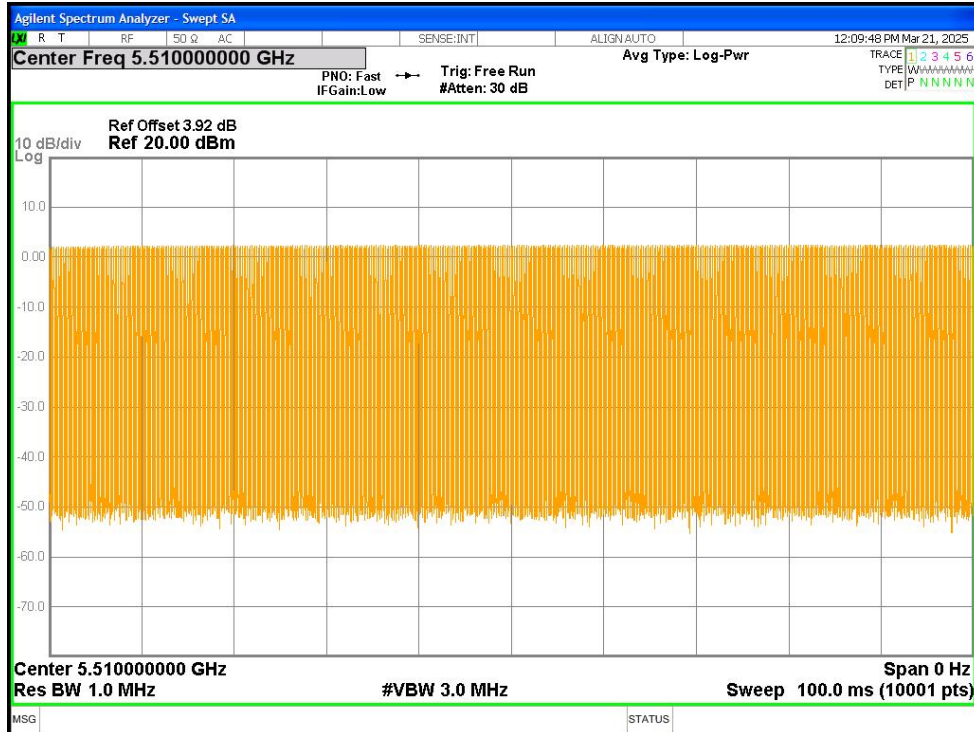
Duty Cycle NVNT n40 5270MHz Ant1



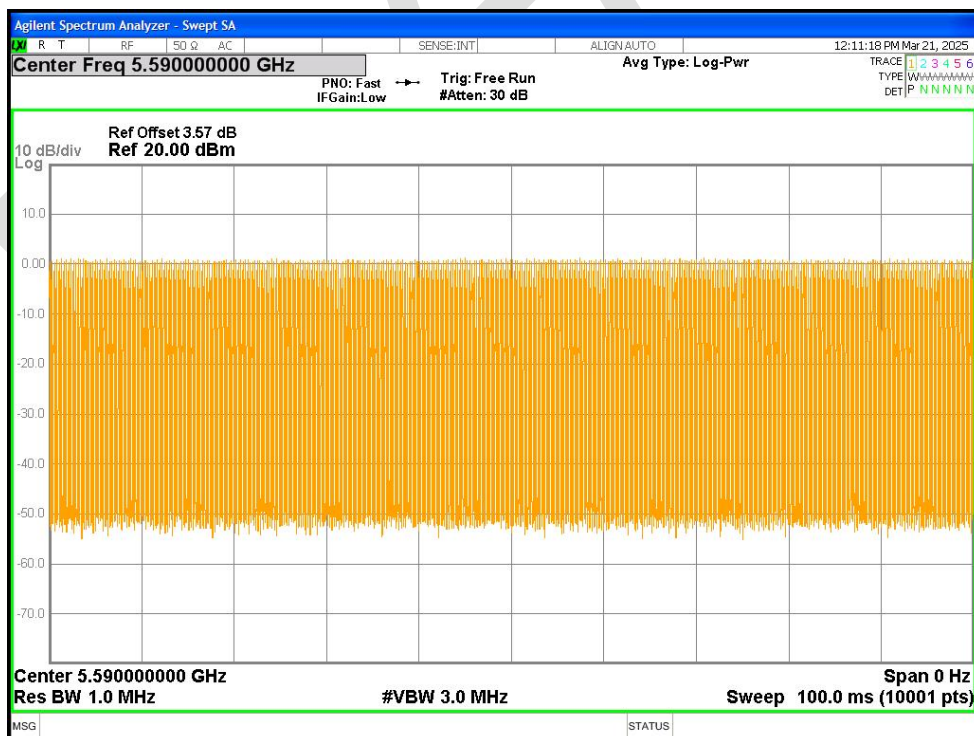
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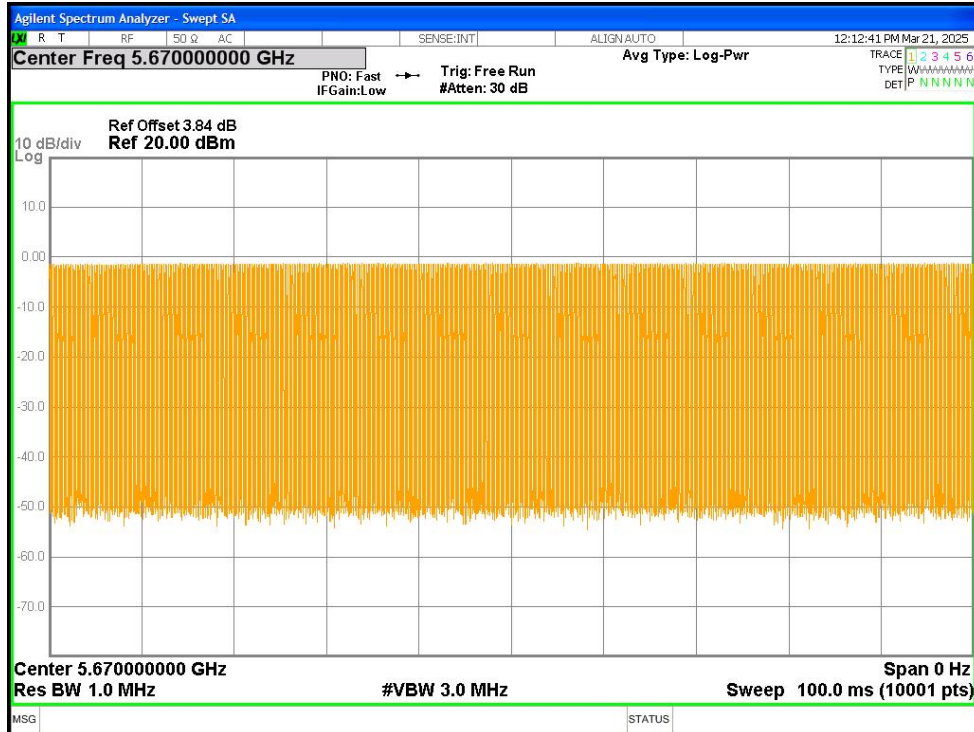
Duty Cycle NVNT n40 5510MHz Ant1



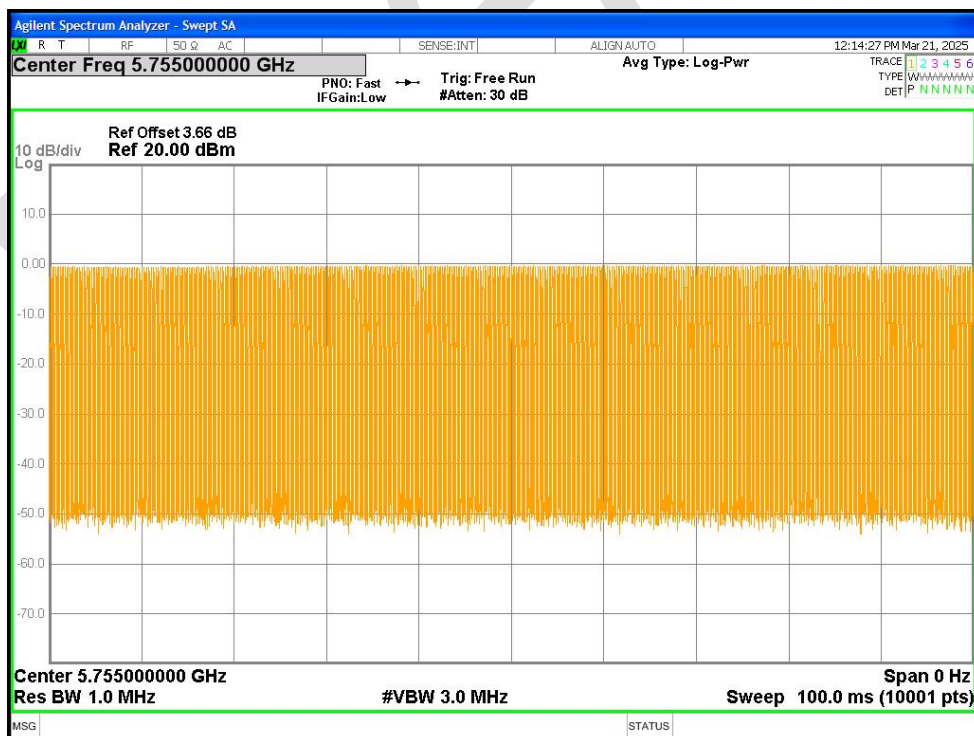
Duty Cycle NVNT n40 5590MHz Ant1



Duty Cycle NVNT n40 5670MHz Ant1



Duty Cycle NVNT n40 5755MHz Ant1



Duty Cycle NVNT n40 5795MHz Ant1

