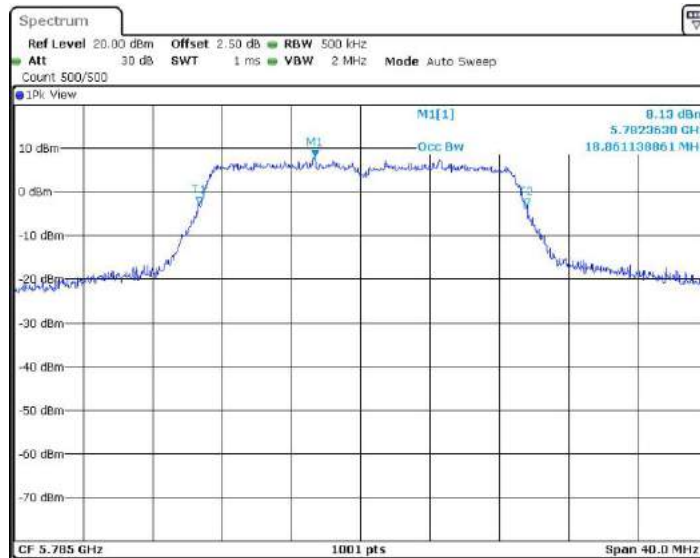
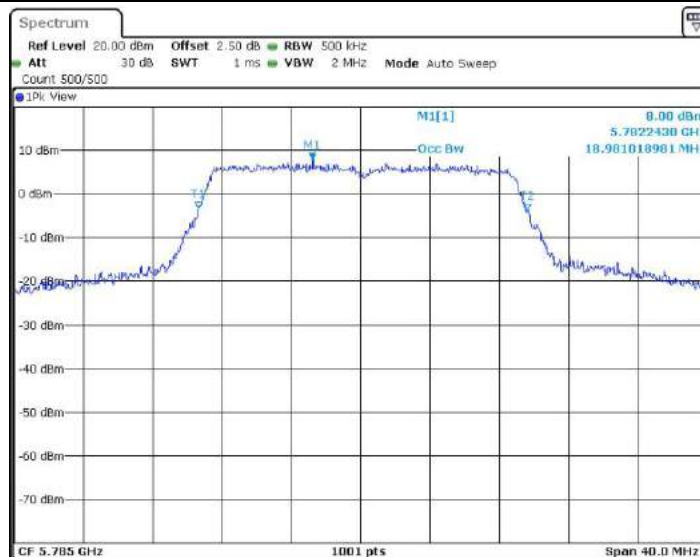
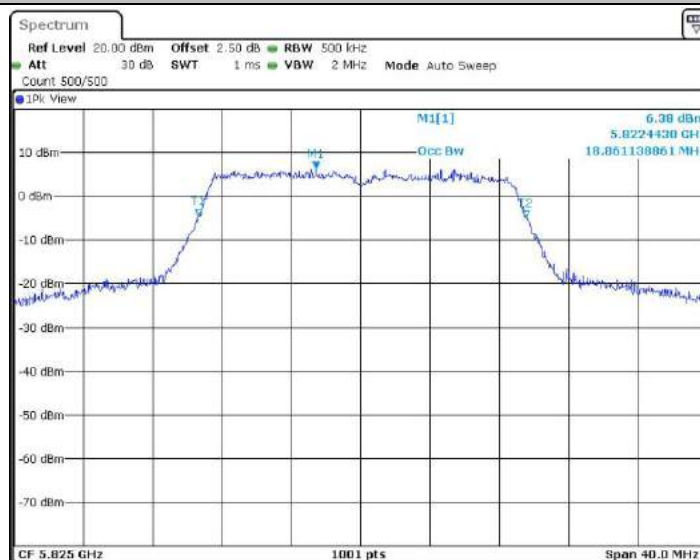




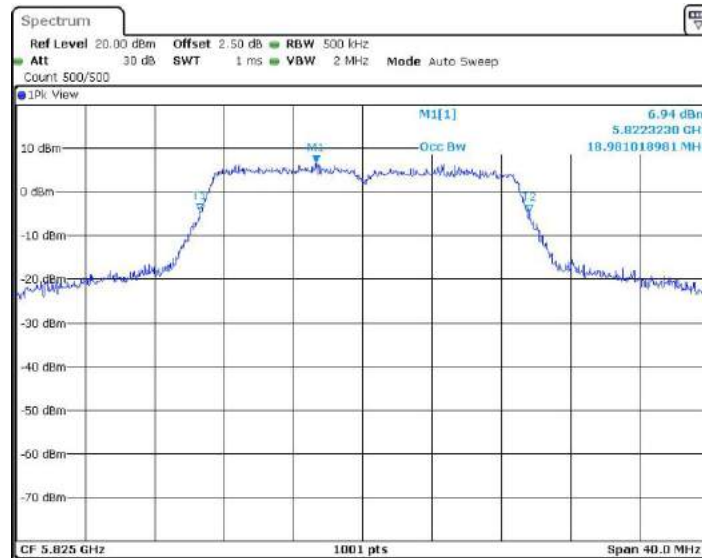
11N20SISO_Ant1_5785



11N20SISO_Ant2_5785



11N20SISO_Ant1_5825



11N20SISO_Ant2_5825



11N40SISO_Ant1_5190



11N40SISO_Ant2_5190



11N40SISO_Ant1_5230



11N40SISO_Ant2_5230



11N40SISO_Ant1_5270



11N40SISO_Ant2_5270



11N40SISO_Ant1_5310



11N40SISO_Ant2_5310



11N40SISO_Ant1_5510



11N40SISO_Ant2_5510



11N40SISO_Ant1_5550



11N40SISO_Ant2_5550



11N40SISO_Ant1_5670



11N40SISO_Ant2_5670



11N40SISO_Ant1_5755



11N40SISO_Ant2_5755



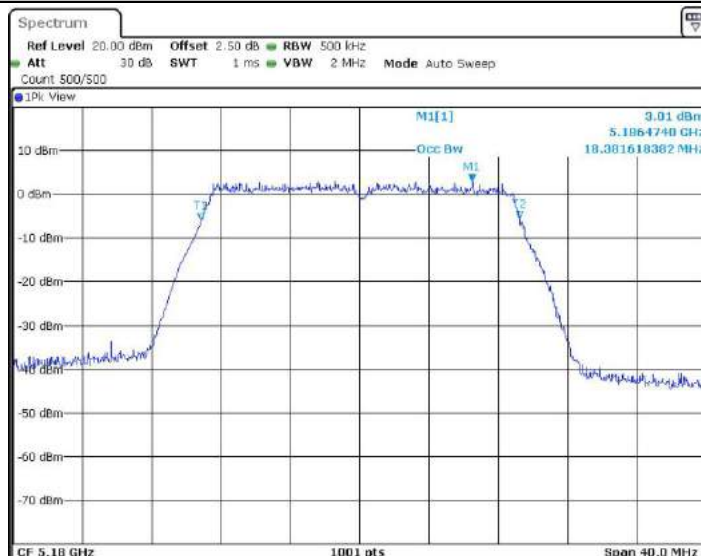
11N40SISO_Ant1_5795



11N40SISO_Ant2_5795



11AC20SISO_Ant1_5180



11AC20SISO_Ant2_5180



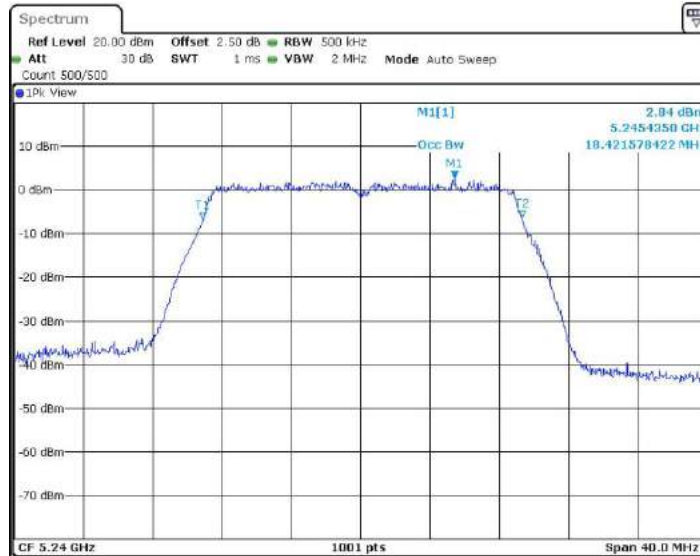
11AC20SISO_Ant1_5200



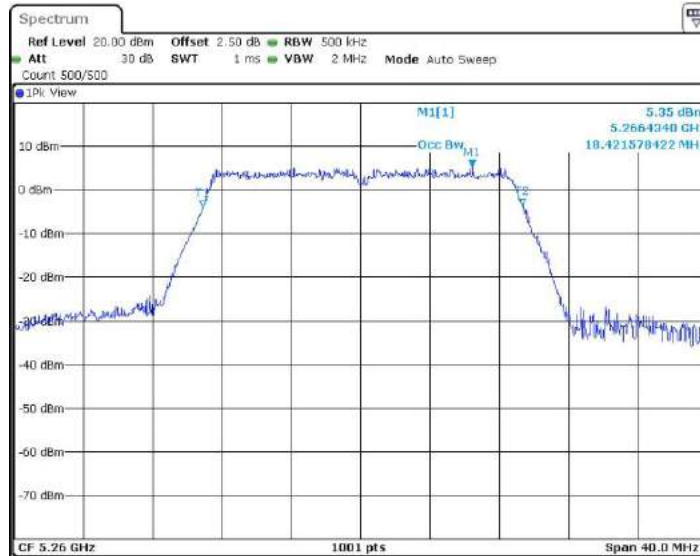
11AC20SISO_Ant2_5200



11AC20SISO_Ant1_5240



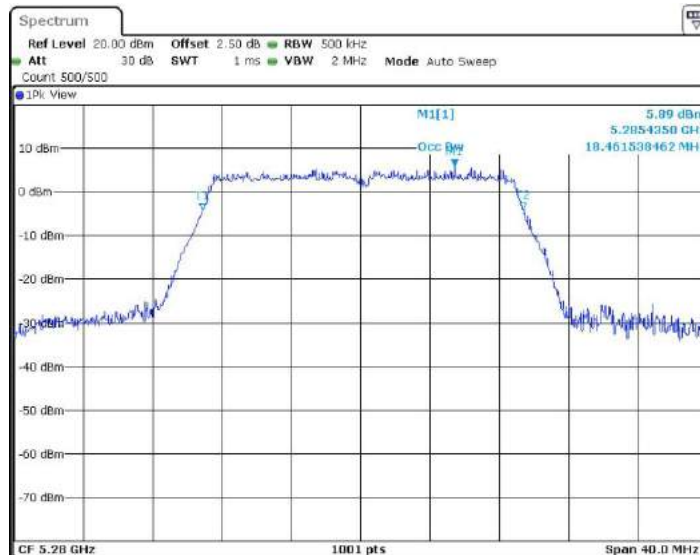
11AC20SISO_Ant2_5240



11AC20SISO_Ant1_5260



11AC20SISO_Ant2_5260



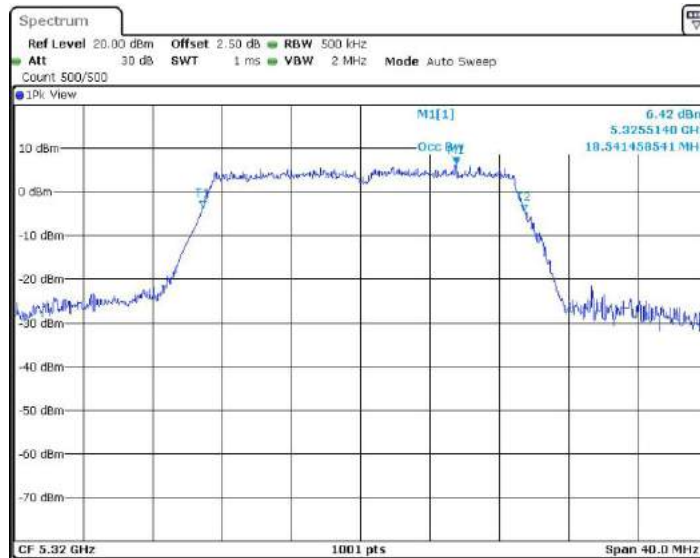
11AC20SISO_Ant1_5280



11AC20SISO_Ant2_5280



11AC20SISO_Ant1_5320



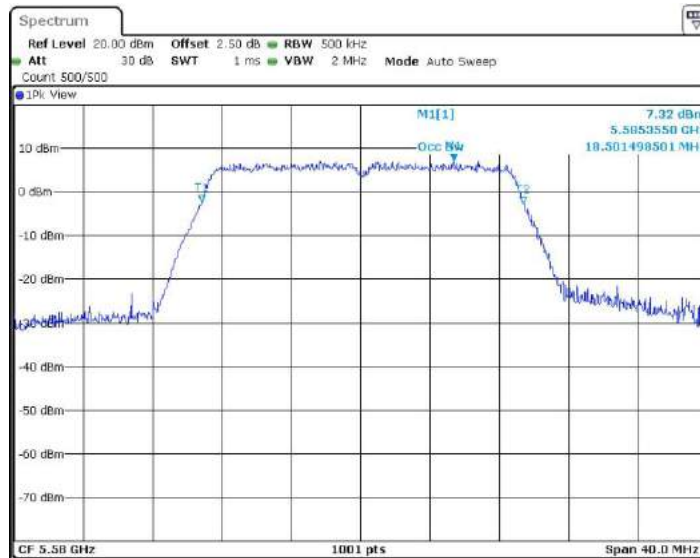
11AC20SISO_Ant2_5320



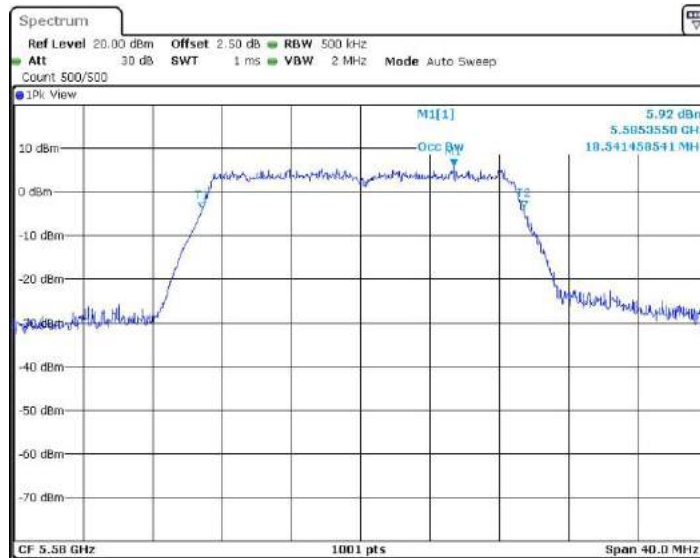
11AC20SISO_Ant1_5500



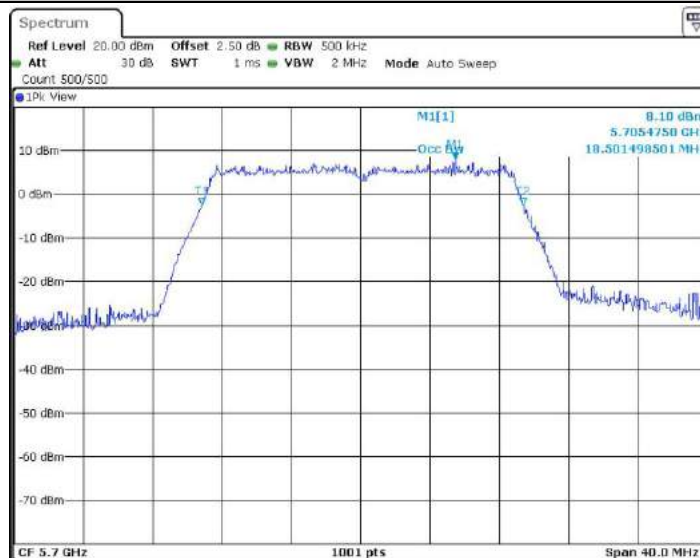
11AC20SISO_Ant2_5500



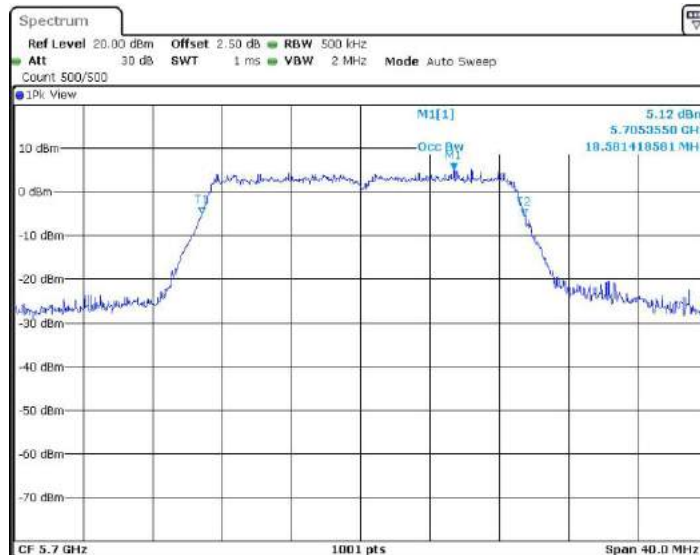
11AC20SISO_Ant1_5580



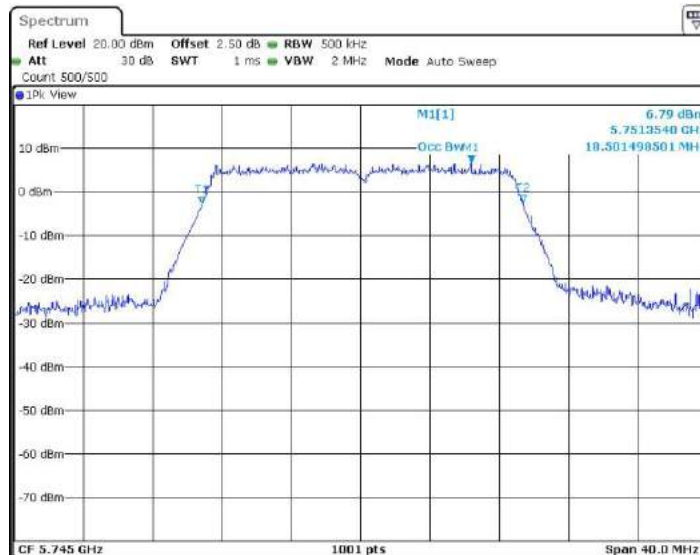
11AC20SISO_Ant2_5580



11AC20SISO_Ant1_5700



11AC20SISO_Ant2_5700



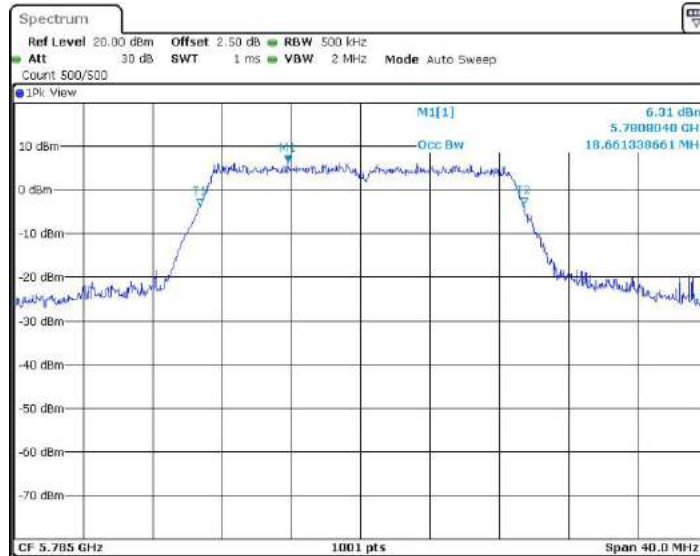
11AC20SISO_Ant1_5745



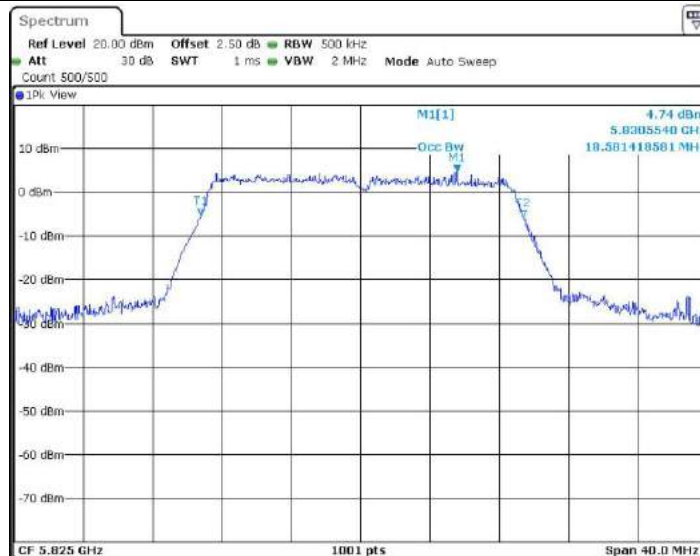
11AC20SISO_Ant2_5745



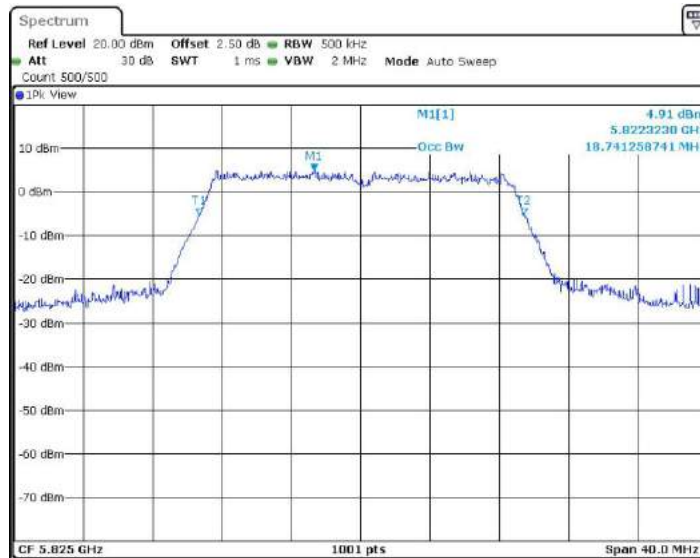
11AC20SISO_Ant1_5785



11AC20SISO_Ant2_5785



11AC20SISO_Ant1_5825



11AC20SISO_Ant2_5825



11AC40SISO_Ant1_5190



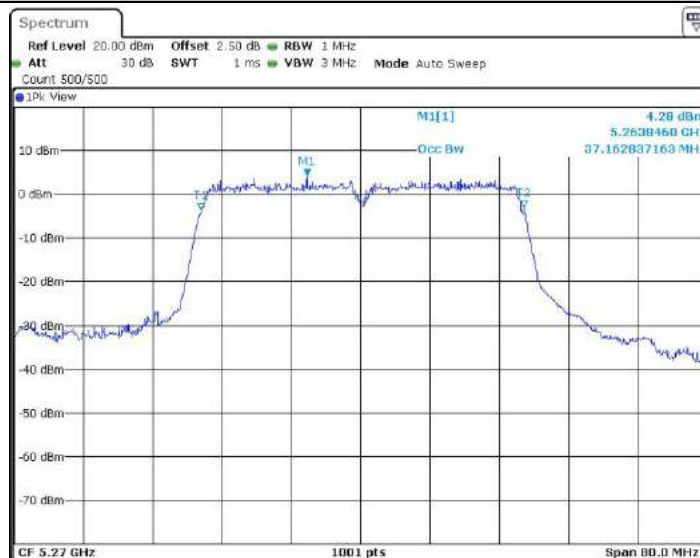
11AC40SISO_Ant2_5190



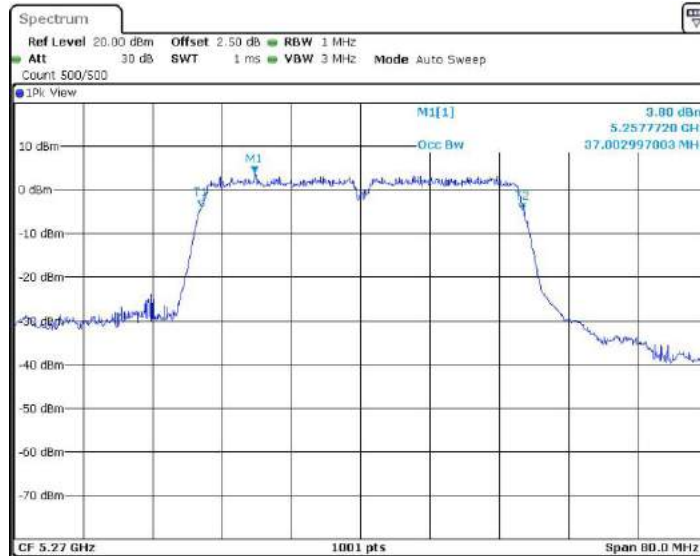
11AC40SISO_Ant1_5230



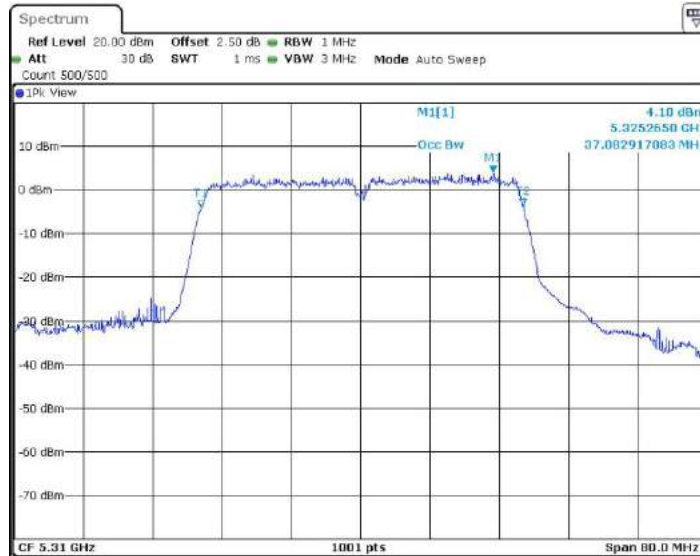
11AC40SISO_Ant2_5230



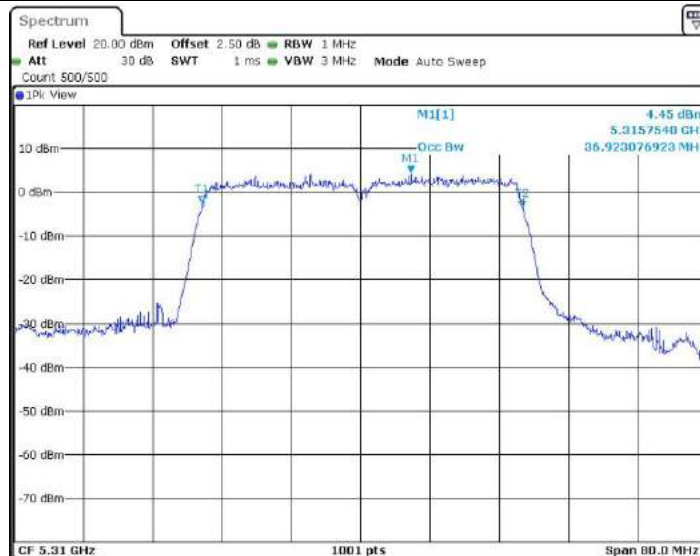
11AC40SISO_Ant1_5270



11AC40SISO_Ant2_5270



11AC40SISO_Ant1_5310



11AC40SISO_Ant2_5310



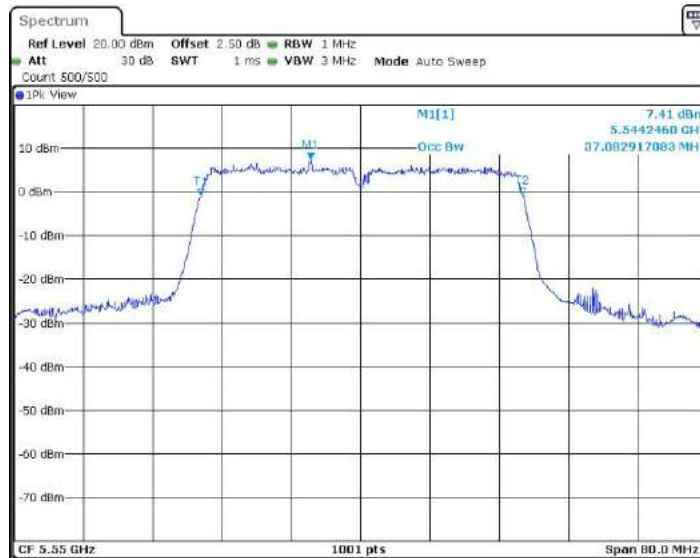
11AC40SISO_Ant1_5510



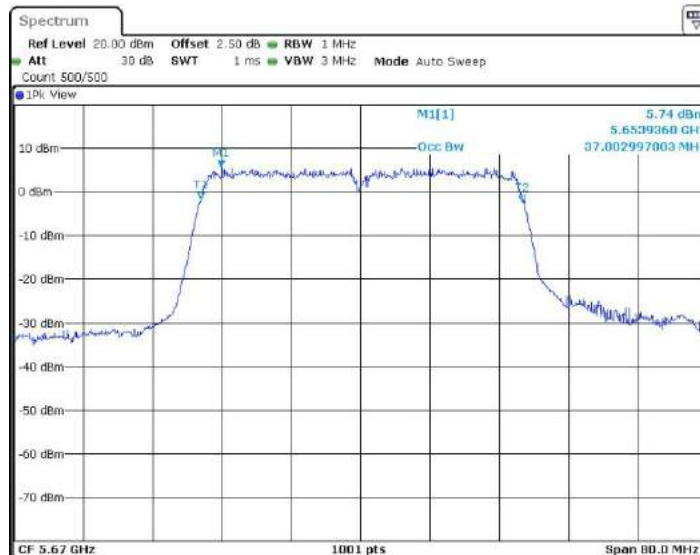
11AC40SISO_Ant2_5510



11AC40SISO_Ant1_5550



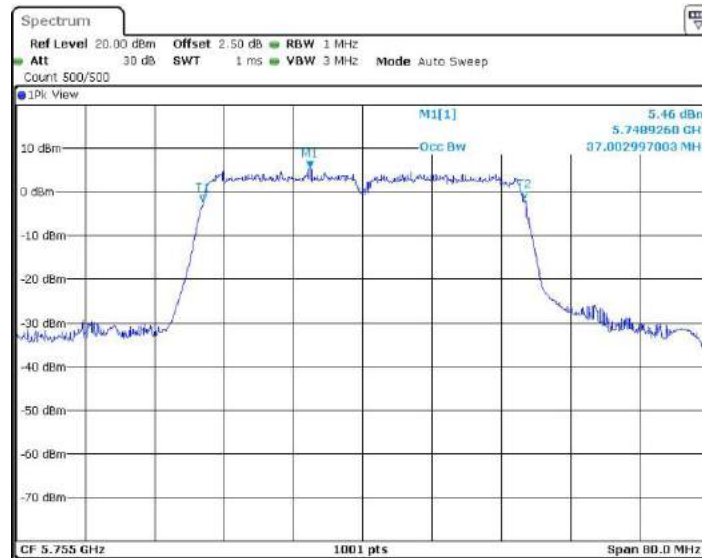
11AC40SISO_Ant2_5550



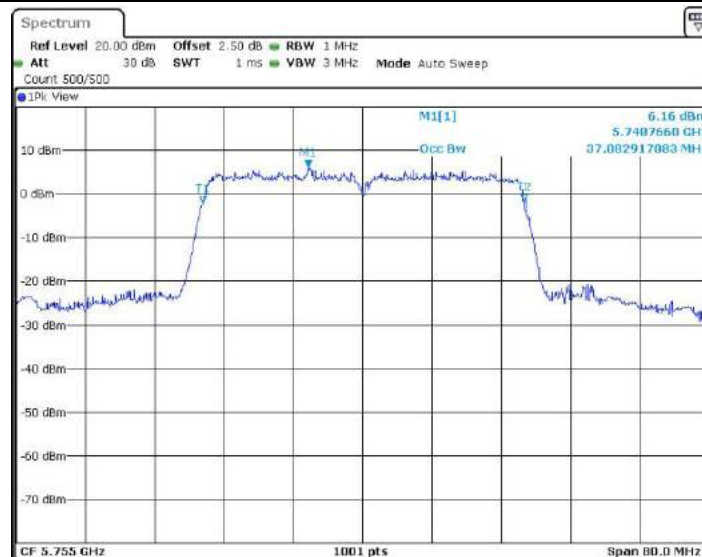
11AC40SISO_Ant1_5670



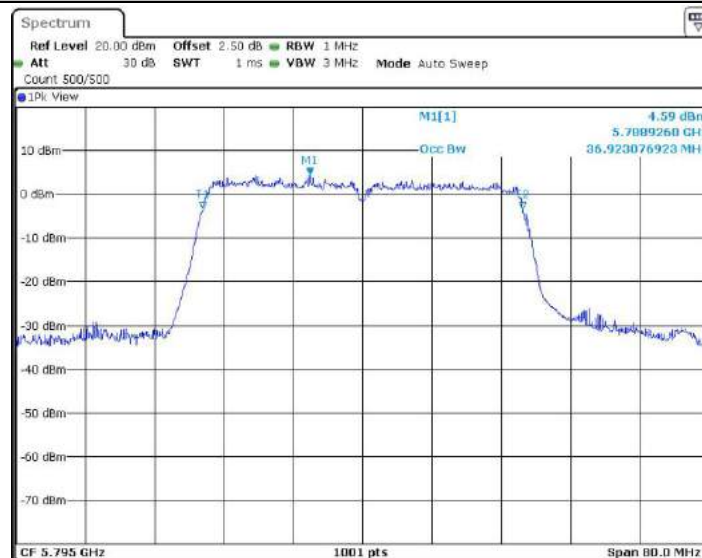
11AC40SISO_Ant2_5670



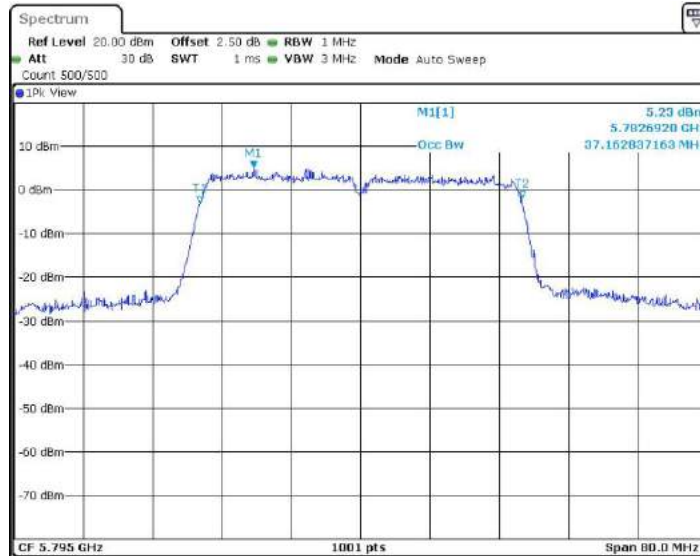
11AC40SISO_Ant1_5755



11AC40SISO_Ant2_5755



11AC40SISO_Ant1_5795



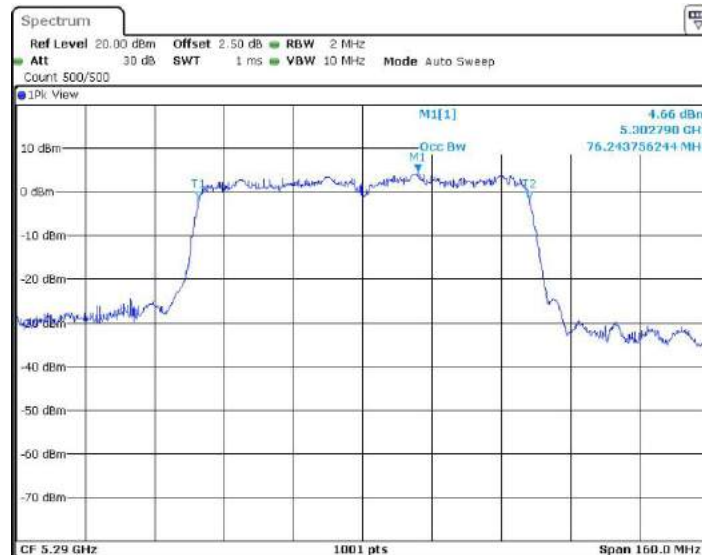
11AC40SISO_Ant2_5795



11AC80SISO_Ant1_5210



11AC80SISO_Ant2_5210



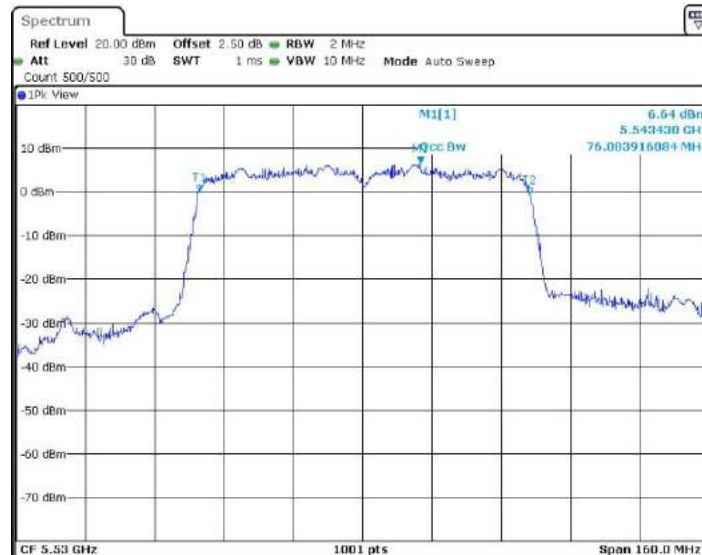
11AC80SISO_Ant1_5290



11AC80SISO_Ant2_5290



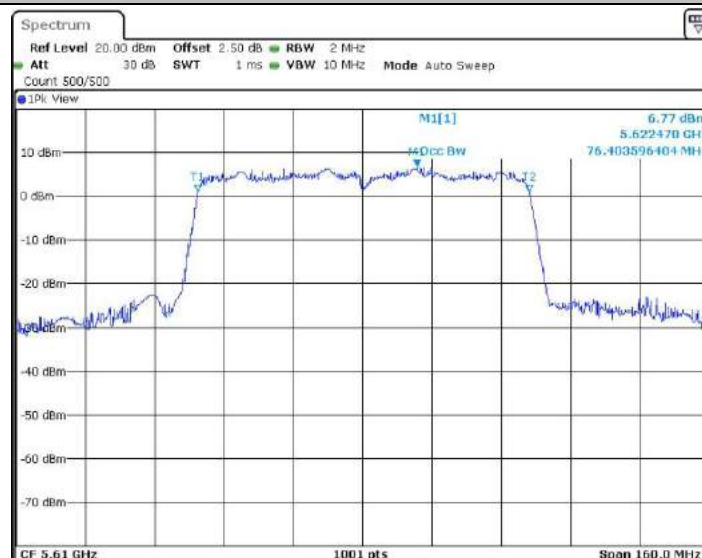
11AC80SISO_Ant1_5530



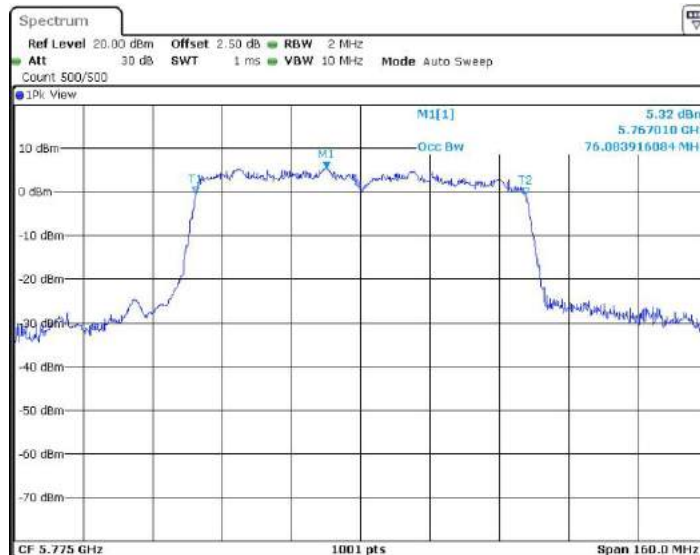
11AC80SISO_Ant2_5530



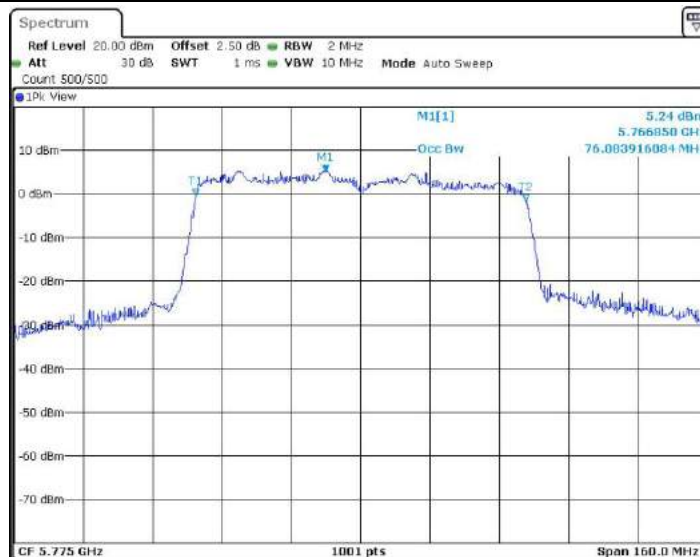
11AC80SISO_Ant1_5610



11AC80SISO_Ant2_5610



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775

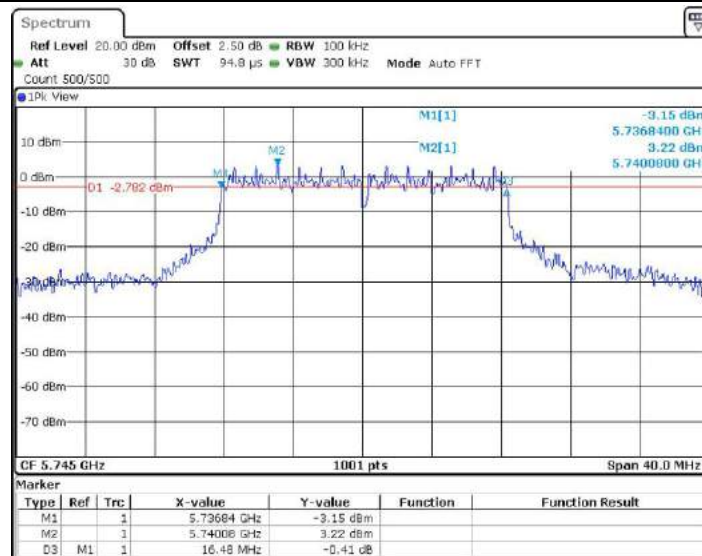
Appendix A3: Min emission bandwidth

Test Result

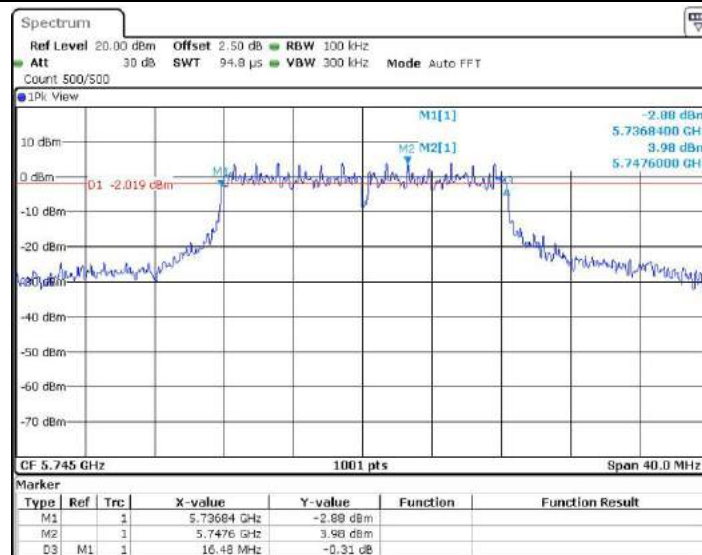
TestMode	Antenna	Channel	6db EBW [MHz]	FL[MHz]	FH[MHz]	Limit[MHz]	Verdict
11A	Ant1	5745	16.480	5736.840	5753.320	0.5	PASS
	Ant2	5745	16.480	5736.840	5753.320	0.5	PASS
	Ant1	5785	16.440	5776.840	5793.280	0.5	PASS
	Ant2	5785	16.400	5776.840	5793.240	0.5	PASS
	Ant1	5825	16.400	5816.840	5833.240	0.5	PASS
	Ant2	5825	16.360	5816.880	5833.240	0.5	PASS
11N20SISO	Ant1	5745	17.800	5736.200	5754.000	0.5	PASS
	Ant2	5745	17.800	5736.200	5754.000	0.5	PASS
	Ant1	5785	17.800	5776.200	5794.000	0.5	PASS
	Ant2	5785	17.800	5776.200	5794.000	0.5	PASS
	Ant1	5825	17.800	5816.160	5833.960	0.5	PASS
	Ant2	5825	17.760	5816.200	5833.960	0.5	PASS
11N40SISO	Ant1	5755	36.640	5736.760	5773.400	0.5	PASS
	Ant2	5755	36.640	5736.840	5773.480	0.5	PASS
	Ant1	5795	36.640	5776.760	5813.400	0.5	PASS
	Ant2	5795	36.640	5776.760	5813.400	0.5	PASS
11AC20SISO	Ant1	5745	17.800	5736.200	5754.000	0.5	PASS
	Ant2	5745	17.800	5736.200	5754.000	0.5	PASS
	Ant1	5785	17.800	5776.200	5794.000	0.5	PASS
	Ant2	5785	17.760	5776.200	5793.960	0.5	PASS
	Ant1	5825	17.800	5816.160	5833.960	0.5	PASS
	Ant2	5825	17.760	5816.200	5833.960	0.5	PASS
11AC40SISO	Ant1	5755	36.720	5736.760	5773.480	0.5	PASS
	Ant2	5755	36.720	5736.760	5773.480	0.5	PASS
	Ant1	5795	36.640	5776.760	5813.400	0.5	PASS
	Ant2	5795	36.640	5776.760	5813.400	0.5	PASS
11AC80SISO	Ant1	5775	75.680	5737.240	5812.920	0.5	PASS
	Ant2	5775	75.680	5737.240	5812.920	0.5	PASS
11N20MIMO	Ant1	5745	17.800	5736.200	5754.000	0.5	PASS
	Ant2	5745	17.760	5736.200	5753.960	0.5	PASS
	Ant1	5785	17.840	5776.200	5794.040	0.5	PASS
	Ant2	5785	17.800	5776.200	5794.000	0.5	PASS
	Ant1	5825	17.800	5816.200	5834.000	0.5	PASS
	Ant2	5825	17.800	5816.200	5834.000	0.5	PASS
11N40MIMO	Ant1	5755	36.640	5736.760	5773.400	0.5	PASS
	Ant2	5755	36.560	5736.840	5773.400	0.5	PASS
	Ant1	5795	36.640	5776.760	5813.400	0.5	PASS

	Ant2	5795	36.640	5776.760	5813.400	0.5	PASS
11AC20MIMO	Ant1	5745	17.800	5736.200	5754.000	0.5	PASS
	Ant2	5745	17.800	5736.200	5754.000	0.5	PASS
	Ant1	5785	17.800	5776.200	5794.000	0.5	PASS
	Ant2	5785	17.800	5776.200	5794.000	0.5	PASS
	Ant1	5825	17.800	5816.160	5833.960	0.5	PASS
	Ant2	5825	17.800	5816.200	5834.000	0.5	PASS
11AC40MIMO	Ant1	5755	36.640	5736.760	5773.400	0.5	PASS
	Ant2	5755	36.640	5736.840	5773.480	0.5	PASS
	Ant1	5795	36.640	5776.760	5813.400	0.5	PASS
	Ant2	5795	36.640	5776.760	5813.400	0.5	PASS
11AC80MIMO	Ant1	5775	76.160	5736.760	5812.920	0.5	PASS
	Ant2	5775	75.680	5737.080	5812.760	0.5	PASS

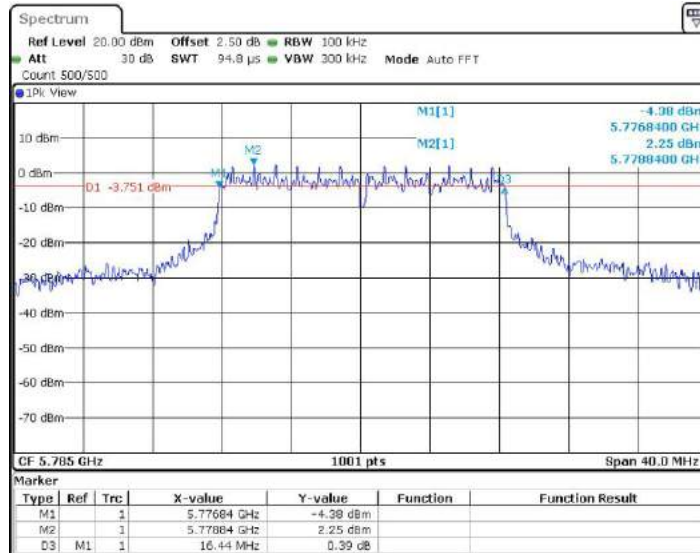
Test Graphs



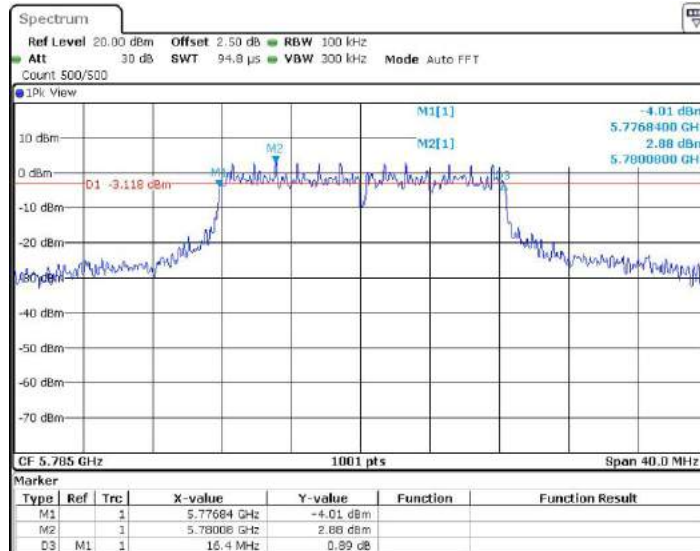
11A_Ant1_5745



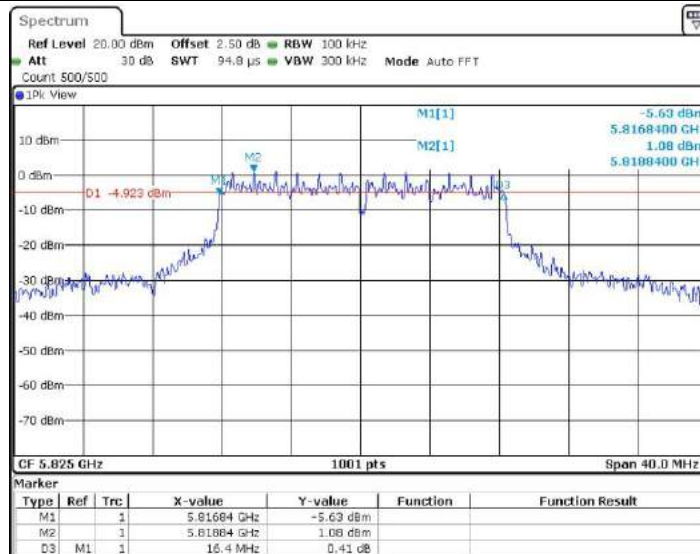
11A_Ant2_5745



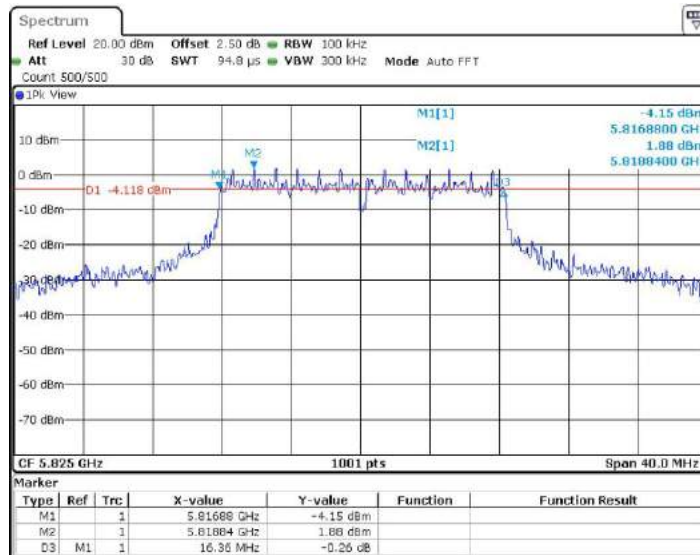
11A_Ant1_5785



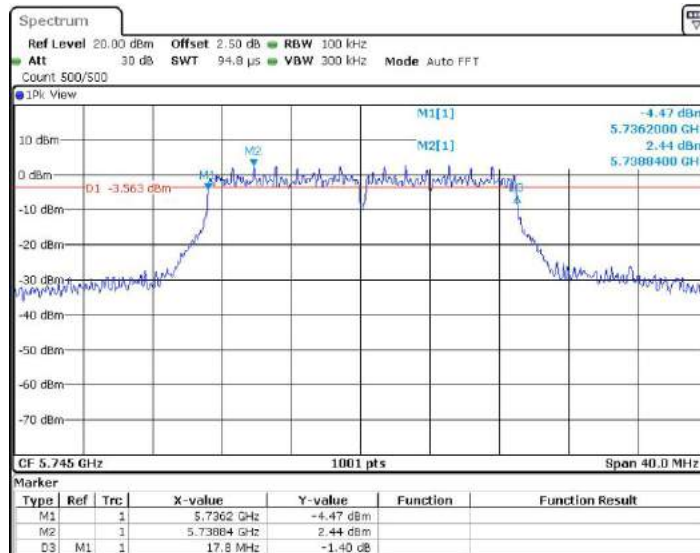
11A_Ant2_5785



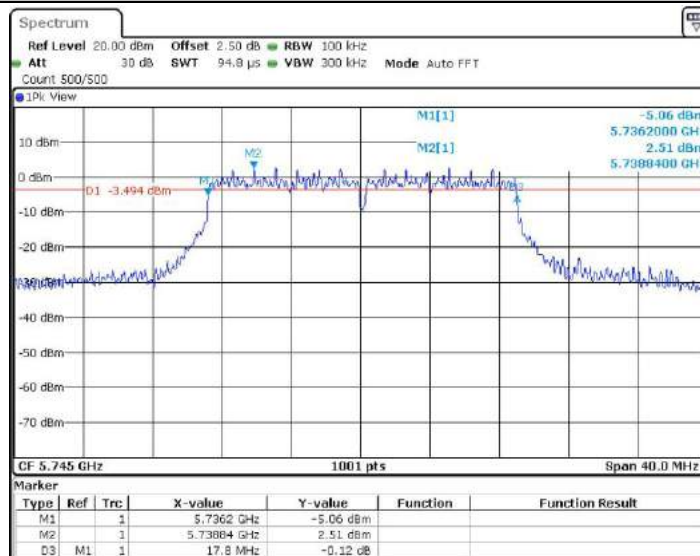
11A_Ant1_5825



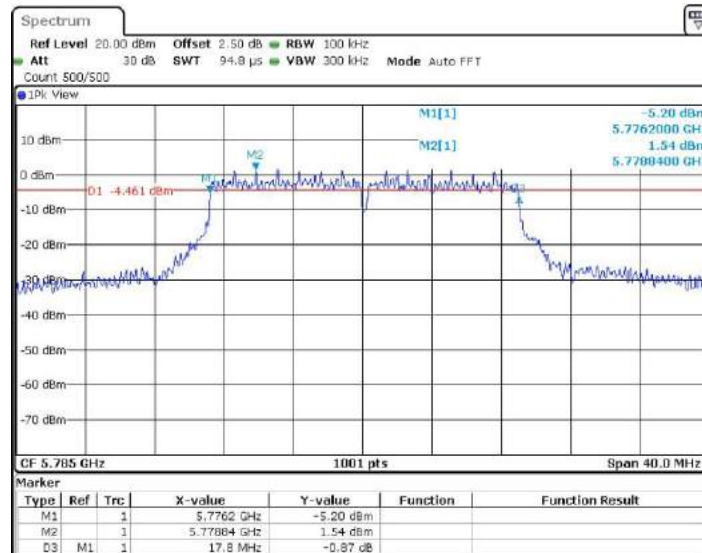
11A_Ant2_5825



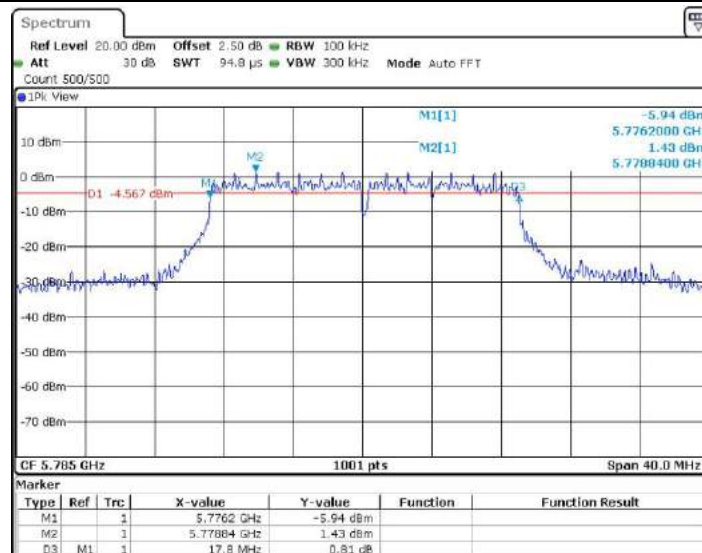
11N20SISO_Ant1_5745



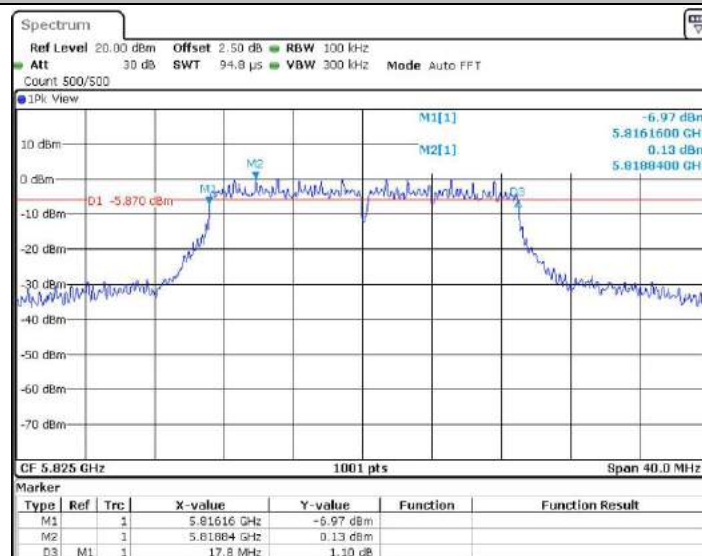
11N20SISO_Ant2_5745



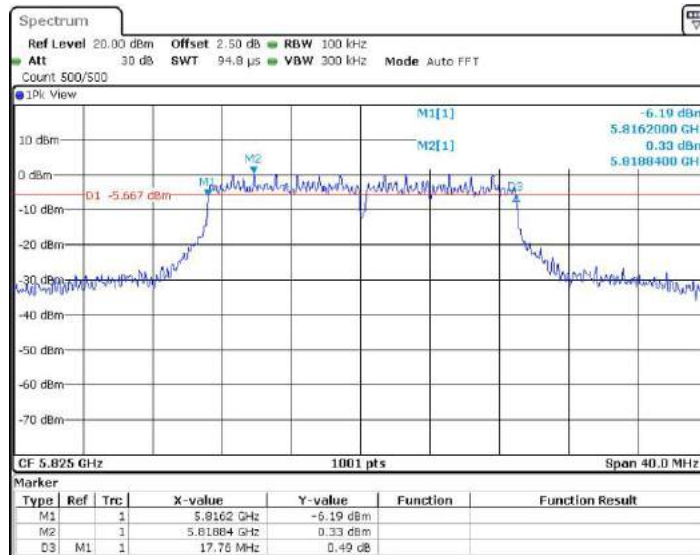
11N20SISO_Ant1_5785



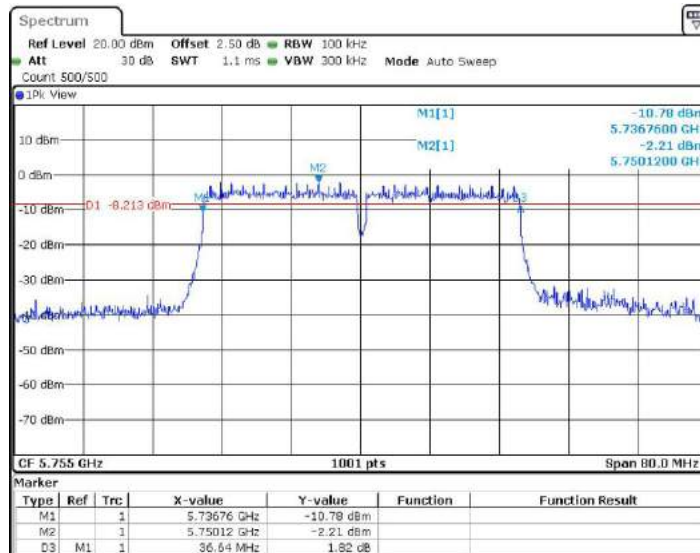
11N20SISO_Ant2_5785



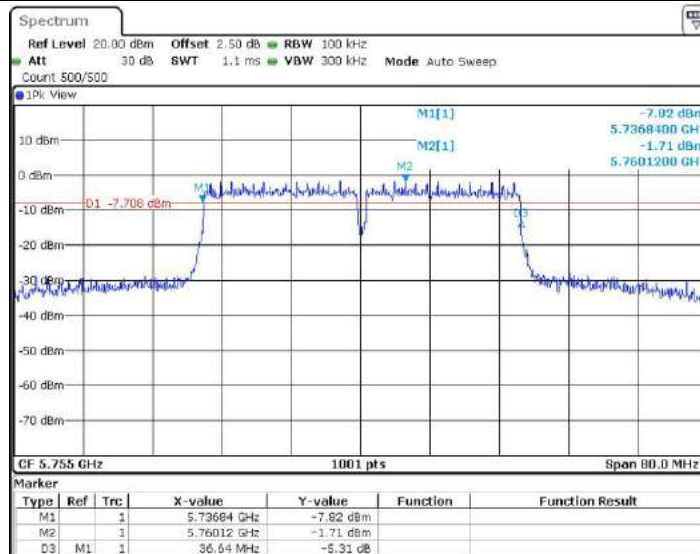
11N20SISO_Ant1_5825



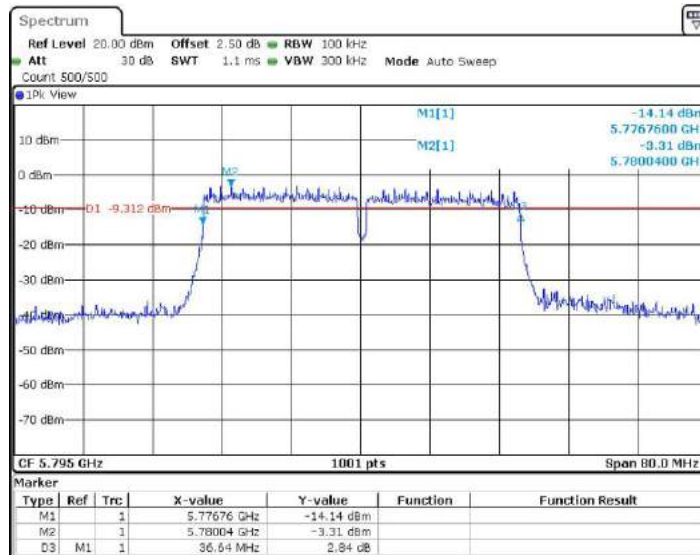
11N20SISO_Ant2_5825



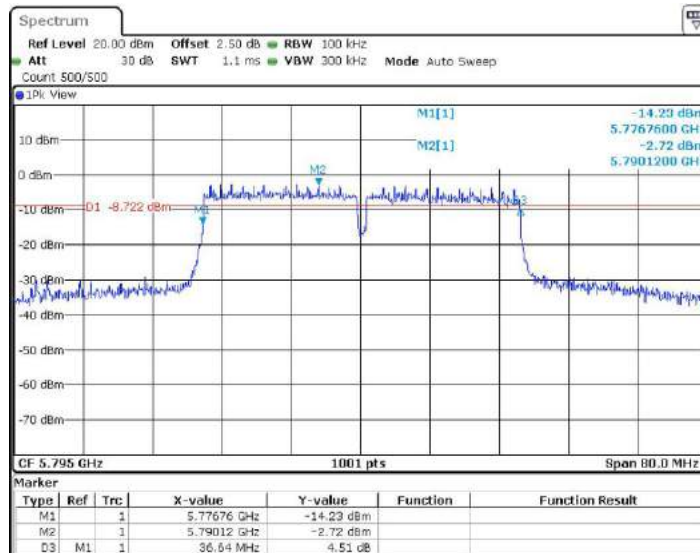
11N40SISO_Ant1_5755



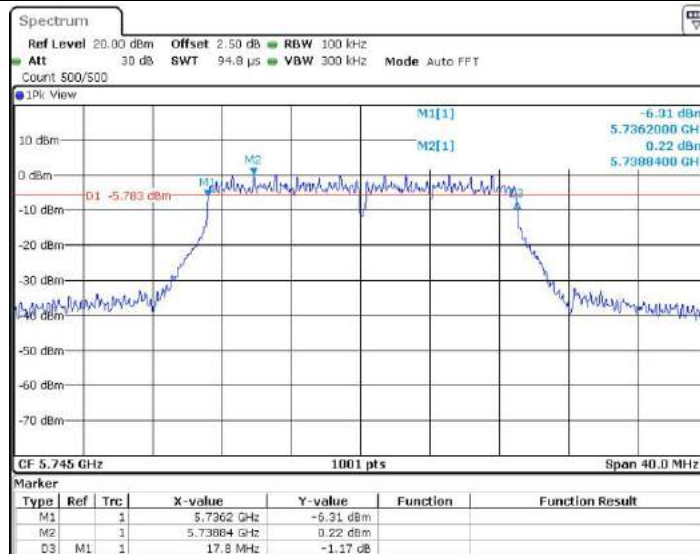
11N40SISO_Ant2_5755



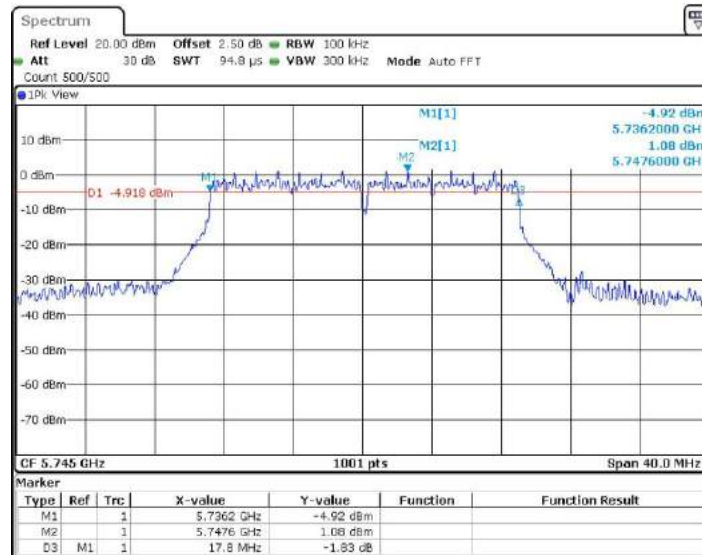
11N40SISO_Ant1_5795



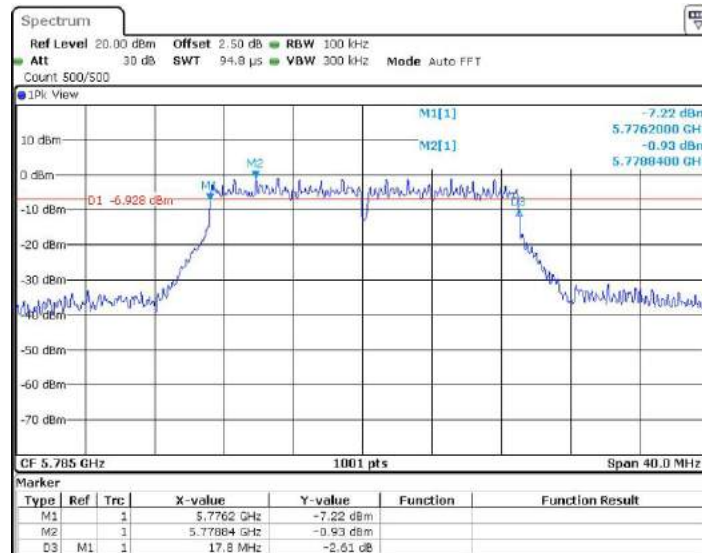
11N40SISO_Ant2_5795



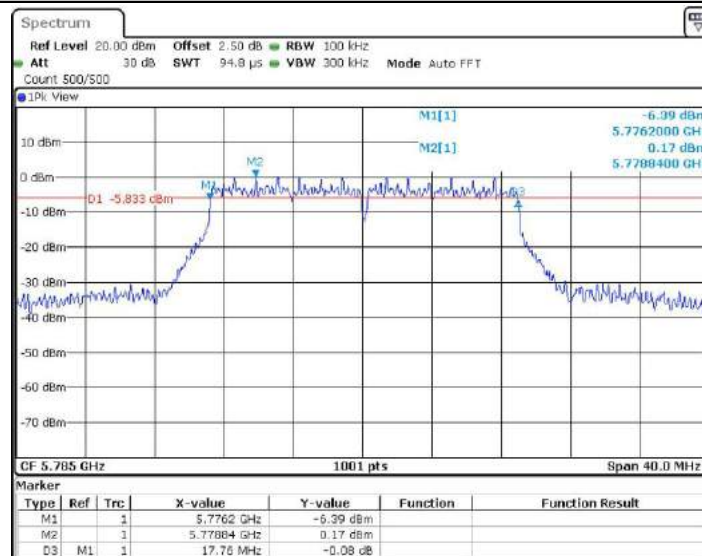
11AC20SISO_Ant1_5745



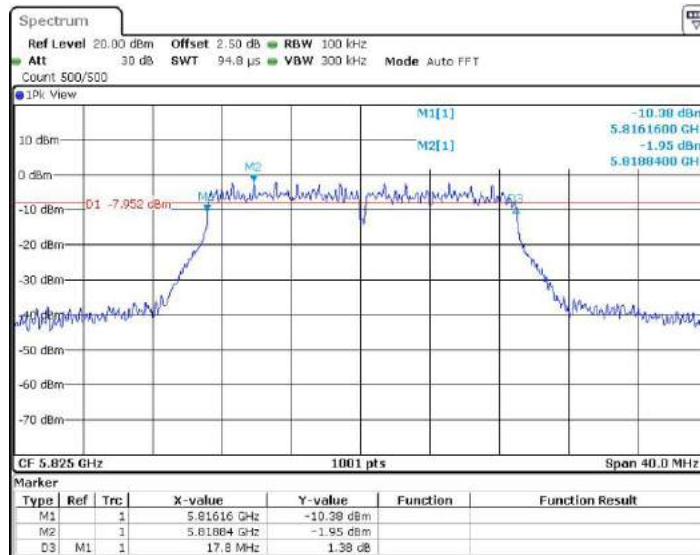
11AC20SISO_Ant2_5745



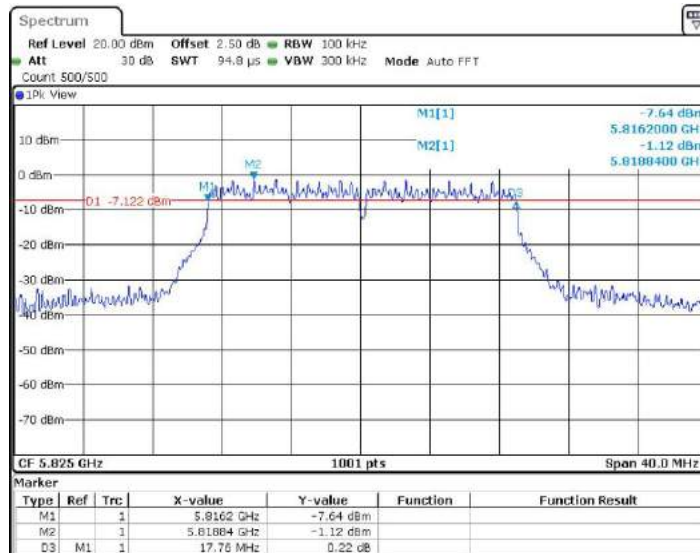
11AC20SISO_Ant1_5785



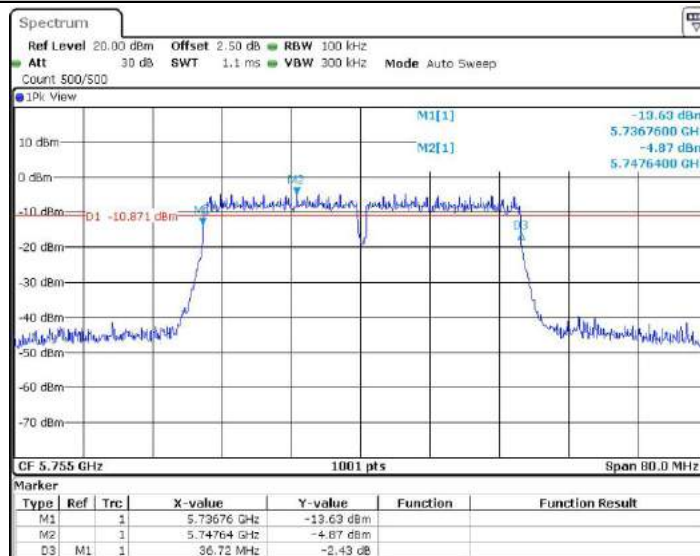
11AC20SISO_Ant2_5785



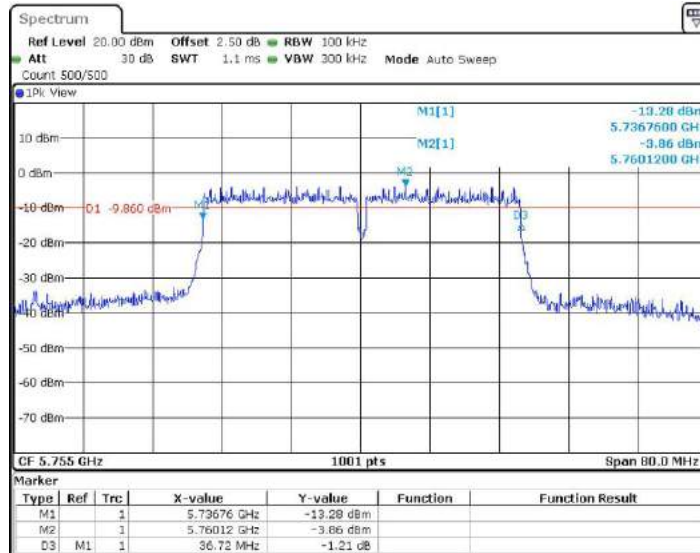
11AC20SISO_Ant1_5825



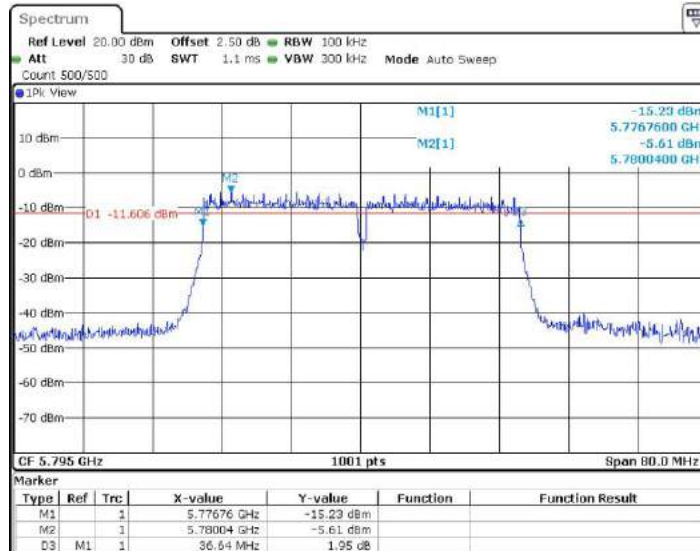
11AC20SISO_Ant2_5825



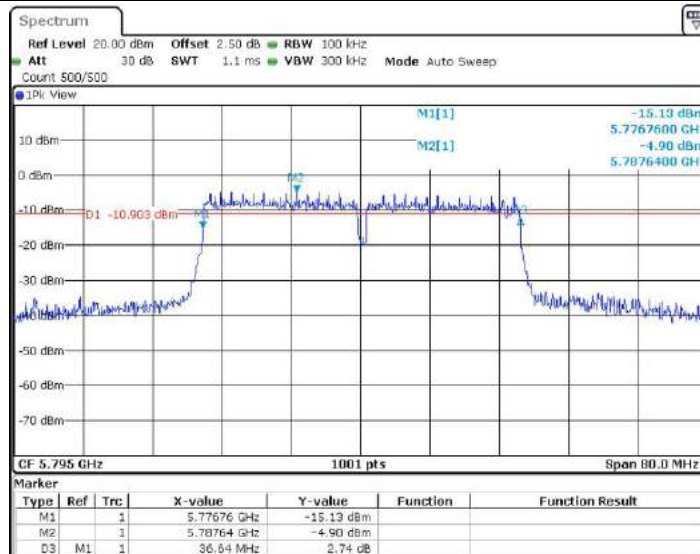
11AC40SISO_Ant1_5755



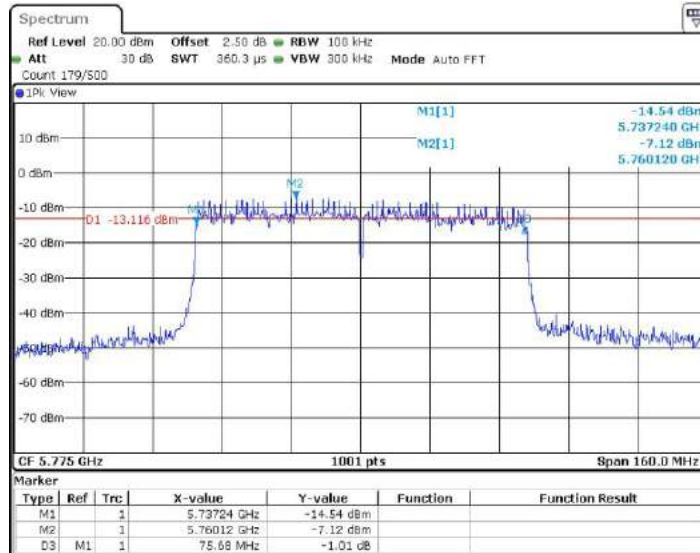
11AC40SISO_Ant2_5755



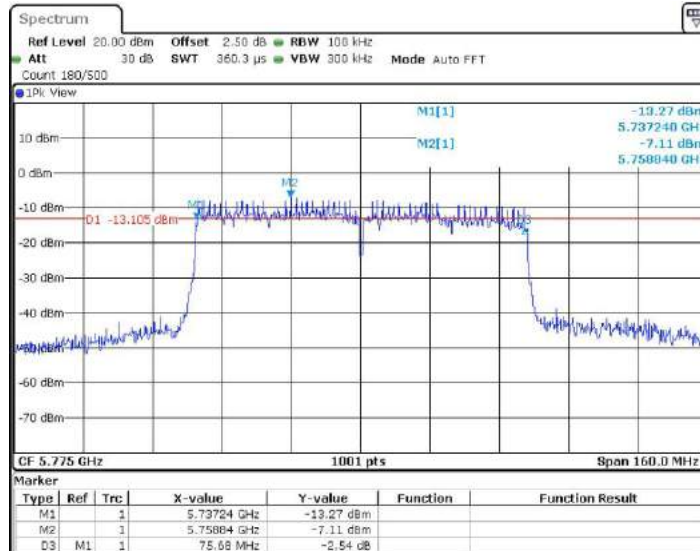
11AC40SISO_Ant1_5795



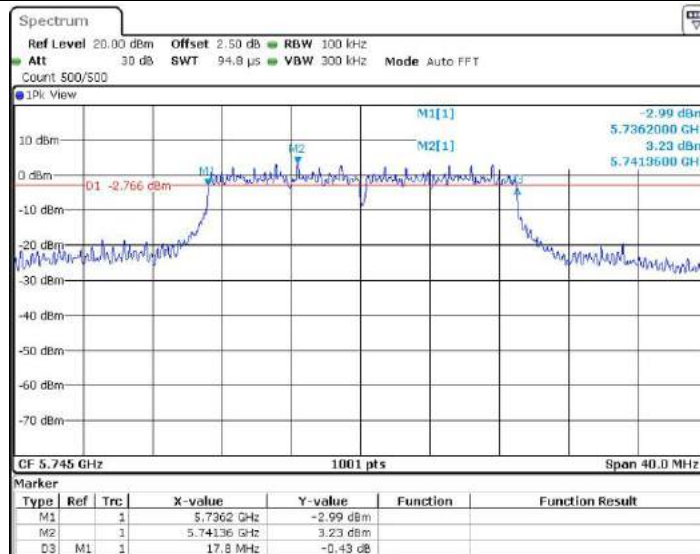
11AC40SISO_Ant2_5795



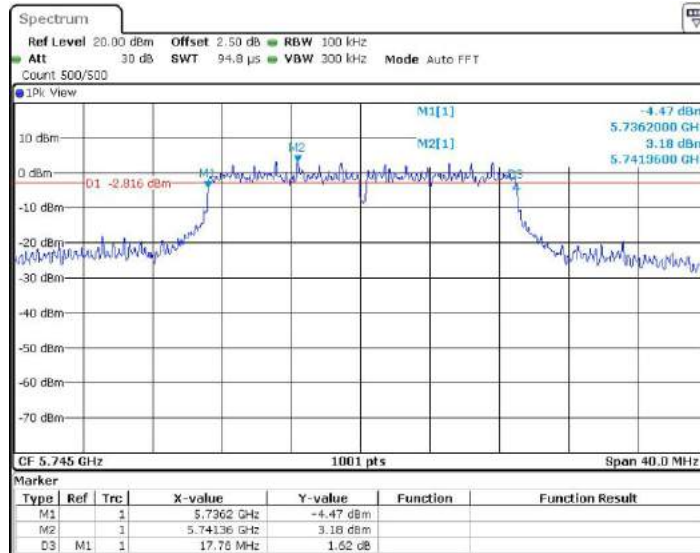
11AC80SISO_Ant1_5775



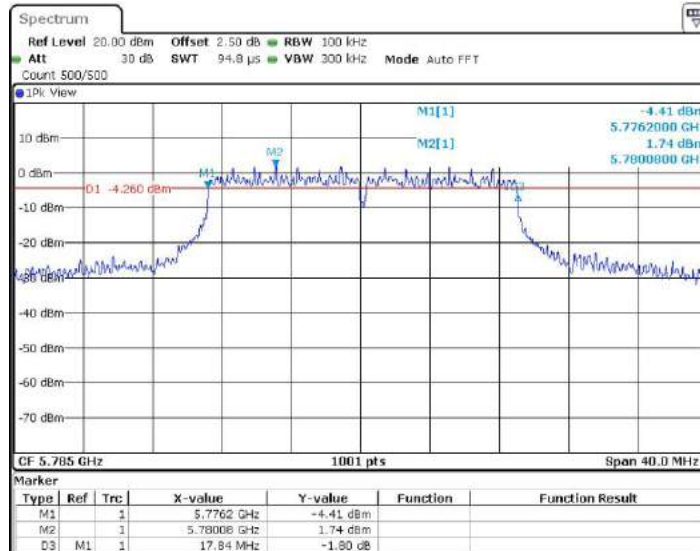
11AC80SISO_Ant2_5775



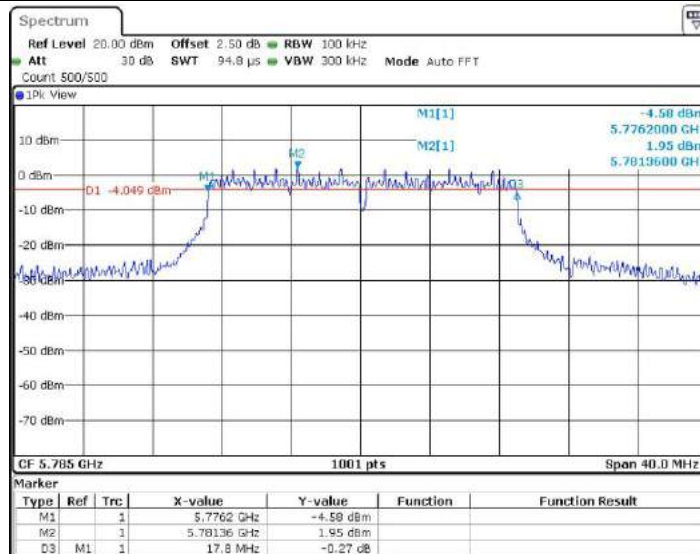
11N20MIMO_Ant1_5745



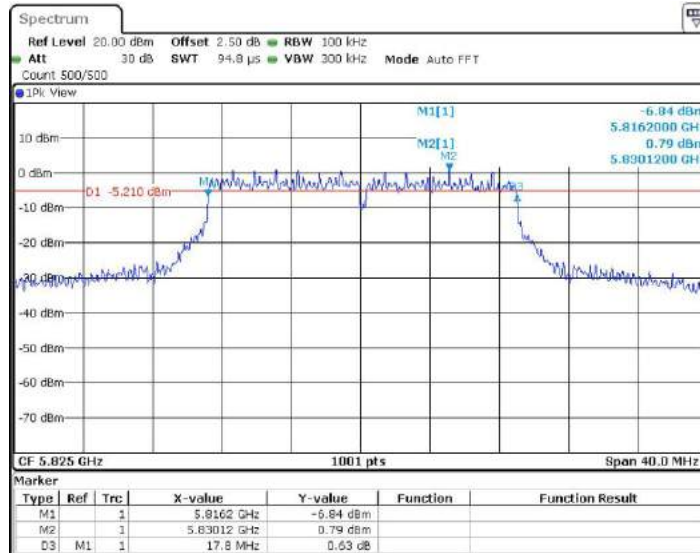
11N20MIMO_Ant2_5745



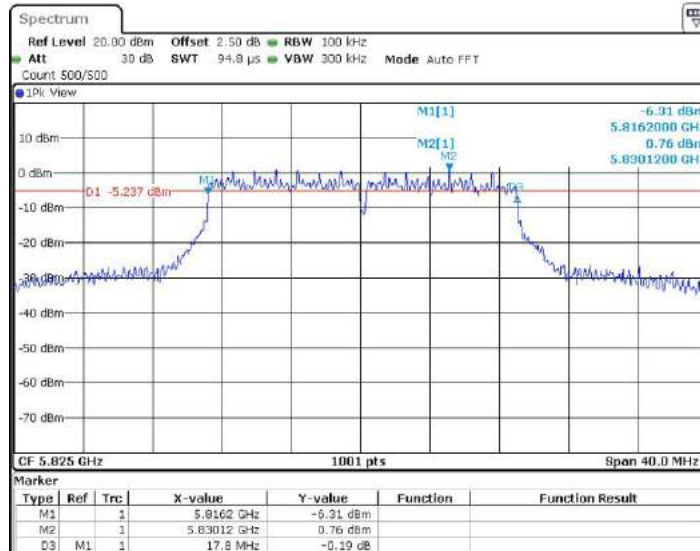
11N20MIMO_Ant1_5785



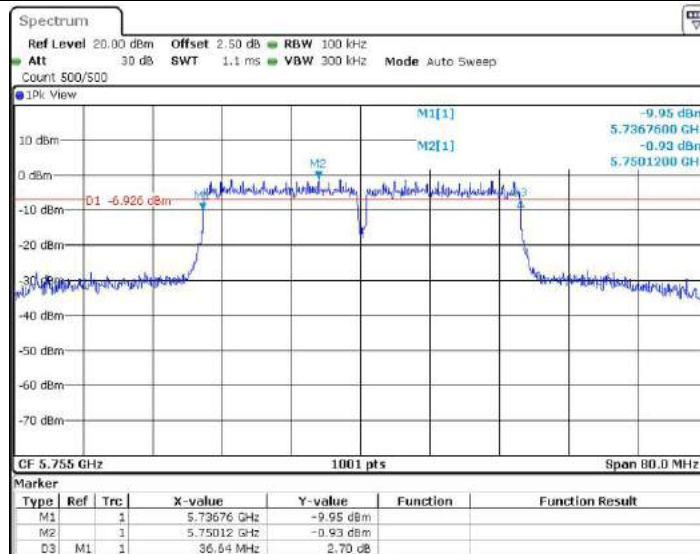
11N20MIMO_Ant2_5785



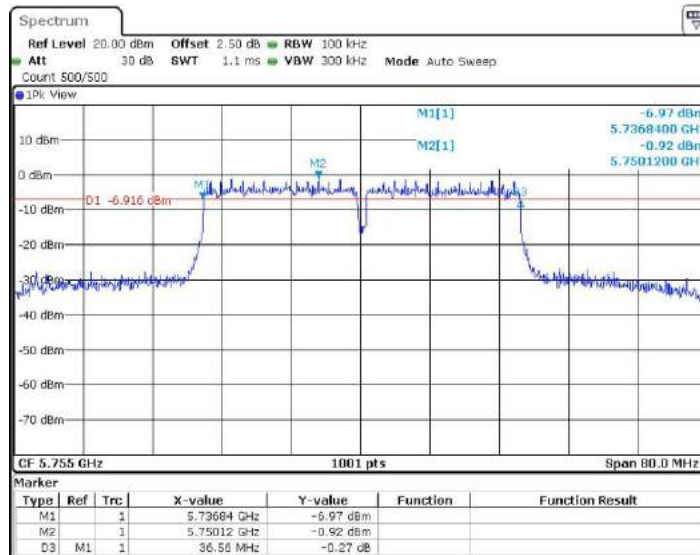
11N20MIMO_Ant1_5825



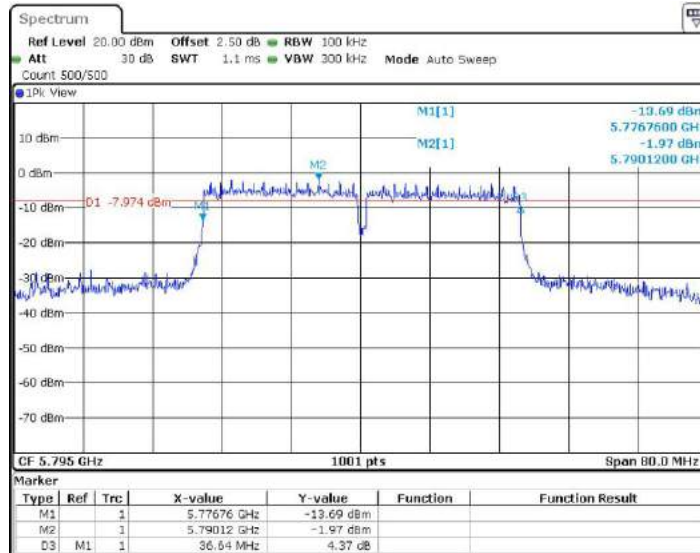
11N20MIMO_Ant2_5825



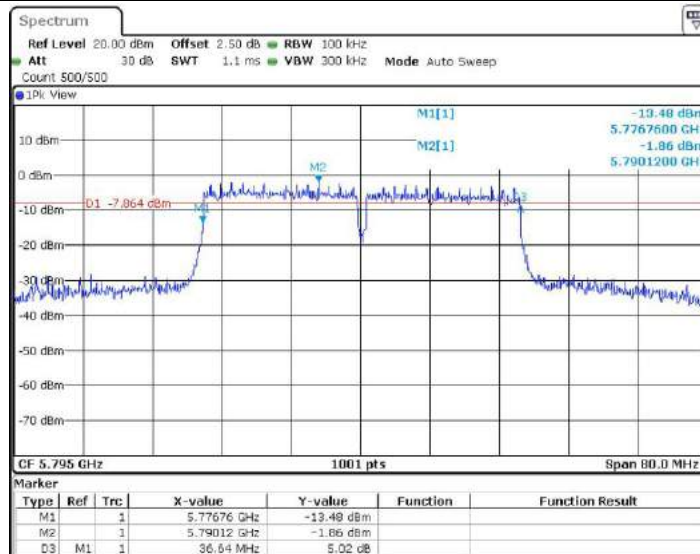
11N40MIMO_Ant1_5755



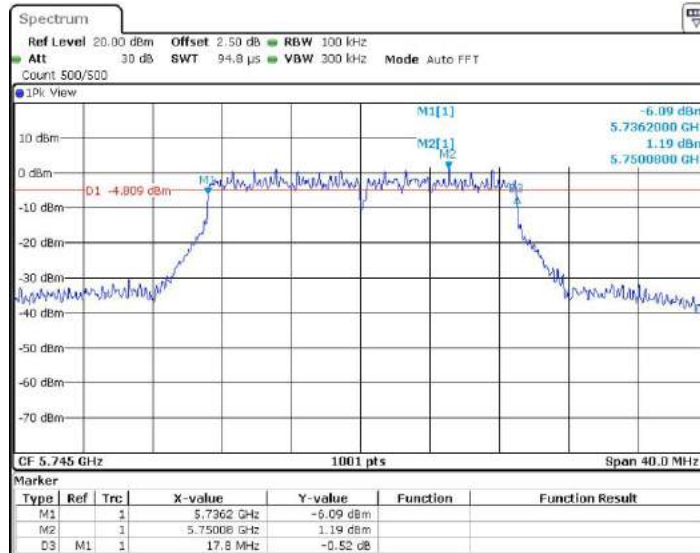
11N40MIMO_Ant2_5755



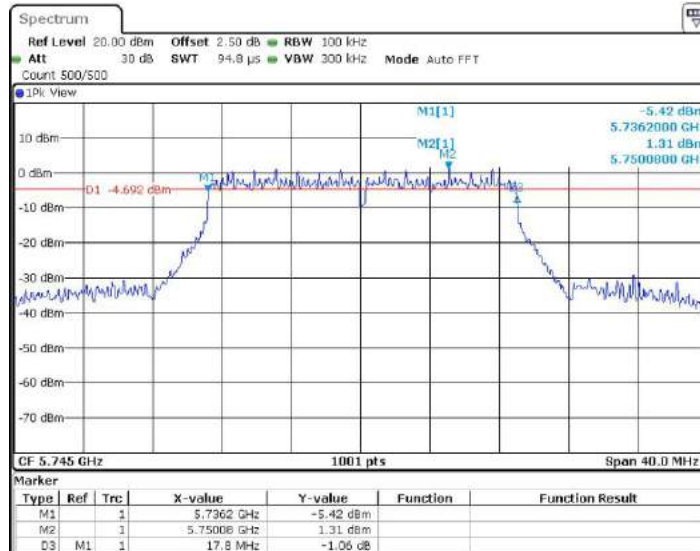
11N40MIMO_Ant1_5795



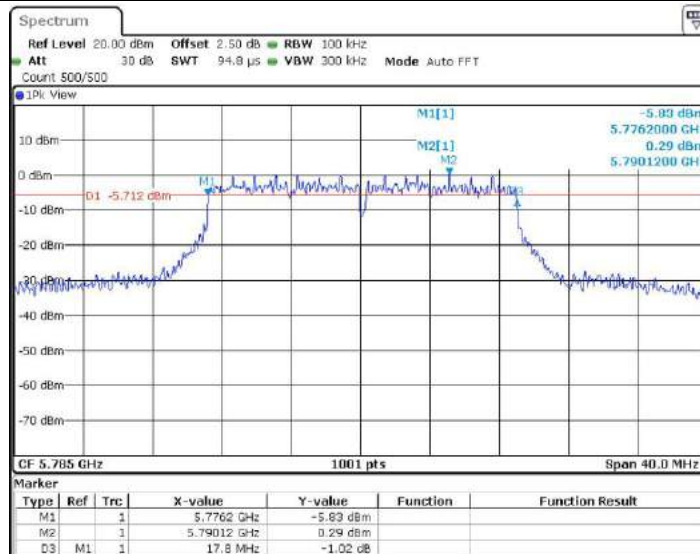
11N40MIMO_Ant2_5795



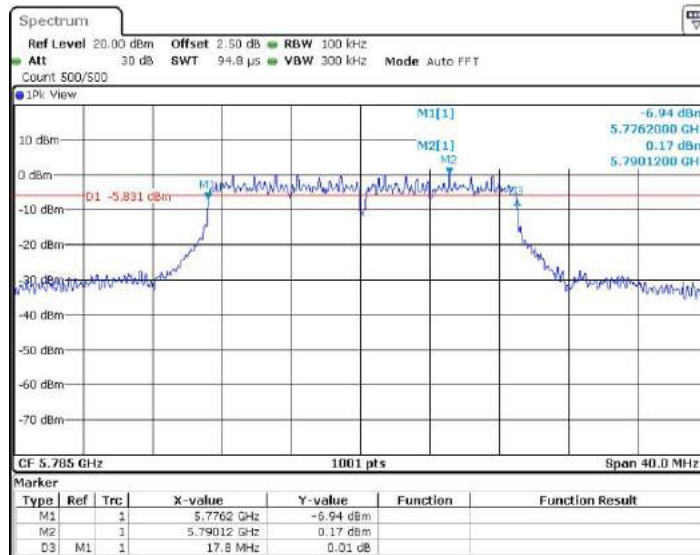
11AC20MIMO_Ant1_5745



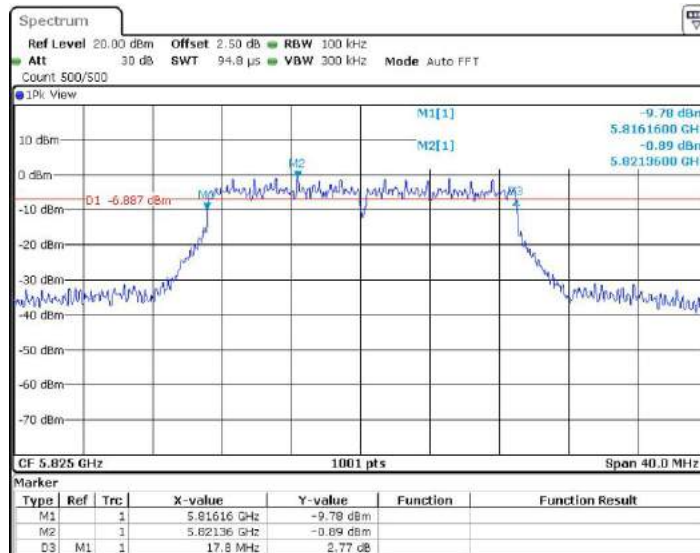
11AC20MIMO_Ant2_5745



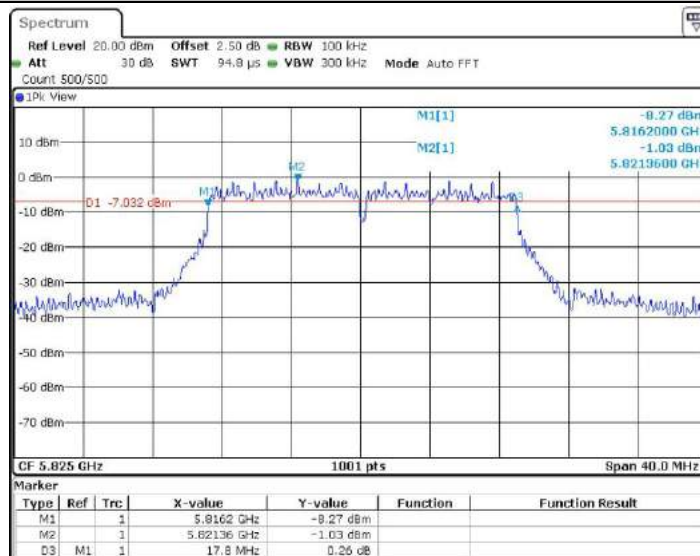
11AC20MIMO_Ant1_5785



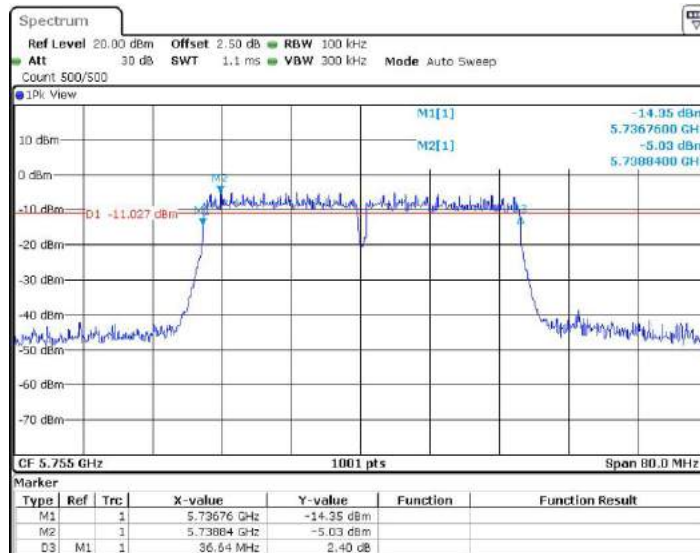
11AC20MIMO_Ant2_5785



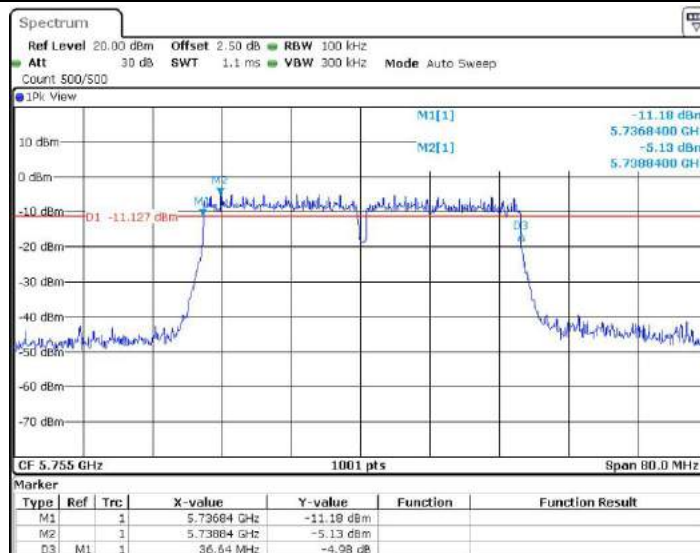
11AC20MIMO_Ant1_5825



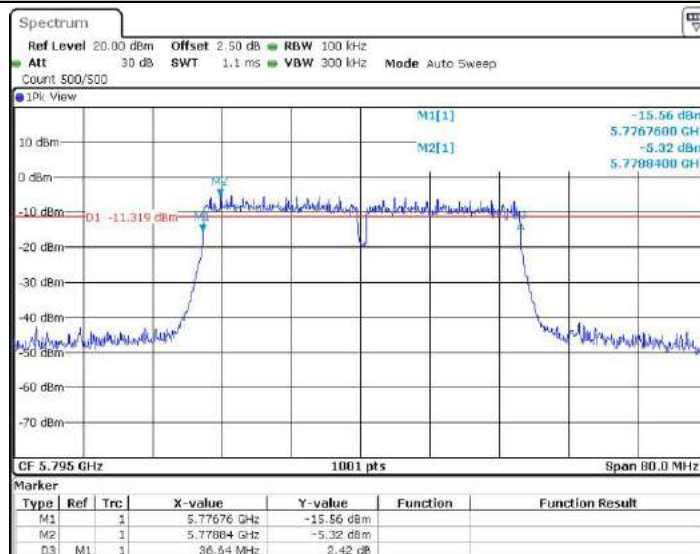
11AC20MIMO_Ant2_5825



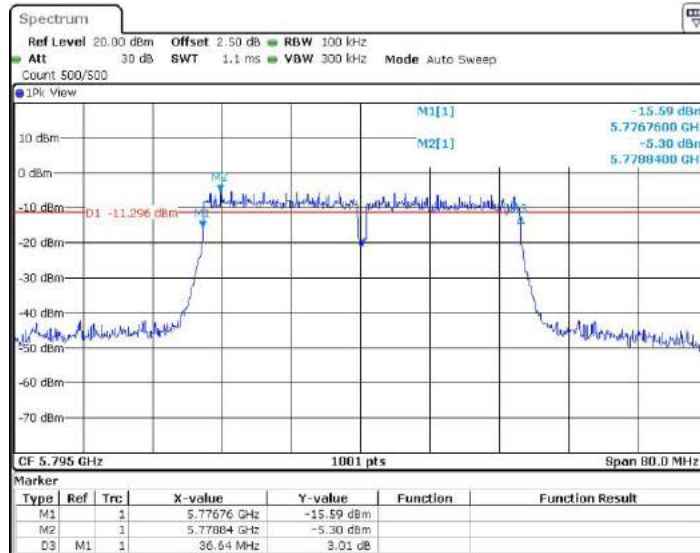
11AC40MIMO_Ant1_5755



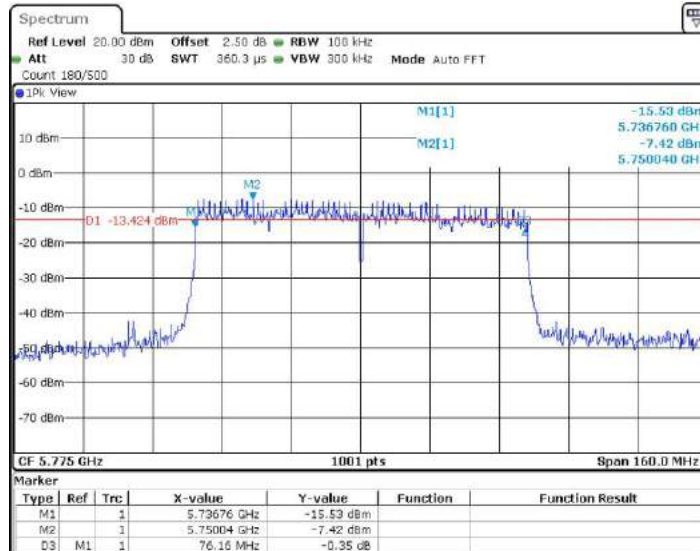
11AC40MIMO_Ant2_5755



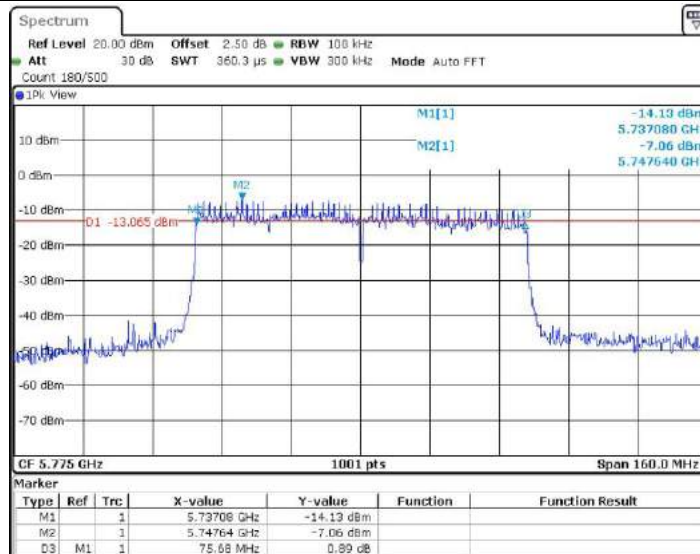
11AC40MIMO_Ant1_5795



11AC40MIMO_Ant2_5795



11AC80MIMO_Ant1_5775



11AC80MIMO_Ant2_5775

Appendix B1: Maximum conducted output power

Test Result

TestMode	Antenna	Frequency (MHz)	Result	Limit	Verdict
11A	Ant1	5180	11.31	<=23.98	PASS
	Ant2	5180	11.45	<=23.98	PASS
	Ant1	5200	10.86	<=23.98	PASS
	Ant2	5200	10.99	<=23.98	PASS
	Ant1	5240	10.14	<=23.98	PASS
	Ant2	5240	10.33	<=23.98	PASS
	Ant1	5260	13.81	<=23.98	PASS
	Ant2	5260	13.55	<=23.98	PASS
	Ant1	5280	13.52	<=23.98	PASS
	Ant2	5280	13.52	<=23.98	PASS
	Ant1	5320	14.02	<=23.98	PASS
	Ant2	5320	13.76	<=23.98	PASS
	Ant1	5500	12.85	<=23.98	PASS
	Ant2	5500	10.95	<=23.98	PASS
	Ant1	5580	13.65	<=23.98	PASS
	Ant2	5580	11.79	<=23.98	PASS
	Ant1	5700	13.55	<=23.98	PASS
	Ant2	5700	11.82	<=23.98	PASS
	Ant1	5745	14.55	<=30	PASS
	Ant2	5745	15.30	<=30	PASS
	Ant1	5785	13.39	<=30	PASS
	Ant2	5785	13.97	<=30	PASS
	Ant1	5825	12.13	<=30	PASS
	Ant2	5825	12.86	<=30	PASS
11N20SISO	Ant1	5180	10.15	<=23.98	PASS
	Ant2	5180	10.28	<=23.98	PASS
	Ant1	5200	9.69	<=23.98	PASS
	Ant2	5200	9.78	<=23.98	PASS
	Ant1	5240	9.08	<=23.98	PASS
	Ant2	5240	8.85	<=23.98	PASS
	Ant1	5260	12.49	<=23.98	PASS
	Ant2	5260	12.46	<=23.98	PASS
	Ant1	5280	12.61	<=23.98	PASS
	Ant2	5280	12.56	<=23.98	PASS
	Ant1	5320	12.80	<=23.98	PASS
	Ant2	5320	12.44	<=23.98	PASS

	Ant1	5500	11.83	<=23.98	PASS
	Ant2	5500	9.32	<=23.98	PASS
	Ant1	5580	12.47	<=23.98	PASS
	Ant2	5580	12.69	<=23.98	PASS
	Ant1	5700	12.26	<=23.98	PASS
	Ant2	5700	10.16	<=23.98	PASS
	Ant1	5745	13.73	<=30	PASS
	Ant2	5745	13.74	<=30	PASS
	Ant1	5785	12.73	<=30	PASS
	Ant2	5785	12.62	<=30	PASS
	Ant1	5825	11.55	<=30	PASS
	Ant2	5825	11.45	<=30	PASS
11N40SISO	Ant1	5190	10.94	<=23.98	PASS
	Ant2	5190	10.60	<=23.98	PASS
	Ant1	5230	12.31	<=23.98	PASS
	Ant2	5230	11.79	<=23.98	PASS
	Ant1	5270	12.76	<=23.98	PASS
	Ant2	5270	12.47	<=23.98	PASS
	Ant1	5310	12.84	<=23.98	PASS
	Ant2	5310	12.47	<=23.98	PASS
	Ant1	5510	11.96	<=23.98	PASS
	Ant2	5510	11.13	<=23.98	PASS
	Ant1	5550	12.80	<=23.98	PASS
	Ant2	5550	11.37	<=23.98	PASS
	Ant1	5670	12.69	<=23.98	PASS
	Ant2	5670	12.55	<=23.98	PASS
	Ant1	5755	11.75	<=30	PASS
	Ant2	5755	12.32	<=30	PASS
	Ant1	5795	10.63	<=30	PASS
	Ant2	5795	11.04	<=30	PASS
11AC20SISO	Ant1	5180	8.30	<=23.98	PASS
	Ant2	5180	8.29	<=23.98	PASS
	Ant1	5200	7.97	<=23.98	PASS
	Ant2	5200	8.00	<=23.98	PASS
	Ant1	5240	7.21	<=23.98	PASS
	Ant2	5240	7.26	<=23.98	PASS
	Ant1	5260	10.56	<=23.98	PASS
	Ant2	5260	10.45	<=23.98	PASS
	Ant1	5280	10.22	<=23.98	PASS
	Ant2	5280	10.57	<=23.98	PASS
	Ant1	5320	10.61	<=23.98	PASS
	Ant2	5320	10.78	<=23.98	PASS
	Ant1	5500	11.15	<=23.98	PASS

	Ant2	5500	9.89	<=23.98	PASS
	Ant1	5580	12.64	<=23.98	PASS
	Ant2	5580	10.35	<=23.98	PASS
	Ant1	5700	12.38	<=23.98	PASS
	Ant2	5700	9.64	<=23.98	PASS
	Ant1	5745	11.81	<=30	PASS
	Ant2	5745	12.44	<=30	PASS
	Ant1	5785	10.76	<=30	PASS
	Ant2	5785	11.47	<=30	PASS
	Ant1	5825	9.35	<=30	PASS
	Ant2	5825	10.38	<=30	PASS
11AC40SISO	Ant1	5190	6.46	<=23.98	PASS
	Ant2	5190	6.68	<=23.98	PASS
	Ant1	5230	7.86	<=23.98	PASS
	Ant2	5230	8.67	<=23.98	PASS
	Ant1	5270	8.22	<=23.98	PASS
	Ant2	5270	8.37	<=23.98	PASS
	Ant1	5310	8.40	<=23.98	PASS
	Ant2	5310	8.56	<=23.98	PASS
	Ant1	5510	9.72	<=23.98	PASS
	Ant2	5510	10.09	<=23.98	PASS
	Ant1	5550	10.61	<=23.98	PASS
	Ant2	5550	11.14	<=23.98	PASS
	Ant1	5670	10.22	<=23.98	PASS
	Ant2	5670	10.96	<=23.98	PASS
	Ant1	5755	9.37	<=30	PASS
	Ant2	5755	10.05	<=30	PASS
	Ant1	5795	8.28	<=30	PASS
	Ant2	5795	8.67	<=30	PASS
11AC80SISO	Ant1	5210	6.10	<=23.98	PASS
	Ant2	5210	6.67	<=23.98	PASS
	Ant1	5290	8.57	<=23.98	PASS
	Ant2	5290	9.19	<=23.98	PASS
	Ant1	5530	10.16	<=23.98	PASS
	Ant2	5530	11.05	<=23.98	PASS
	Ant1	5610	11.07	<=23.98	PASS
	Ant2	5610	11.04	<=23.98	PASS
	Ant1	5775	9.62	<=30	PASS
	Ant2	5775	9.66	<=30	PASS
11N20MIMO	Ant1	5180	10.12	<=23.98	PASS
	Ant2	5180	10.62	<=23.98	PASS
	total	5180	13.4	<=23.98	PASS
	Ant1	5200	10.15	<=23.98	PASS

	Ant2	5200	10.19	<=23.98	PASS
	total	5200	13.2	<=23.98	PASS
	Ant1	5240	9.66	<=23.98	PASS
	Ant2	5240	9.53	<=23.98	PASS
	total	5240	12.6	<=23.98	PASS
	Ant1	5260	10.83	<=23.98	PASS
	Ant2	5260	10.15	<=23.98	PASS
	total	5260	13.5	<=23.98	PASS
	Ant1	5280	10.74	<=23.98	PASS
	Ant2	5280	10.86	<=23.98	PASS
	total	5280	13.8	<=23.98	PASS
	Ant1	5320	9.92	<=23.98	PASS
	Ant2	5320	11.15	<=23.98	PASS
	total	5320	13.6	<=23.98	PASS
	Ant1	5500	10.76	<=23.98	PASS
	Ant2	5500	10.81	<=23.98	PASS
	total	5500	13.8	<=23.98	PASS
	Ant1	5580	10.81	<=23.98	PASS
	Ant2	5580	11.27	<=23.98	PASS
	total	5580	14.1	<=23.98	PASS
	Ant1	5700	10.92	<=23.98	PASS
	Ant2	5700	10.86	<=23.98	PASS
	total	5700	13.9	<=23.98	PASS
	Ant1	5745	14.47	<=30	PASS
	Ant2	5745	14.46	<=30	PASS
	total	5745	17.5	<=30	PASS
	Ant1	5785	13.21	<=30	PASS
	Ant2	5785	13.22	<=30	PASS
	total	5785	16.2	<=30	PASS
	Ant1	5825	12.03	<=30	PASS
	Ant2	5825	12.01	<=30	PASS
	total	5825	15.0	<=30	PASS
11N40MIMO	Ant1	5190	11.37	<=23.98	PASS
	Ant2	5190	11.33	<=23.98	PASS
	total	5190	14.4	<=23.98	PASS
	Ant1	5230	12.51	<=23.98	PASS
	Ant2	5230	12.51	<=23.98	PASS
	total	5230	15.5	<=23.98	PASS
	Ant1	5270	13.10	<=23.98	PASS
	Ant2	5270	13.12	<=23.98	PASS
	total	5270	16.1	<=23.98	PASS
	Ant1	5310	13.10	<=23.98	PASS
	Ant2	5310	13.25	<=23.98	PASS

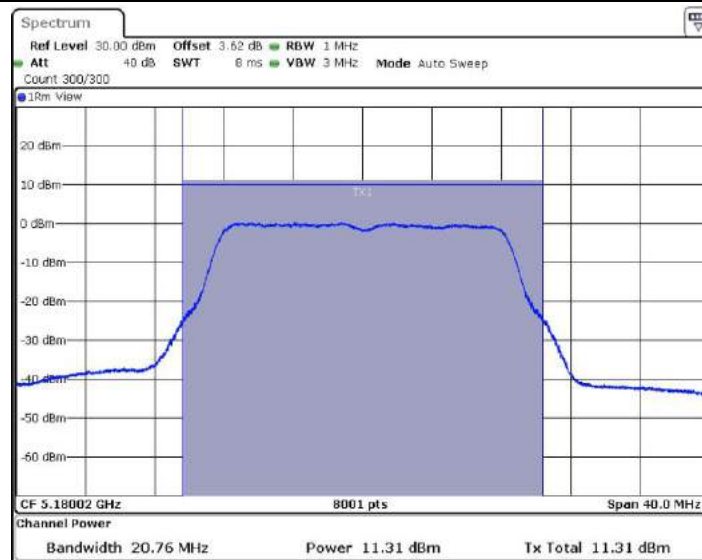
	total	5310	16.2	<=23.98	PASS
	Ant1	5510	12.78	<=23.98	PASS
	Ant2	5510	12.77	<=23.98	PASS
	total	5510	15.8	<=23.98	PASS
	Ant1	5550	13.69	<=23.98	PASS
	Ant2	5550	13.69	<=23.98	PASS
	total	5550	16.7	<=23.98	PASS
	Ant1	5670	13.33	<=23.98	PASS
	Ant2	5670	13.37	<=23.98	PASS
	total	5670	16.4	<=23.98	PASS
	Ant1	5755	12.99	<=30	PASS
	Ant2	5755	13.02	<=30	PASS
	total	5755	16.0	<=30	PASS
	Ant1	5795	11.81	<=30	PASS
	Ant2	5795	11.91	<=30	PASS
	total	5795	14.9	<=30	PASS
11AC20MIMO	Ant1	5180	9.04	<=23.98	PASS
	Ant2	5180	8.98	<=23.98	PASS
	total	5180	12.0	<=23.98	PASS
	Ant1	5200	8.58	<=23.98	PASS
	Ant2	5200	8.55	<=23.98	PASS
	total	5200	11.6	<=23.98	PASS
	Ant1	5240	7.68	<=23.98	PASS
	Ant2	5240	7.80	<=23.98	PASS
	total	5240	10.8	<=23.98	PASS
	Ant1	5260	10.93	<=23.98	PASS
	Ant2	5260	10.90	<=23.98	PASS
	total	5260	13.9	<=23.98	PASS
	Ant1	5280	10.78	<=23.98	PASS
	Ant2	5280	10.81	<=23.98	PASS
	total	5280	13.8	<=23.98	PASS
	Ant1	5320	11.07	<=23.98	PASS
	Ant2	5320	11.10	<=23.98	PASS
	total	5320	14.1	<=23.98	PASS
	Ant1	5500	11.10	<=23.98	PASS
	Ant2	5500	11.13	<=23.98	PASS
	total	5500	14.1	<=23.98	PASS
	Ant1	5580	12.53	<=23.98	PASS
	Ant2	5580	12.59	<=23.98	PASS
	total	5580	15.6	<=23.98	PASS
	Ant1	5700	10.88	<=23.98	PASS
	Ant2	5700	10.96	<=23.98	PASS
	total	5700	13.9	<=23.98	PASS

	Ant1	5745	12.58	<=30	PASS
	Ant2	5745	12.56	<=30	PASS
	total	5745	15.6	<=30	PASS
	Ant1	5785	11.63	<=30	PASS
	Ant2	5785	11.64	<=30	PASS
	total	5785	14.6	<=30	PASS
	Ant1	5825	10.66	<=30	PASS
	Ant2	5825	10.56	<=30	PASS
	total	5825	13.6	<=30	PASS
11AC40MIMO	Ant1	5190	5.87	<=23.98	PASS
	Ant2	5190	5.92	<=23.98	PASS
	total	5190	8.9	<=23.98	PASS
	Ant1	5230	8.69	<=23.98	PASS
	Ant2	5230	8.53	<=23.98	PASS
	total	5230	11.6	<=23.98	PASS
	Ant1	5270	10.36	<=23.98	PASS
	Ant2	5270	10.37	<=23.98	PASS
	total	5270	13.4	<=23.98	PASS
	Ant1	5310	10.76	<=23.98	PASS
	Ant2	5310	10.76	<=23.98	PASS
	total	5310	13.8	<=23.98	PASS
	Ant1	5510	9.94	<=23.98	PASS
	Ant2	5510	9.96	<=23.98	PASS
	total	5510	13.0	<=23.98	PASS
	Ant1	5550	9.35	<=23.98	PASS
	Ant2	5550	9.38	<=23.98	PASS
	total	5550	12.4	<=23.98	PASS
	Ant1	5670	9.70	<=23.98	PASS
	Ant2	5670	9.71	<=23.98	PASS
	total	5670	12.7	<=23.98	PASS
	Ant1	5755	9.39	<=30	PASS
	Ant2	5755	9.33	<=30	PASS
	total	5755	12.4	<=30	PASS
	Ant1	5795	8.88	<=30	PASS
	Ant2	5795	8.86	<=30	PASS
	total	5795	11.9	<=30	PASS
11AC80MIMO	Ant1	5210	6.44	<=23.98	PASS
	Ant2	5210	7.19	<=23.98	PASS
	total	5210	9.8	<=23.98	PASS
	Ant1	5290	11.03	<=23.98	PASS
	Ant2	5290	11.02	<=23.98	PASS
	total	5290	14.0	<=23.98	PASS
	Ant1	5530	9.76	<=23.98	PASS

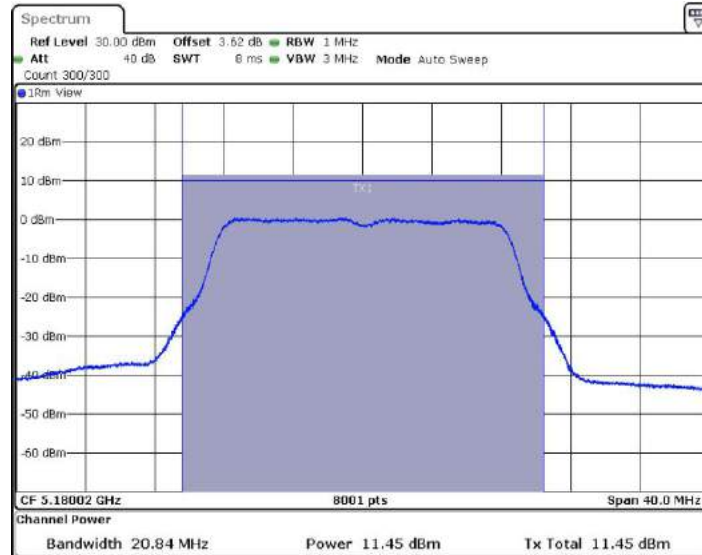
	Ant2	5530	9.78	<=23.98	PASS
	total	5530	12.8	<=23.98	PASS
	Ant1	5610	9.58	<=23.98	PASS
	Ant2	5610	9.55	<=23.98	PASS
	total	5610	12.6	<=23.98	PASS
	Ant1	5775	9.98	<=30	PASS
	Ant2	5775	9.92	<=30	PASS
	total	5775	13.0	<=30	PASS

Note: The Duty Cycle Factor is compensated in the graph.

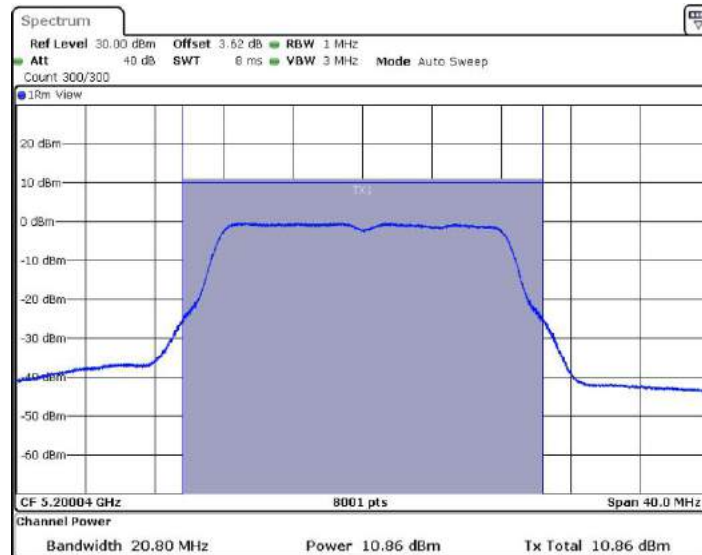
Test Graphs



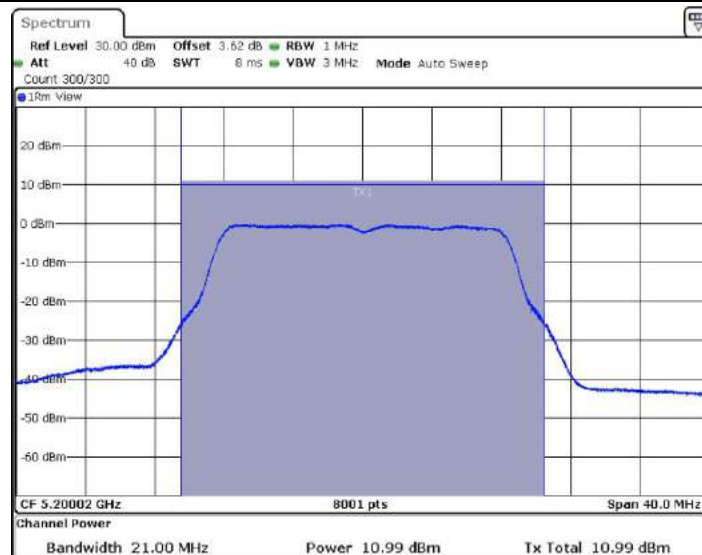
11A_Ant1_5180



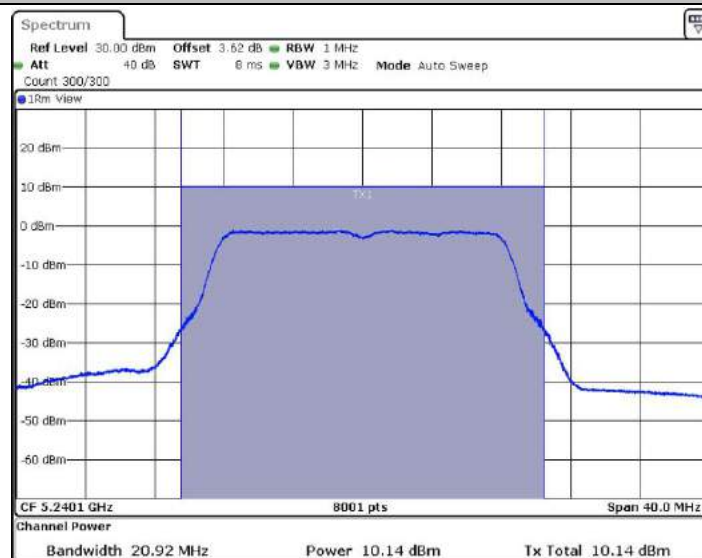
11A_Ant2_5180



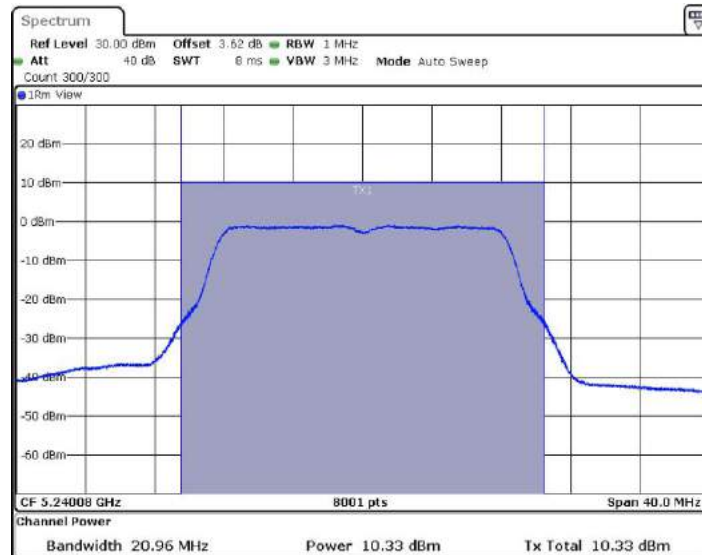
11A_Ant1_5200



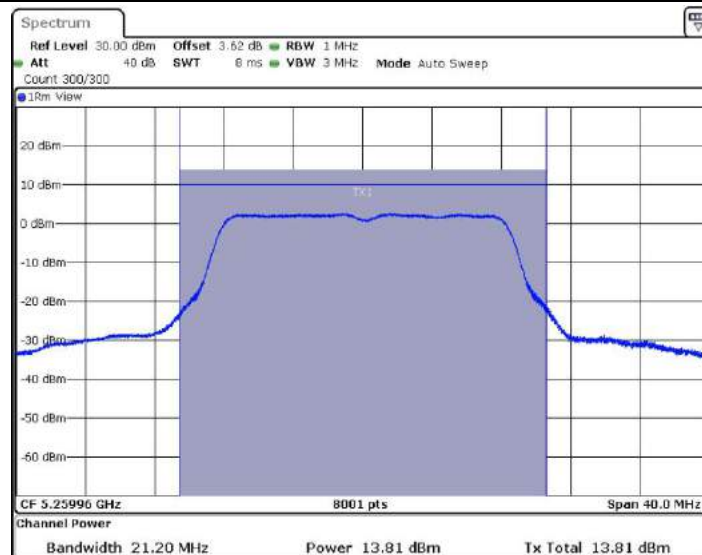
11A_Ant2_5200



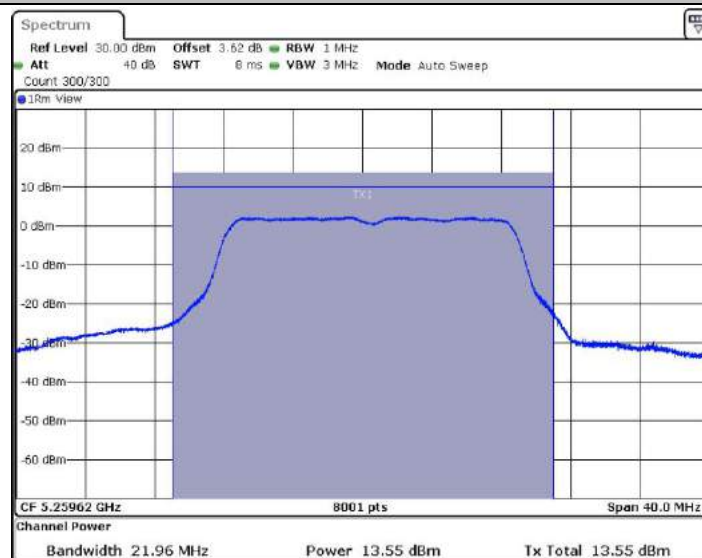
11A_Ant1_5240



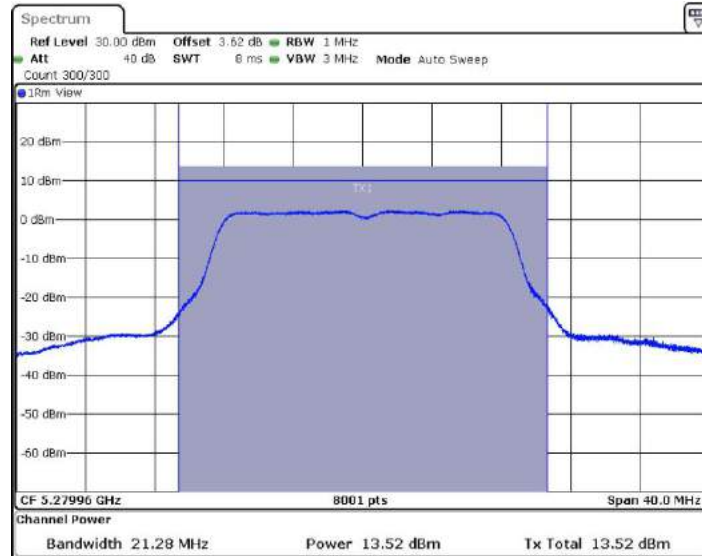
11A_Ant2_5240



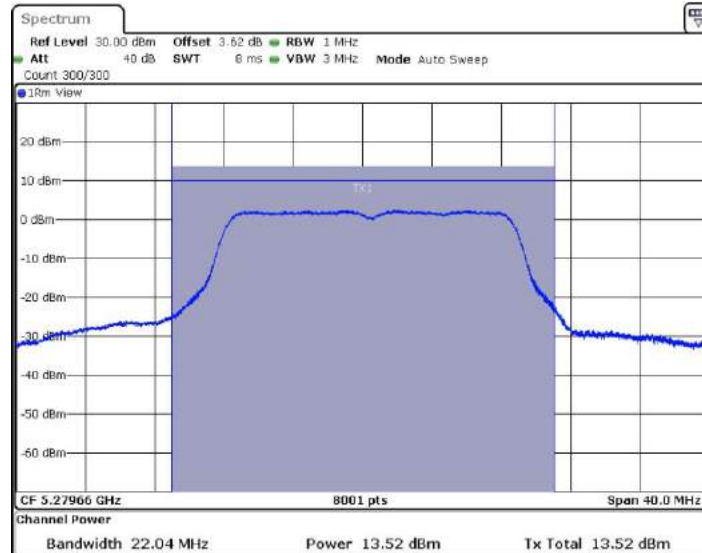
11A_Ant1_5260



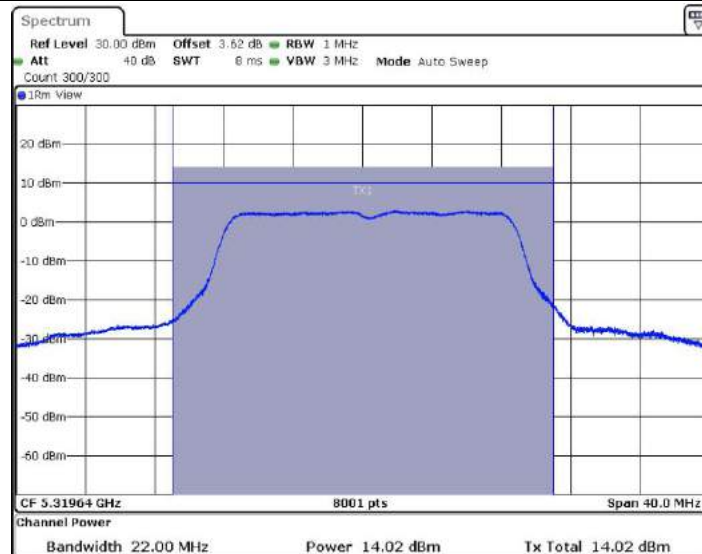
11A_Ant2_5260



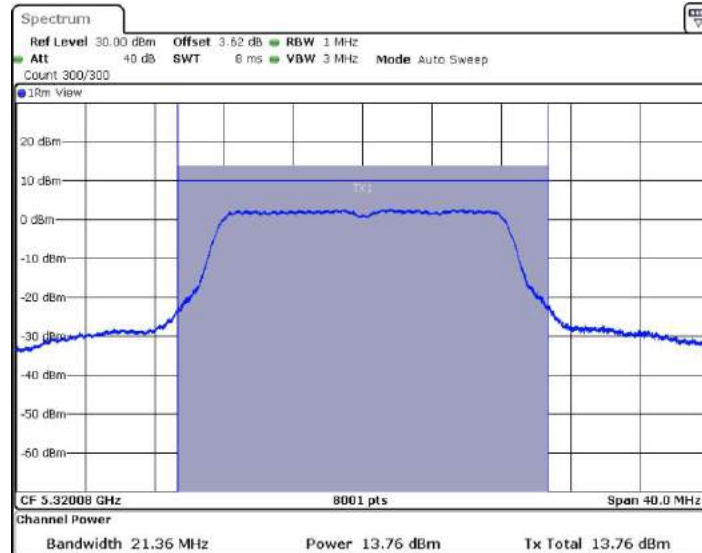
11A_Ant1_5280



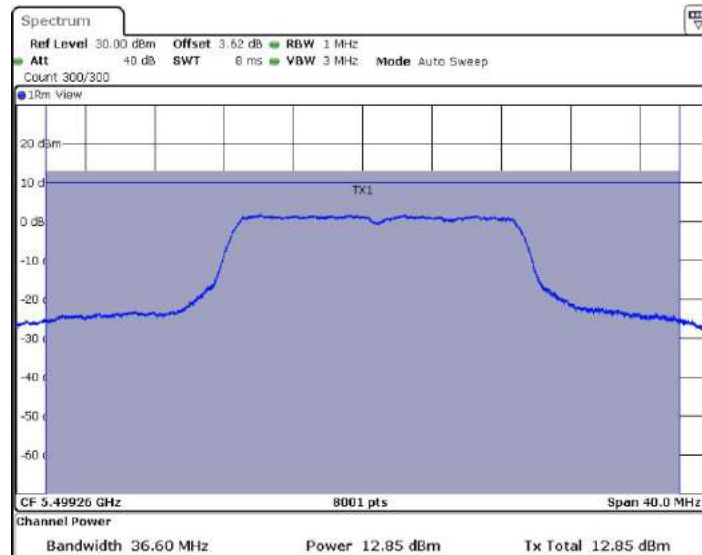
11A_Ant2_5280



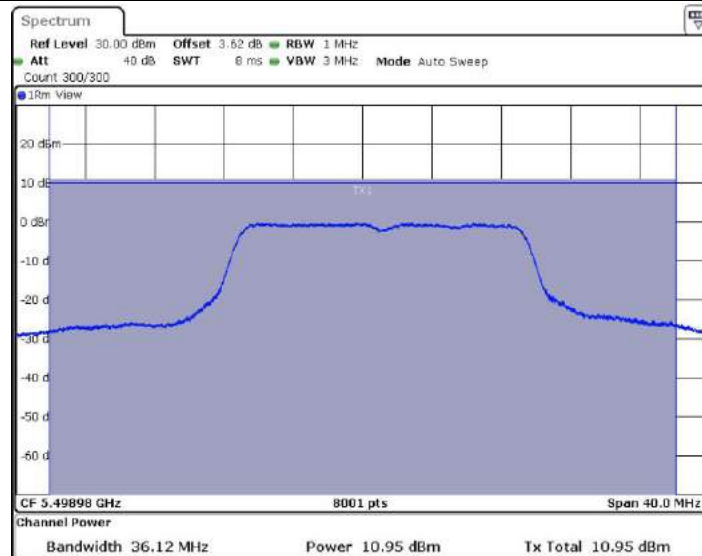
11A_Ant1_5320



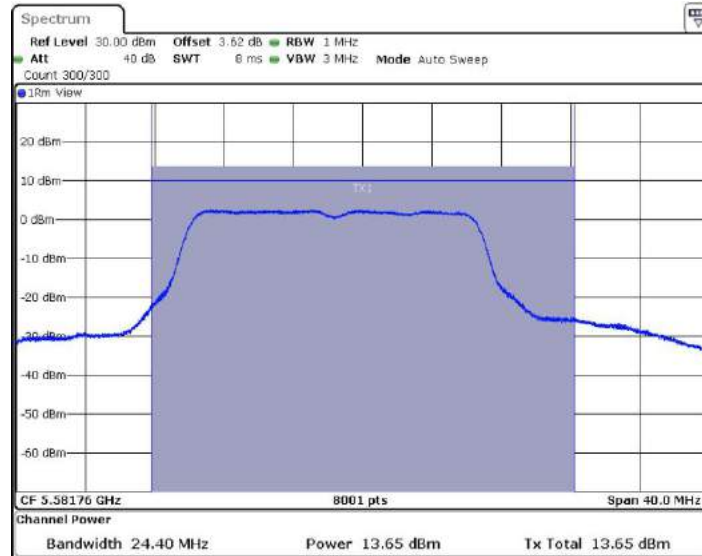
11A_Ant2_5320



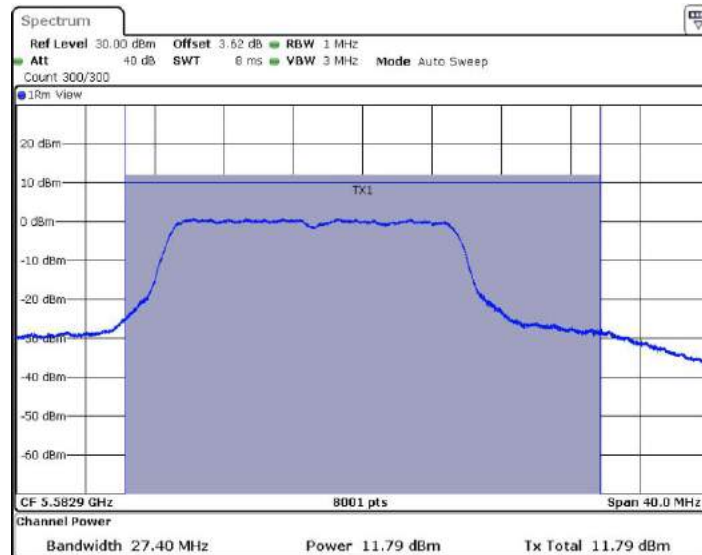
11A_Ant1_5500



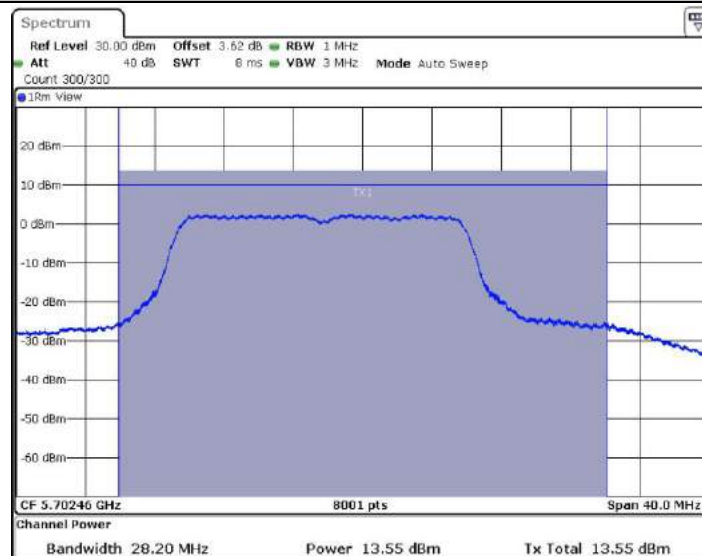
11A_Ant2_5500



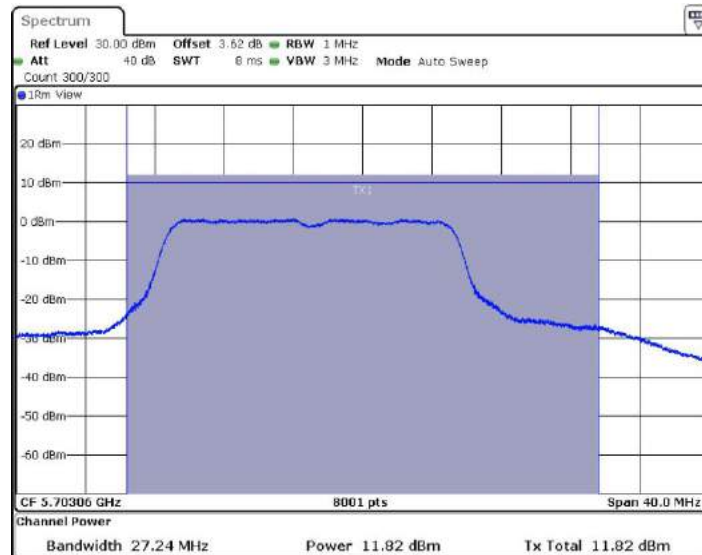
11A_Ant1_5580



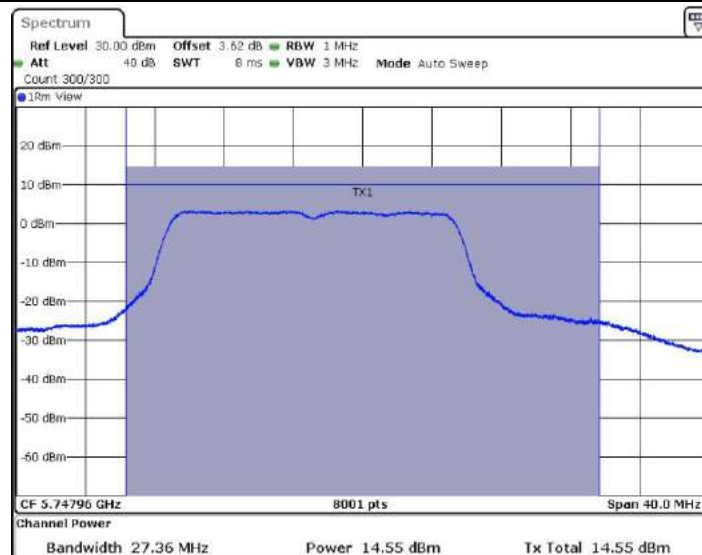
11A_Ant2_5580



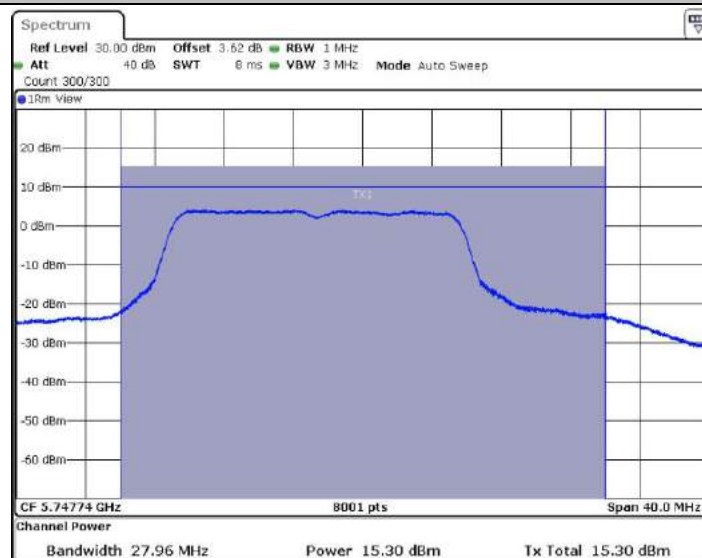
11A_Ant1_5700



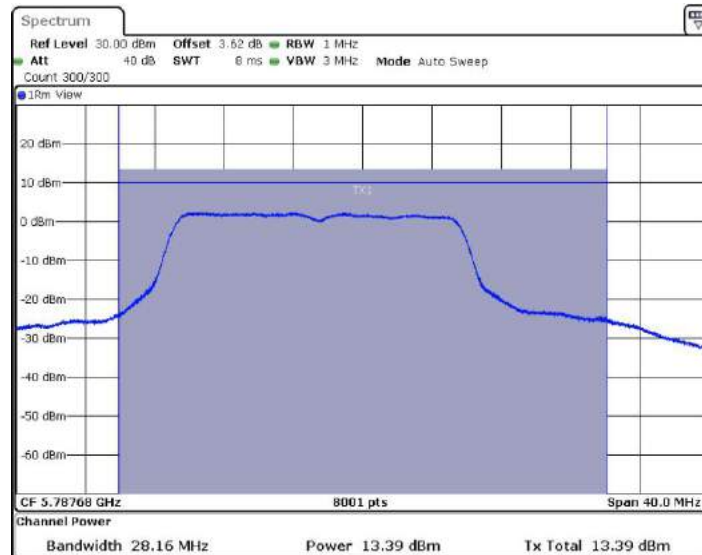
11A_Ant2_5700



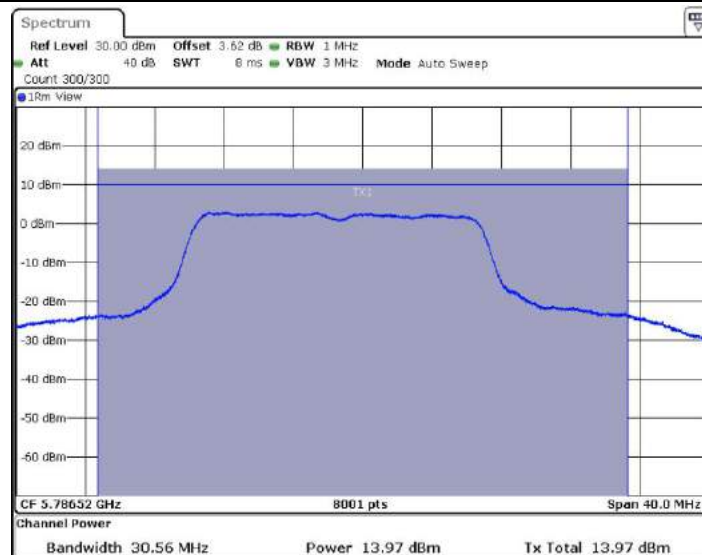
11A_Ant1_5745



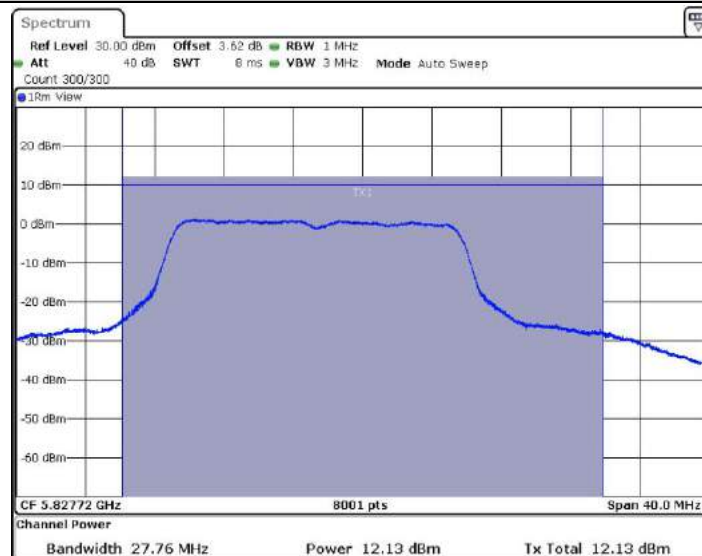
11A_Ant2_5745



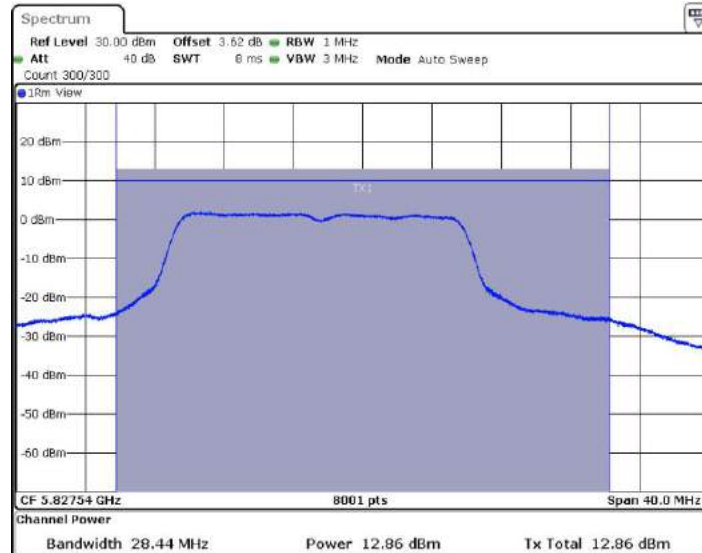
11A_Ant1_5785



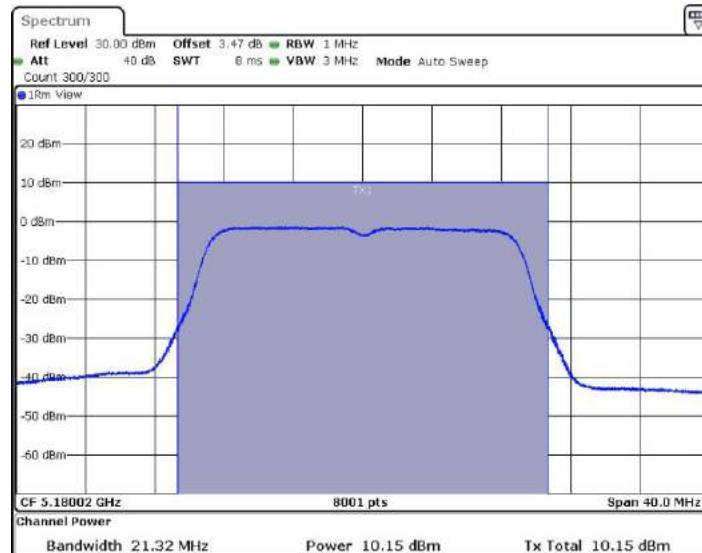
11A_Ant2_5785



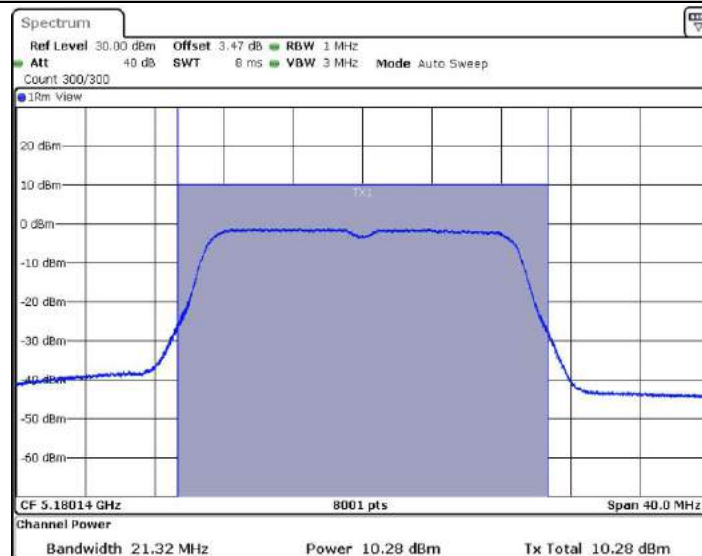
11A_Ant1_5825



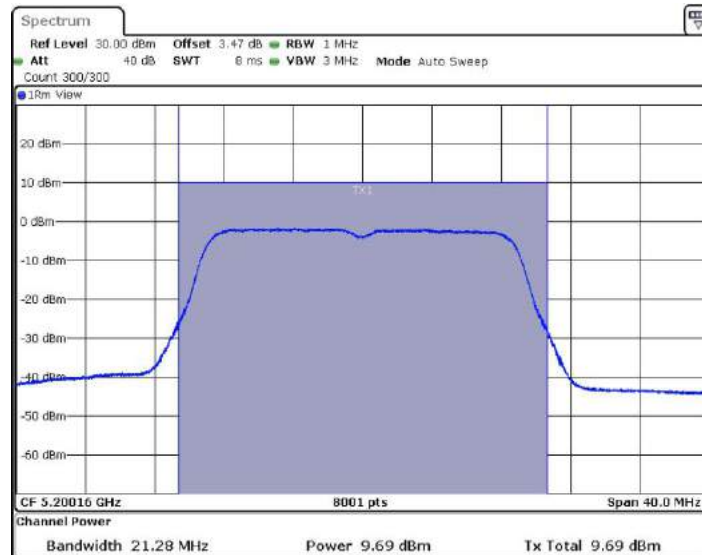
11A_Ant2_5825



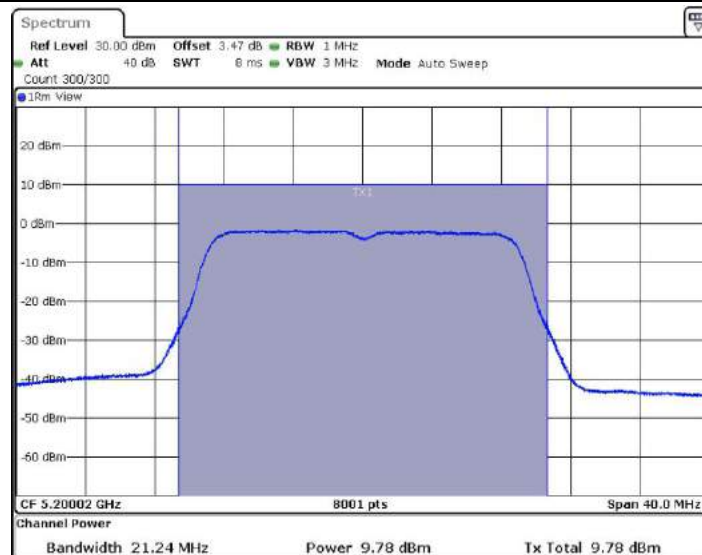
11N20SISO_Ant1_5180



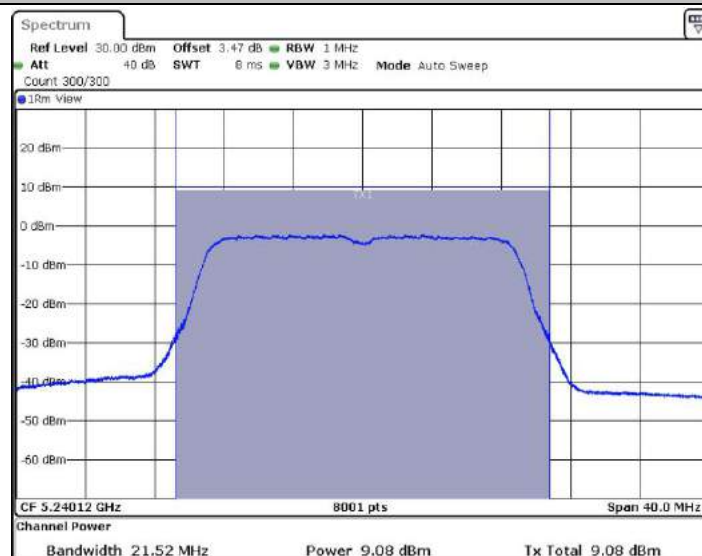
11N20SISO_Ant2_5180



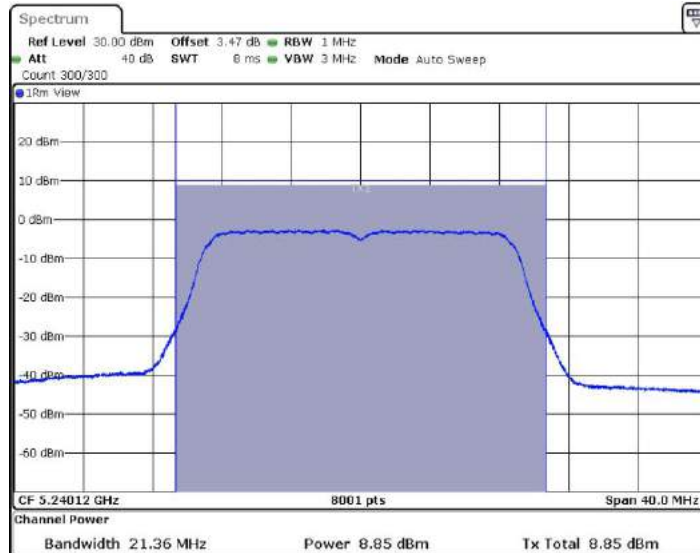
11N20SISO_Ant1_5200



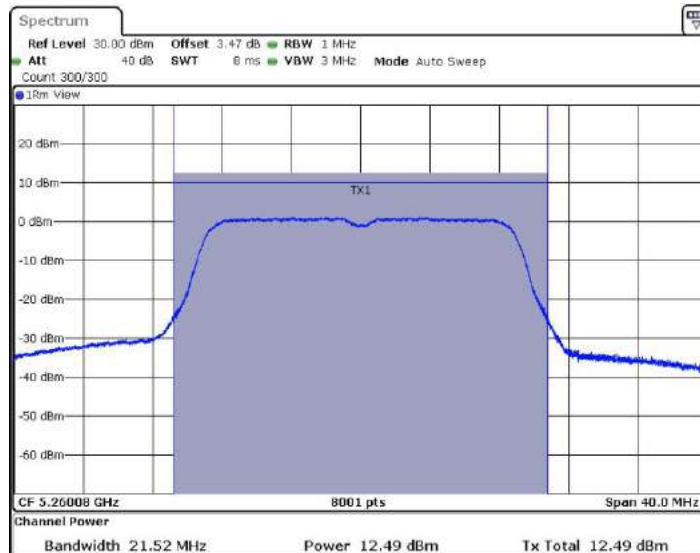
11N20SISO_Ant2_5200



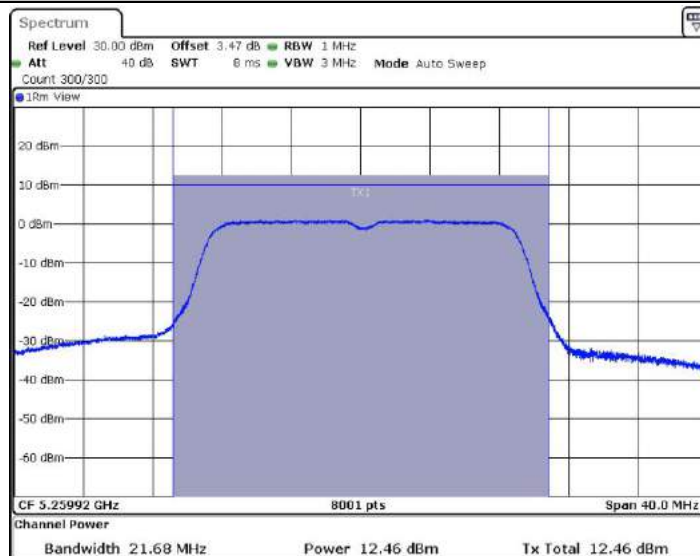
11N20SISO_Ant1_5240



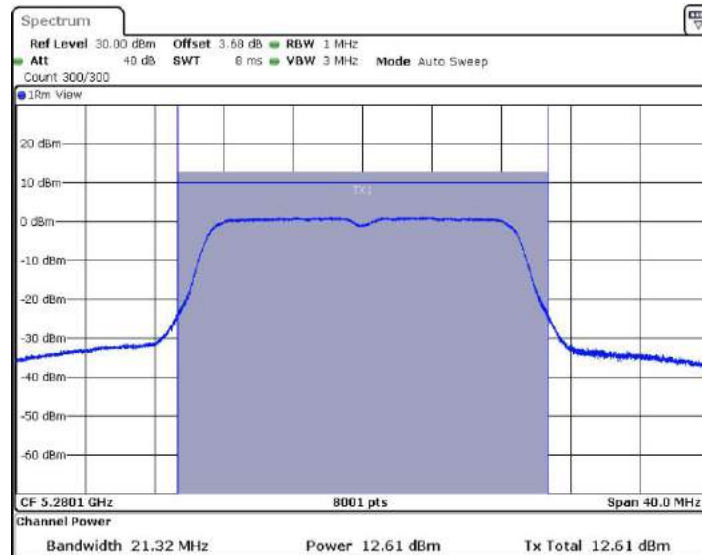
11N20SISO_Ant2_5240



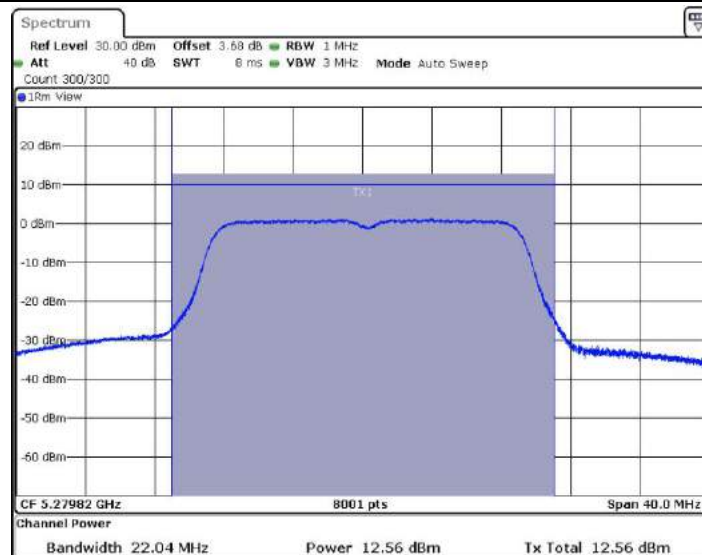
11N20SISO_Ant1_5260



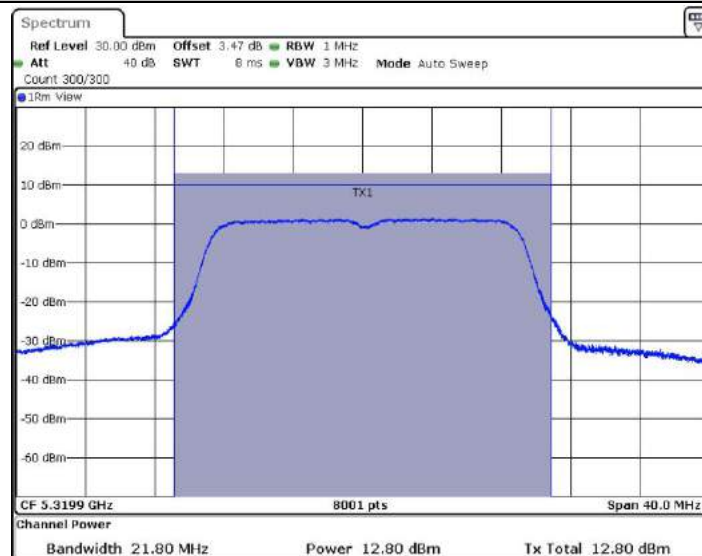
11N20SISO_Ant2_5260



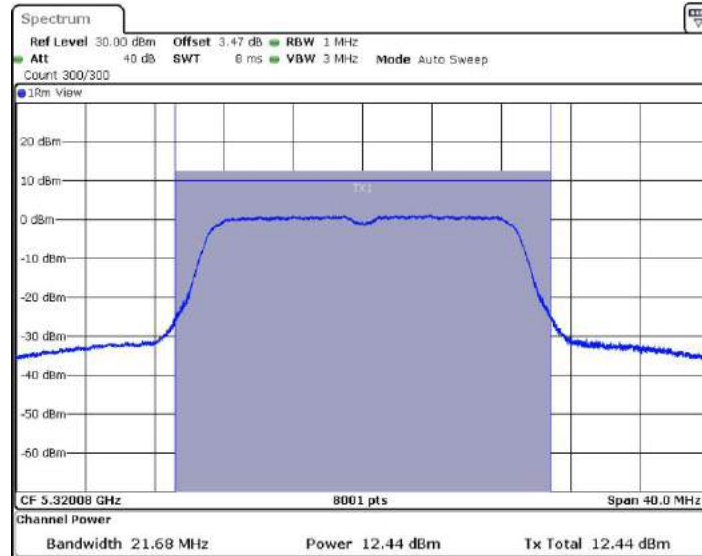
11N20SISO_Ant1_5280



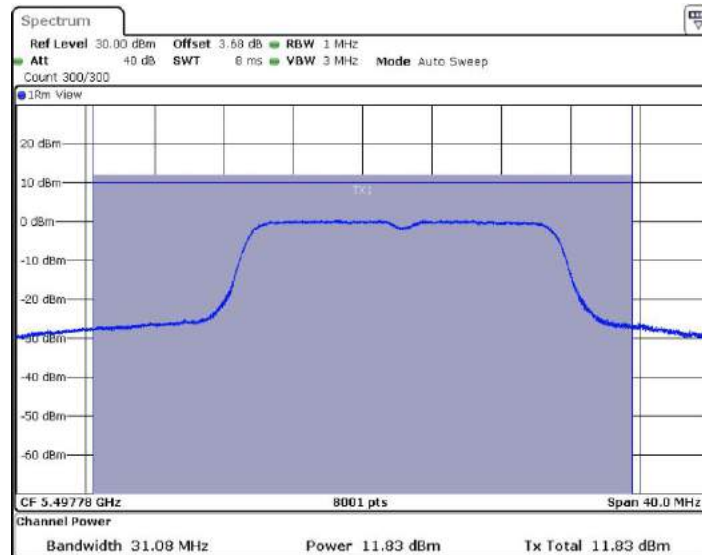
11N20SISO_Ant2_5280



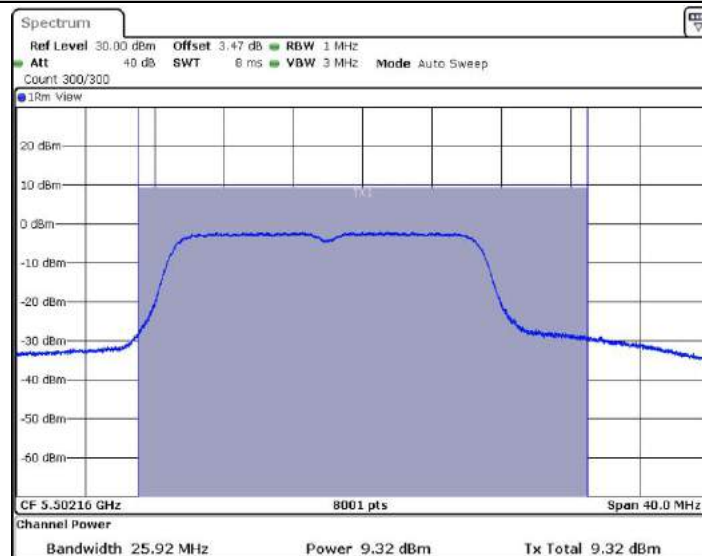
11N20SISO_Ant1_5320



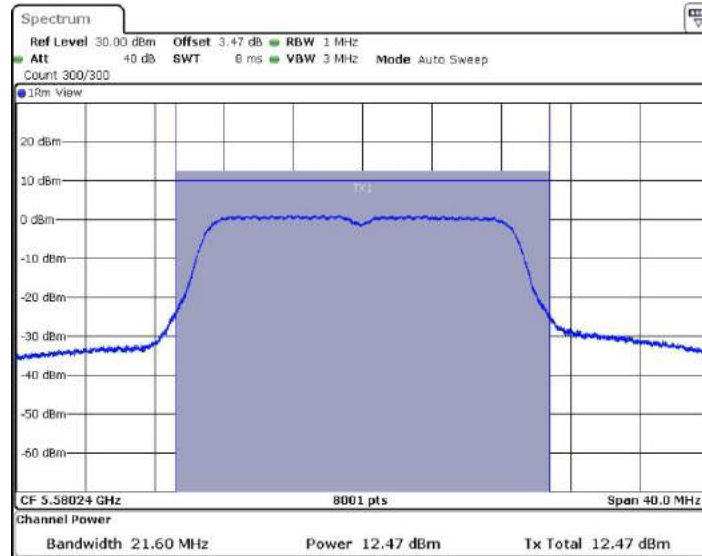
11N20SISO_Ant2_5320



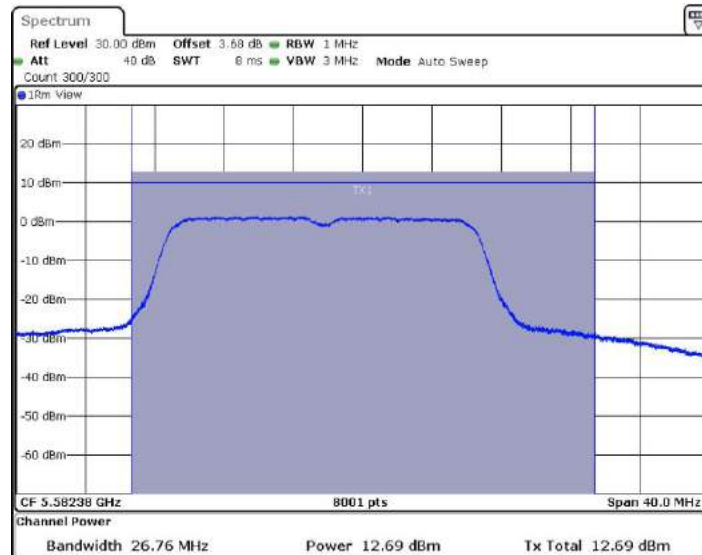
11N20SISO_Ant1_5500



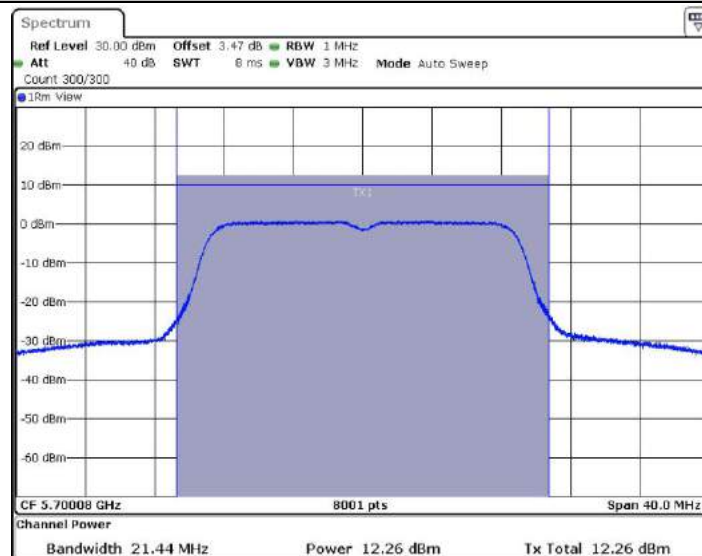
11N20SISO_Ant2_5500



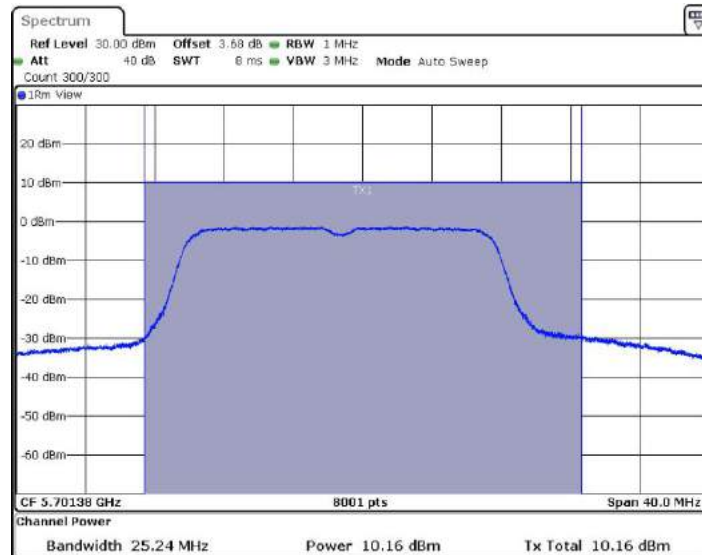
11N20SISO_Ant1_5580



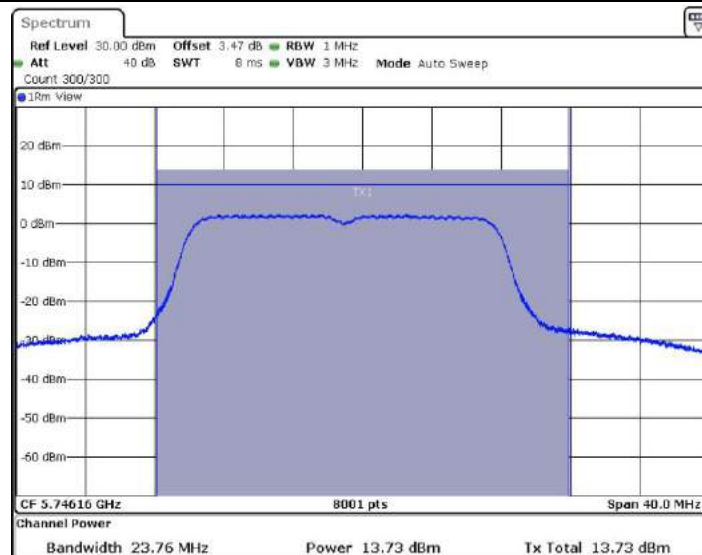
11N20SISO_Ant2_5580



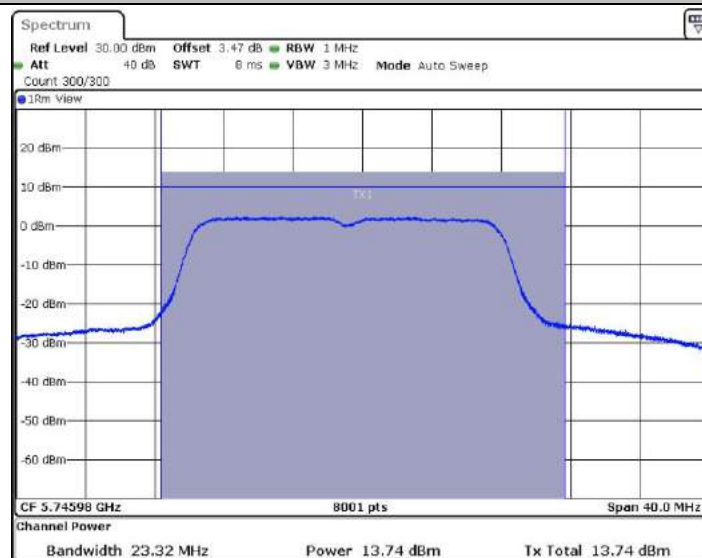
11N20SISO_Ant1_5700



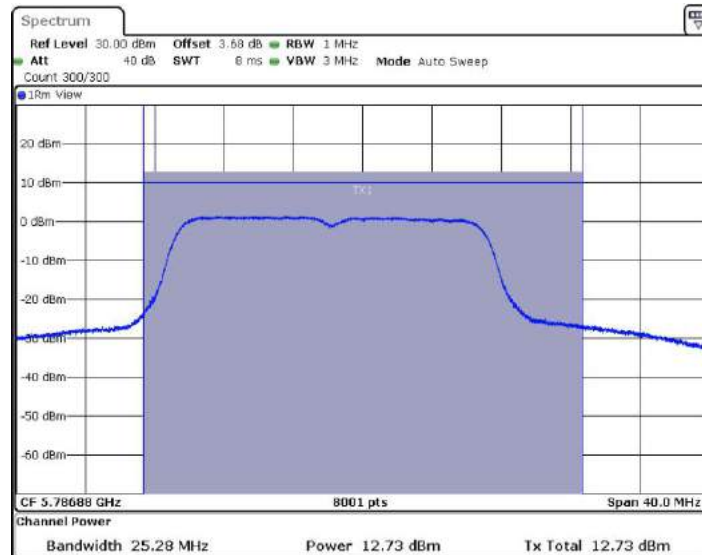
11N20SISO_Ant2_5700



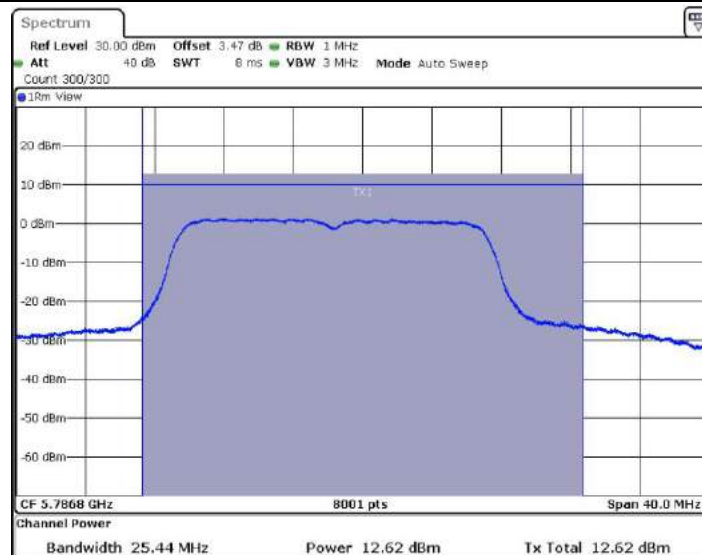
11N20SISO_Ant1_5745



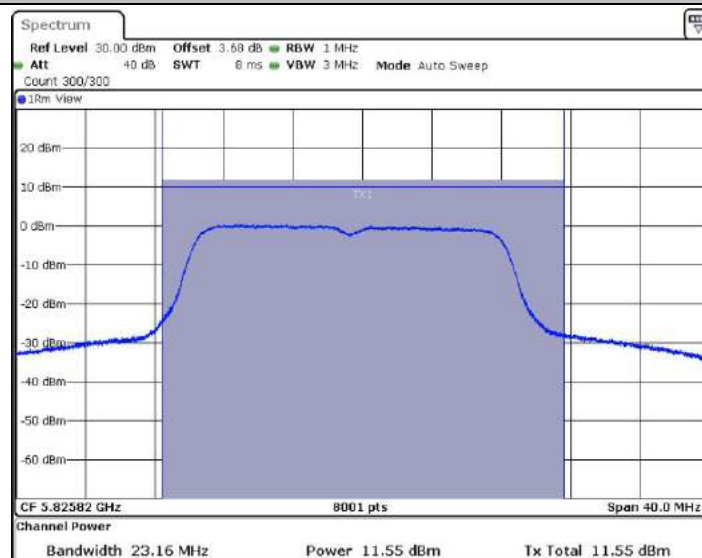
11N20SISO_Ant2_5745



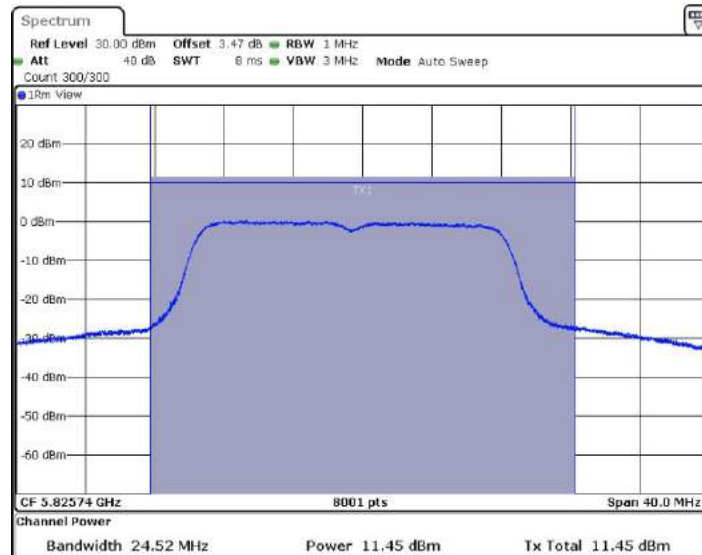
11N20SISO_Ant1_5785



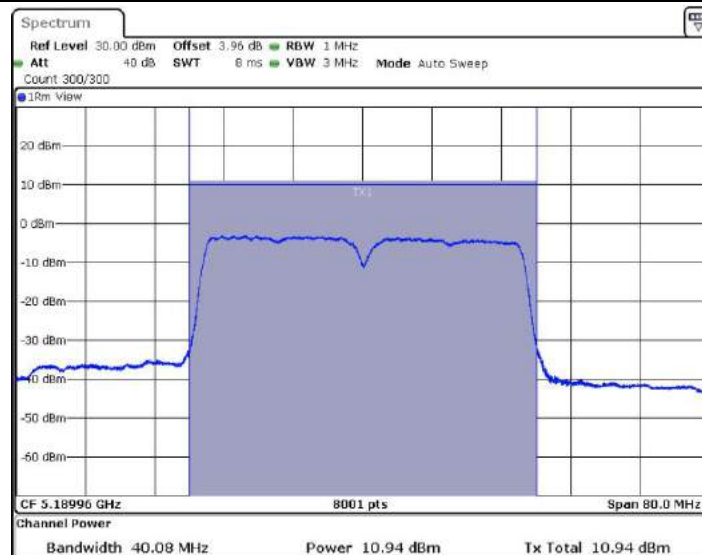
11N20SISO_Ant2_5785



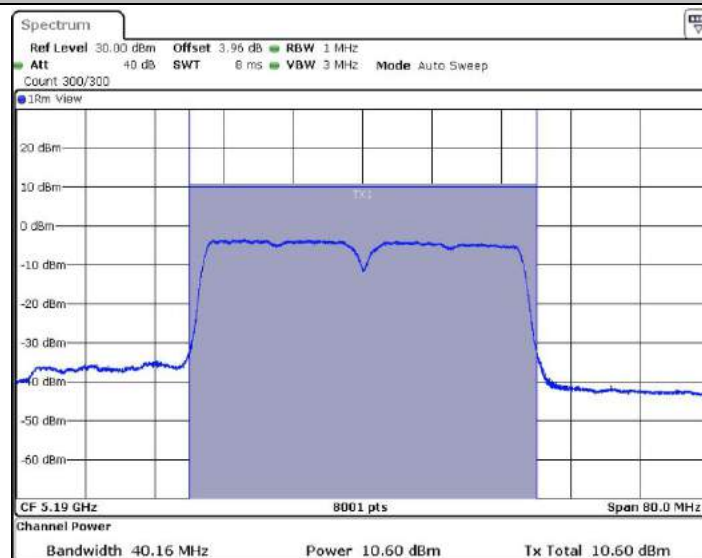
11N20SISO_Ant1_5825



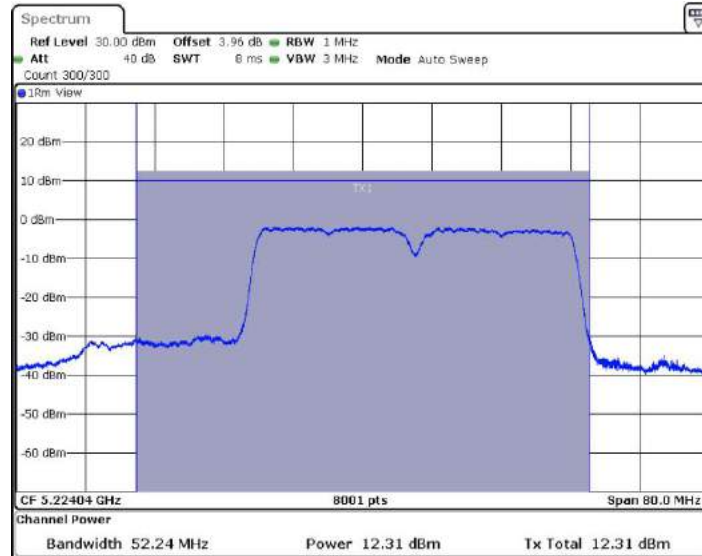
11N20SISO_Ant2_5825



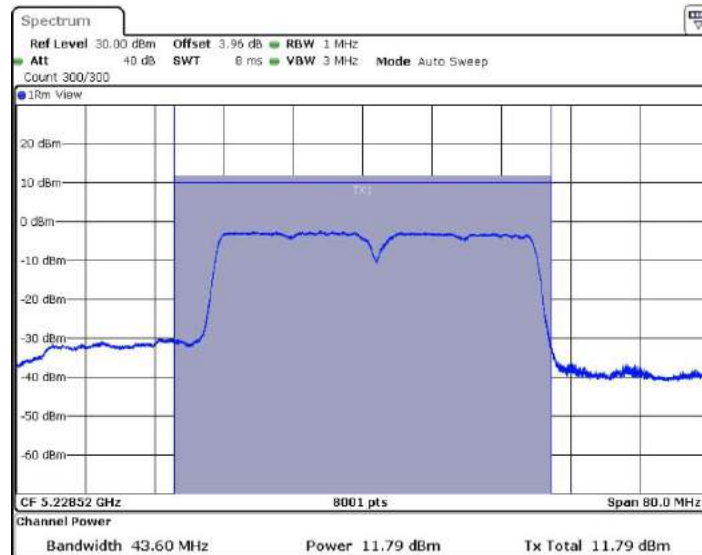
11N40SISO_Ant1_5190



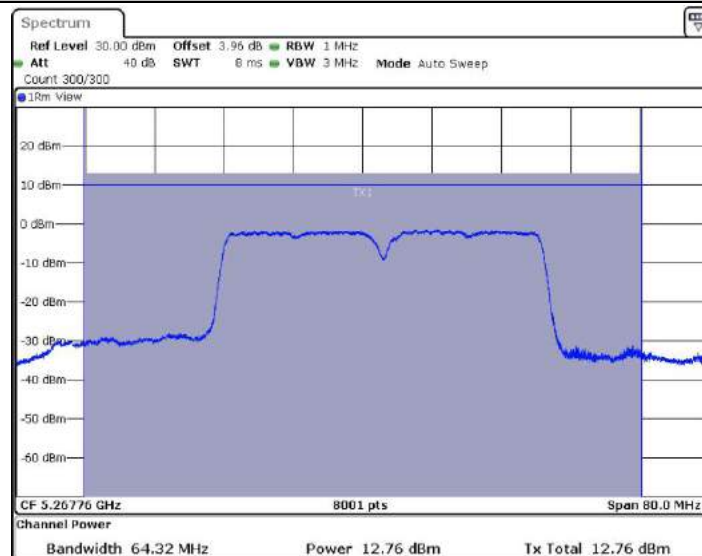
11N40SISO_Ant2_5190



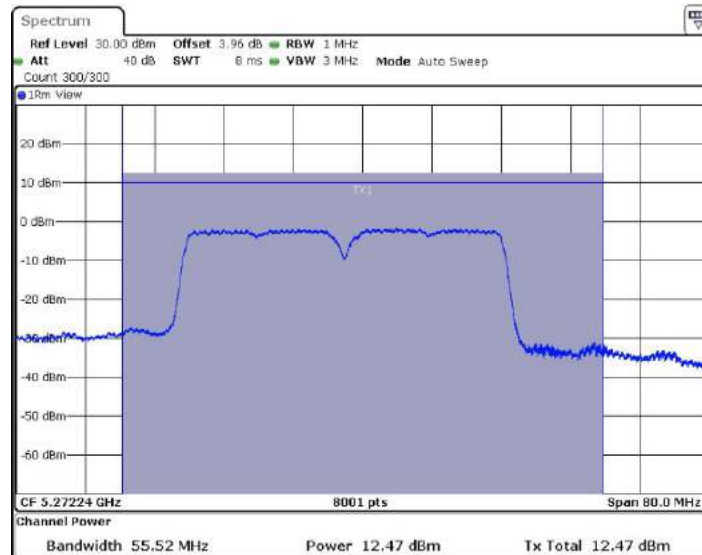
11N40SISO_Ant1_5230



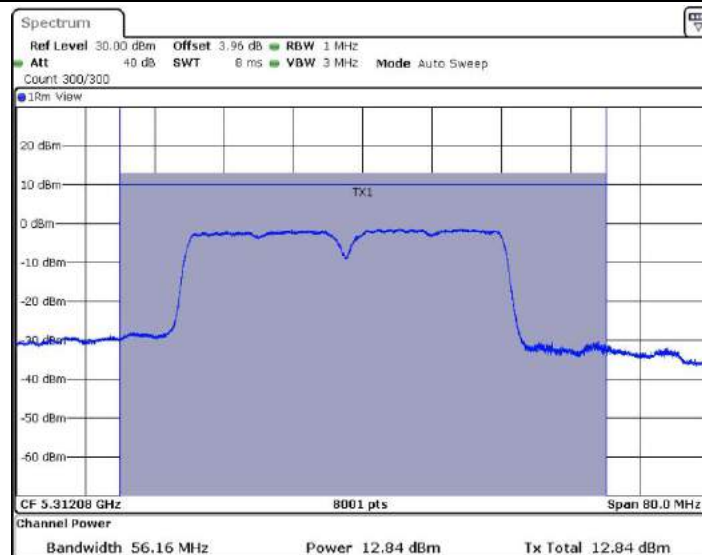
11N40SISO_Ant2_5230



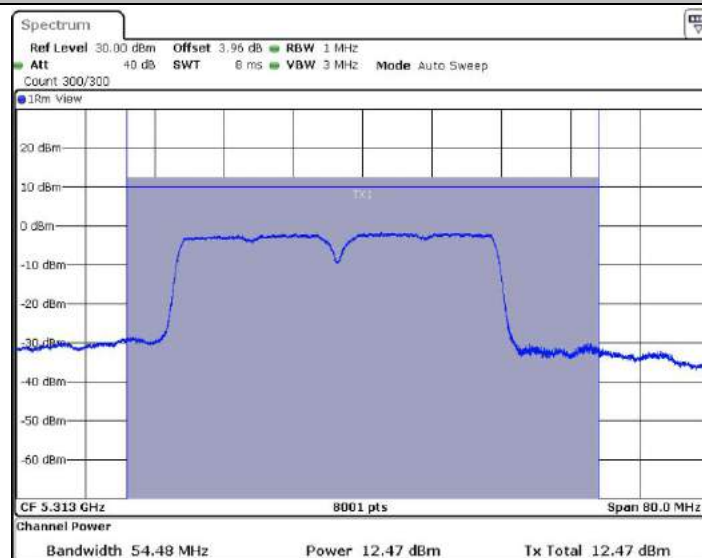
11N40SISO_Ant1_5270



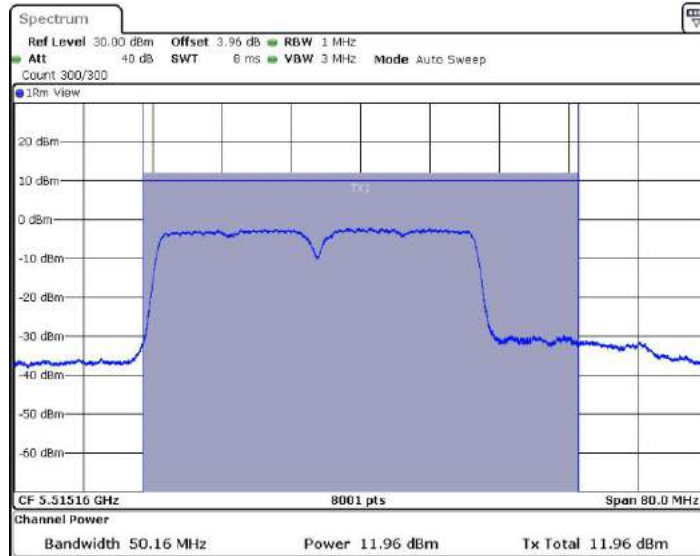
11N40SISO_Ant2_5270



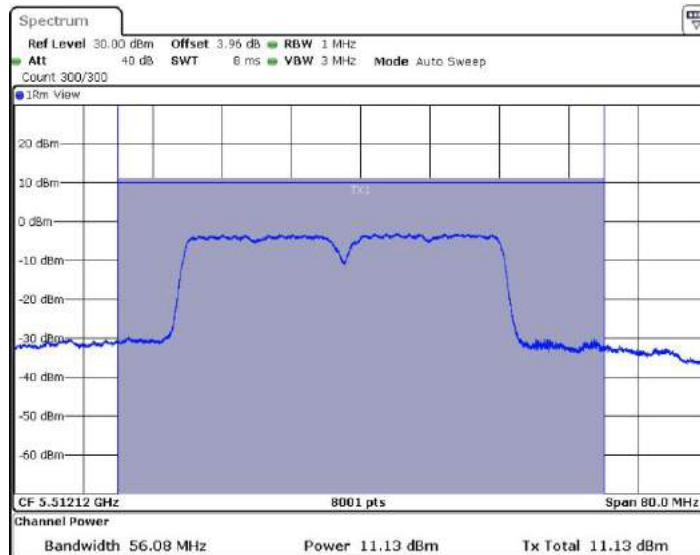
11N40SISO_Ant1_5310



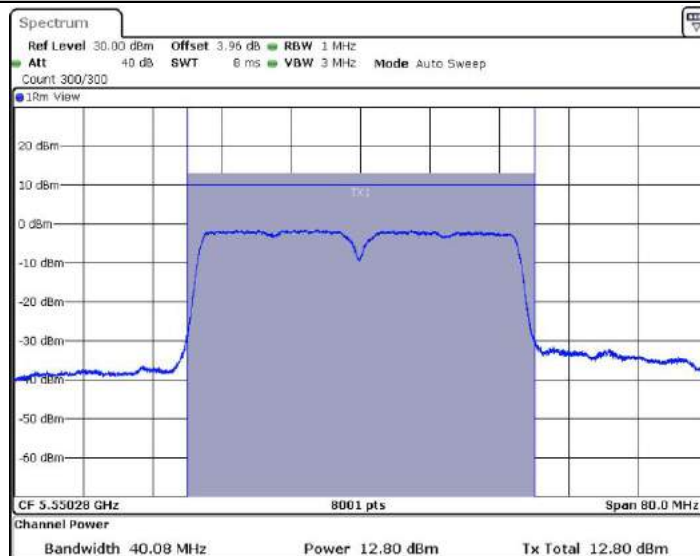
11N40SISO_Ant2_5310



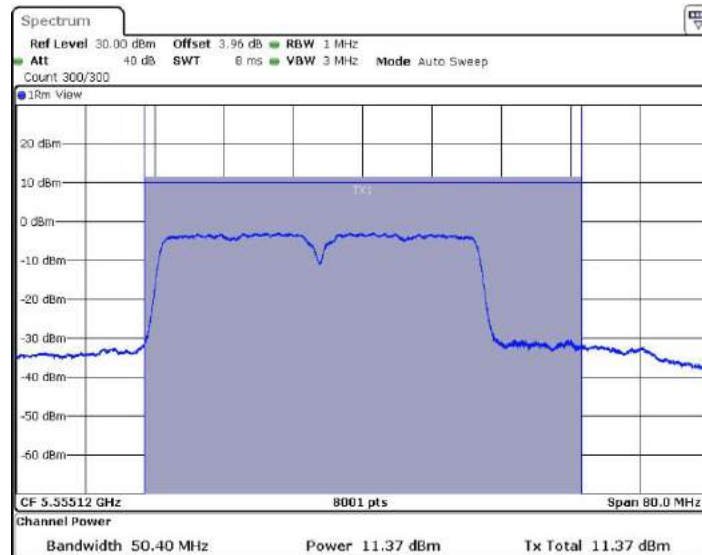
11N40SISO_Ant1_5510



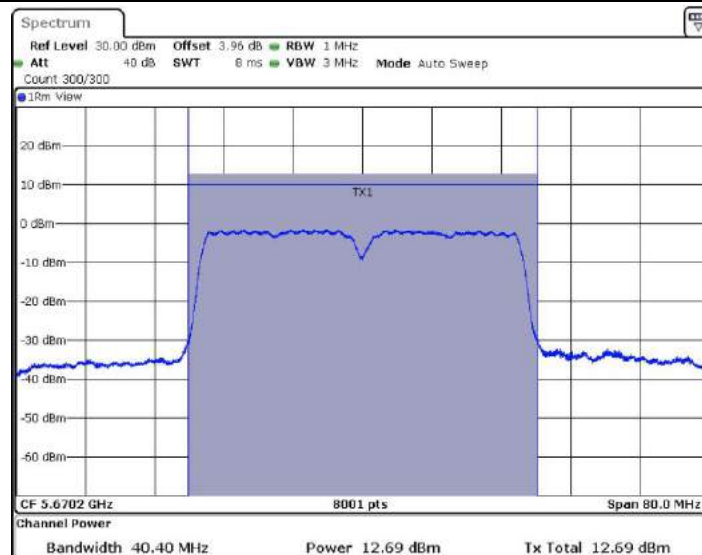
11N40SISO_Ant2_5510



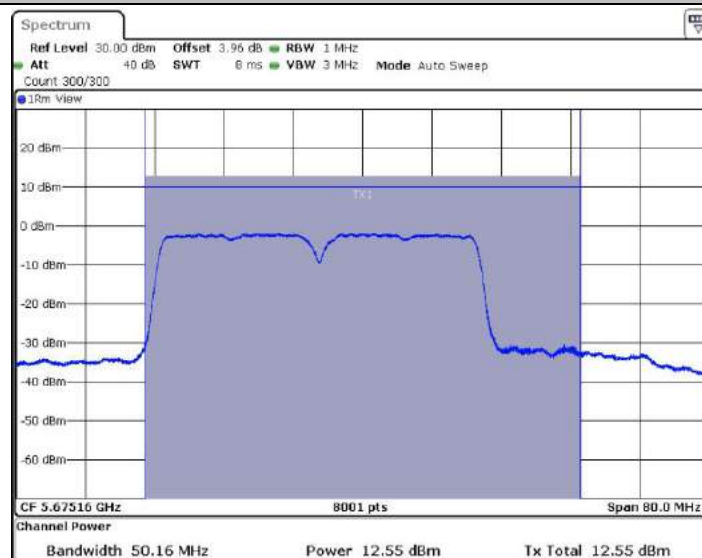
11N40SISO_Ant1_5550



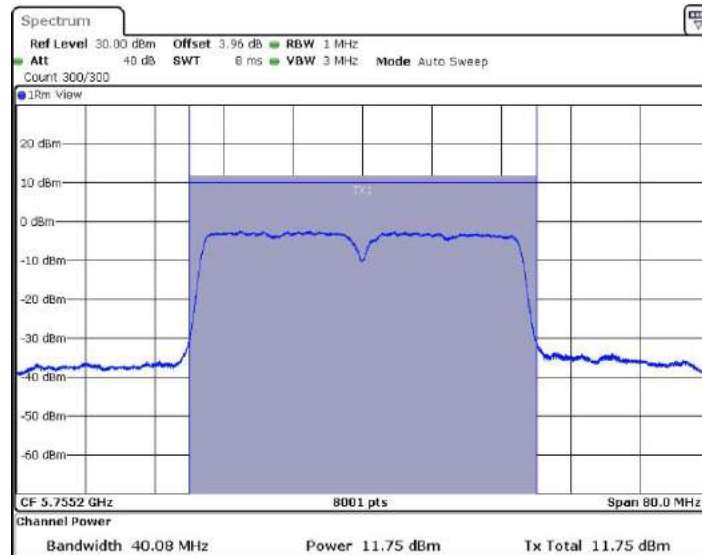
11N40SISO_Ant2_5550



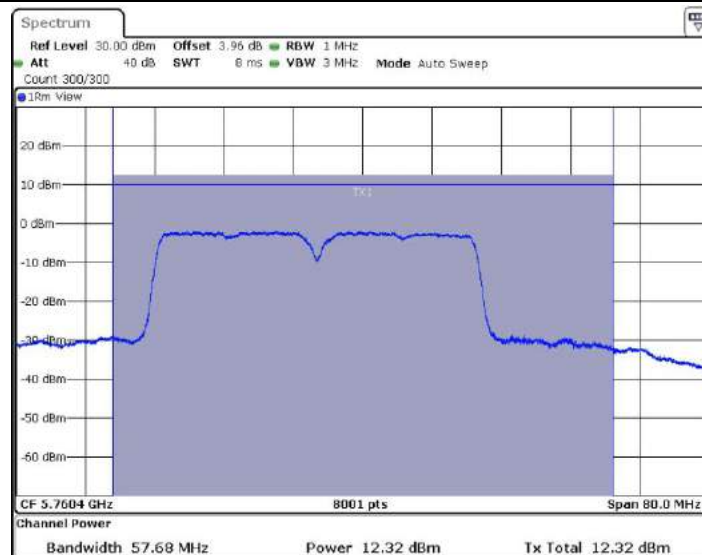
11N40SISO_Ant1_5670



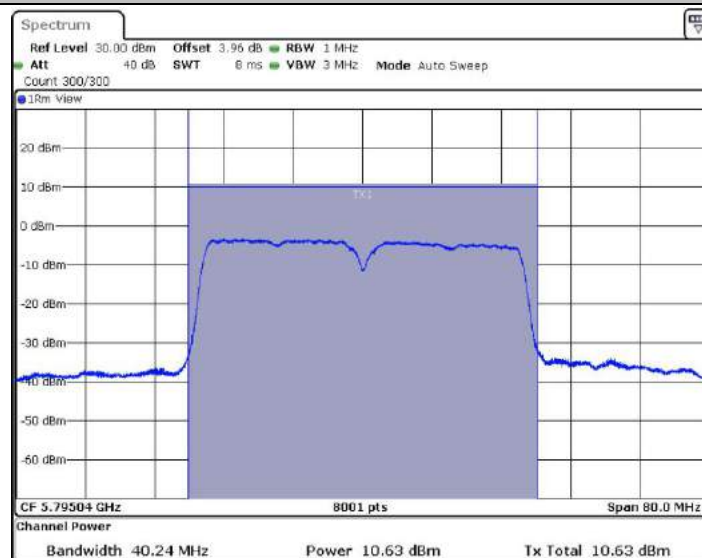
11N40SISO_Ant2_5670



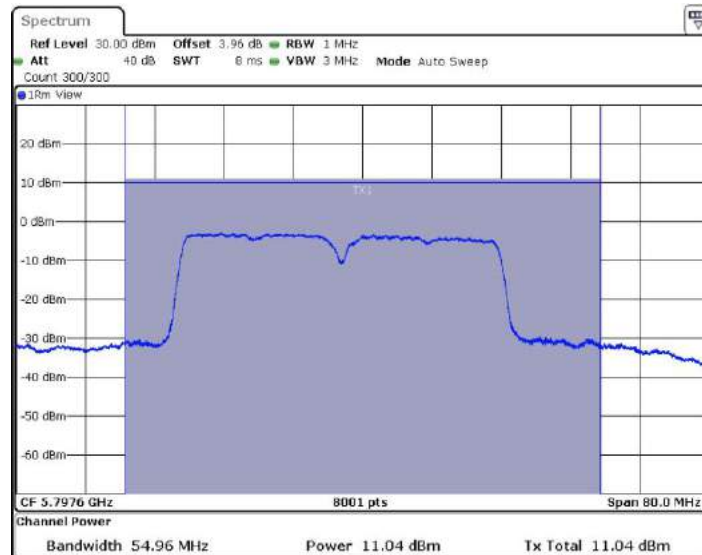
11N40SISO_Ant1_5755



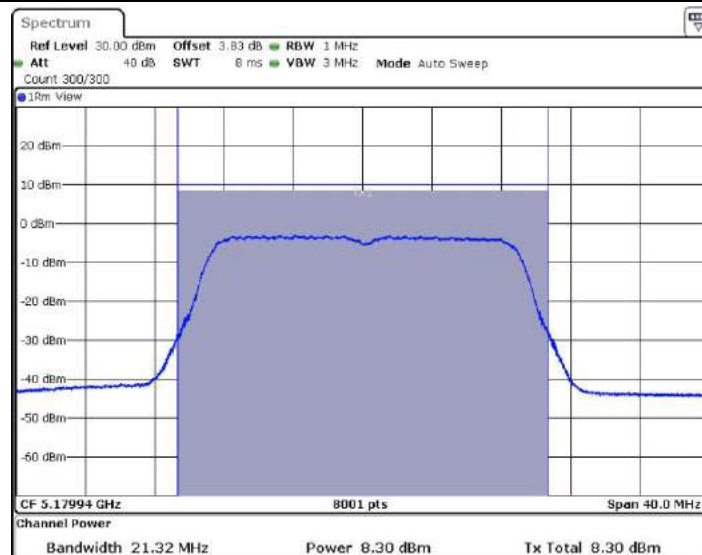
11N40SISO_Ant2_5755



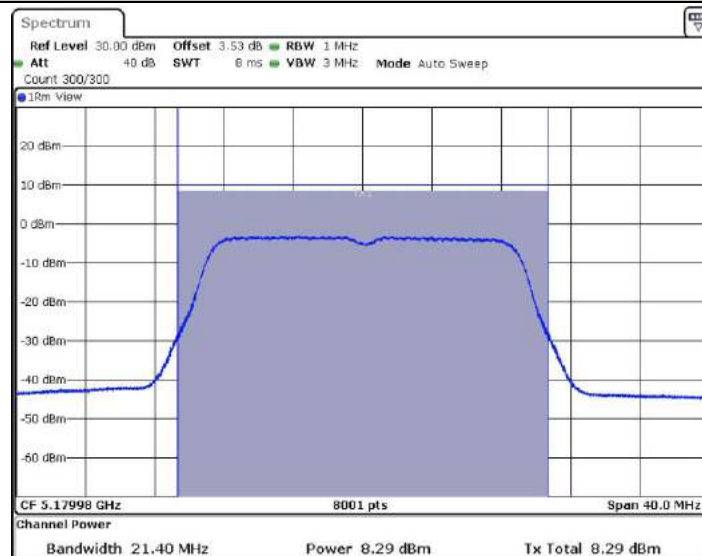
11N40SISO_Ant1_5795



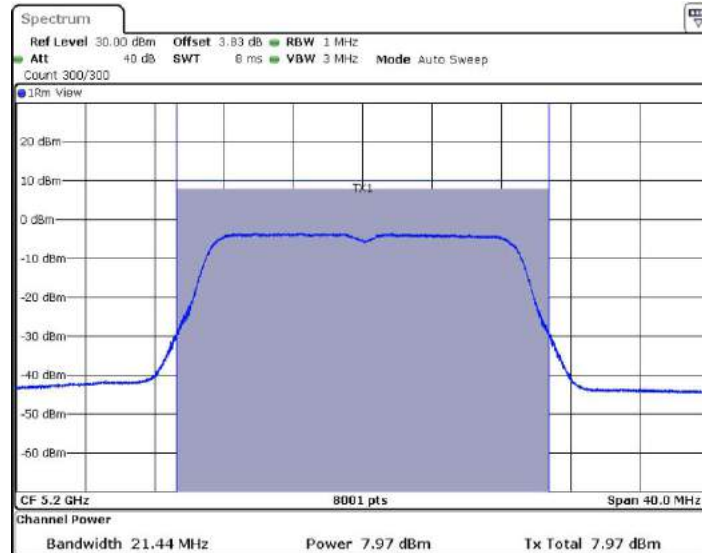
11N40SISO_Ant2_5795



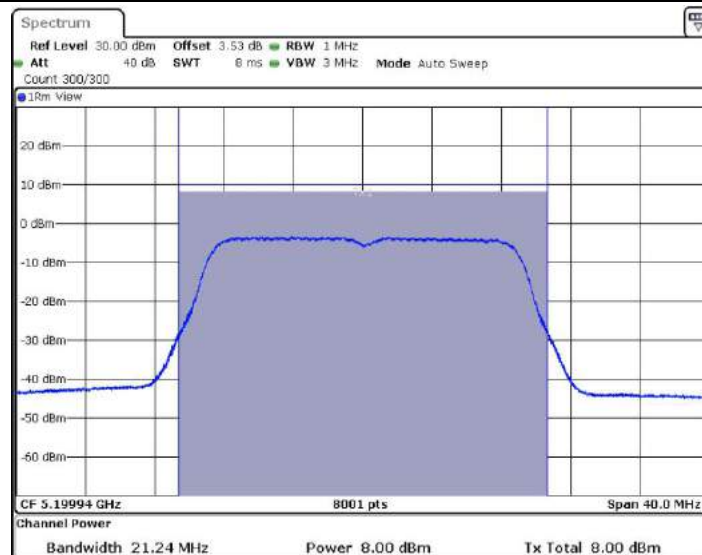
11AC20SISO_Ant1_5180



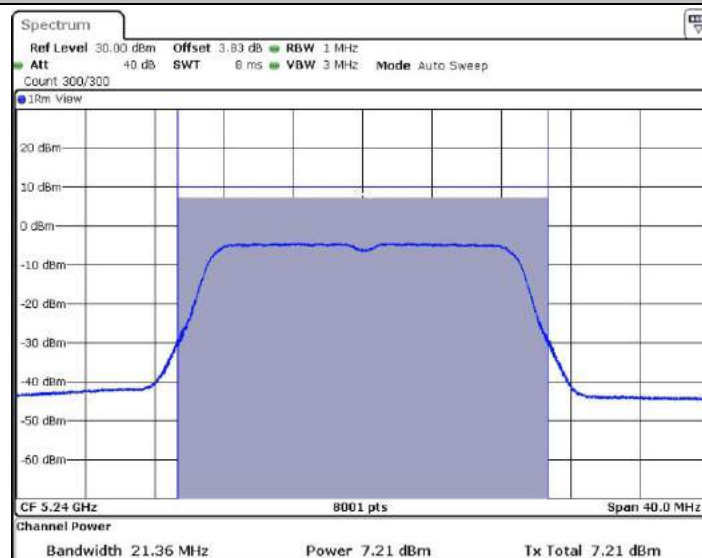
11AC20SISO_Ant2_5180



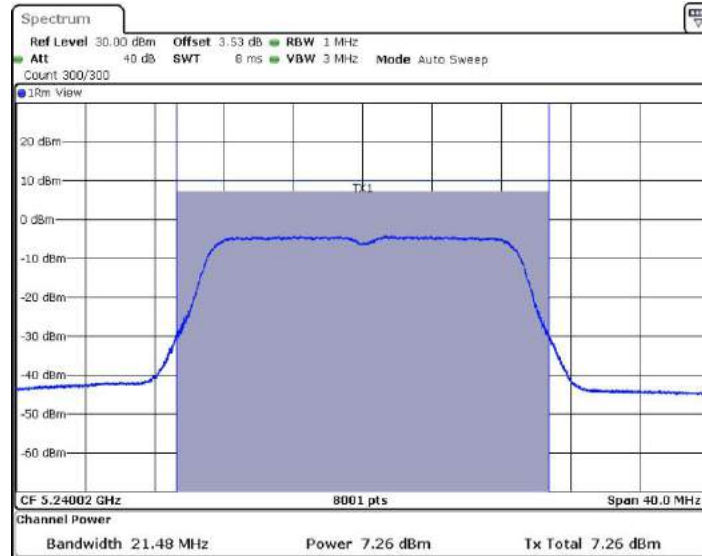
11AC20SISO_Ant1_5200



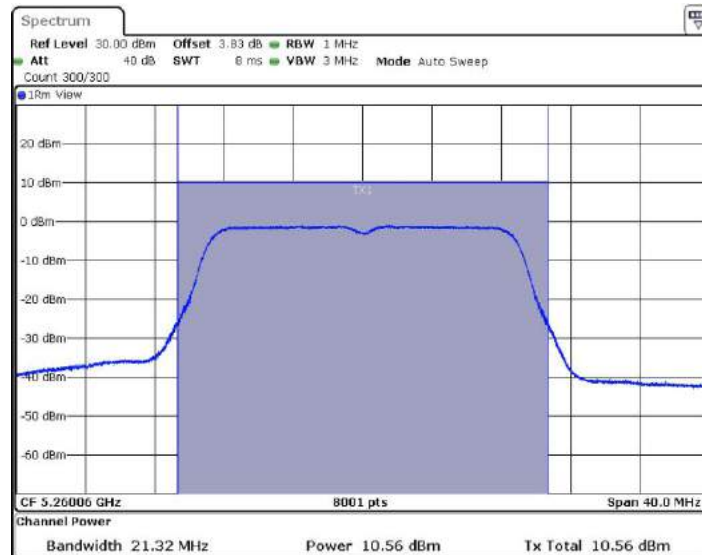
11AC20SISO_Ant2_5200



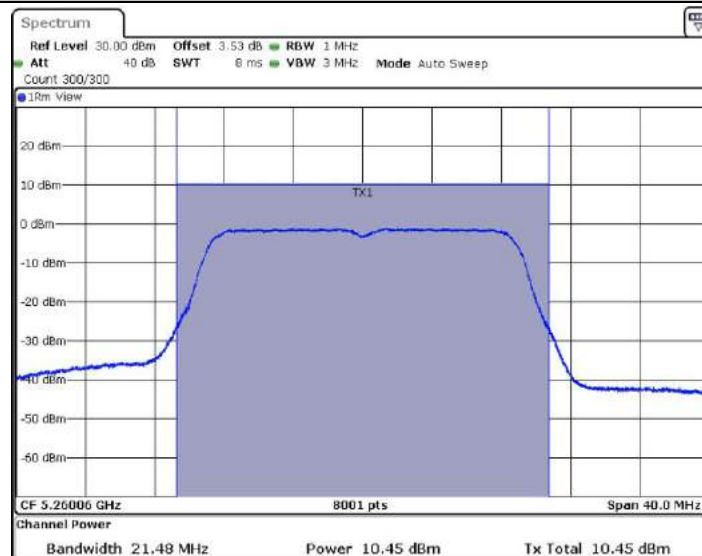
11AC20SISO_Ant1_5240



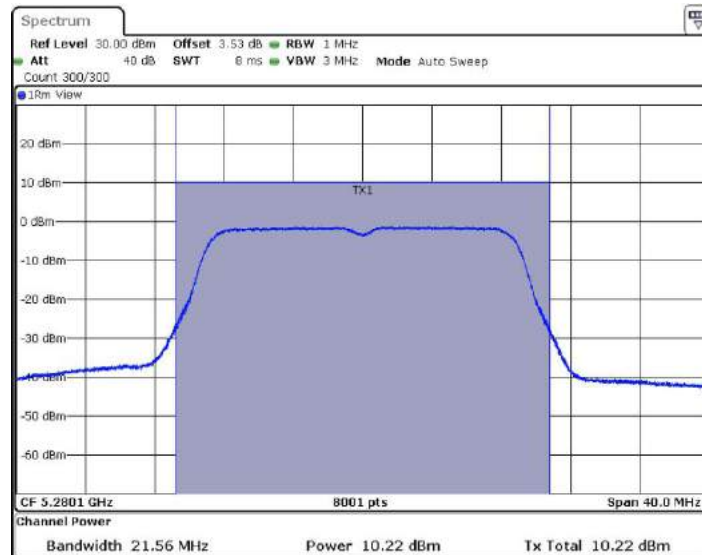
11AC20SISO_Ant2_5240



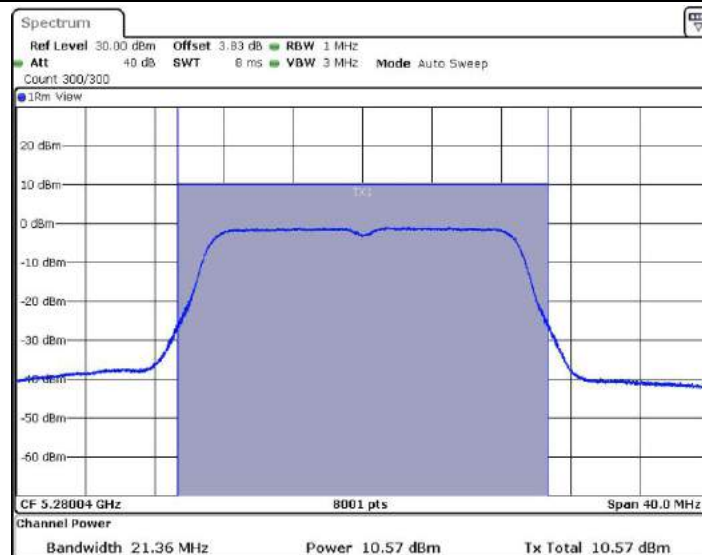
11AC20SISO_Ant1_5260



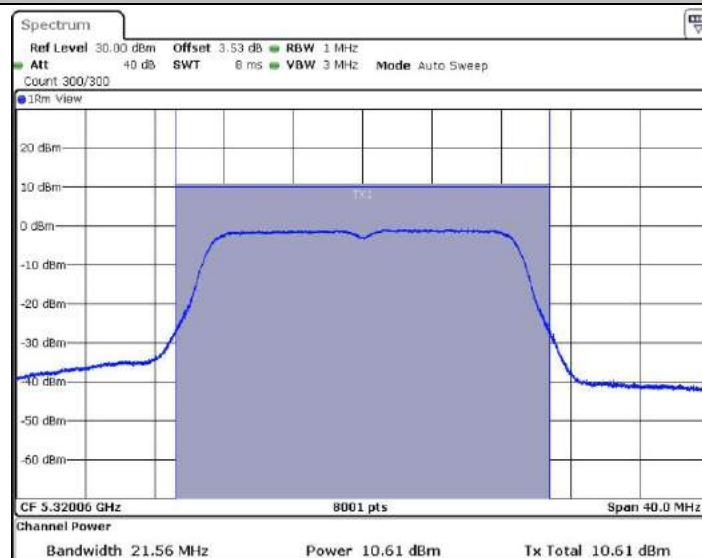
11AC20SISO_Ant2_5260



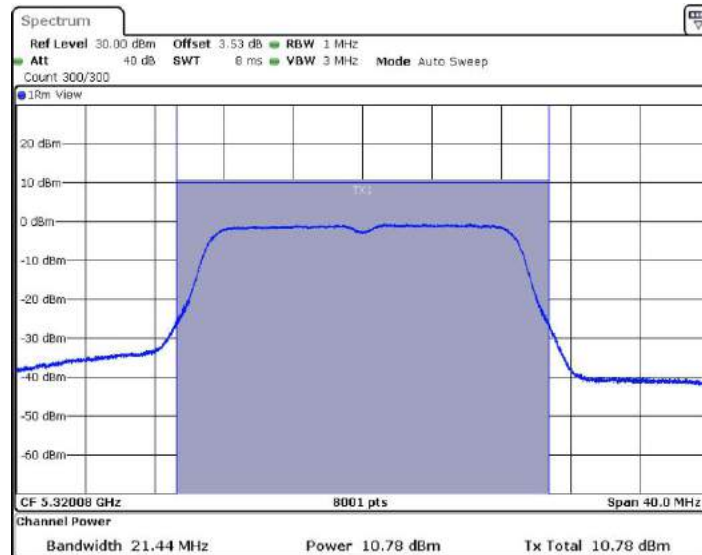
11AC20SISO_Ant1_5280



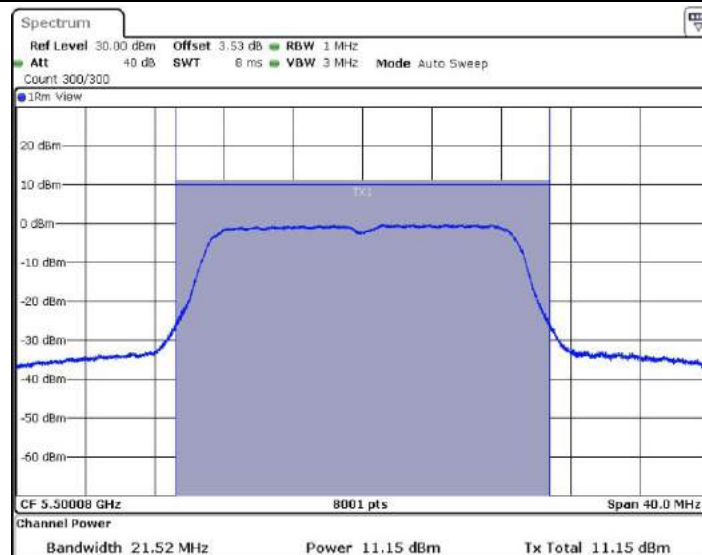
11AC20SISO_Ant2_5280



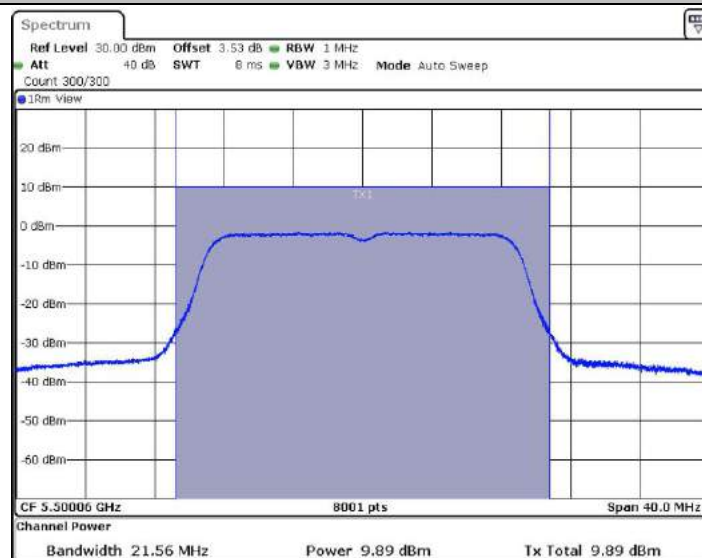
11AC20SISO_Ant1_5320



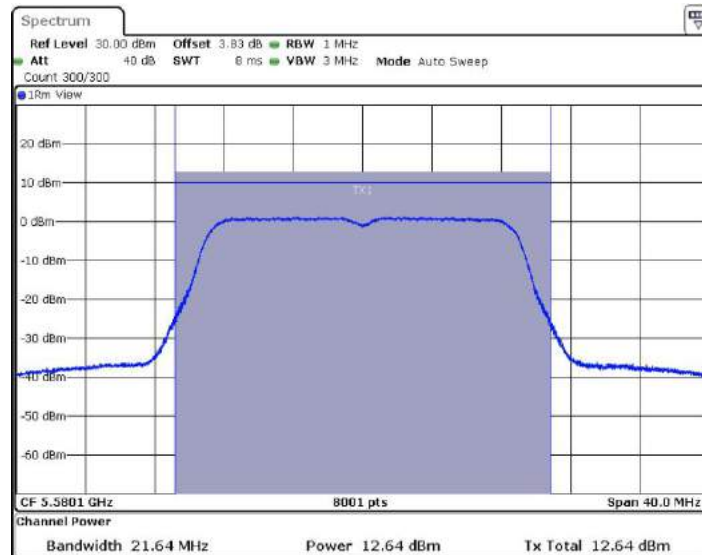
11AC20SISO_Ant2_5320



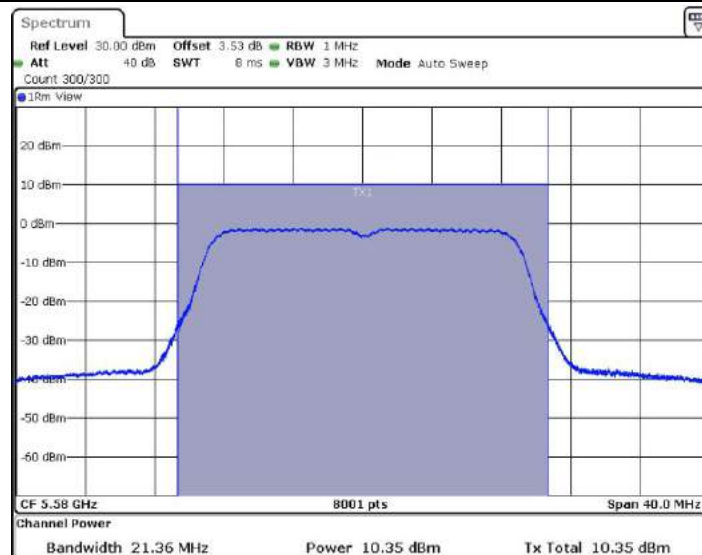
11AC20SISO_Ant1_5500



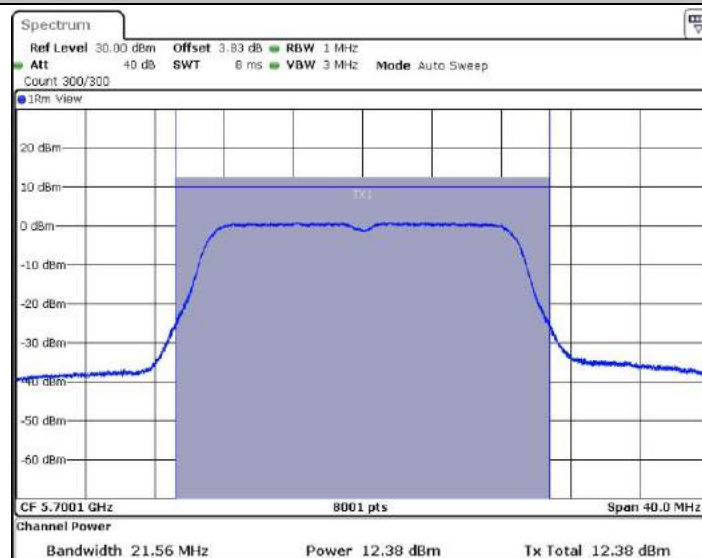
11AC20SISO_Ant2_5500



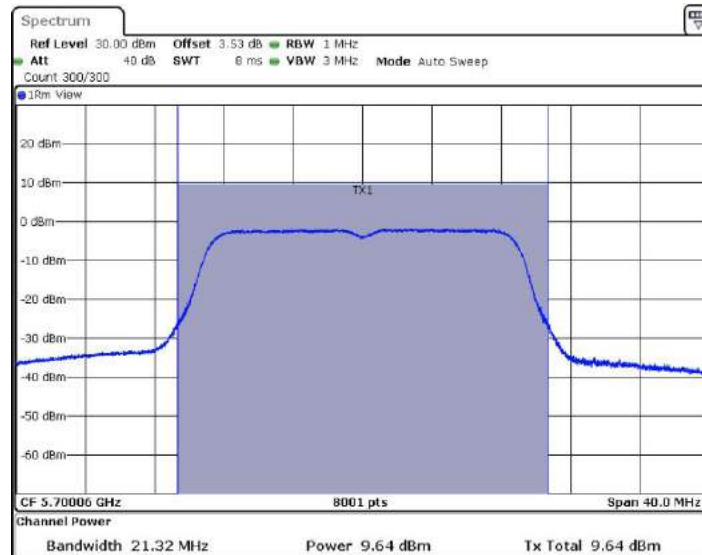
11AC20SISO_Ant1_5580



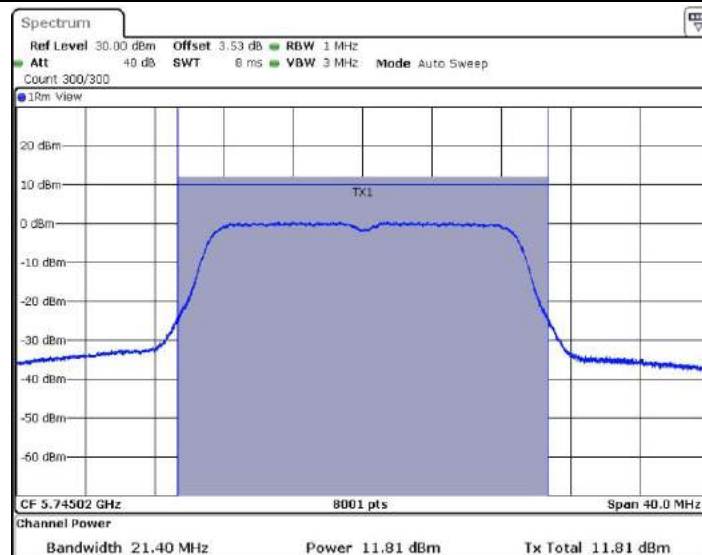
11AC20SISO_Ant2_5580



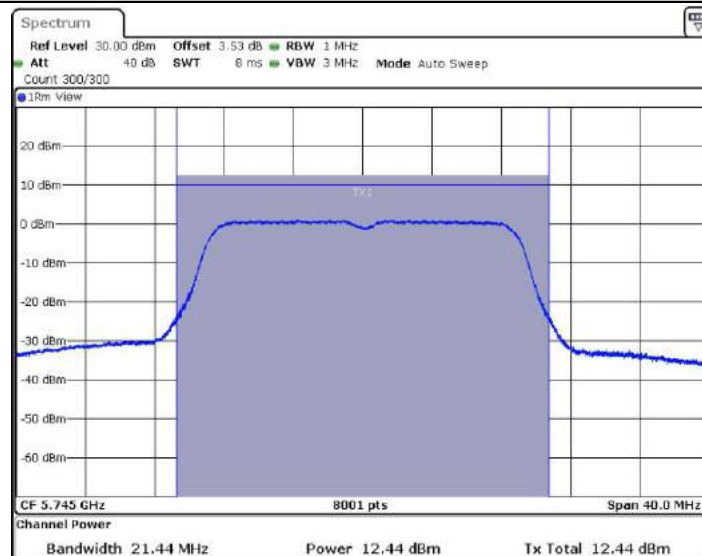
11AC20SISO_Ant1_5700



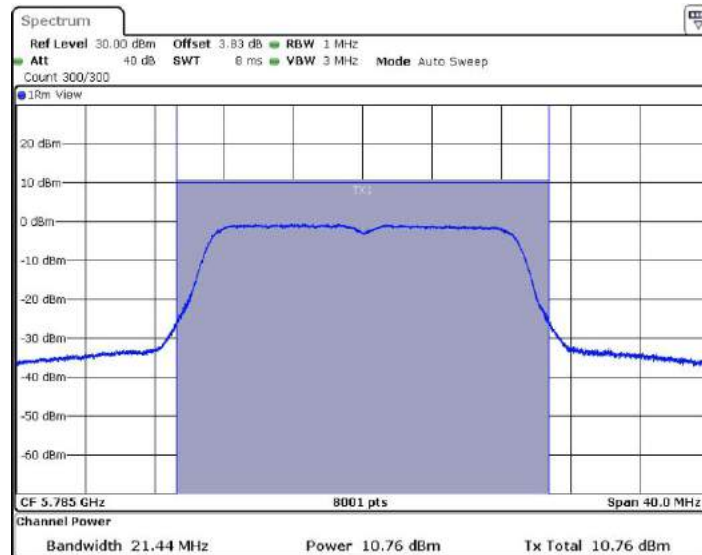
11AC20SISO_Ant2_5700



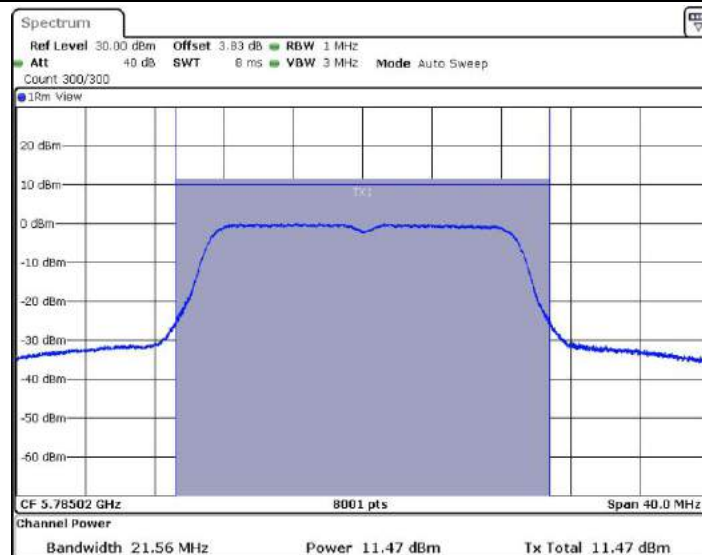
11AC20SISO_Ant1_5745



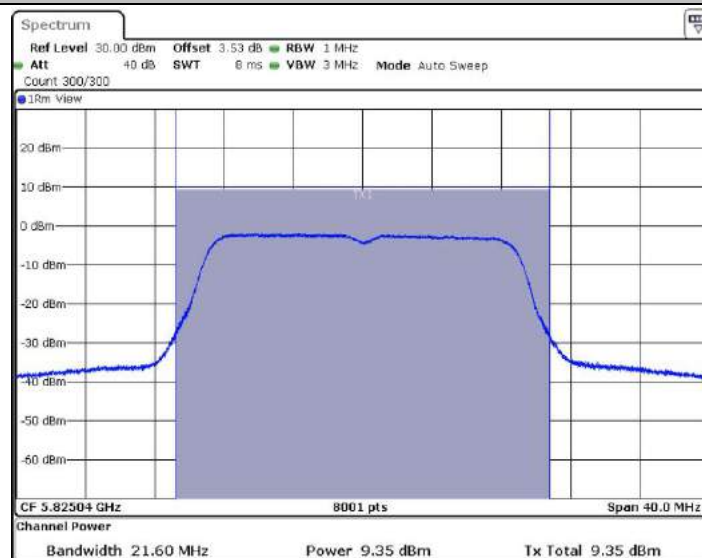
11AC20SISO_Ant2_5745



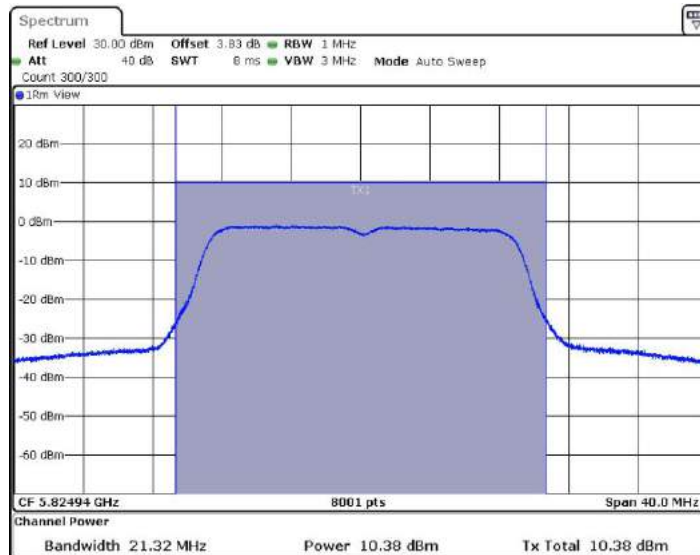
11AC20SISO_Ant1_5785



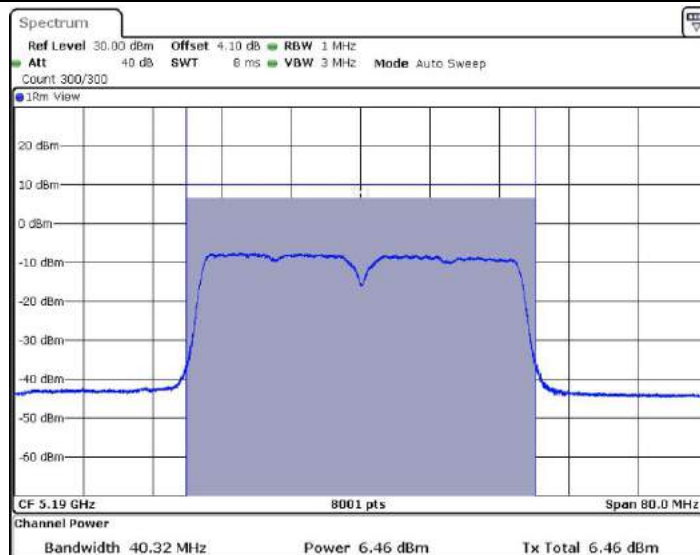
11AC20SISO_Ant2_5785



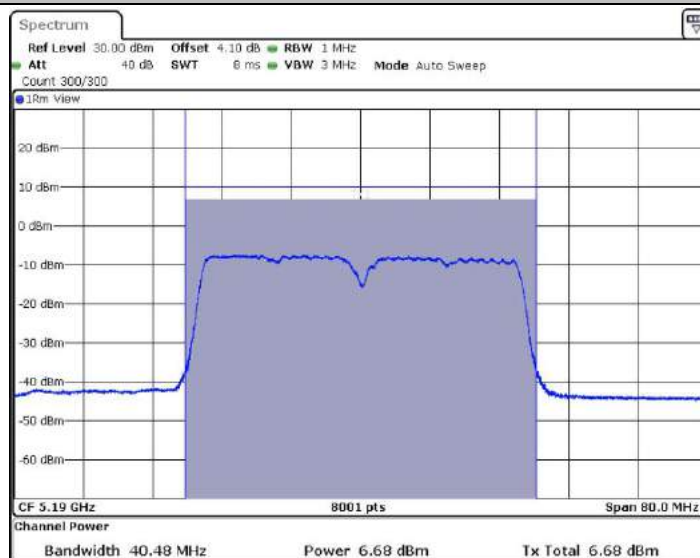
11AC20SISO_Ant1_5825



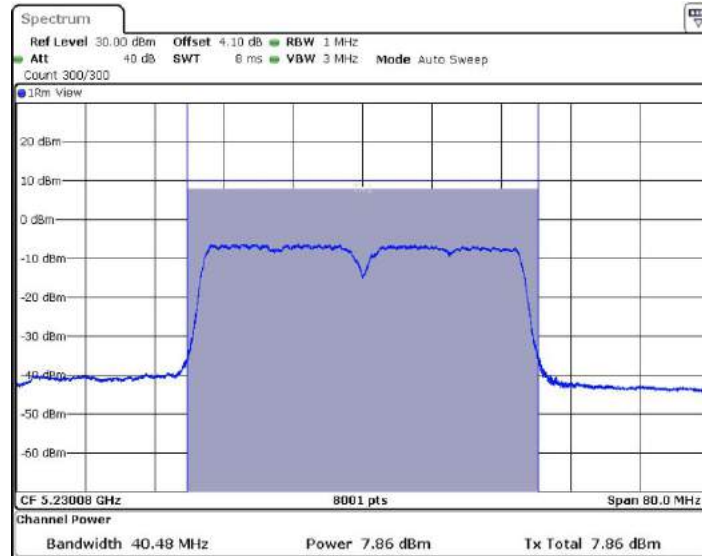
11AC20SISO_Ant2_5825



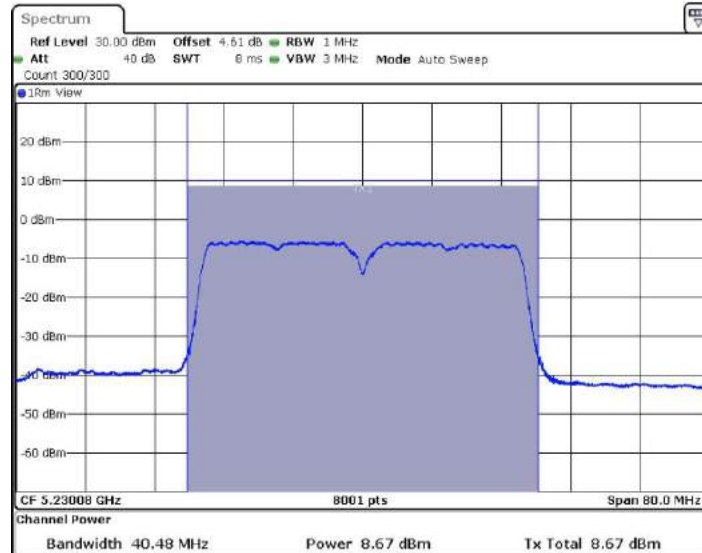
11AC40SISO_Ant1_5190



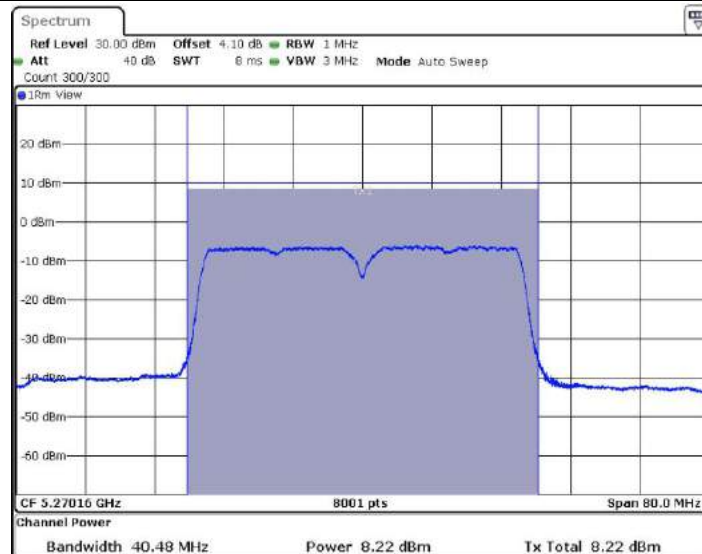
11AC40SISO_Ant2_5190



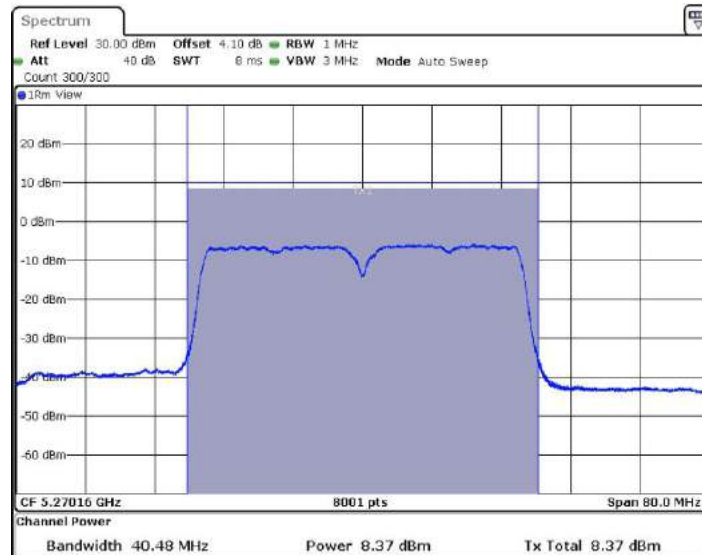
11AC40SISO_Ant1_5230



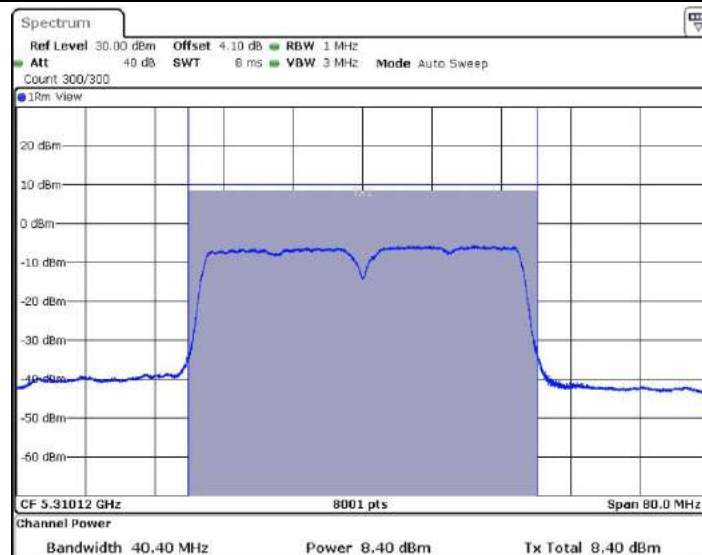
11AC40SISO_Ant2_5230



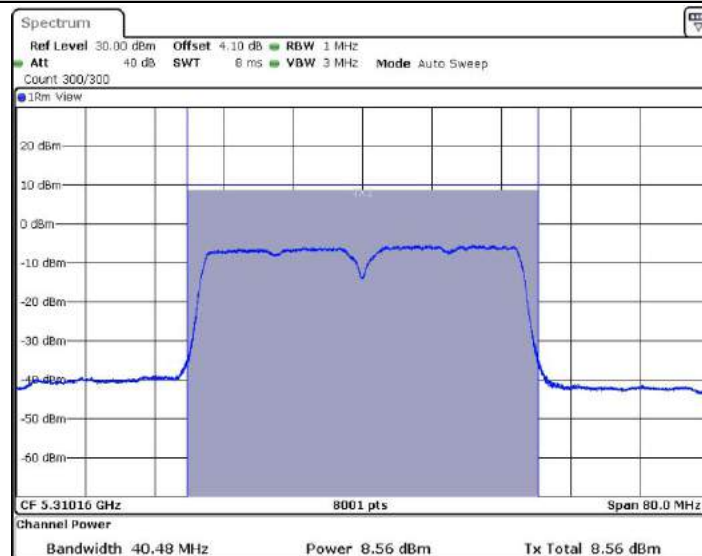
11AC40SISO_Ant1_5270



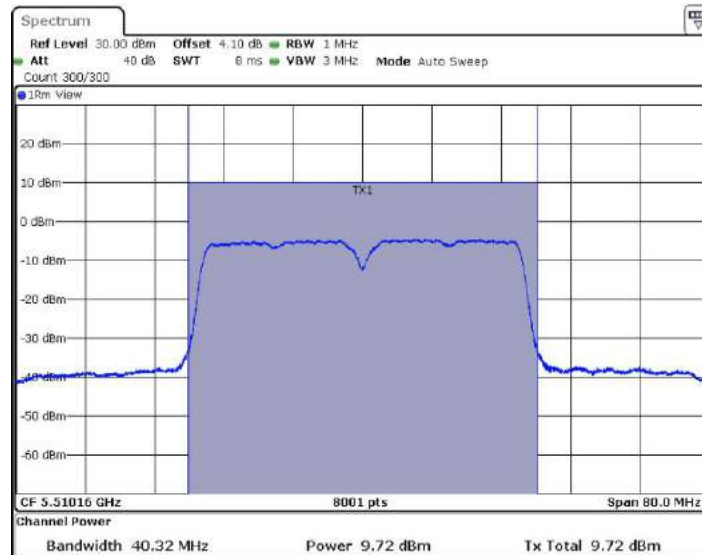
11AC40SISO_Ant2_5270



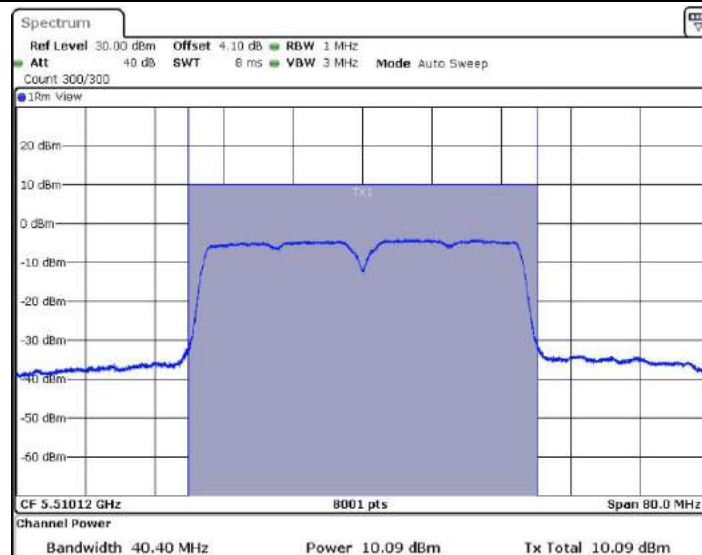
11AC40SISO_Ant1_5310



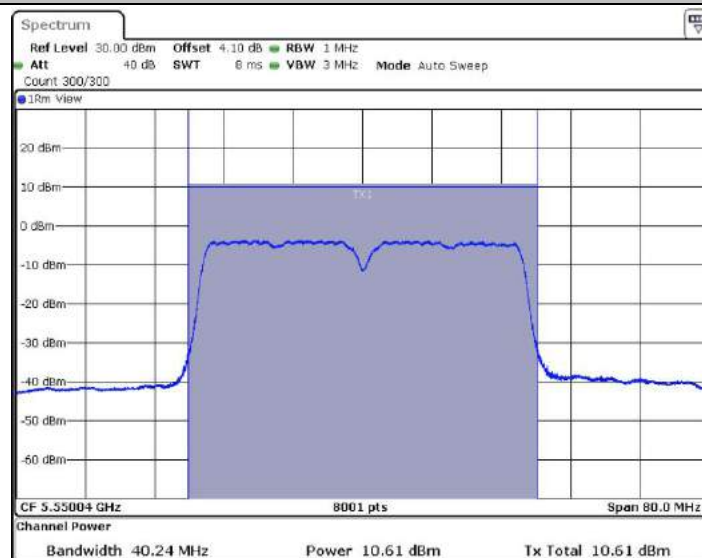
11AC40SISO_Ant2_5310



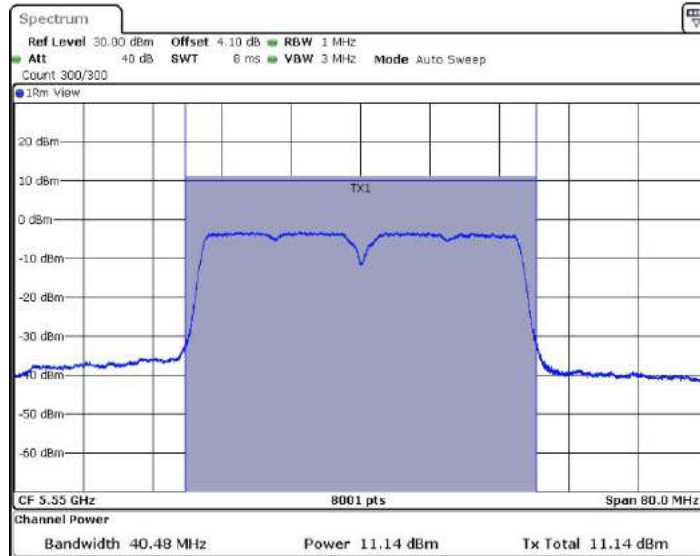
11AC40SISO_Ant1_5510



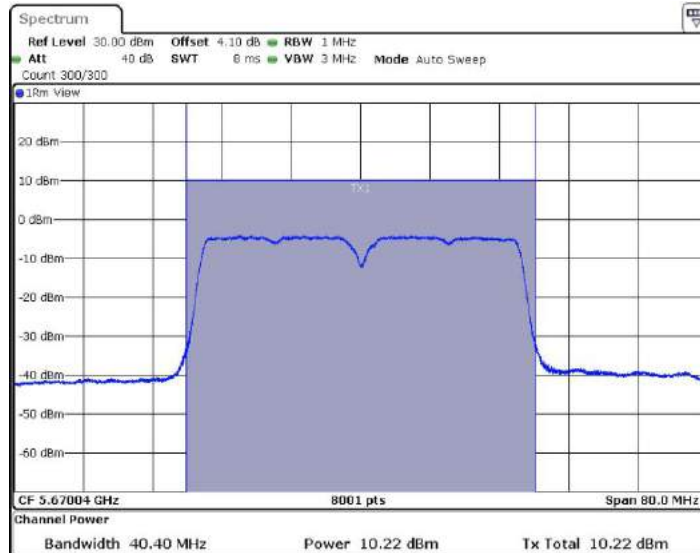
11AC40SISO_Ant2_5510



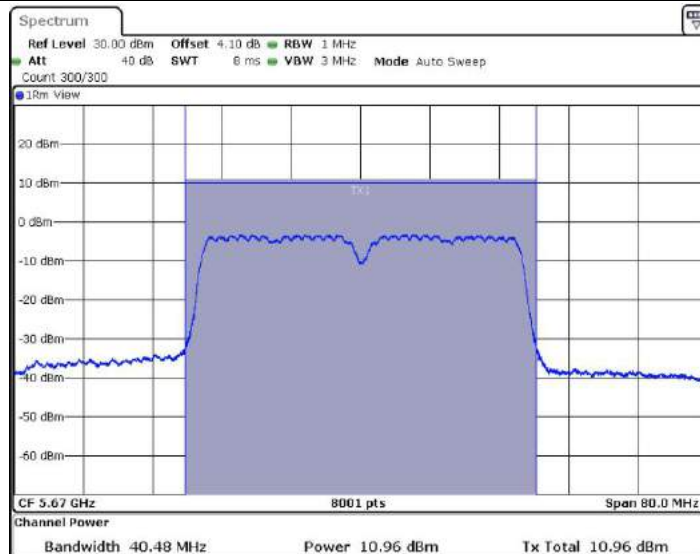
11AC40SISO_Ant1_5550



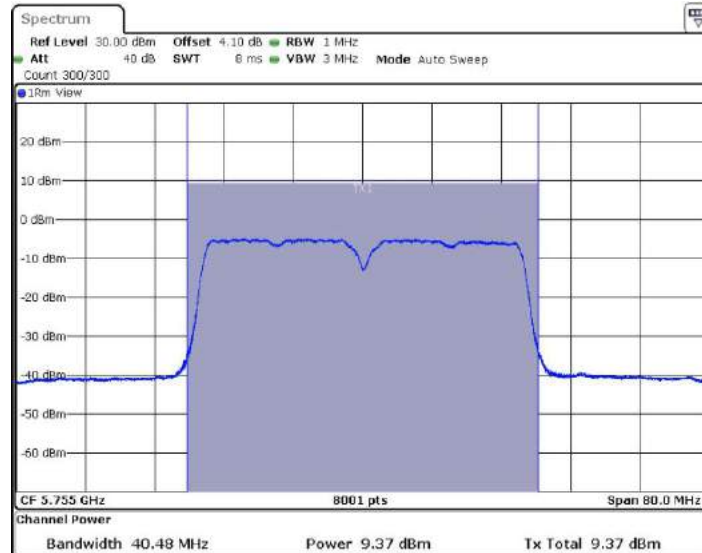
11AC40SISO_Ant2_5550



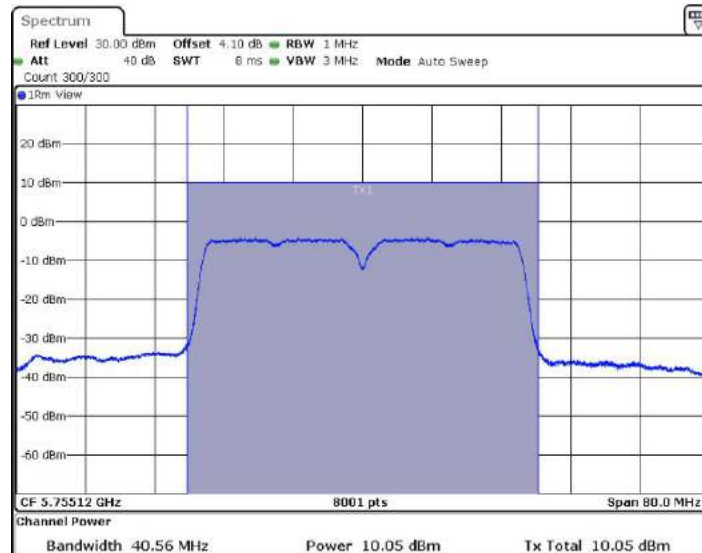
11AC40SISO_Ant1_5670



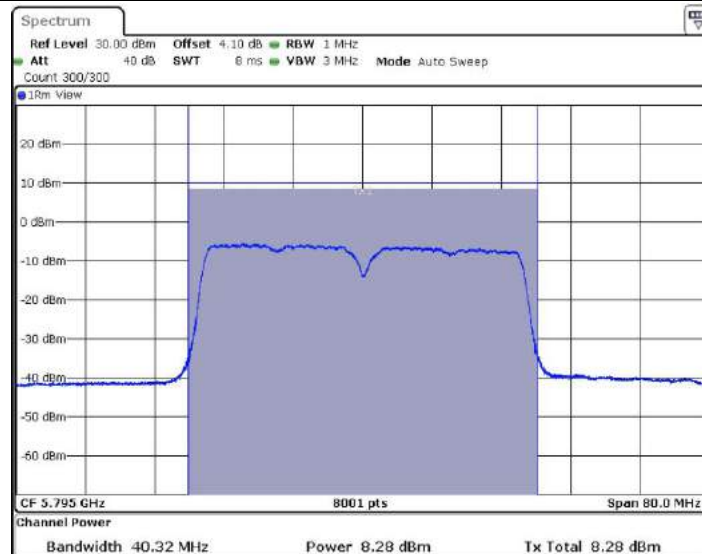
11AC40SISO_Ant2_5670



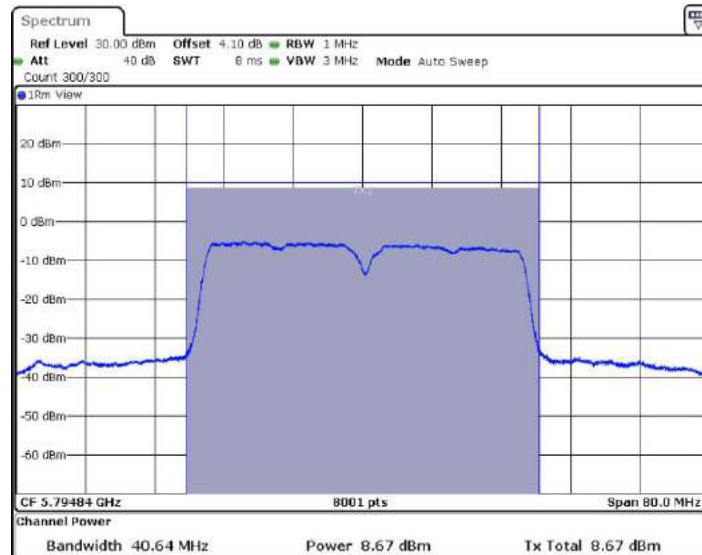
11AC40SISO_Ant1_5755



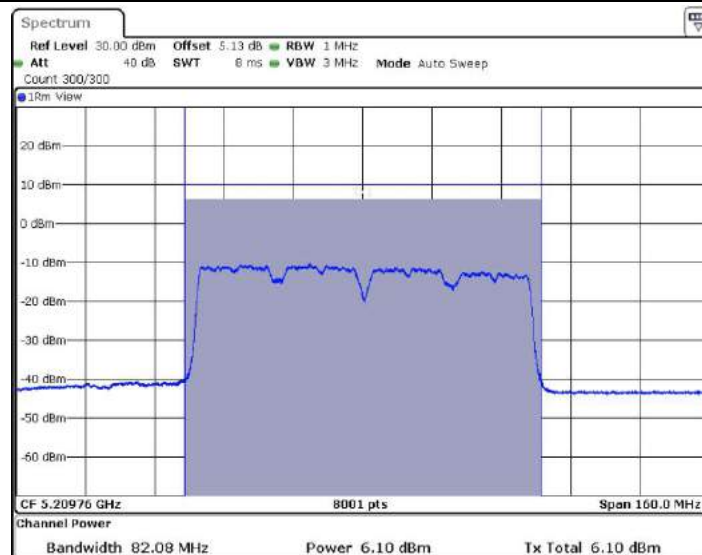
11AC40SISO_Ant2_5755



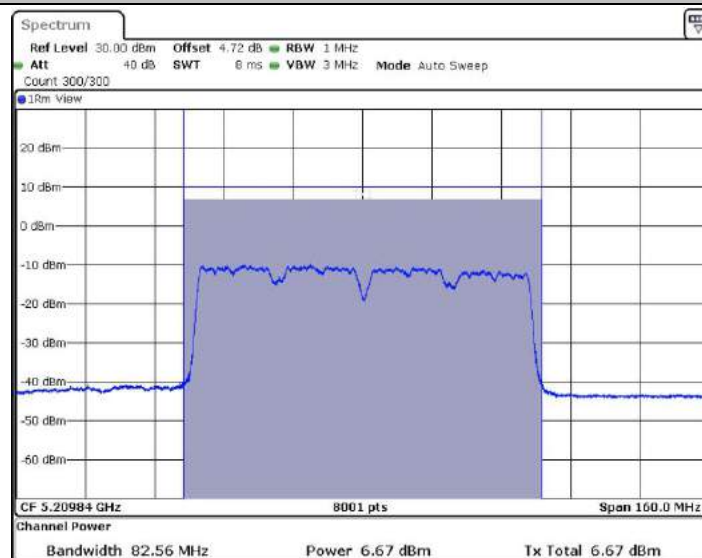
11AC40SISO_Ant1_5795



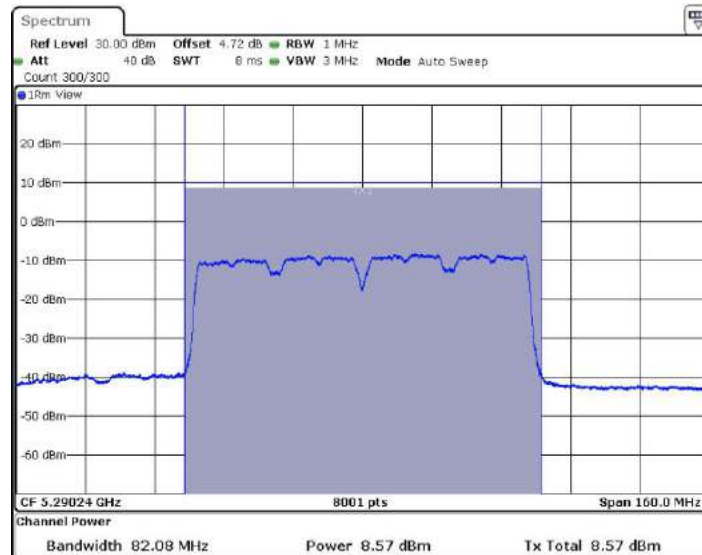
11AC40SISO_Ant2_5795



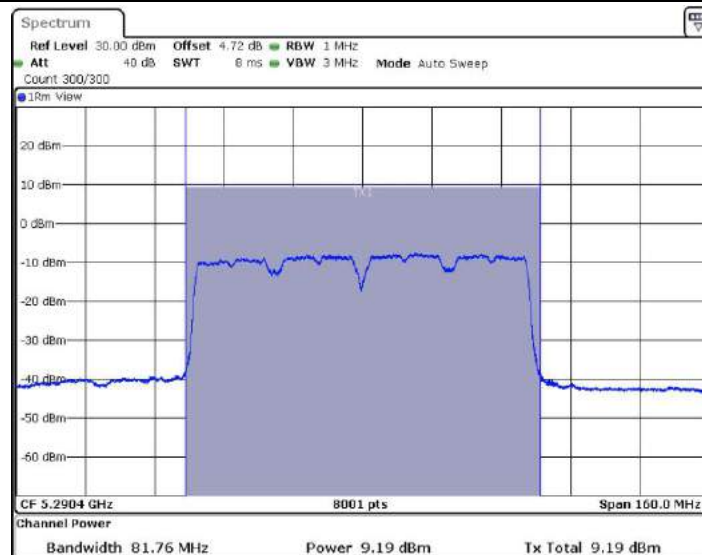
11AC80SISO_Ant1_5210



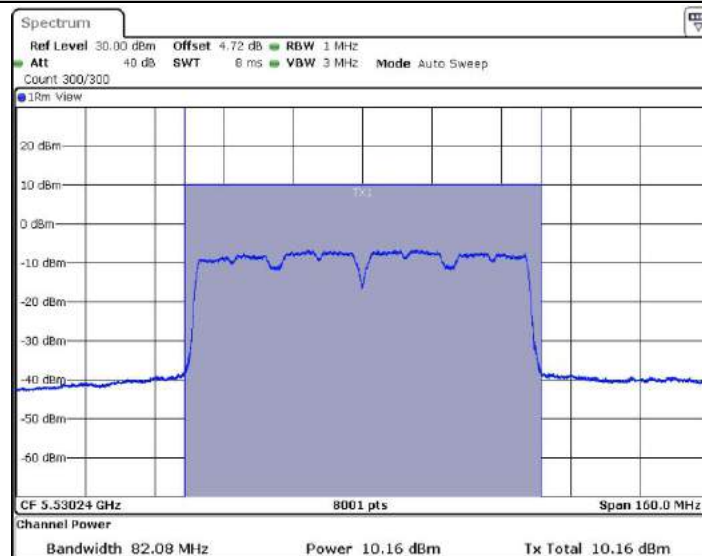
11AC80SISO_Ant2_5210



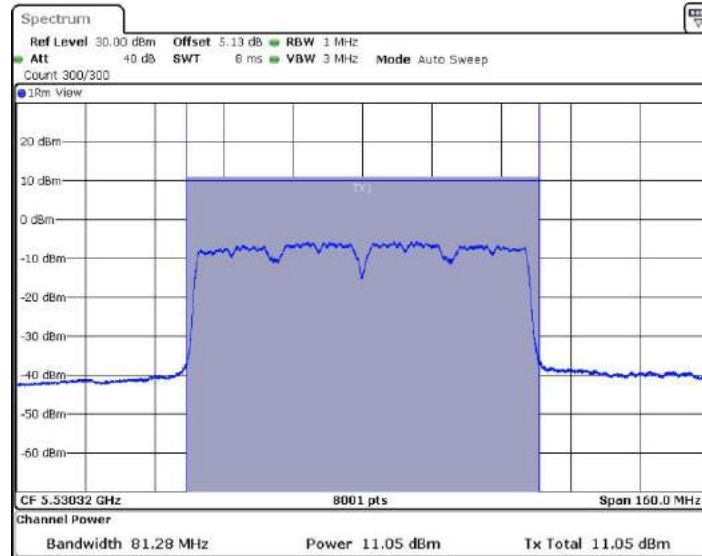
11AC80SISO_Ant1_5290



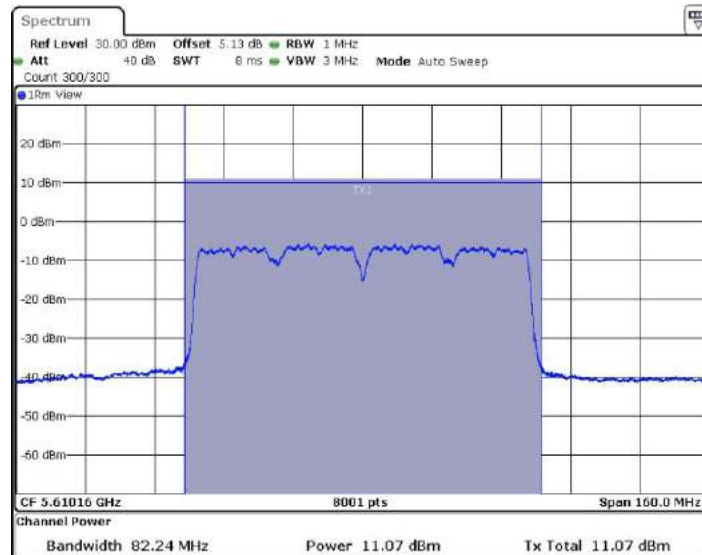
11AC80SISO_Ant2_5290



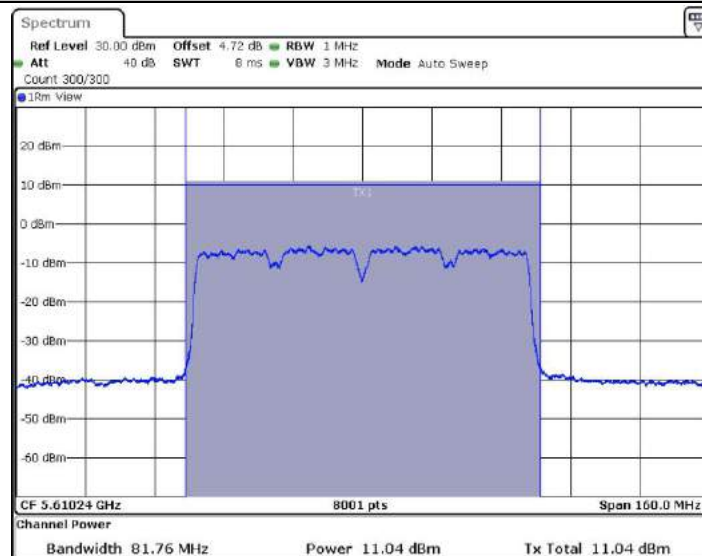
11AC80SISO_Ant1_5530



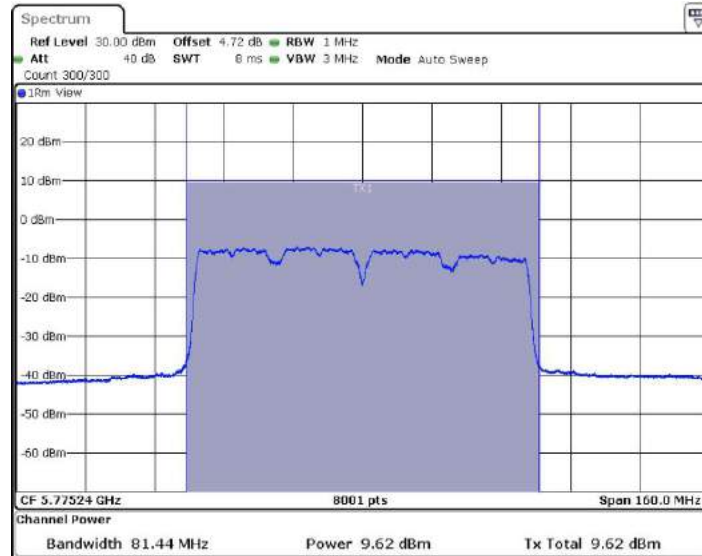
11AC80SISO_Ant2_5530



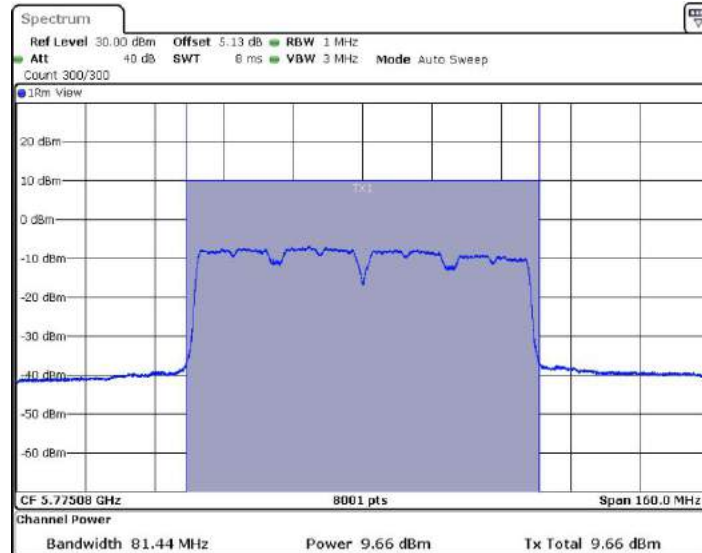
11AC80SISO_Ant1_5610



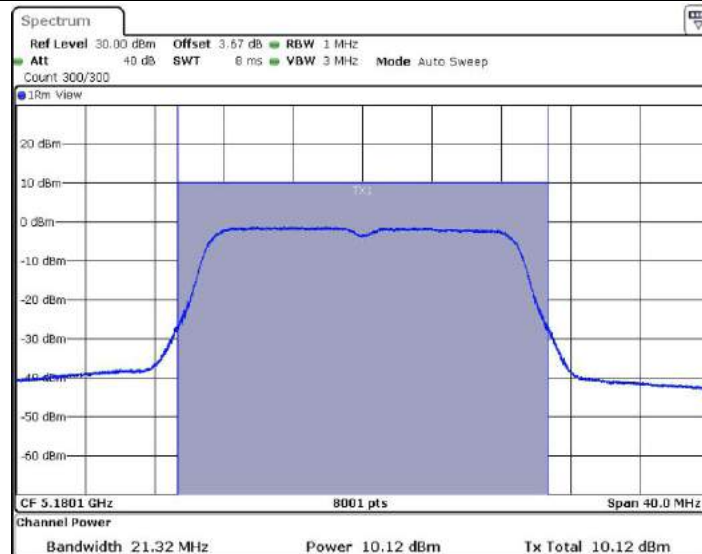
11AC80SISO_Ant2_5610



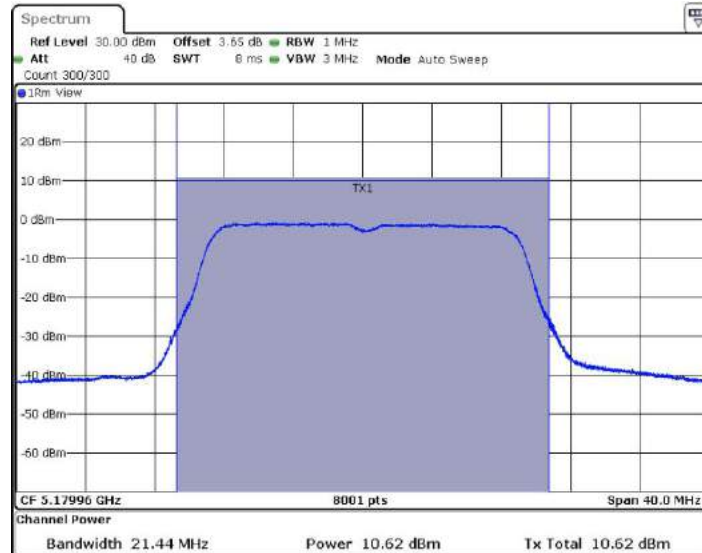
11AC80SISO_Ant1_5775



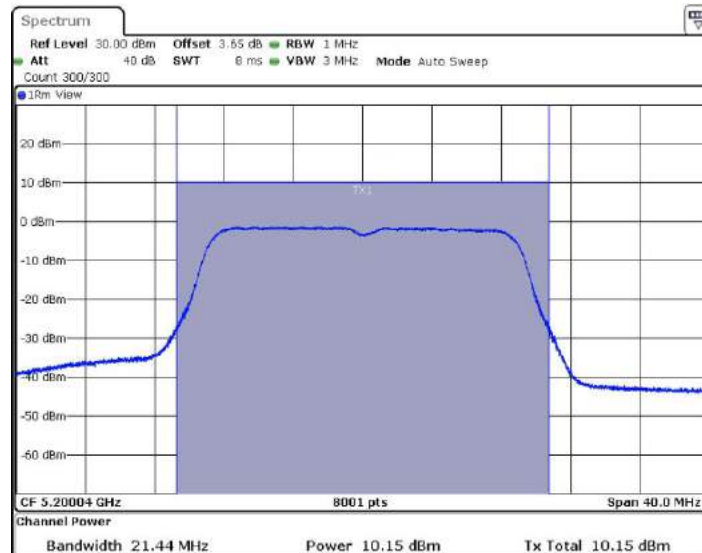
11AC80SISO_Ant2_5775



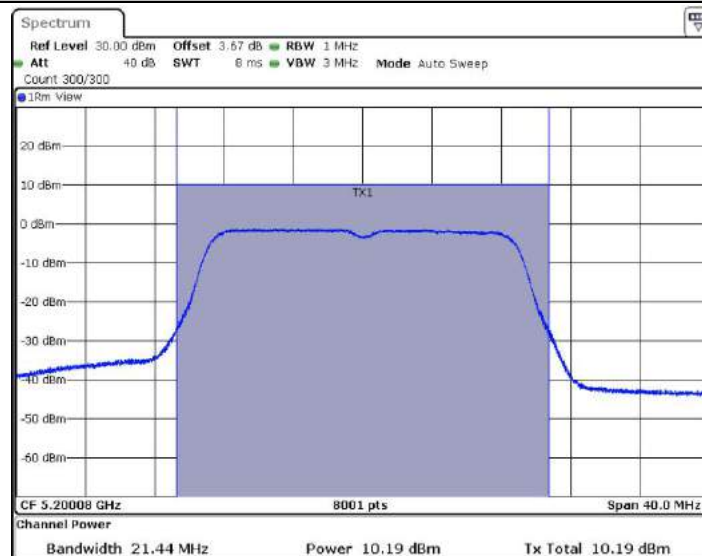
11N20MIMO_Ant1_5180



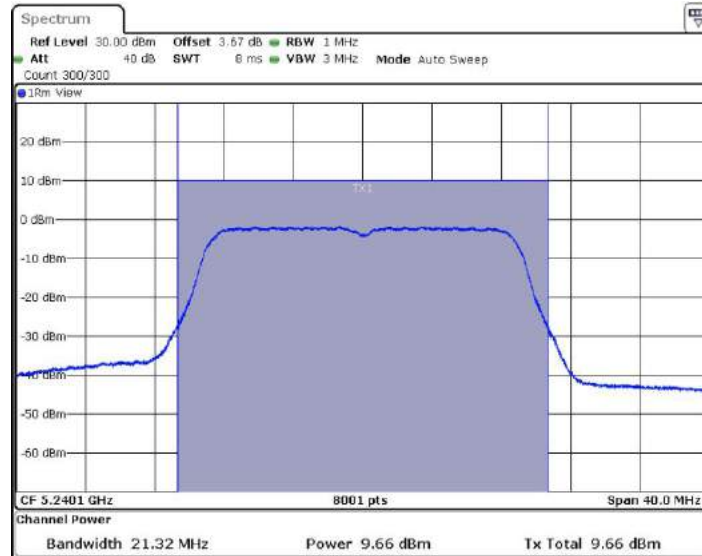
11N20MIMO_Ant2_5180



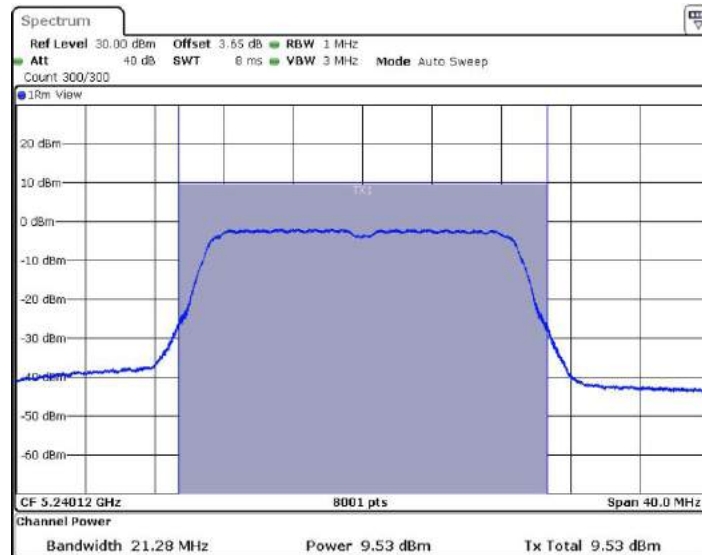
11N20MIMO_Ant1_5200



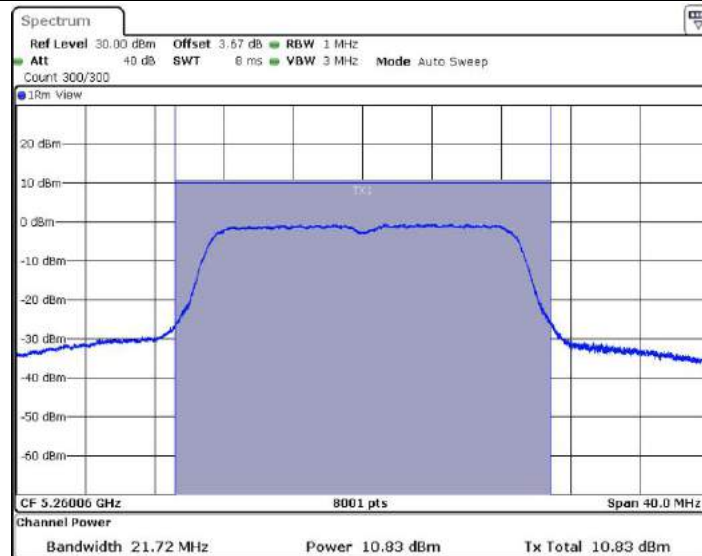
11N20MIMO_Ant2_5200



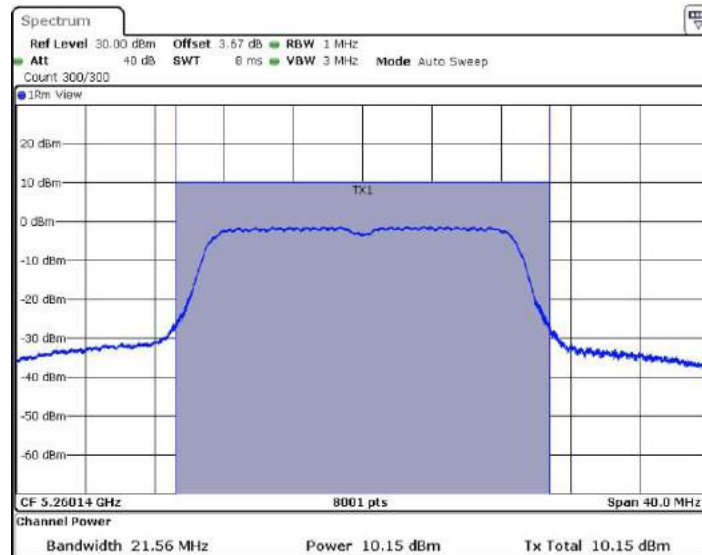
11N20MIMO_Ant1_5240



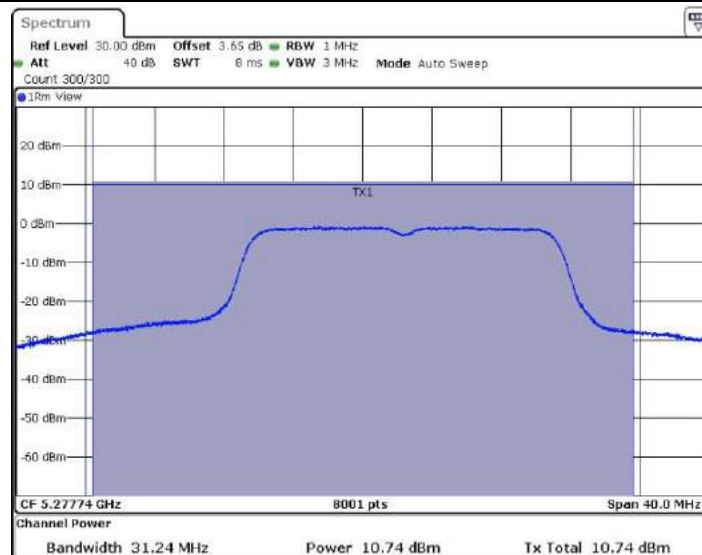
11N20MIMO_Ant2_5240



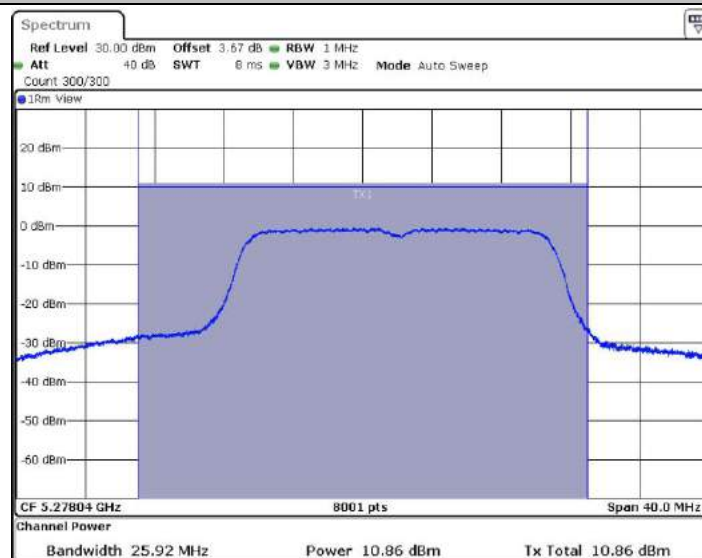
11N20MIMO_Ant1_5260



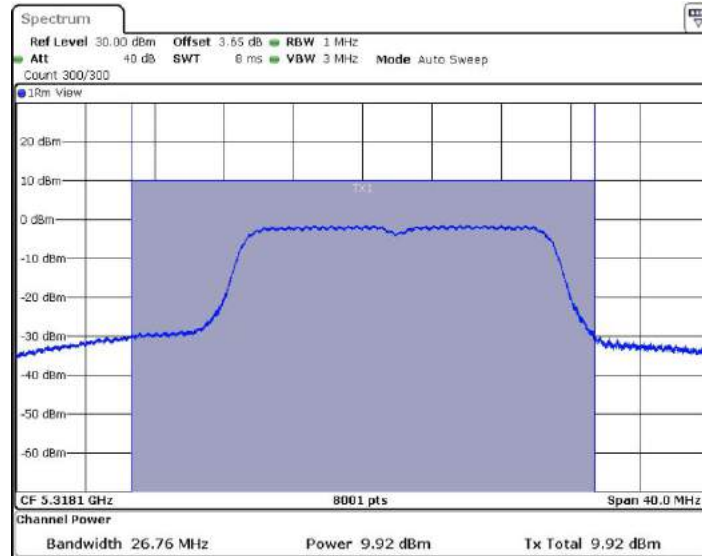
11N20MIMO_Ant2_5260



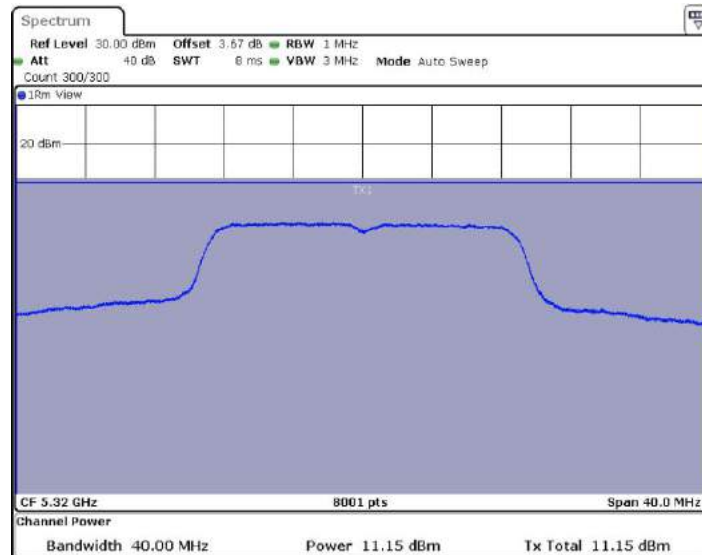
11N20MIMO_Ant1_5280



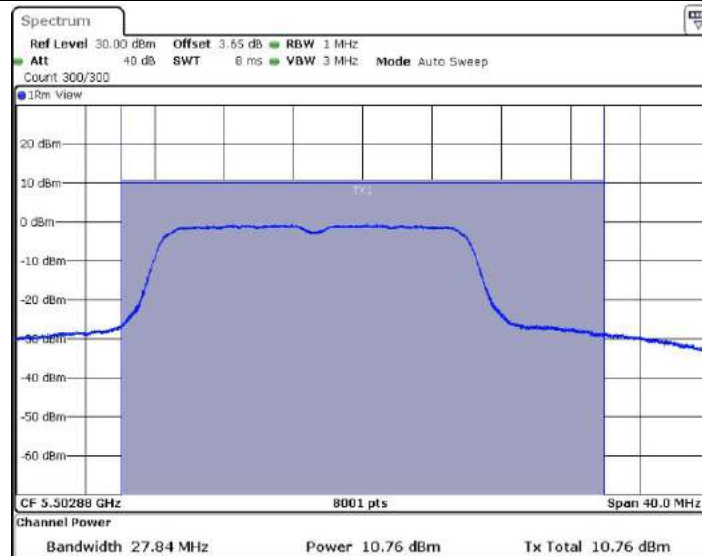
11N20MIMO_Ant2_5280



11N20MIMO_Ant1_5320



11N20MIMO_Ant2_5320



11N20MIMO_Ant1_5500