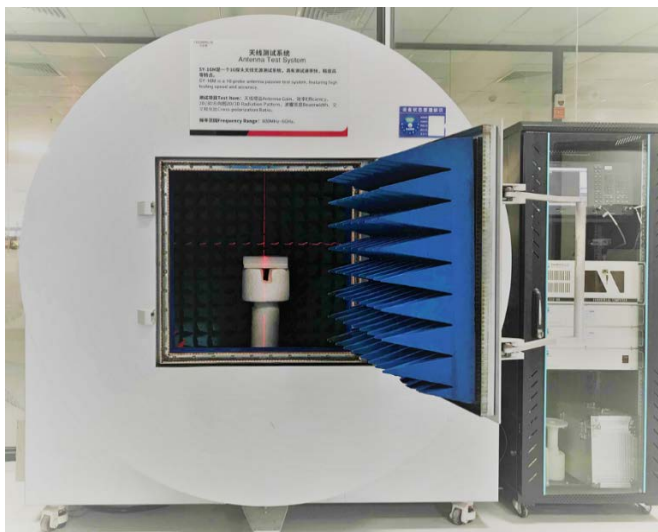
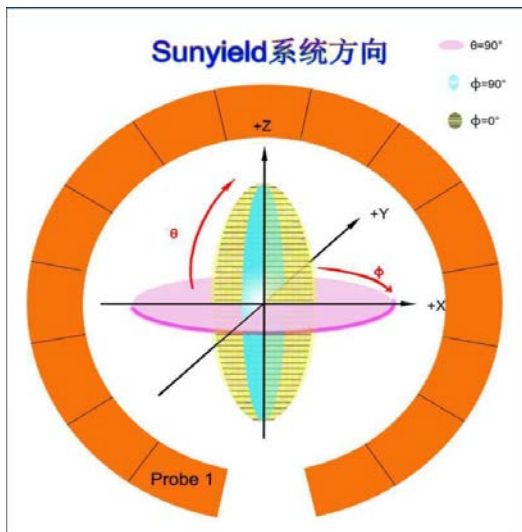
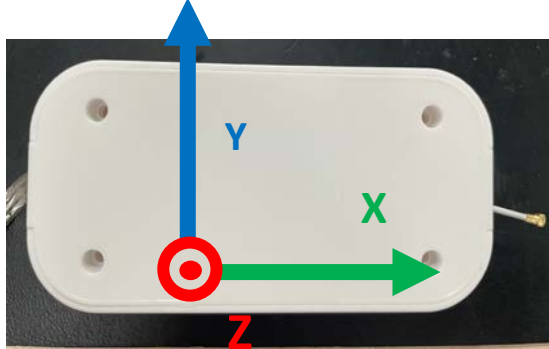
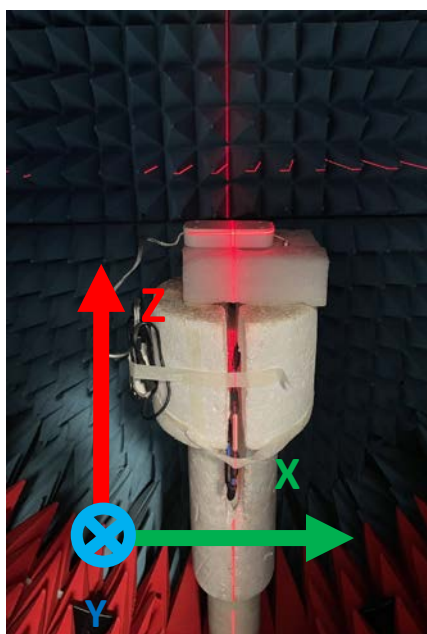
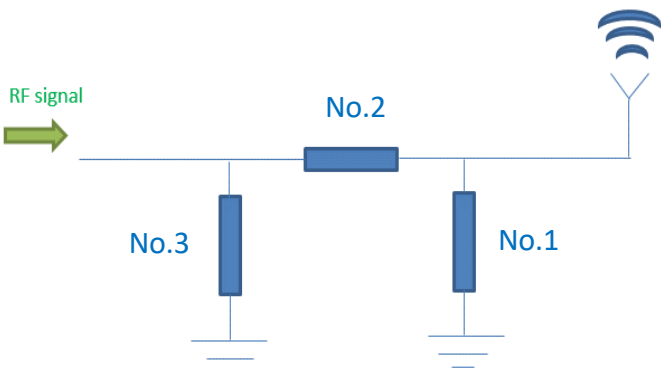
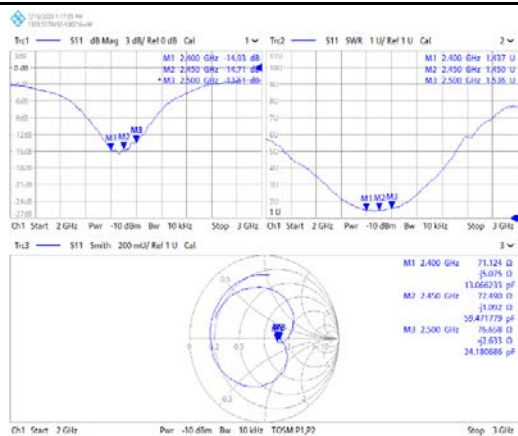
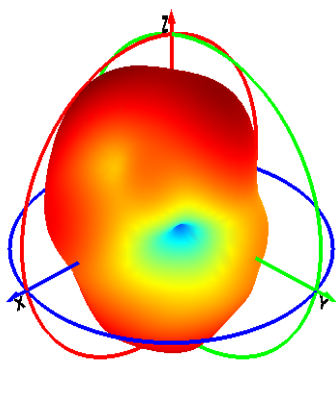
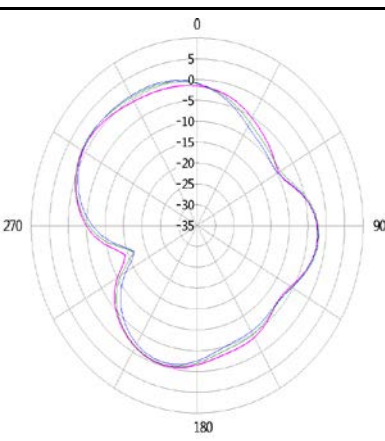
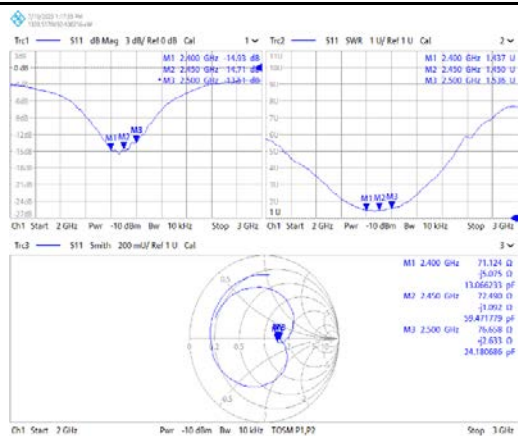
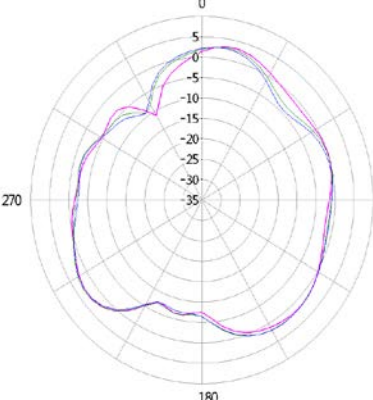
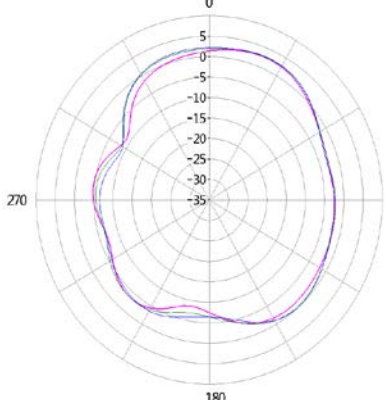


LEEDARSON 立达信		Antenna Test Report		Version: 2024-04-02_V2.3			
Product Information							
Client		HT5 圣诞树灯		Protocol		WIFI+BLE	
Project Number				Sample Quantity		1	
Model Number		LDE42B000B		PCB Version		HT5 1V1 20250625	
Chip Type		ESP32-C3		Structure Version		正式模	
Sample Picutres							
Summary							
No.	Iterms	Remark					
1	<u>Passive Test</u>	/					
2	<u>Rawdata</u>	/					
3	<u>Raw File</u>	/					
Author		Zhu Wei		Test Date		2025. 7. 10	
Verifier		Wang Xiaoyong		Review Date		2025. 7. 10	
Date		Version		Revise Content		Reviser	
2025. 7. 10		V2. 0		Establish Report		Zhu Wei	

Test setting							
SY-16M Test System			Sample Picture				
							
Antenna Matching							
			Matching Component				
			Position	No. 1	No. 2	No. 3	
			Item No.				
			Value	NC	1.5pF	NC	
			1st				
			2nd				
			3rd				
Remark							
Passive Test Results							
VNA Data			Chamber Data				
Original Result of Simulation Setting			Frequency (MHz)	Gain (dBi)	Efficiency (dB)	3D	$\theta = 90^\circ$ (XY Plane)
			2400	3.32	-1.57		
			2410	3.46	-1.43		
			2420	3.54	-1.37		
			2430	3.41	-1.52		
			2440	3.49	-1.41		
			2450	3.48	-1.38		
			2460	3.38	-1.49		
Final Tuning Result			2470	3.27	-1.62	$\phi = 0^\circ$ (XZ Plane)	$\phi = 90^\circ$ (YZ Plane)
			2480	3.30	-1.56		
			2490	3.20	-1.58		
			2500	2.99	-1.70		
			MIN	2.99	-1.70		
			MAX	3.54	-1.37		
AVG	3.35	-1.51					

Raw File List						
No.	Item	File Type	Sample #1		Sample #2	Remark
1	VSWR	SnP				
2	Passive data	trx				
3						