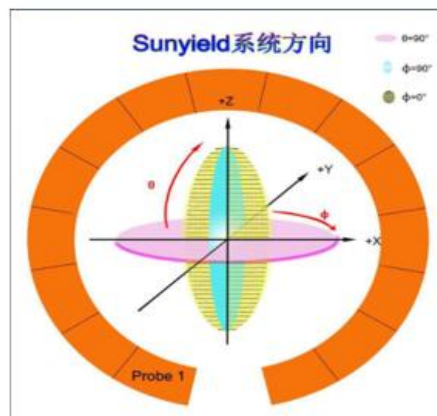
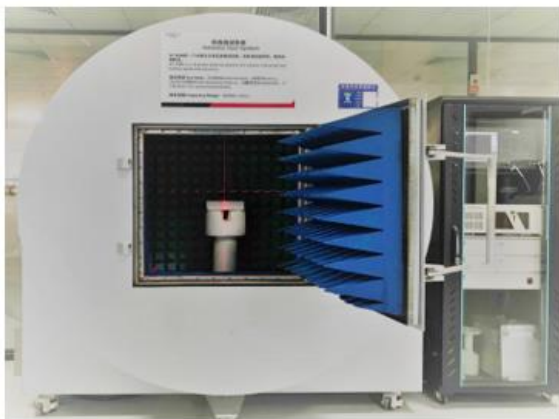
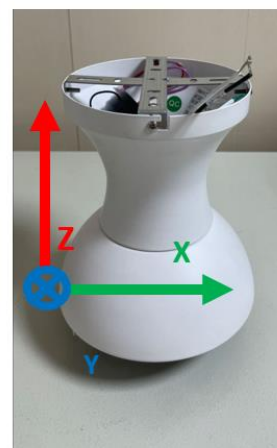
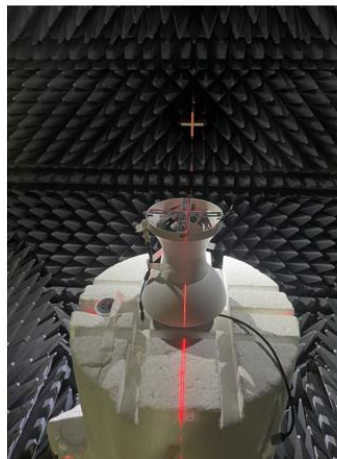


Product Information			
Client	风扇灯	Protocol	WIFI+BLE
Project Number	\	Sample Quantity	1
Model Number	\	PCB Version	13298A-Y425_241223
Chip Type	\	Structure Version	正式模
Sample Picutres			
Summary			
No.	Iterms	Remark	
1	<u>Passive Test</u>	/	
2	<u>Rawdata</u>	/	
3	<u>Raw File</u>	/	
Author		Zhu Wei	Test Date 2025.1.20
Verifier		Wang Xiaoyong	Review Date 2025.1.20
Date		Version	Revise Content Reviser
2025.1.20		V2.0	Establish Report Zhu Wei

SY-16M Test System

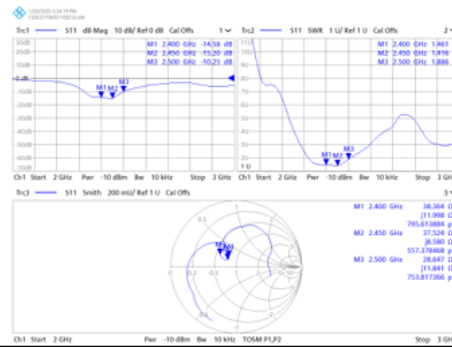


Sample Picture

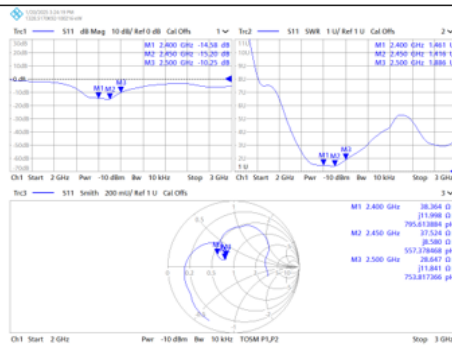


VNA Data

Original Result of Simulation Setting



Final Tuning Result



Chamber Data

Frequency (MHz)	Gain (dBi)	Efficiency (dR)	3D	$\theta = 90^\circ$ (XY Plane)
2400	-0.67	-8.22		
2410	-0.75	-8.34		
2420	-0.76	-8.38		
2430	-0.74	-8.40		
2440	-0.75	-8.36		
2450	-0.79	-8.40		
2460	-1.00	-8.56		
2470	-1.29	-8.76	$\phi = 0^\circ$ (XZ Plane)	$\phi = 90^\circ$ (YZ Plane)
2480	-1.46	-8.89		
2490	-1.58	-8.98		
2500	-1.67	-8.97		
-	-	-		
MIN	-1.67	-8.98		
MAX	-0.67	-8.22		
AVG	-1.04	-8.57		