TEST REPORT

Applicant:	Shenzhen Guye Technology Co., Ltd.
Address of Applicant:	B1604, Jieshun Technology Center, No.5 Guansheng 2nd Road, Luhu Community, Longhua District, Shenzhen
Manufacturer/Factory:	Dongguan Xinyue electronic Technology Co., LTD
Address of Manufacturer/Factory:	Fenggang Town, Dongguan City, Yantian Nanshan second Industrial Park A2 building 3 floor
Product Name:	P6 bluetooth speaker
Model No.:	P6,P1,P2,P3,P4,P5,P7,P8,P9,P10,P11,P12,P13,P14,P15,P16,P17, P18,P19,P20,P21,P22,P23,P24,P25,P26,P27,P28,P29,P30
Trade Mark:	N/A
FCC ID:	2AW5N-P6
Applicable standards:	FCC CFR Title 47 Part 15 Subpart C Section 15.247
Date of Test:	Jun.26, 2023- Jul.13, 2023
Date of report issued:	Jul.25, 2023
Test Result :	PASS *

* In the configuration tested, the EUT complied with the standards specified above.

Remark:

The results shown in this test report refer only to the sample(s) tested , this test report cannot be reproduced, except in full without prior written permission of the company.

The report would be invalid without specific stamp of test institute and the signatures of compiler and approver

Prepared By

Shenzhen ETR Standard Technology Co., Ltd.

Address: No.103, No.10, Phase I, Zone 3, Xinxing Industrial Park, Xinhe, Fuhai Street, Bao'an District, Shenzhen, Guangdong, China

Compiled by:	Reviewed by:	Approved by:
Carr Kang	Smith Chen	Smith Chen
Project Engineer	Project Manager	Authorized Signature

Report Revision History		
Report No.	Description	Issue Date
ET-23060703E	Original	Jul.25, 2023

Contents

Page

1 TEST SUMMARY 4

2 GENERAL INFORMATION 5

2.1 GENERAL DESCRIPTION OF EUT 5
2.2 TEST MODE 7
2.3 DESCRIPTION OF SUPPORT UNITS 7
2.4 DEVIATION FROM STANDARDS 7
2.5 ABNORMALITIES FROM STANDARD CONDITIONS 7
2.6 TEST FACILITY 7
2.7 TEST LOCATION 7
2.8 ADDITIONAL INSTRUCTIONS 7

3 TEST INSTRUMENTS LIST 8

4 TEST RESULTS AND MEASUREMENT DATA 9

4.1 ANTENNA REQUIREMENT 9
4.2 CONDUCTED EMISSIONS 10
4.3 CONDUCTED PEAK OUTPUT POWER 13
4.4 20DB EMISSION BANDWIDTH 15
4.5 CARRIER FREQUENCIES SEPARATION 23
4.6 HOPPING CHANNEL NUMBER 31
4.7 DWELL TIME 34
4.8 BAND EDGE 43
4.8.1 Conducted Emission Method 43
4.8.2 Radiated Emission Method 50
4.9 SPURIOUS EMISSION 52
4.9.1 Conducted Emission Method 52
4.9.2 Radiated Emission Method 59

5 TEST SETUP PHOTO 67

6 EUT CONSTRUCTIONAL DETAILS 67

1 Test Summary

Test Item	Section in CFR 47	Result	Test by
Antenna Requirement	15.203/15.247 (c)	Pass	/
AC Power Line Conducted Emission	15.207	Pass	Qiao Li
Conducted Peak Output Power	15.247 (b)(1)	Pass	Yvan Fan
20dB Occupied Bandwidth	15.247 (a)(1)	Pass	Yvan Fan
Carrier Frequencies Separation	15.247 (a)(1)	Pass	Yvan Fan
Hopping Channel Number	15.247 (a)(1)(iii)	Pass	Yvan Fan
Dwell Time	15.247 (a)(1)(iii)	Pass	Yvan Fan
Radiated Emission	15.205/15.209	Pass	Qiao Li
Band Edge	15.247(d)	Pass	Yvan Fan

Remarks:

1. *Pass: The EUT complies with the essential requirements in the standard.*
2. *Test according to ANSI C63.10:2013 and KDB 558074 D01 15.247 Meas Guidance v05r02*

Measurement Uncertainty

Test Item	Uncertainty Criterion	Measurement Uncertainty	Notes
Occupied Channel Bandwidth	±5%	±0.55%	(1)
RF output power, conducted	±1.5dB	±0.99dB	(1)
Power Spectral Density, conducted	±3dB	±0.61dB	(1)
Unwanted Emissions, conducted	±3dB	±0.64dB	(1)
AC Power Line Conducted Emission	±6dB	± 3.02 dB	(1)
Radiated emissions Below 1GHz	±6dB	±4.30 dB	(1)
Radiated emissions Above 1GHz	±6dB	±4.35 dB	(1)
Note (1): The measurement uncertainty is for coverage factor of k=2 and a level of confidence of 95%.			

2 General Information

2.1 General Description of EUT

Product Name:	P6 bluetooth speaker
Model No.:	P6
Test Model:	P6,P1,P2,P3,P4,P5,P7,P8,P9,P10,P11,P12,P13,P14,P15,P16,P17,P18, P19,P20,P21,P22,P23,P24,P25,P26,P27,P28,P29,P30
Difference of model(s)	All the model are the same circuit and RF module, except the model names and colors
Hardware Version:	N/A
Software Version:	N/A
Operation Frequency:	2402MHz~2480MHz
Channel numbers:	79
Channel separation:	1MHz
Modulation type:	GFSK,π/4-DQPSK,8DPSK
Antenna Type:	PCB Antenna
Antenna gain:	-0.68 dBi(Note: Antenna information is provided by applicant, Testing lab is not responsible for the accuracy of the information.)
Battery	DC 3.7V 3000mAh
Power supply:	DC 5V

Operation Frequency each of channel							
Channel	Frequency	Channel	Frequency	Channel	Frequency	Channel	Frequency
1	2402MHz	21	2422MHz	41	2442MHz	61	2462MHz
2	2403MHz	22	2423MHz	42	2443MHz	62	2463MHz
3	2404MHz	23	2424MHz	43	2444MHz	63	2464MHz
4	2405MHz	24	2425MHz	44	2445MHz	64	2465MHz
5	2406MHz	25	2426MHz	45	2446MHz	65	2466MHz
6	2407MHz	26	2427MHz	46	2447MHz	66	2467MHz
7	2408MHz	27	2428MHz	47	2448MHz	67	2468MHz
8	2409MHz	28	2429MHz	48	2449MHz	68	2469MHz
9	2410MHz	29	2430MHz	49	2450MHz	69	2470MHz
10	2411MHz	30	2431MHz	50	2451MHz	70	2471MHz
11	2412MHz	31	2432MHz	51	2452MHz	71	2472MHz
12	2413MHz	32	2433MHz	52	2453MHz	72	2473MHz
13	2414MHz	33	2434MHz	53	2454MHz	73	2474MHz
14	2415MHz	34	2435MHz	54	2455MHz	74	2475MHz
15	2416MHz	35	2436MHz	55	2456MHz	75	2476MHz
16	2417MHz	36	2437MHz	56	2457MHz	76	2477MHz
17	2418MHz	37	2438MHz	57	2458MHz	77	2478MHz
18	2419MHz	38	2439MHz	58	2459MHz	78	2479MHz
19	2420MHz	39	2440MHz	59	2460MHz	79	2480MHz
20	2421MHz	40	2441MHz	60	2461MHz		

Note:

In section 15.31(m), regards to the operating frequency range over 10 MHz, the Lowest frequency, the middle frequency, and the highest frequency of channel were selected to perform the test, and the selected channel see below:

Channel	Frequency
The lowest channel	2402MHz
The middle channel	2441MHz
The Highest channel	2480MHz

2.2 Test mode

Transmitting mode	Keep the EUT in continuously transmitting mode.
Remark: For battery operated equipment, the EUT was performed using a new DC 3.7V battery.	

2.3 Description of Support Units

Equipment	Model	S/N	Manufacturer
Adapter	HW-050200CH0	/	HUAWEI
USB Cable	0.32m	/	/

2.4 Deviation from Standards

None.

2.5 Abnormalities from Standard Conditions

None.

2.6 Test Facility

Test laboratory:	Shenzhen ETR Standard Technology Co., Ltd.
CNAS Registration Number:	L11864
A2LA Certificate Number:	6640.01
FCC Designation Number:	CN1326
FCC Test Firm Registration:	183064

2.7 Test Location

All tests were performed at:

Laboratory location:	No.103, No.10, Phase I, Zone 3, Xinxing Industrial Park, Xinhe, Fuhai Street, Bao'an District, Shenzhen, Guangdong, China
Telephone:	+86 755 85259392
Fax:	+86 755 27219460

2.8 Additional Instructions

Test Software	FCC_assist_1.0.2.2
Power level setup	Default

3 Test Instruments list

Item	Equipment name	Manufacturer	Model	Serial No.	Calibration date	Due date
1	EMI Test Receiver	Rohde&schwarz	ESCI7	100605	2023.3.02	2024.3.01
2	EMI Test Receiver	Rohde&schwarz	ESCI3	102696	2023.3.02	2024.3.01
3	Loop Antenna	schwarabeck	FMZB 1519 B	FMZB 1519 B	2022.3.11	2024.3.10
4	Broadband antenna	schwarabeck	VULB9168	1064	2022.3.11	2024.3.10
5	Horn antenna	schwarabeck	BBHA9120D	9120D-1145	2022.3.11	2024.3.10
6	amplifier	EMtrace	RP01A	50117	2023.3.02	2024.3.01
7	Artificial power network	schwarabeck	NSLK8127	8127483	2023.3.02	2024.3.01
8	Artificial power network	ETS	3186/2NM	1132	2023.3.02	2024.3.01
9	10dB attenuator	HUBER+SUHNER	10dB	/	2023.3.02	2024.3.01
10	amplifier	Space-Dtronics	EWLAN0118G-P40	19113001	2023.3.02	2024.3.01
11	Filter	Xingbo	XBLBQ-GTA19	210410-3-1	2023.3.06	2024.3.05
12	Spectrum analyzer	KEYSIGHT	N9020A	MY55370280	2023.3.02	2024.3.01
13	Power detector box	MWRFtest	MW100-PSB	MW201020JYT	2022.11.18	2023.11.17
14	Power meter	Rohde&Schwarz	NRP-Z11	1138.3004.02-117725-vh	2023.03.30	2024.03.29

Note: the calibration interval of the above test instruments is 12 or 24 months and the calibrations are traceable to international system unit (SI).

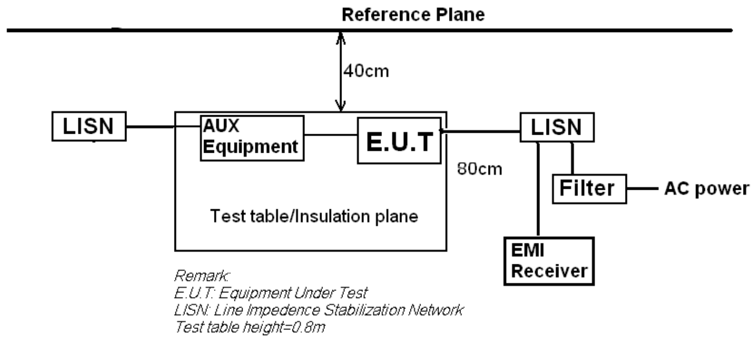
Software Name	Manufacturer	Model	Version
RF test software	MWRFtest	MTS 8310	V2.0.0.0
Conducted test software	EZ-EMC	Farad	Ver.EMC-CON 3A1.1
Radiated test software	EZ-EMC	Farad	Ver.FA-03A2 RE

4 Test results and Measurement Data

4.1 Antenna requirement

Standard requirement:	FCC Part15 C Section 15.203 /247(c)
15.203 requirement: An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited. **15.247(c) (1)(i) requirement:** (i) Systems operating in the 2400-2483.5 MHz band that is used exclusively for fixed. Point-to-point operations may employ transmitting antennas with directional gain greater than 6dBi provided the maximum conducted output power of the intentional radiator is reduced by 1 dB for every 3 dB that the directional gain of the antenna exceeds 6dBi.	
E.U.T Antenna:	
The antenna is pcb antenna, the best case gain of the is -0.68*dBi, reference to the appendix II for details*	

4.2 Conducted Emissions

<table>
<tr><td>Test Requirement:</td><td colspan="6">FCC Part15 C Section 15.207</td></tr>
<tr><td>Test Method:</td><td colspan="6">ANSI C63.10:2013</td></tr>
<tr><td>Test Frequency Range:</td><td colspan="6">150KHz to 30MHz</td></tr>
<tr><td>Class / Severity:</td><td colspan="6">Class B</td></tr>
<tr><td>Receiver setup:</td><td colspan="6">RBW=9KHz, VBW=30KHz, Sweep time=auto</td></tr>
<tr><td>Limit:</td><td colspan="6">
<table>
<tr><td rowspan="2">Frequency range (MHz)</td><td colspan="2">Limit (dBuV)</td></tr>
<tr><td>Quasi-peak</td><td>Average</td></tr>
<tr><td>0.15-0.5</td><td>66 to 56*</td><td>56 to 46*</td></tr>
<tr><td>0.5-5</td><td>56</td><td>46</td></tr>
<tr><td>5-30</td><td>60</td><td>50</td></tr>
</table>
* Decreases with the logarithm of the frequency.</td></tr>
<tr><td>Test setup:</td><td colspan="6">
</td></tr>
<tr><td>Test procedure:</td><td colspan="6">1. The E.U.T and simulators are connected to the main power through a line impedance stabilization network (L.I.S.N.). This provides a 50ohm/50uH coupling impedance for the measuring equipment.
2. The peripheral devices are also connected to the main power through a LISN that provides a 50ohm/50uH coupling impedance with 50ohm termination. (Please refer to the block diagram of the test setup and photographs).
3. Both sides of A.C. line are checked for maximum conducted interference. In order to find the maximum emission, the relative positions of equipment and all of the interface cables must be changed according to ANSI C63.10:2013 on conducted measurement.</td></tr>
<tr><td>Test Instruments:</td><td colspan="6">Refer to section 3.0 for details</td></tr>
<tr><td>Test mode:</td><td colspan="6">Refer to section 2.2 for details</td></tr>
<tr><td>Test environment:</td><td>Temp.:</td><td>26.2°C</td><td>Humid.:</td><td>46%</td><td>Press.:</td><td>1017mbar</td></tr>
<tr><td>Test voltage:</td><td colspan="6">AC 120V, 60Hz</td></tr>
<tr><td>Test results:</td><td colspan="6">Pass</td></tr>
</table>

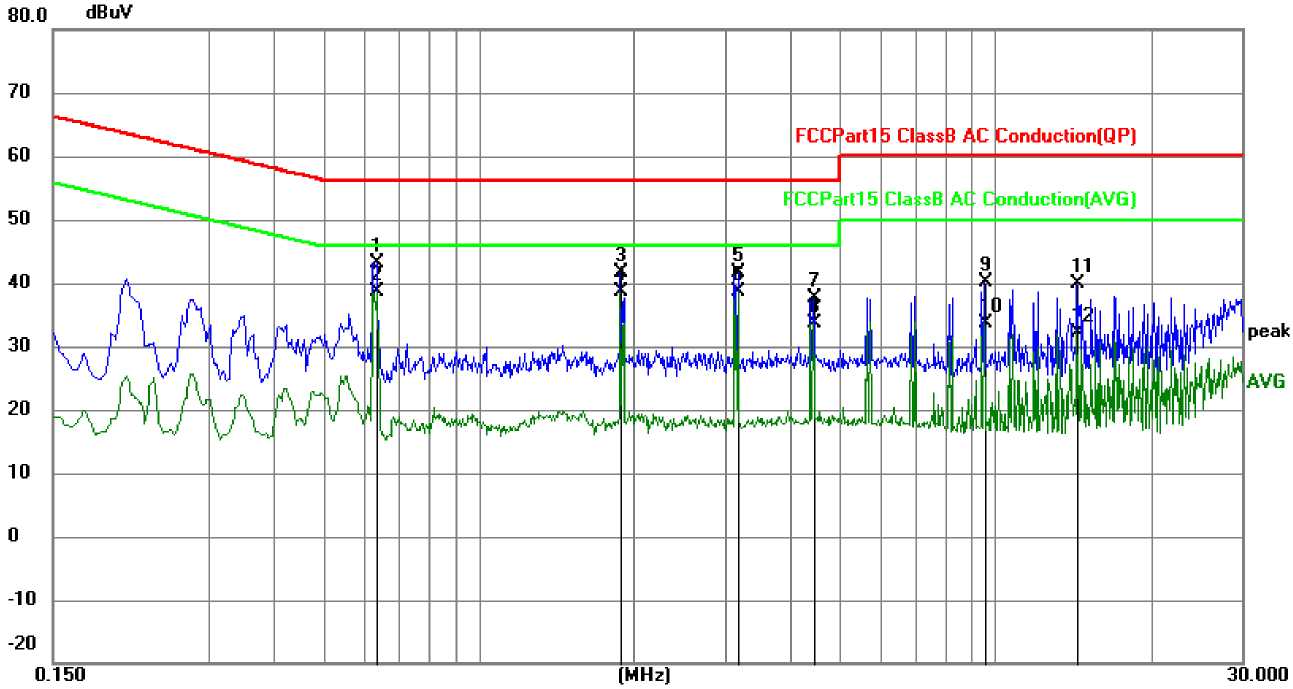
Remark:

1. Both high voltage and low voltage have been tested, and the report only shows the worst case data with AC 120V/60Hz.

2. Pre-scan all test modes, the report only shows the worst mode data, and the worst mode is two module operate simultaneously on 8-DPSK (2480MHz) .

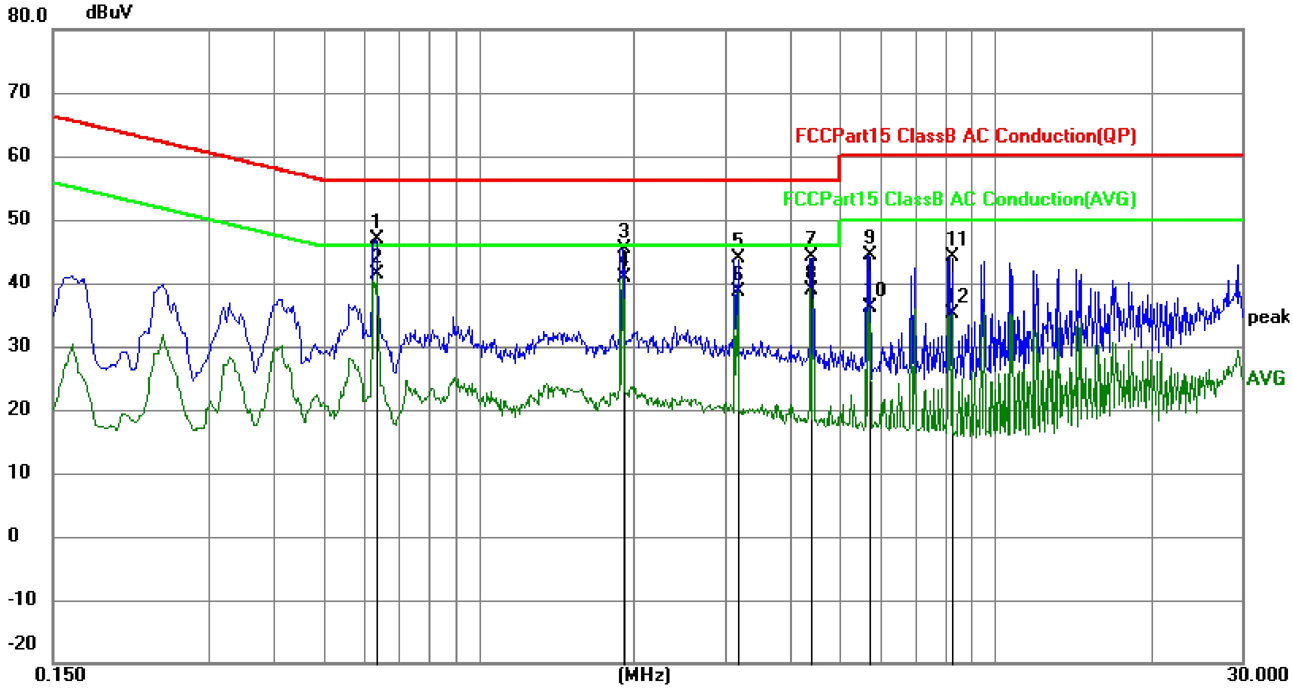
Measurement data:

Line:



No.	Frequency (MHz)	Reading (dBuV)	Factor (dB)	Level (dBuV)	Limit (dBuV)	Margin (dB)	Detector
1	0.6360	33.12	9.94	43.06	56.00	-12.94	QP
2	0.6360	28.58	9.94	38.52	46.00	-7.48	AVG
3	1.8825	31.86	9.86	41.72	56.00	-14.28	QP
4	1.8825	28.81	9.86	38.67	46.00	-7.33	AVG
5	3.1785	31.83	9.84	41.67	56.00	-14.33	QP
6	3.1785	28.74	9.84	38.58	46.00	-7.42	AVG
7	4.4520	27.82	9.83	37.65	56.00	-18.35	QP
8	4.4520	23.88	9.83	33.71	46.00	-12.29	AVG
9	9.5370	30.37	9.84	40.21	60.00	-19.79	QP
10	9.5370	23.76	9.84	33.60	50.00	-16.40	AVG
11	14.4329	30.00	9.76	39.76	60.00	-20.24	QP
12	14.4329	22.40	9.76	32.16	50.00	-17.84	AVG

Neutral:

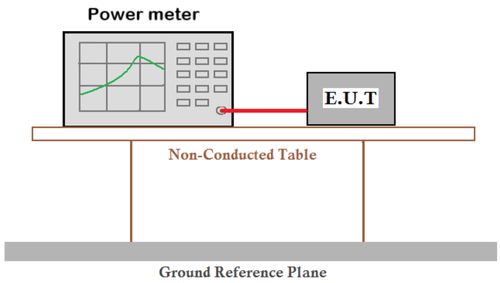


No.	Frequency (MHz)	Reading (dBuV)	Factor (dB)	Level (dBuV)	Limit (dBuV)	Margin (dB)	Detector
1	0.6360	36.87	9.94	46.81	56.00	-9.19	QP
2	0.6360	31.33	9.94	41.27	46.00	-4.73	AVG
3	1.9050	35.62	9.86	45.48	56.00	-10.52	QP
4	1.9050	31.03	9.86	40.89	46.00	-5.11	AVG
5	3.1740	34.09	9.84	43.93	56.00	-12.07	QP
6	3.1740	28.87	9.84	38.71	46.00	-7.29	AVG
7	4.3890	34.41	9.83	44.24	56.00	-11.76	QP
8	4.3890	29.17	9.83	39.00	46.00	-7.00	AVG
9	5.7120	34.66	9.83	44.49	60.00	-15.51	QP
10	5.7120	26.29	9.83	36.12	50.00	-13.88	AVG
11	8.2500	34.42	9.83	44.25	60.00	-15.75	QP
12	8.2500	25.37	9.83	35.20	50.00	-14.80	AVG

Notes:

1. An initial pre-scan was performed on the line and neutral lines with peak detector.
2. Quasi-Peak and Average measurement were performed at the frequencies with maximized peak emission.
3. Final Level =Receiver Read level + LISN Factor + Cable Loss

4.3 Conducted Peak Output Power

Test Requirement:	FCC Part15 C Section 15.247 (b)(1)
Test Method:	ANSI C63.10:2013 and KDB558074 D01 15.247 Meas Guidance v05r02
Limit:	20.97dBm
Test setup:	
Test Instruments:	Refer to section 3.0 for details
Test mode:	Refer to section 2.2 for details
Test environment:	Temp.: 23.4°C Humid.: 54%RH
Test voltage:	DC 3.7V
Test results:	Pass

Measurement Result

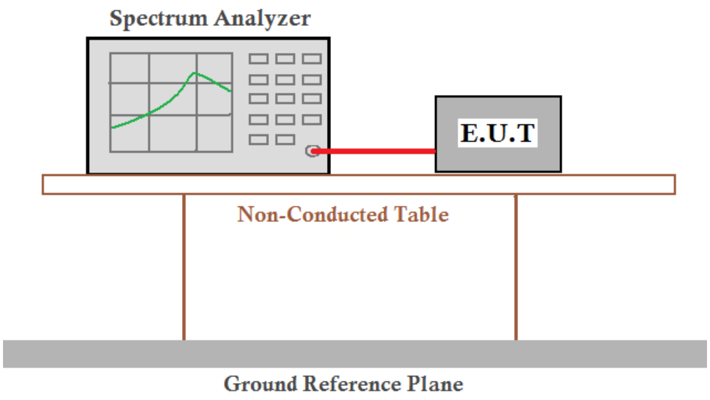
AC6976D Module

Mode	Test channel	Peak Output Power (dBm)	Limit (dBm)	Result
GFSK	Lowest	-3.63	20.97	Pass
	Middle	-4.16		
	Highest	-3.58		
π/4-DQPSK	Lowest	-2.99	20.97	Pass
	Middle	-3.30		
	Highest	-2.77		
8-DPSK	Lowest	-2.52	20.97	Pass
	Middle	-2.92		
	Highest	-2.39		

AC6956C Module

Mode	Test channel	Peak Output Power (dBm)	Limit (dBm)	Result
GFSK	Lowest	-4.07	20.97	Pass
	Middle	-4.27		
	Highest	-3.49		
π/4-DQPSK	Lowest	-3.22	20.97	Pass
	Middle	-3.47		
	Highest	-2.56		
8-DPSK	Lowest	-2.62	20.97	Pass
	Middle	-3.03		
	Highest	-2.16		

4.4 20dB Emission Bandwidth

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)
Test Method:	ANSI C63.10:2013 and KDB558074 D01 15.247 Meas Guidance v05r02
Limit:	N/A
Test setup:	
Test Instruments:	Refer to section 3.0 for details
Test mode:	Refer to section 2.2 for details
Test environment:	Temp.: 23.4°C Humid.: 54%RH
Test voltage:	DC 3.7V
Test results:	Pass

Measurement Result

AC6976D Module

Mode	Test channel	20dB Emission Bandwidth (MHz)	Result
GFSK	Lowest	0.9512	Pass
	Middle	0.9510	
	Highest	0.9526	
π/4-DQPSK	Lowest	1.332	Pass
	Middle	1.283	
	Highest	1.277	
8-DPSK	Lowest	1.295	Pass
	Middle	1.295	
	Highest	1.294	

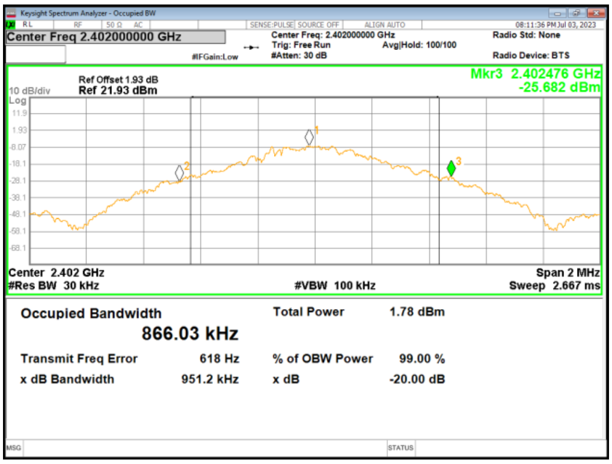
AC6956C Module

Mode	Test channel	20dB Emission Bandwidth (MHz)	Result
GFSK	Lowest	0.9480	Pass
	Middle	0.9483	
	Highest	0.9481	
π/4-DQPSK	Lowest	1.304	Pass
	Middle	1.279	
	Highest	1.280	
8-DPSK	Lowest	1.286	Pass
	Middle	1.330	
	Highest	1.287	

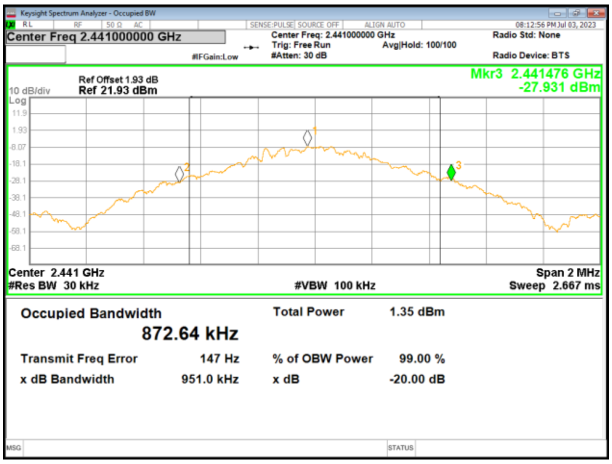
AC6976D Module

Test plot as follows:

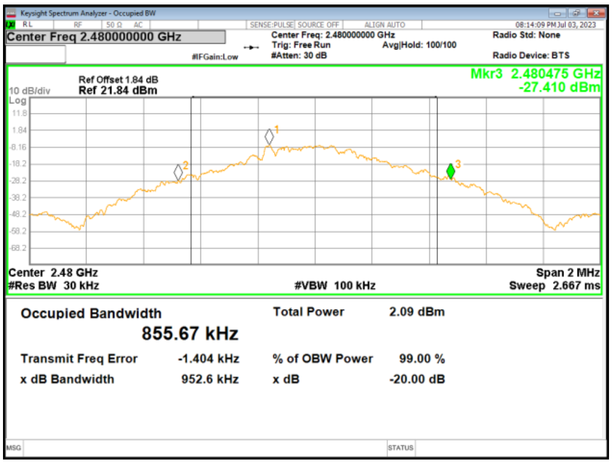
Test mode:	GFSK mode



Lowest channel

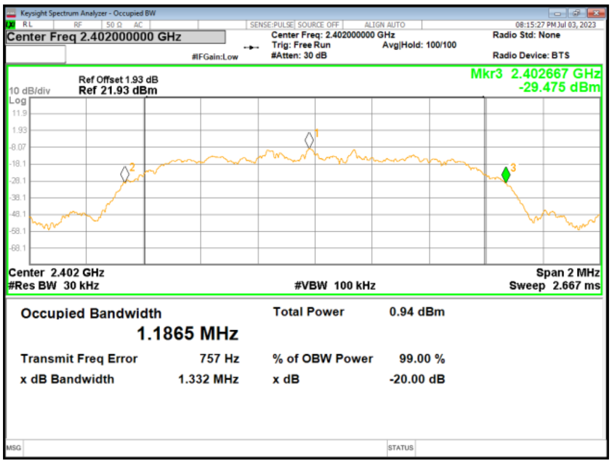


Middle channel

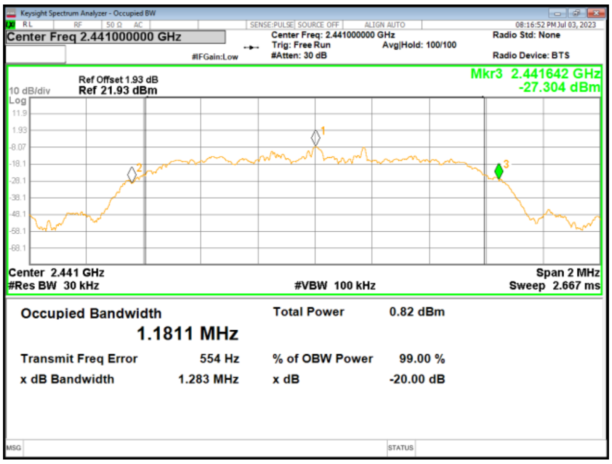


Highest channel

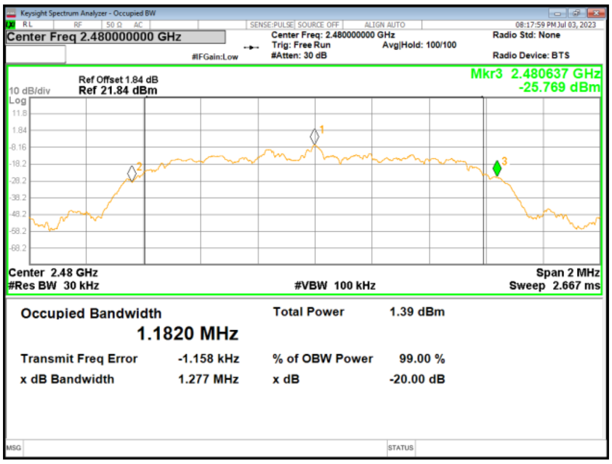
Test mode:	π/4-DQPSK mode



Lowest channel

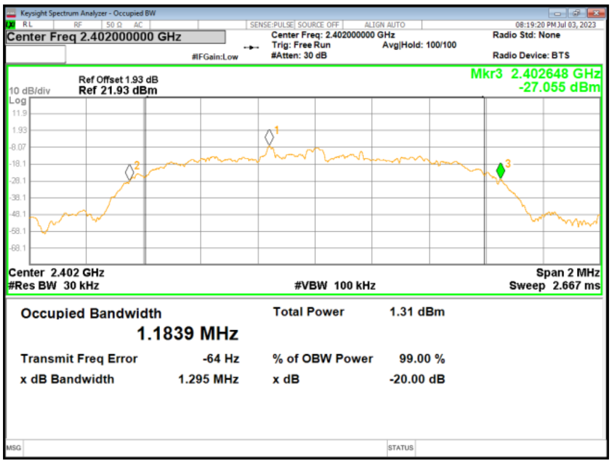


Middle channel

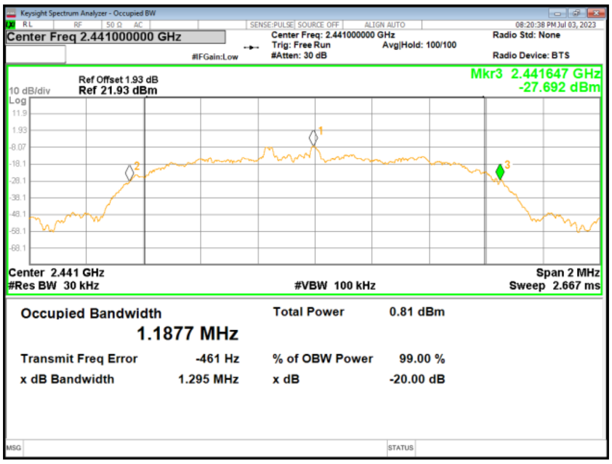


Highest channel

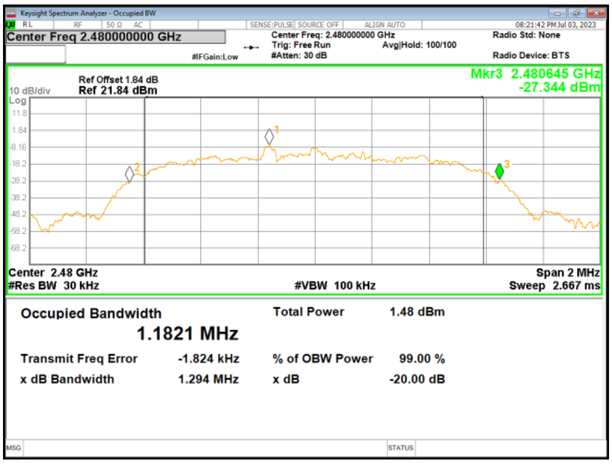
Test mode:	8-DPSK mode



Lowest channel



Middle channel

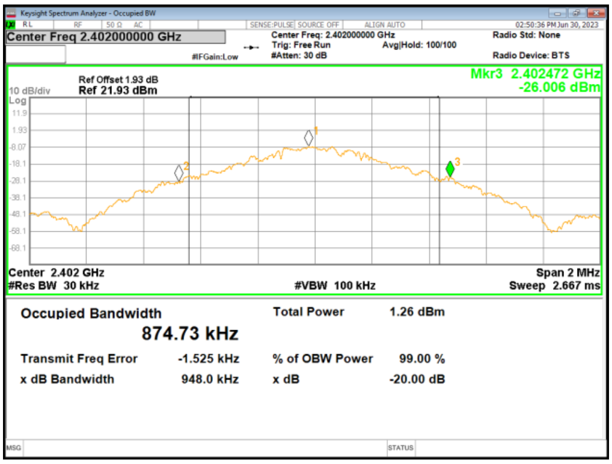


Highest channel

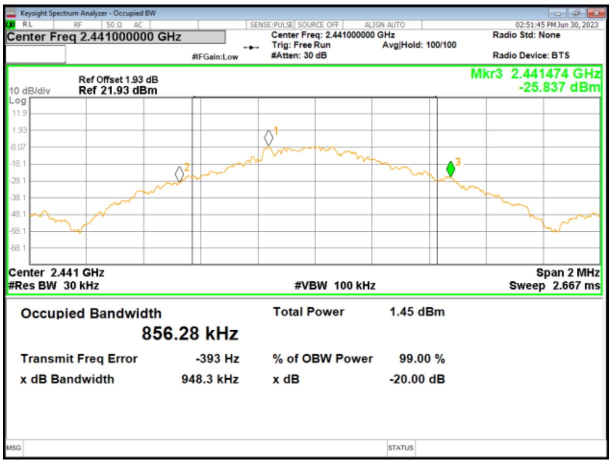
AC6956C Module

Test plot as follows:

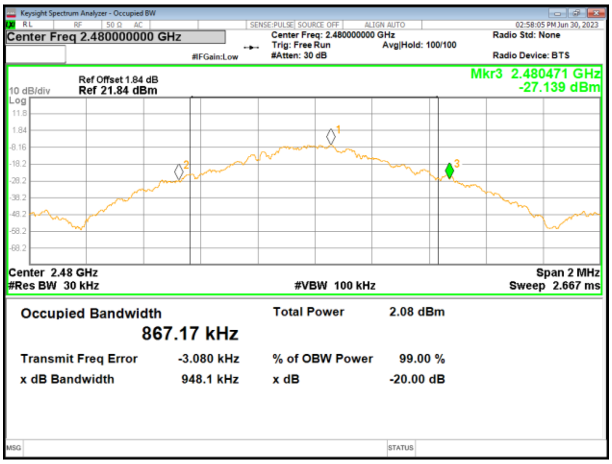
Test mode:	GFSK mode



Lowest channel

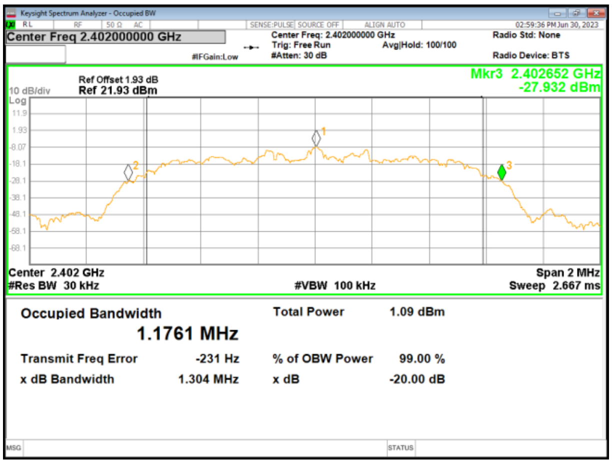


Middle channel

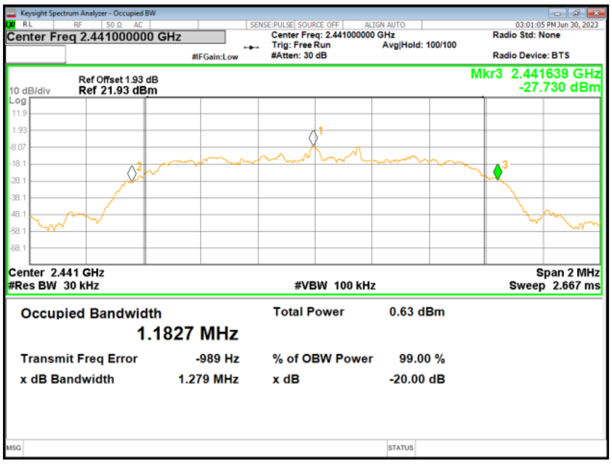


Highest channel

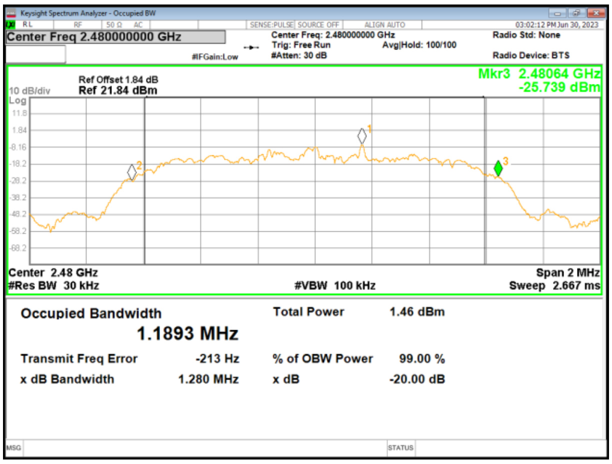
Test mode:	π/4-DQPSK mode



Lowest channel

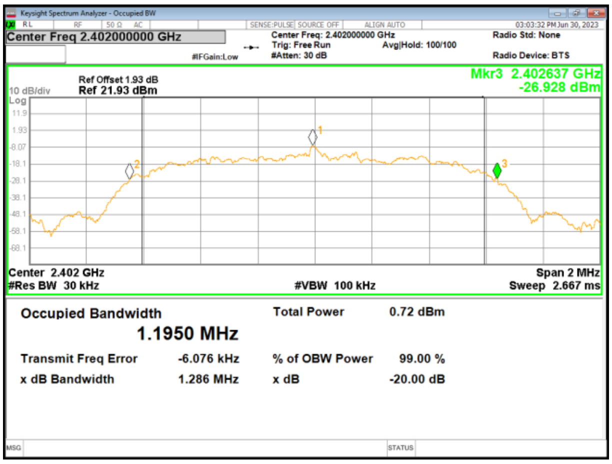


Middle channel

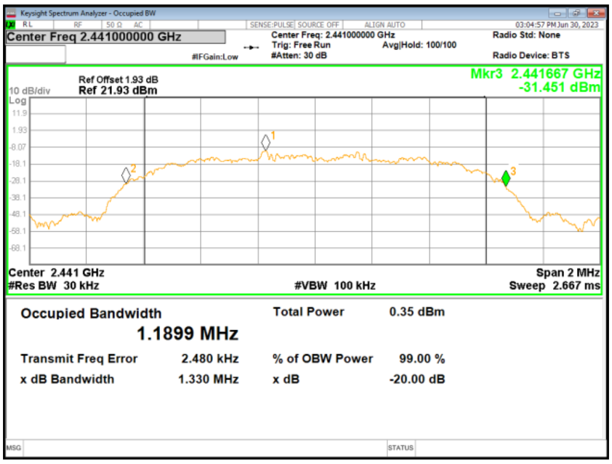


Highest channel

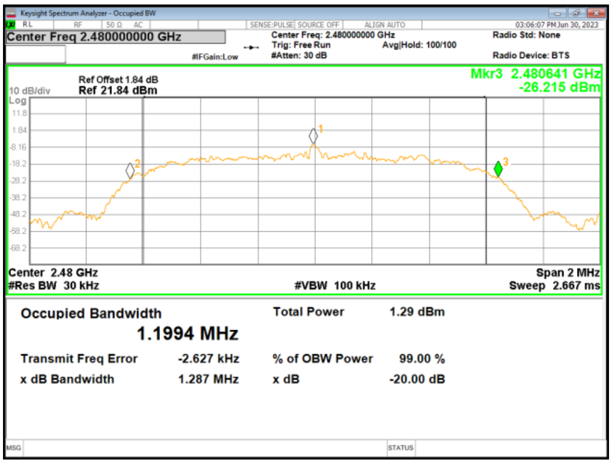
Test mode:	8-DPSK mode



Lowest channel

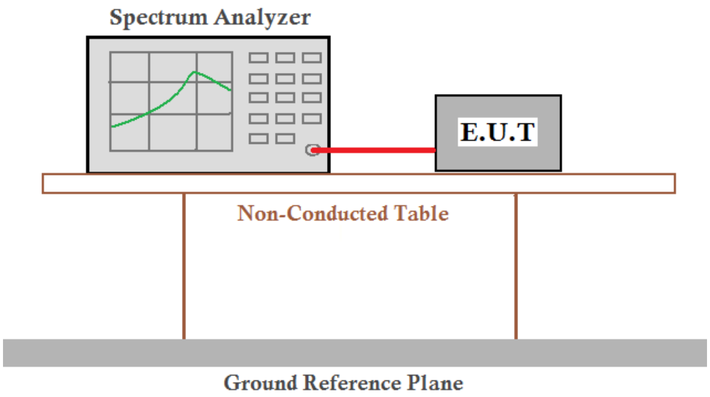


Middle channel



Highest channel

4.5 Carrier Frequencies Separation

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)	
Test Method:	ANSI C63.10:2013	
Receiver setup:	RBW=30KHz, VBW=100KHz, detector=Peak	
Limit:	>0.025MHz or 2/3 of the 20dB bandwidth (whichever is greater)	
Test setup:		
Test Instruments:	Refer to section 3.0 for details	
Test mode:	Refer to section 2.2 for details	
Test environment:	Temp.: 23.4°C	Humid.: 54%RH
Test voltage:	DC 3.7V	
Test results:	Pass	

Measurement Result

AC6976D Module

Mode	Test channel	Carrier Frequencies Separation (MHz)	Limit (MHz)	Result
GFSK	Lowest	0.9945	0.6341	Pass
	Middle	1.0005	0.6340	Pass
	Highest	1.0020	0.6351	Pass
π/4-DQPSK	Lowest	1.0050	0.8880	Pass
	Middle	0.9465	0.8553	Pass
	Highest	0.9165	0.8513	Pass
8-DPSK	Lowest	1.1850	0.8633	Pass
	Middle	1.0560	0.8633	Pass
	Highest	1.0005	0.8627	Pass

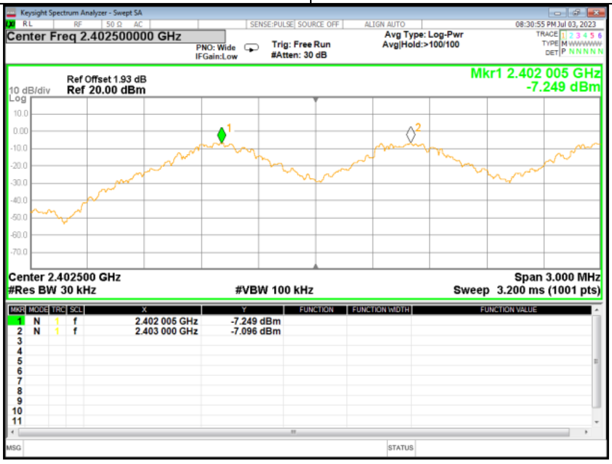
AC6956C Module

Mode	Test channel	Carrier Frequencies Separation (MHz)	Limit (MHz)	Result
GFSK	Lowest	0.9945	0.6320	Pass
	Middle	1.0200	0.6322	Pass
	Highest	1.0035	0.6321	Pass
π/4-DQPSK	Lowest	1.1835	0.8693	Pass
	Middle	0.8685	0.8527	Pass
	Highest	0.9960	0.8533	Pass
8-DPSK	Lowest	1.0170	0.8573	Pass
	Middle	0.8895	0.8867	Pass
	Highest	1.1265	0.8580	Pass

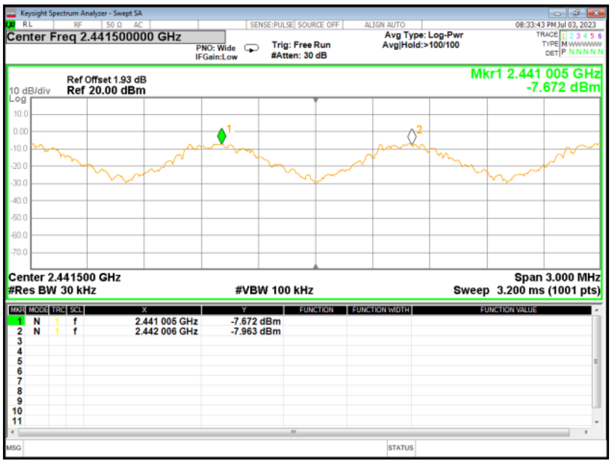
Note: According to section 4.4

Test plot as follows: **AC6976D Module**

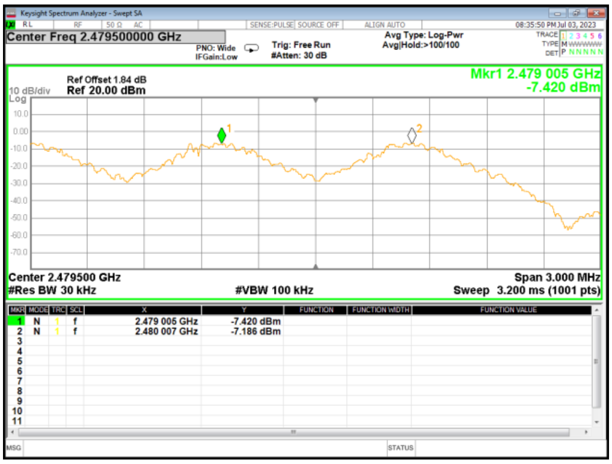
Modulation mode:	GFSK



Lowest channel

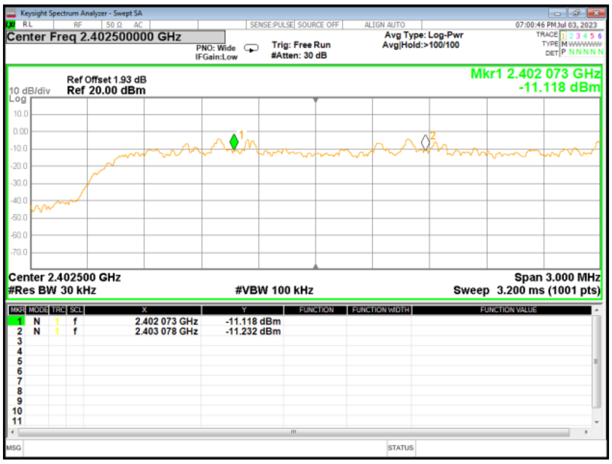


Middle channel



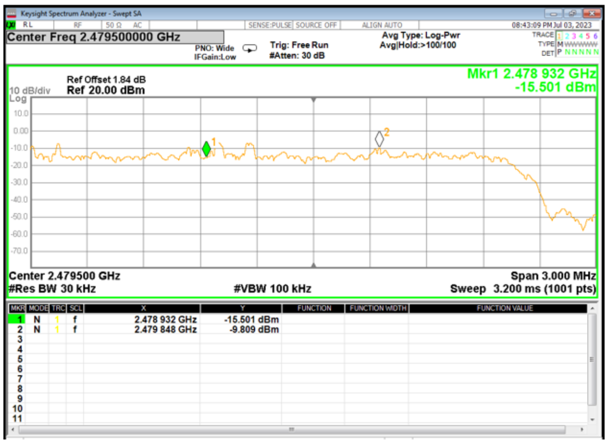
Highest channel

Test mode:	π/4-DQPSK



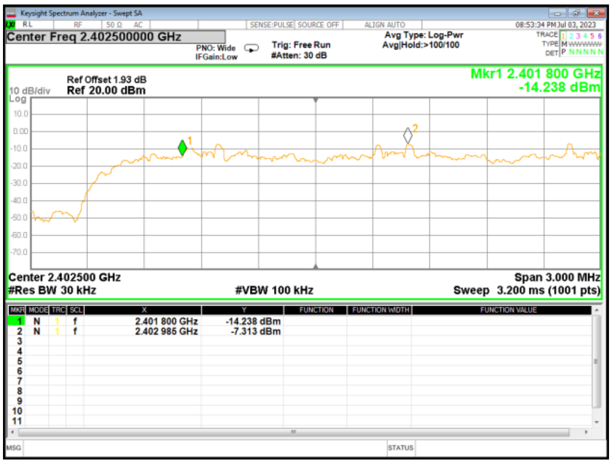
Lowest channel

Middle channel

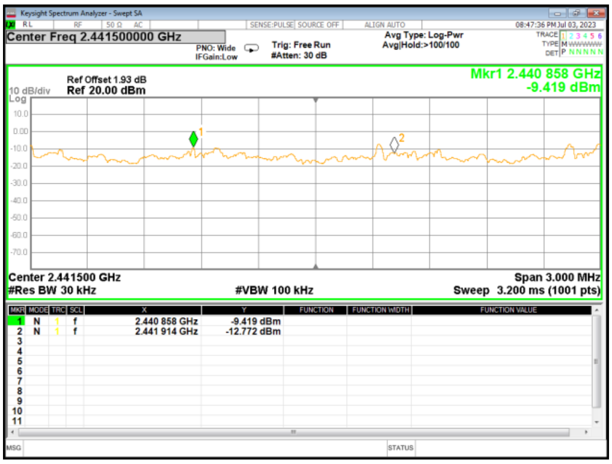


Highest channel

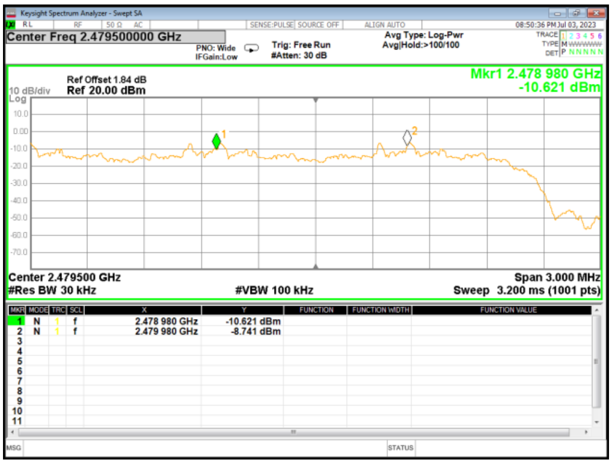
Test mode:	8-DPSK



Lowest channel



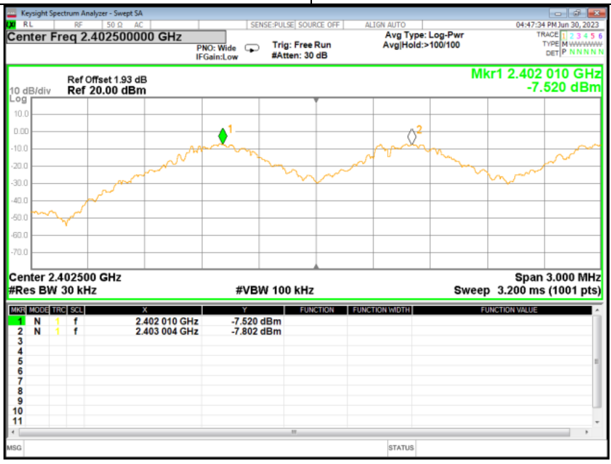
Middle channel



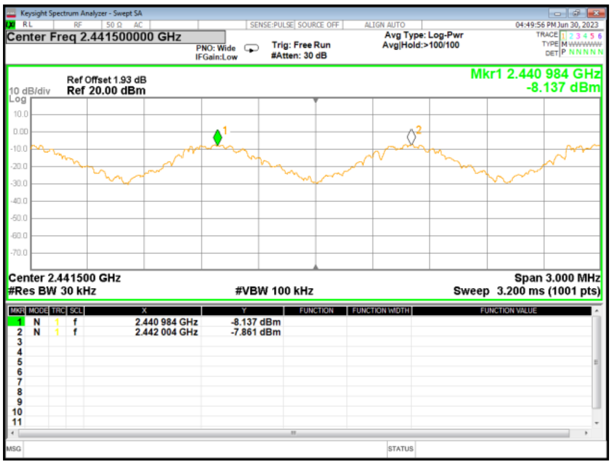
Highest channel

Test plot as follows: **AC6956C Module**

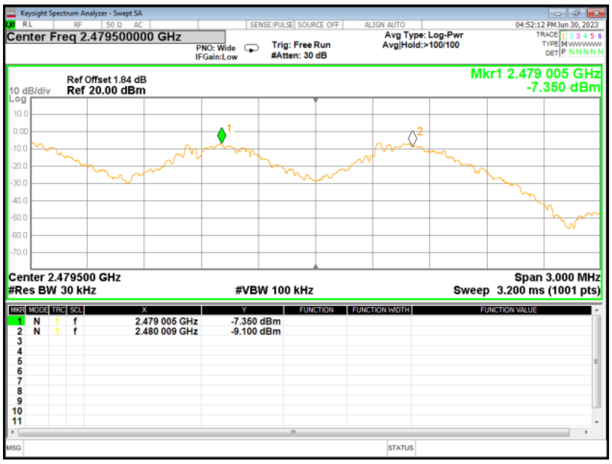
Modulation mode:	GFSK



Lowest channel

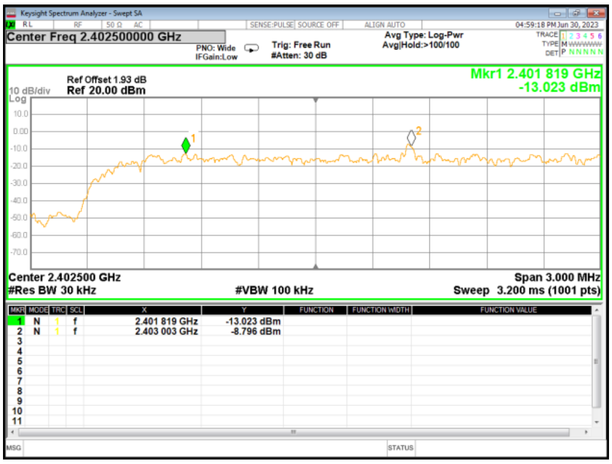


Middle channel

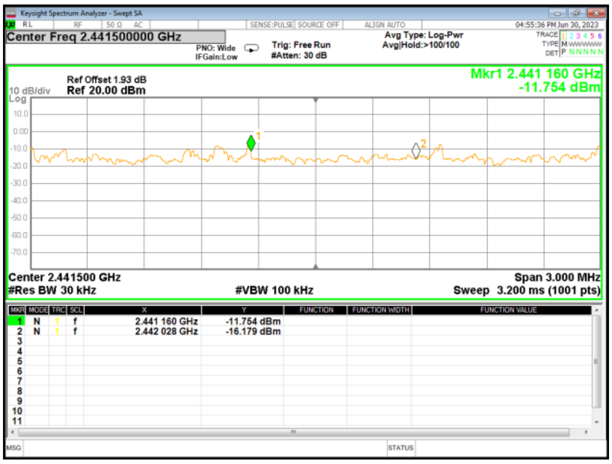


Highest channel

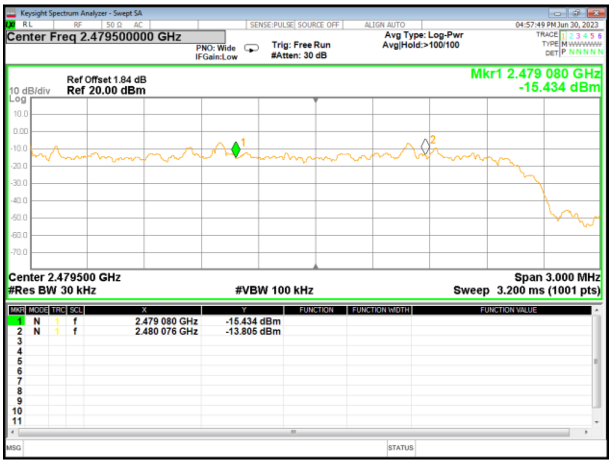
Test mode:	π/4-DQPSK



Lowest channel

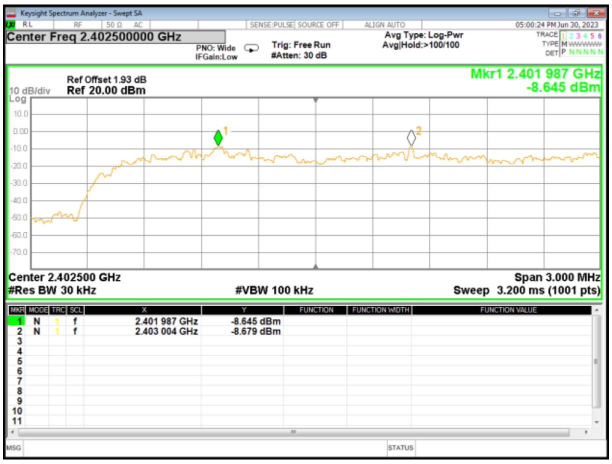


Middle channel

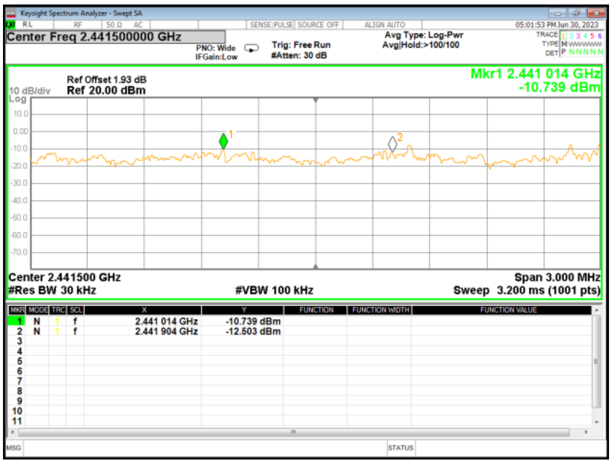


Highest channel

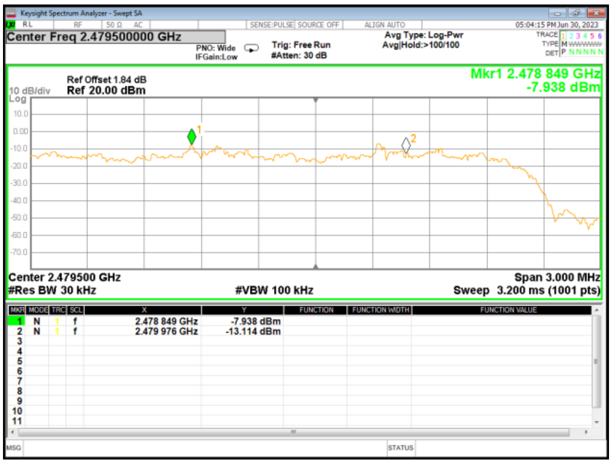
Test mode:	8-DPSK



Lowest channel

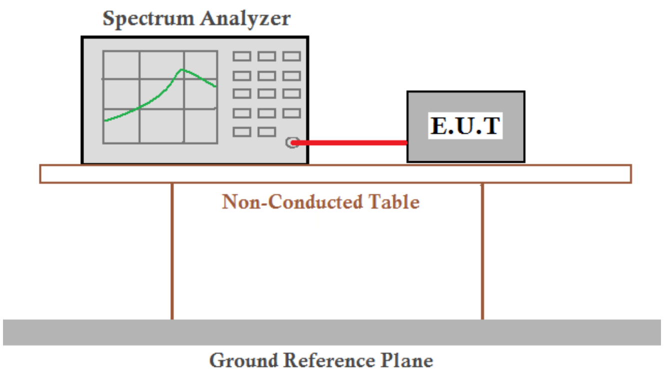


Middle channel



Highest channel

4.6 Hopping Channel Number

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)(iii)
Test Method:	ANSI C63.10:2013
Receiver setup:	RBW=100kHz, VBW=300kHz, Frequency range=2400MHz-2483.5MHz, Detector=Peak
Limit:	15 channels
Test setup:	
Test Instruments:	Refer to section 3.0 for details
Test mode:	Refer to section 2.2 for details
Test environment:	Temp.: 23.4°C Humid.: 54%RH
Test voltage:	DC 3.7V
Test results:	Pass

Measurement Data:

AC6976D Module

Mode	Hopping channel numbers	Limit	Result
GFSK	79	15	Pass
π/4-DQPSK	79	15	Pass
8-DPSK	79	15	Pass

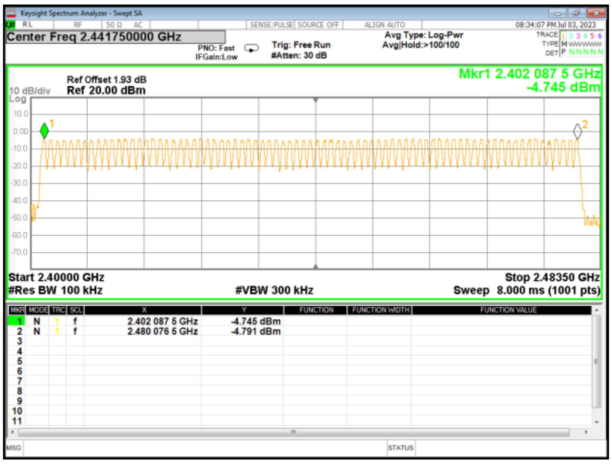
AC6956C Module

Mode	Hopping channel numbers	Limit	Result
GFSK	79	15	Pass
π/4-DQPSK	79	15	Pass
8-DPSK	79	15	Pass

Test plot as follows:

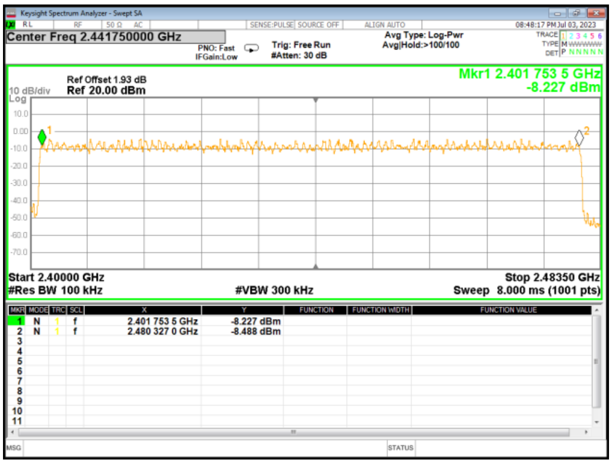
AC6976D Module

Test mode:	GFSK



Test mode:	π/4-DQPSK

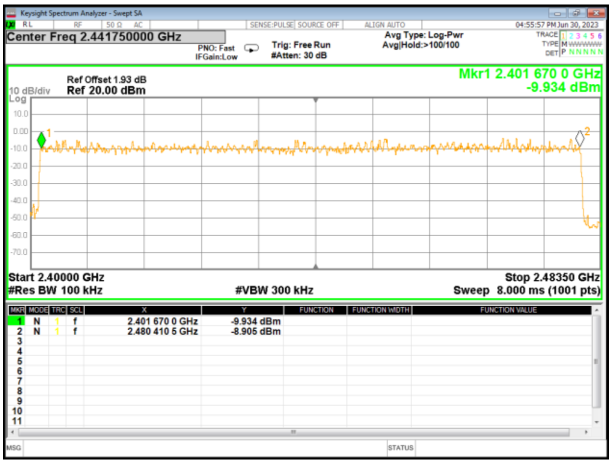
Test mode:	8-DPSK



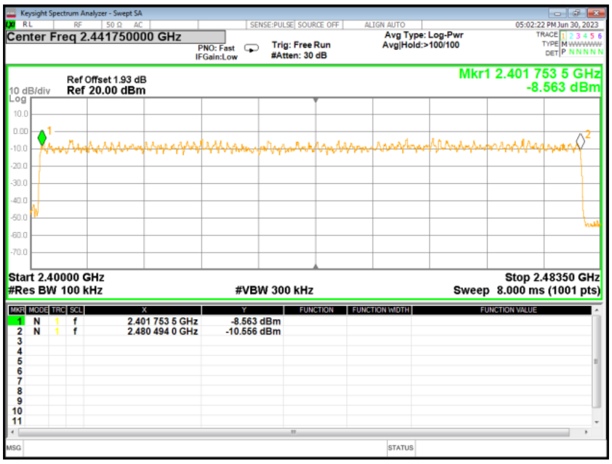
AC6956C Module

Test mode:	GFSK

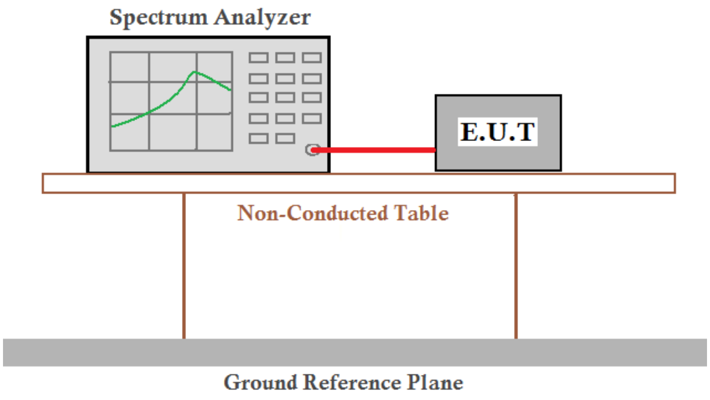
Test mode:	π/4-DQPSK



Test mode:	8-DPSK



4.7 Dwell Time

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)(iii)
Test Method:	ANSI C63.10:2013
Receiver setup:	RBW=1MHz, VBW=3MHz, Span=0Hz, Detector=Peak
Limit:	0.4 Second
Test setup:	
Test Instruments:	Refer to section 3.0 for details
Test mode:	Refer to section 2.2 for details
Test environment:	Temp.: 23.4°C Humid.: 54%RH
Test voltage:	DC 3.7V
Test results:	Pass

Measurement Result

AC6976D Module

GFSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	DH1	120.64	400	Pass
2441MHz	DH3	261.28	400	Pass
2441MHz	DH5	307.20	400	Pass

Remarks:

The test period: T= 0.4 Second/Channel x 79 Channel = 31.6 s

Test channel: 2441MHz as blow

DH1 time slot=0.377 (ms)*(1600/ (2*79))*31.6=120.64ms

DH3 time slot=1.633 (ms)*(1600/ (4*79))*31.6=261.28ms

DH5 time slot=2.880 (ms)*(1600/ (6*79))*31.6=307.20ms

π/4-DQPSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	2DH1	123.52	400	Pass
2441MHz	2DH3	262.08	400	Pass
2441MHz	2DH5	307.95	400	Pass

Remarks:

The test period: T= 0.4 Second/Channel x 79 Channel = 31.6 s

Test channel: 2441MHz as blow

2DH1 time slot=0.386 (ms)*(1600/ (2*79))*31.6=123.52 ms

2DH3 time slot=1.638 (ms)*(1600/ (4*79))*31.6=262.08 ms

2DH5 time slot=2.887(ms)*(1600/ (6*79))*31.6=307.95 ms

8-DPSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	3DH1	123.52	400	Pass
2441MHz	3DH3	261.76	400	Pass
2441MHz	3DH5	307.95	400	Pass

Remarks:

The test period: T= 0.4 Second/Channel x 79 Channel = 31.6 s

Test channel: 2441MHz as blow

3DH1 time slot=0.386(ms)*(1600/ (2*79))*31.6=123.52ms

3DH3 time slot=1.636 (ms)*(1600/ (4*79))*31.6=261.76ms

3DH5 time slot=2.887(ms)*(1600/ (6*79))*31.6=307.95ms

AC6956C Module

GFSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	DH1	120.64	400	Pass
2441MHz	DH3	261.12	400	Pass
2441MHz	DH5	307.31	400	Pass

Remarks:

The test period: T= 0.4 Second/Channel x 79 Channel = 31.6 s

Test channel: 2441MHz as blow

DH1 time slot=0.377 (ms)*(1600/ (2*79))*31.6=120.64ms

DH3 time slot=1.632 (ms)*(1600/ (4*79))*31.6=261.12ms

DH5 time slot=2.881 (ms)*(1600/ (6*79))*31.6=307.31ms

π/4-DQPSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	2DH1	123.52	400	Pass
2441MHz	2DH3	262.24	400	Pass
2441MHz	2DH5	307.84	400	Pass

Remarks:

The test period: T= 0.4 Second/Channel x 79 Channel = 31.6 s

Test channel: 2441MHz as blow

2DH1 time slot=0.386 (ms)*(1600/ (2*79))*31.6=123.52 ms

2DH3 time slot=1.639 (ms)*(1600/ (4*79))*31.6=262.24 ms

2DH5 time slot=2.886(ms)*(1600/ (6*79))*31.6=307.84 ms

8-DPSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	3DH1	123.84	400	Pass
2441MHz	3DH3	261.76	400	Pass
2441MHz	3DH5	308.05	400	Pass

Remarks:

The test period: T= 0.4 Second/Channel x 79 Channel = 31.6 s

Test channel: 2441MHz as blow

3DH1 time slot=0.387 (ms)*(1600/ (2*79))*31.6=123.84ms

3DH3 time slot=1.636 (ms)*(1600/ (4*79))*31.6=261.76ms

3DH5 time slot=2.888 (ms)*(1600/ (6*79))*31.6=308.05ms

Test plot as follows:

AC6976D Module

GFSK mode:

Test channel:	2441MHz

DH1

DH3

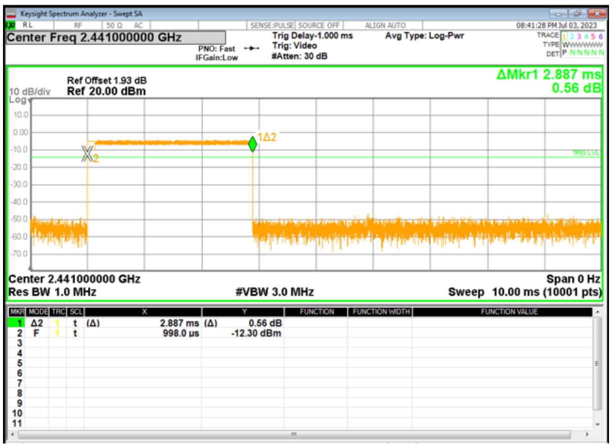
DH5

π/4-DQPSK mode

Test channel:	2441MHz

2DH1

2DH3



2DH5

8-DPSK

Test channel:	2441MHz

3DH1

3DH3

3DH5

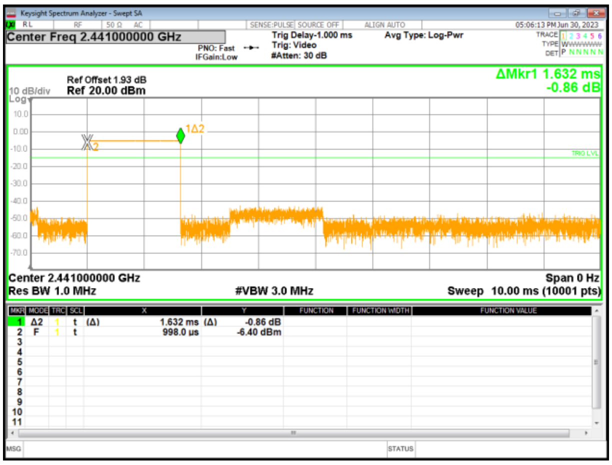
Test plot as follows:

AC6956C Module

GFSK mode:

Test channel:	2441MHz

DH1



DH3

DH5