

APPENDIX B: PLOTS FOR SYSTEM VERIFICATION

System Performance Check Report

Summary

Dipole	Frequency [MHz]	TSL	Power [dBm]	Dev. 1g [%]	Dev. 10g [%]	Dev. Peak [%]	Iso. Error [%]
D2450V2 – SN902	2450.0	HSL	17.0	4.5	2.4	9.0	–2.5

Exposure Conditions

Phantom Section, TSL	Position, Test Distance [mm]	Band	Group, UID	Frequency [MHz], Channel Number	Conversion Factor	TSL Conductivity [S/m]	TSL Permittivity
Flat, HSL	,		, 0–	2450.0, 0	7.59	1.84	39.7

Hardware Setup

Phantom	TSL, Measured Date	Probe, Calibration Date	DAE, Calibration Date
Twin–SAM V4.0 (30deg probe tilt) – 1351	HBBL–600–10000 Charge:xxxx, 2020–May–18	EX3DV4 – SN7374, 2019–10–28	DAE4 Sn640, 2019–10–11

Scans Setup

	Area Scan	Zoom Scan
Grid Extents [mm]	60.0 x 90.0	30.0 x 30.0 x 30.0
Grid Steps [mm]	15.0 x 15.0	6.0 x 6.0 x 5.0
Sensor Surface [mm]	3.0	1.4
Graded Grid	No	No
Grading Ratio	n/a	n/a
MAIA	N/A	N/A
Surface Detection	VMS	VMS + 6p
Scan Method	Measured	Measured

Measurement Results

	Area Scan	Zoom Scan
Date	2020–05–18, 10:27	2020–05–18, 10:32
psSAR1g [W/Kg]	2.74	2.80
psSAR10g [W/Kg]	1.25	1.28
Power Drift [dB]	0.00	0.02
Power Scaling	Disabled	Disabled
Scaling Factor [dB]		
TSL Correction	No correction	No correction

