

Appendix D.4: Maximum conducted output power

Test Result Channel Power

Test Mode	Antenna	Freq(MHz)	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant0	5180	7.31	100.00	0.00	7.31	≤23.98	PASS
		5200	7.69	100.00	0.00	7.69	≤23.98	PASS
		5240	8.25	100.00	0.00	8.25	≤23.98	PASS
11N20SISO	Ant0	5180	7.82	100.00	0.00	7.82	≤23.98	PASS
		5200	7.67	100.00	0.00	7.67	≤23.98	PASS
		5240	8.42	100.00	0.00	8.42	≤23.98	PASS
11N40SISO	Ant0	5190	7.32	100.00	0.00	7.32	≤23.98	PASS
		5230	8.03	100.00	0.00	8.03	≤23.98	PASS
11AC20SISO	Ant0	5180	7.27	100.00	0.00	7.27	≤23.98	PASS
		5200	7.73	100.00	0.00	7.73	≤23.98	PASS
		5240	8.49	100.00	0.00	8.49	≤23.98	PASS
11AC40SISO	Ant0	5190	7.26	100.00	0.00	7.26	≤23.98	PASS
		5230	7.88	100.00	0.00	7.88	≤23.98	PASS
11AC80SISO	Ant0	5210	6.77	100.00	0.00	6.77	≤23.98	PASS
11AX20SISO	Ant0	5180	6.88	100.00	0.00	6.88	≤23.98	PASS
		5200	7.33	100.00	0.00	7.33	≤23.98	PASS
		5240	8.04	100.00	0.00	8.04	≤23.98	PASS
11AX40SISO	Ant0	5190	7.17	100.00	0.00	7.17	≤23.98	PASS
		5230	7.74	100.00	0.00	7.74	≤23.98	PASS
11AX80SISO	Ant0	5210	7.71	100.00	0.00	7.71	≤23.98	PASS

Appendix D.5: Maximum power spectral density

Test Result

TestMode	Antenna	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant0	5180	-4.50	≤11.00	PASS
		5200	-4.04	≤11.00	PASS
		5240	-3.43	≤11.00	PASS
11N20SISO	Ant0	5180	-4.26	≤11.00	PASS
		5200	-3.31	≤11.00	PASS
		5240	-3.50	≤11.00	PASS
11N40SISO	Ant0	5190	-7.54	≤11.00	PASS
		5230	-6.80	≤11.00	PASS
11AC20SISO	Ant0	5180	-4.75	≤11.00	PASS
		5200	-4.23	≤11.00	PASS
		5240	-3.44	≤11.00	PASS
11AC40SISO	Ant0	5190	-7.56	≤11.00	PASS
		5230	-6.95	≤11.00	PASS
11AC80SISO	Ant0	5210	-9.93	≤11.00	PASS
11AX20SISO	Ant0	5180	-5.46	≤11.00	PASS
		5200	-4.95	≤11.00	PASS
		5240	-4.11	≤11.00	PASS
11AX40SISO	Ant0	5190	-7.89	≤11.00	PASS
		5230	-7.32	≤11.00	PASS
11AX80SISO	Ant0	5210	-10.03	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.

2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs

11A_Ant0_5180



11A_Ant0_5200



11A_Ant0_5240



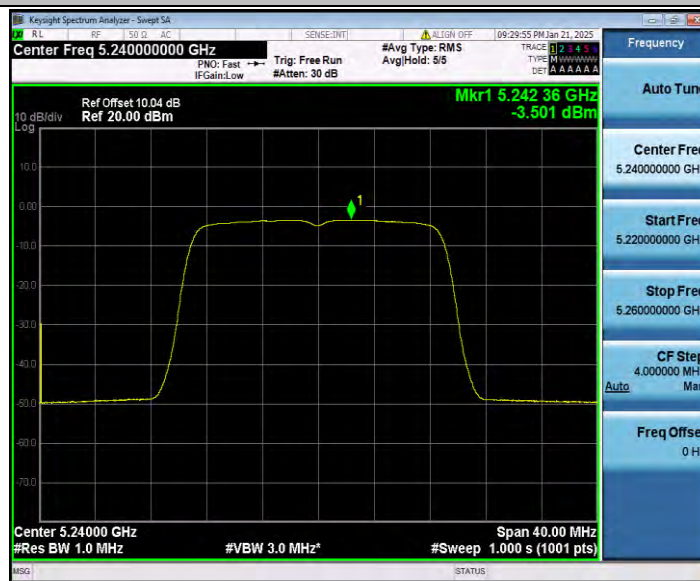
11N20SISO_Ant0_5180



11N20SISO_Ant0_5200



11N20SISO_Ant0_5240



11N40SISO_Ant0_5190



11N40SISO_Ant0_5230



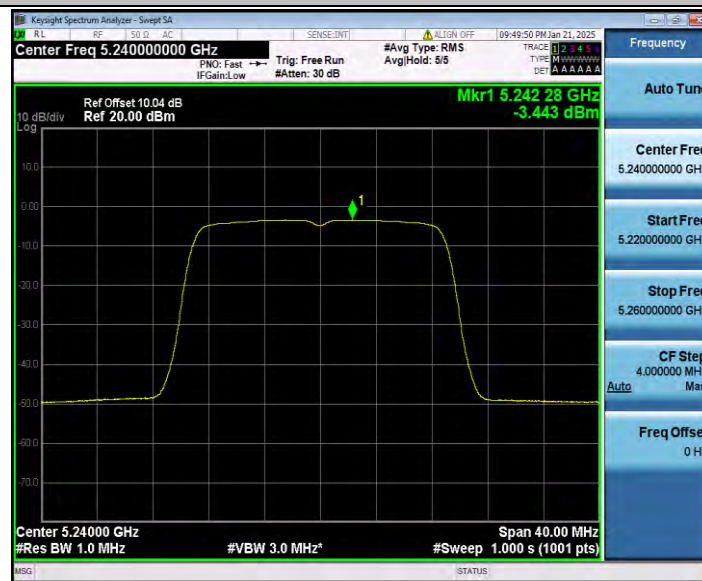
11AC20SISO_Ant0_5180



11AC20SISO_Ant0_5200



11AC20SISO_Ant0_5240



11AC40SISO_Ant0_5190



11AC40SISO_Ant0_5230



11AC80SISO_Ant0_5210



11AX20SISO_Ant0_5180



11AX20SISO_Ant0_5200



11AX20SISO_Ant0_5240



11AX40SISO_Ant0_5190



11AX40SISO_Ant0_5230



11AX80SISO_Ant0_5210

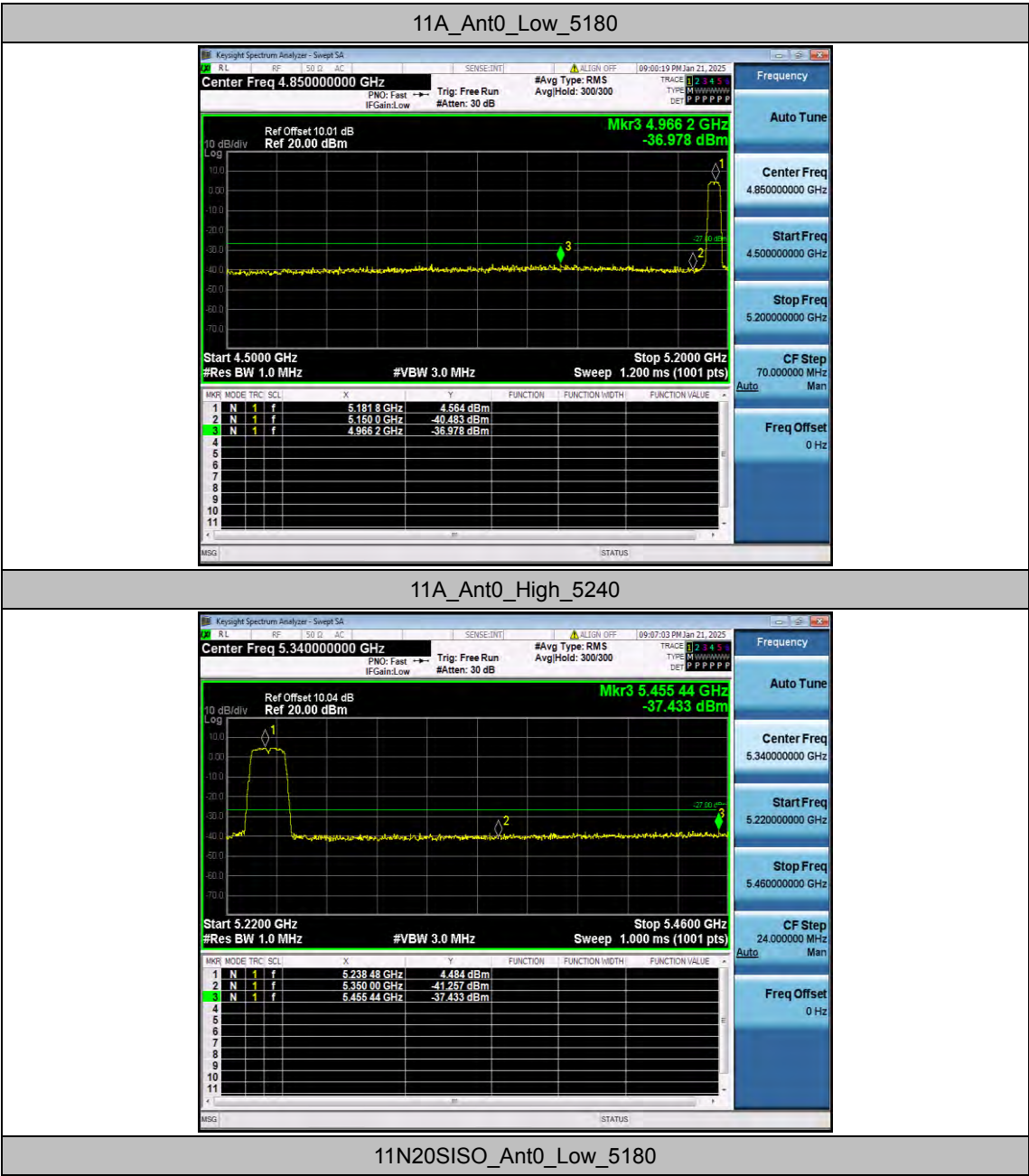


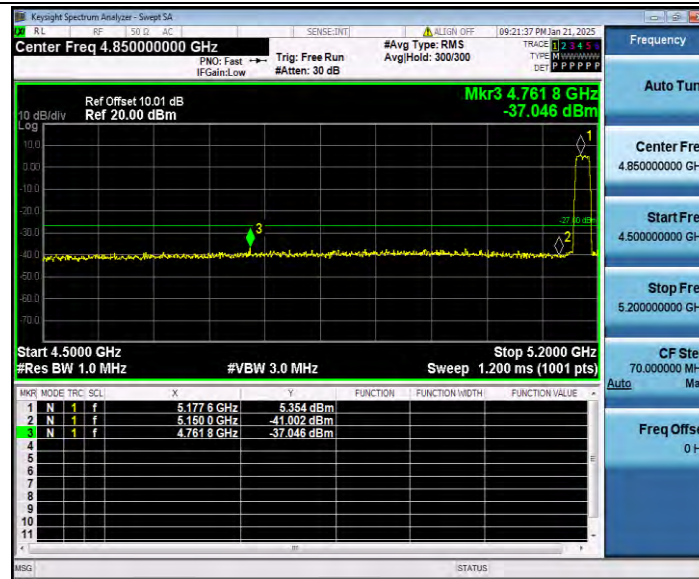
Appendix D.6: Band edge measurements

Test Result B1

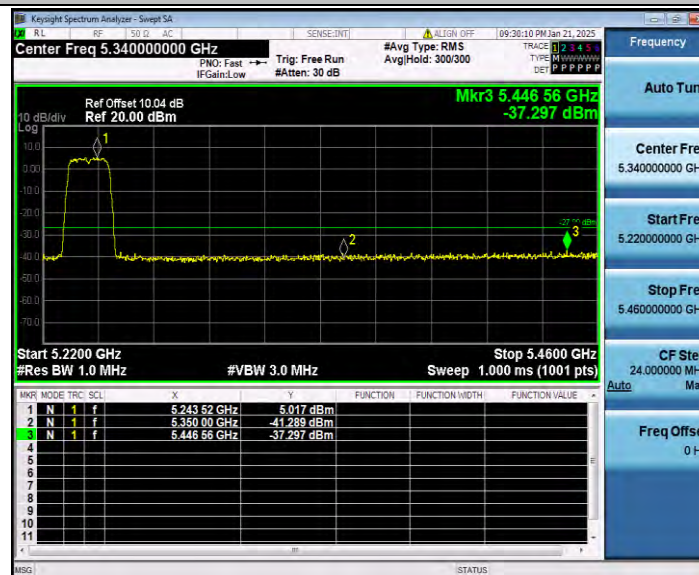
TestMode	Antenna	ChName	Freq(MHz)	Result[dBm]	Limit[dBm]	Verdict
11A	Ant0	Low	5180	-36.98	≤ -27	PASS
		High	5240	-37.43	≤ -27	PASS
11N20SISO	Ant0	Low	5180	-37.05	≤ -27	PASS
		High	5240	-37.3	≤ -27	PASS
11N40SISO	Ant0	Low	5190	-36.5	≤ -27	PASS
		High	5230	-37.88	≤ -27	PASS
11AC20SISO	Ant0	Low	5180	-37.18	≤ -27	PASS
		High	5240	-37.4	≤ -27	PASS
11AC40SISO	Ant0	Low	5190	-37.81	≤ -27	PASS
		High	5230	-37.24	≤ -27	PASS
11AC80SISO	Ant0	Low	5210	-37.63	≤ -27	PASS
		High	5210	-37.1	≤ -27	PASS
11AX20SISO	Ant0	Low	5180	-37.66	≤ -27	PASS
		High	5240	-37.62	≤ -27	PASS
11AX40SISO	Ant0	Low	5190	-37.69	≤ -27	PASS
		High	5230	-37.87	≤ -27	PASS
11AX80SISO	Ant0	Low	5210	-37.44	≤ -27	PASS
		High	5210	-37.77	≤ -27	PASS

Test Graphs B1

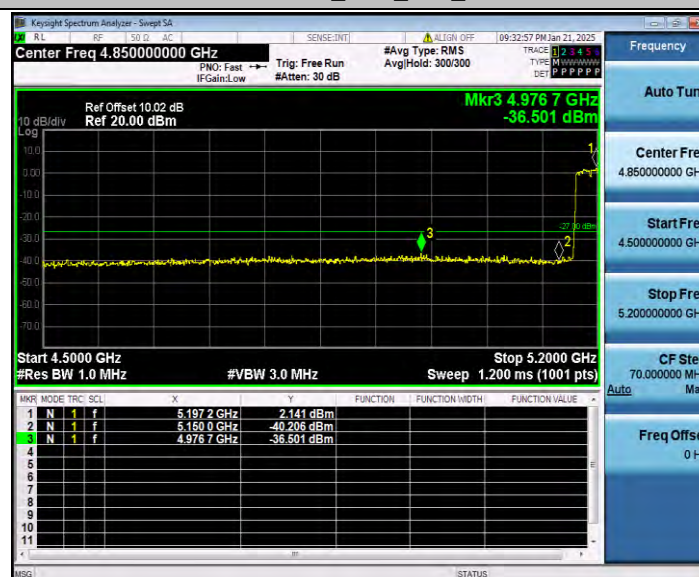




11N20SISO_Ant0_High_5240



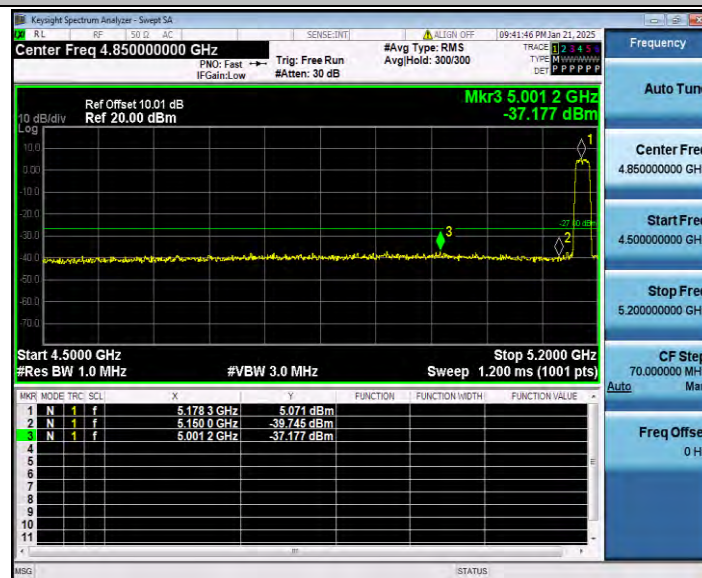
11N40SISO_Ant0_Low_5190



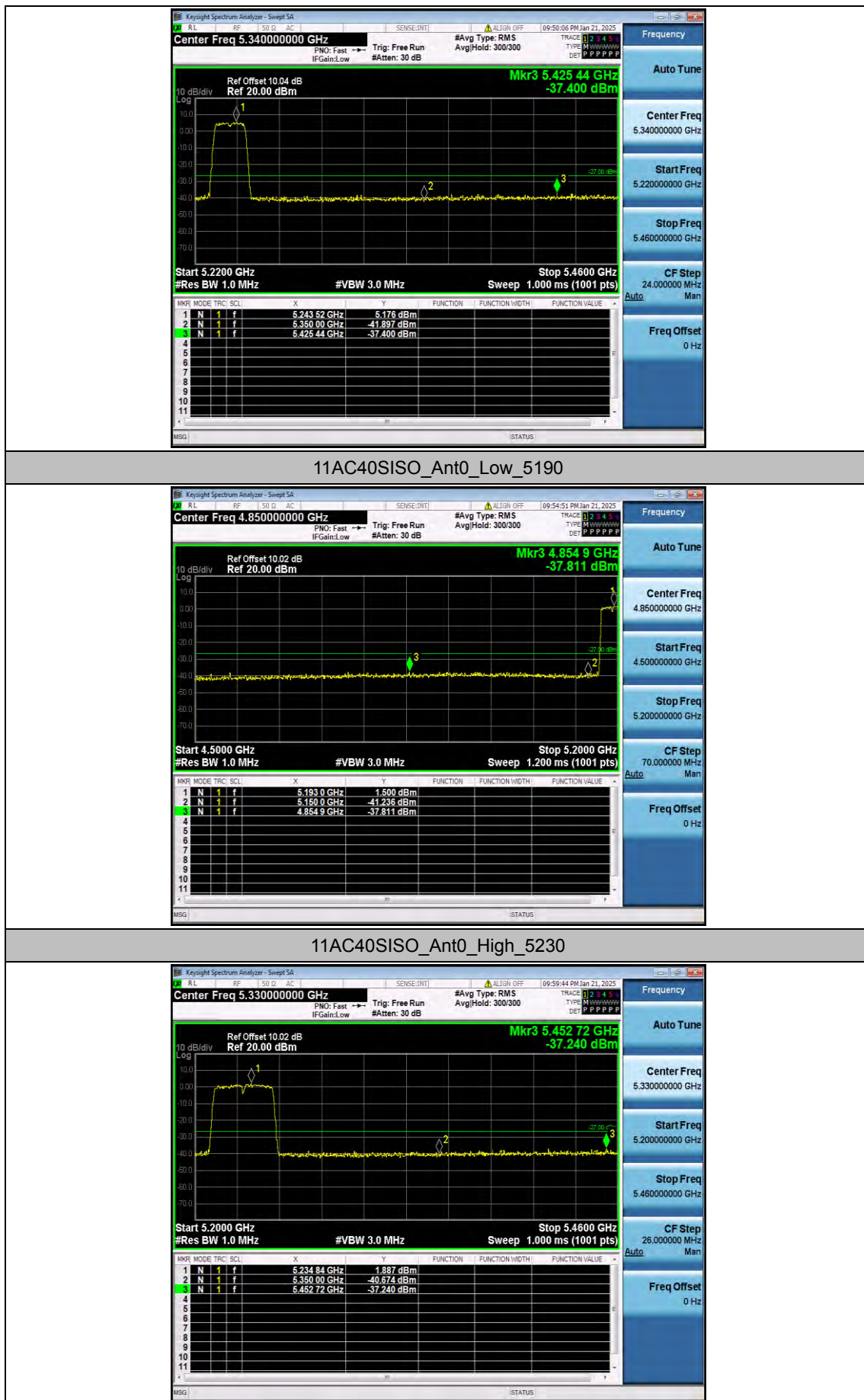
11N40SISO_Ant0_High_5230



11AC20SISO_Ant0_Low_5180



11AC20SISO_Ant0_High_5240



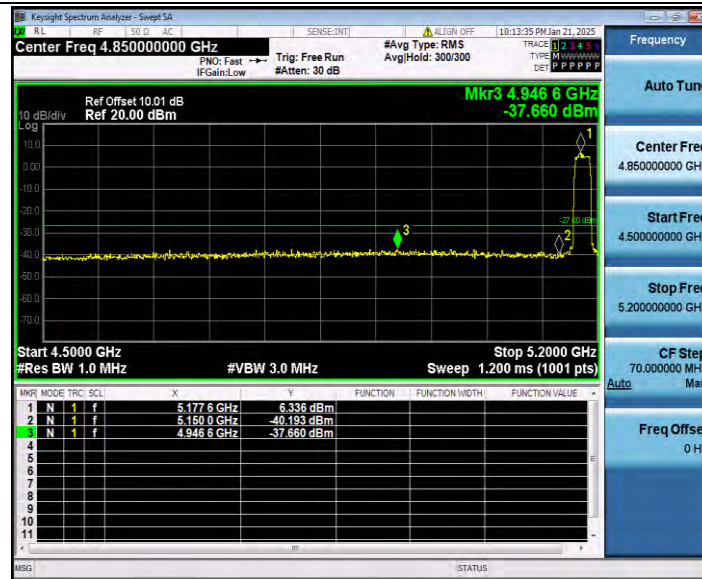
11AC80SISO_Ant0_Low_5210



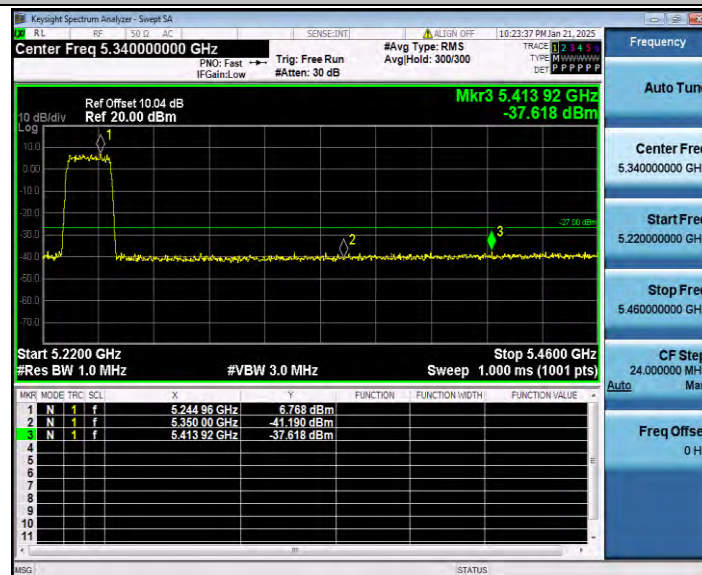
11AC80SISO_Ant0_High_5210



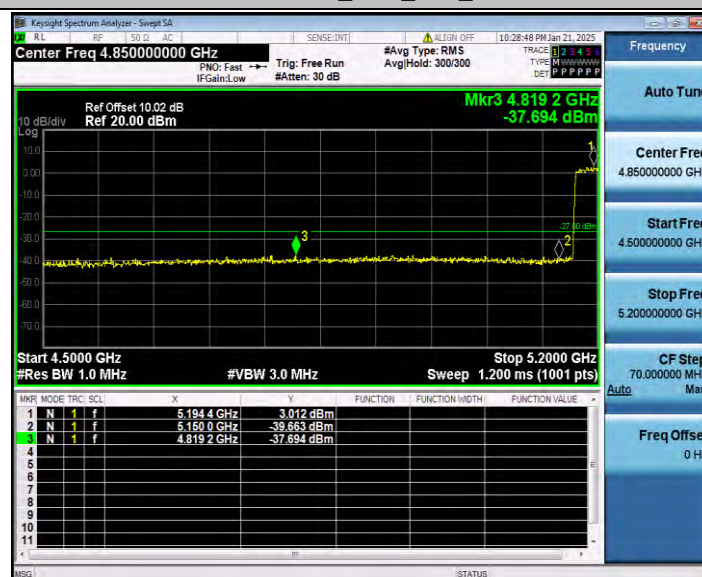
11AX20SISO_Ant0_Low_5180



11AX20SISO_Ant0_High_5240



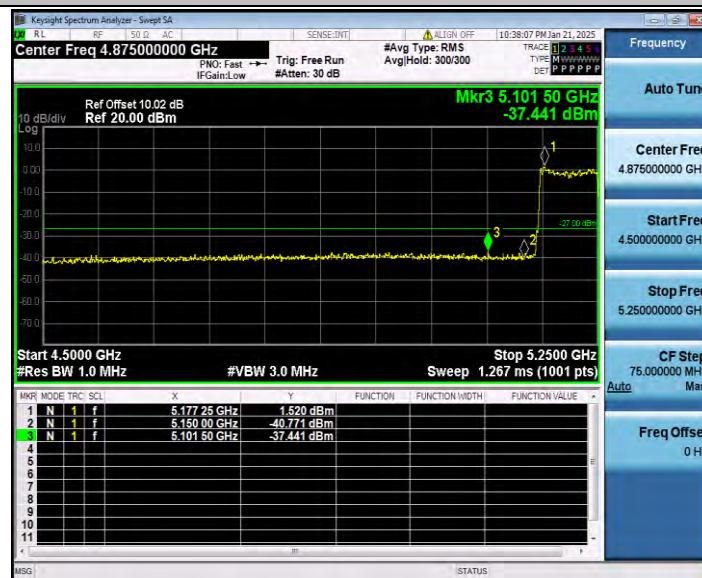
11AX40SISO_Ant0_Low_5190



11AX40SISO_Ant0_High_5230



11AX80SISO_Ant0_Low_5210



11AX80SISO_Ant0_High_5210

Appendix D.7: Conducted Spurious Emission

Test Result

TestMode	Antenna	Freq(MHz)	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant0	5180	30~5140	4935.6	-46.22	≤-27	PASS
			5360~27000	26581.63	-37.39	≤-27	PASS
		5200	30~5140	2683.96	-46.59	≤-27	PASS
			5360~27000	26090.4	-37.33	≤-27	PASS
		5240	30~5140	2519.59	-42.89	≤-27	PASS
			5360~27000	24793.44	-37.29	≤-27	PASS
11N20SISO	Ant0	5180	30~5140	2600.67	-44.77	≤-27	PASS
			5360~27000	24227.19	-37.63	≤-27	PASS
		5200	30~5140	2525.72	-38.58	≤-27	PASS
			5360~27000	25238.5	-36.98	≤-27	PASS
		5240	30~5140	4957.06	-45.87	≤-27	PASS
			5360~27000	25907.18	-37.53	≤-27	PASS
11N40SISO	Ant0	5190	30~5140	3302.44	-46.26	≤-27	PASS
			5360~27000	24251	-37.36	≤-27	PASS
		5230	30~5140	3171.46	-46.02	≤-27	PASS
			5360~27000	24278.41	-36.93	≤-27	PASS
11AC20SISO	Ant0	5180	30~5140	4835.1	-46.74	≤-27	PASS
			5360~27000	26521.03	-37.52	≤-27	PASS
		5200	30~5140	2688.05	-46.61	≤-27	PASS
			5360~27000	26828.32	-36.84	≤-27	PASS
		5240	30~5140	2587.04	-38	≤-27	PASS
			5360~27000	24210.6	-37.46	≤-27	PASS
11AC40SISO	Ant0	5190	30~5140	2521.3	-39.38	≤-27	PASS
			5360~27000	25240.67	-36.98	≤-27	PASS
		5230	30~5140	4931.17	-46.46	≤-27	PASS
			5360~27000	24251.72	-36.67	≤-27	PASS
11AC80SISO	Ant0	5210	30~5140	2521.47	-44.57	≤-27	PASS
			5360~27000	24217.82	-36	≤-27	PASS
11AX20SISO	Ant0	5180	30~5140	2586.7	-38.53	≤-27	PASS
			5360~27000	26055.05	-37.12	≤-27	PASS
		5200	30~5140	2621.11	-46.22	≤-27	PASS
			5360~27000	26572.25	-36.62	≤-27	PASS
		5240	30~5140	2589.6	-43.67	≤-27	PASS
			5360~27000	26544.12	-37.34	≤-27	PASS
11AX40SISO	Ant0	5190	30~5140	3271.27	-46.15	≤-27	PASS
			5360~27000	23546.98	-37.13	≤-27	PASS

		5230	30~5140	2519.25	-45.74	≤ -27	PASS
			5360~27000	25623.7	-37.22	≤ -27	PASS
11AX80SISO	Ant0	5210	30~5140	5132.51	-44.66	≤ -27	PASS
			5360~27000	26056.5	-36.44	≤ -27	PASS

Test Graphs

11A_Ant0_5180_30~5140



11A_Ant0_5180_5360~27000



11A_Ant0_5200_30~5140



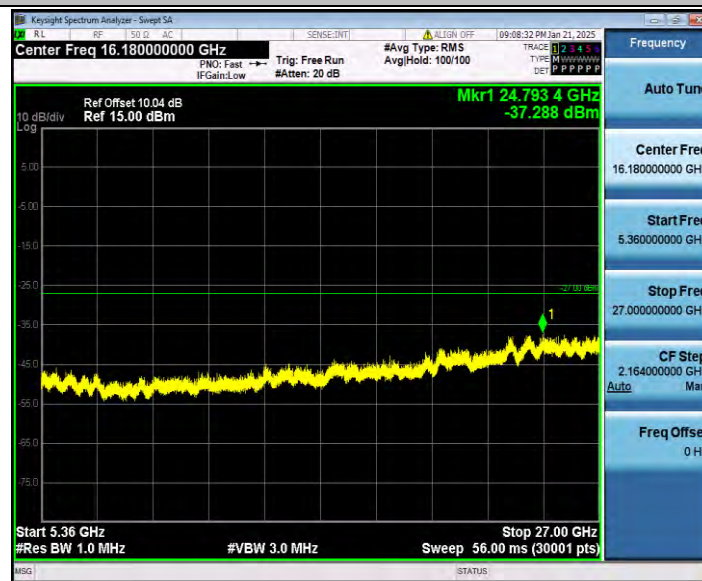
11A_Ant0_5200_5360~27000



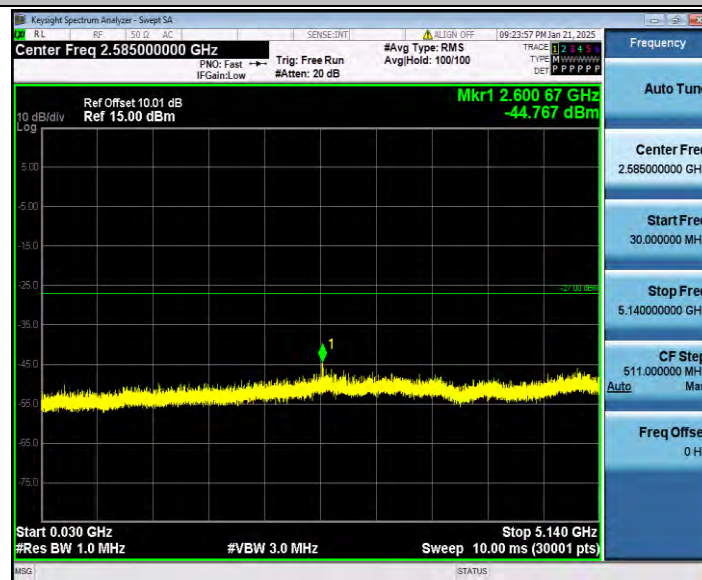
11A_Ant0_5240_30~5140



11A_Ant0_5240_5360~27000



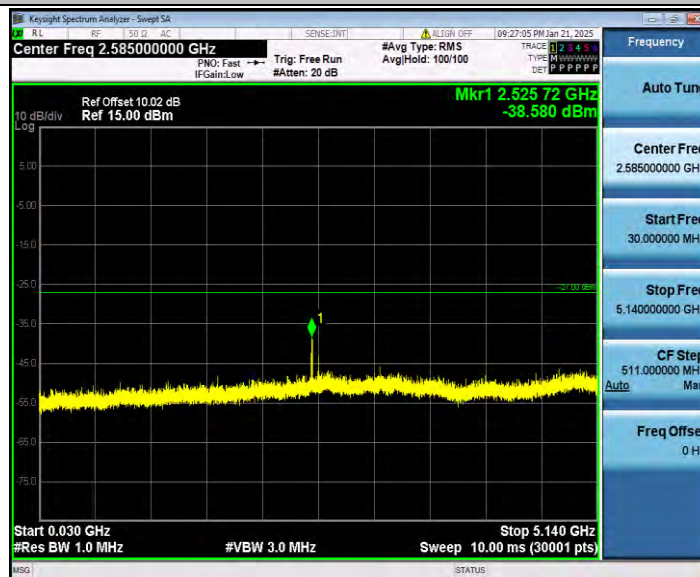
11N20SISO_Ant0_5180_30~5140



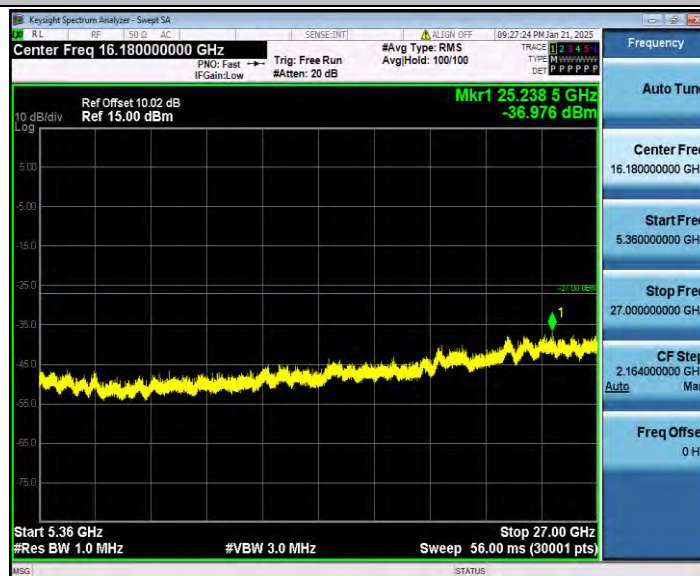
11N20SISO_Ant0_5180_5360~27000



11N20SISO_Ant0_5200_30~5140



11N20SISO_Ant0_5200_5360~27000



11N20SISO_Ant0_5240_30~5140



11N20SISO_Ant0_5240_5360~27000



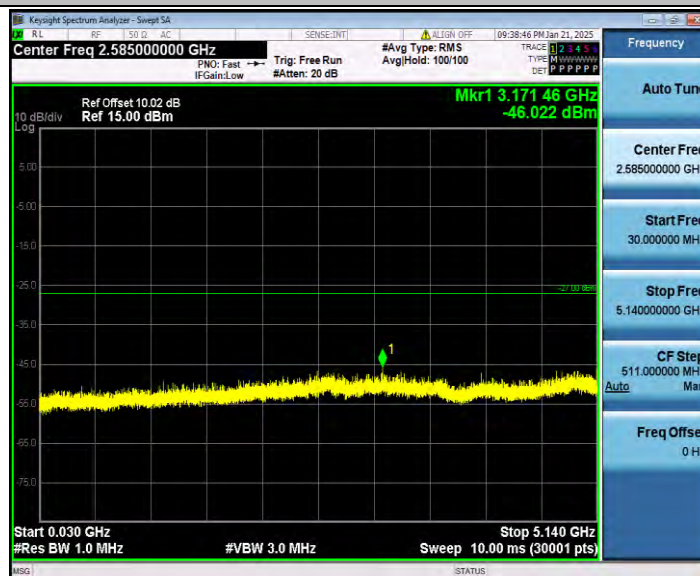
11N40SISO_Ant0_5190_30~5140

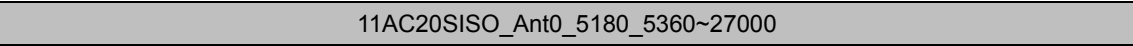
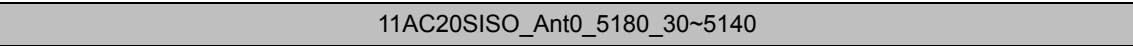


11N40SISO_Ant0_5190_5360~27000



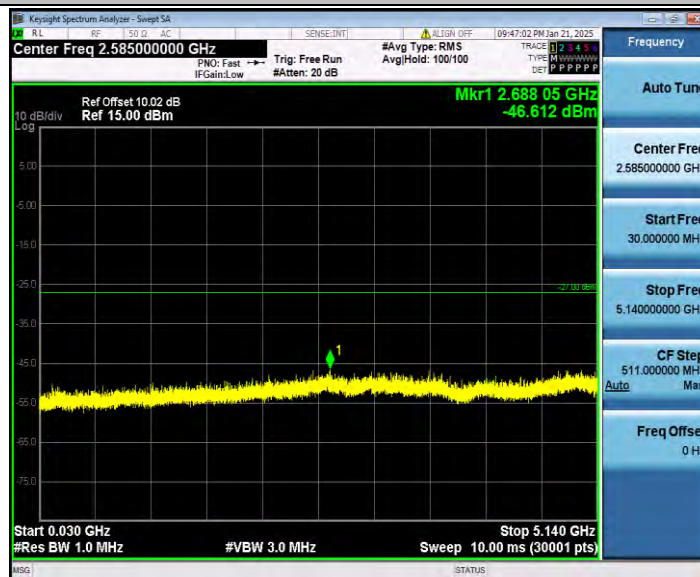
11N40SISO_Ant0_5230_30~5140







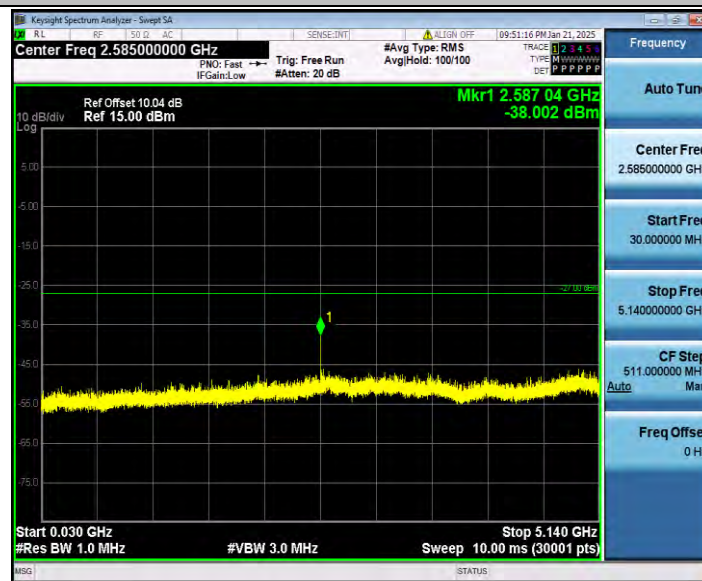
11AC20SISO_Ant0_5200_30~5140



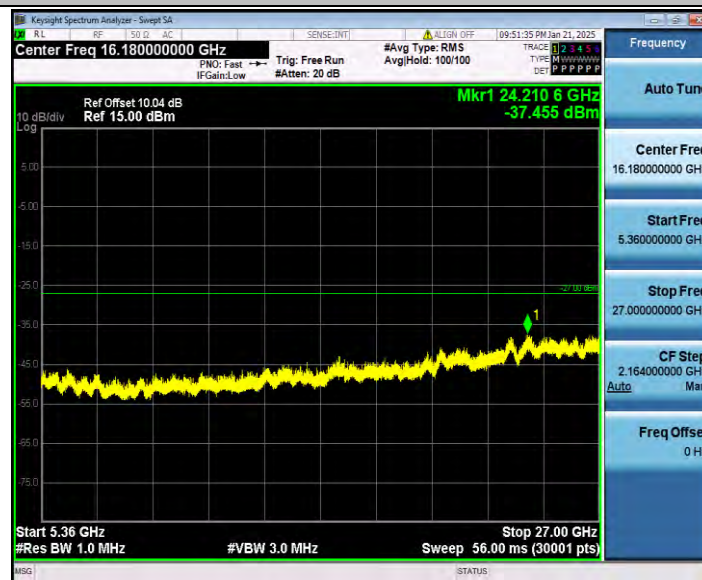
11AC20SISO_Ant0_5200_5360~27000



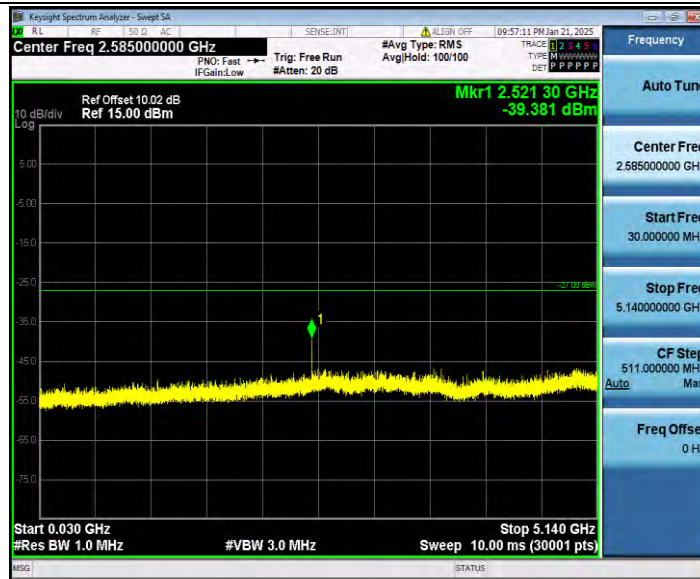
11AC20SISO_Ant0_5240_30~5140



11AC20SISO_Ant0_5240_5360~27000



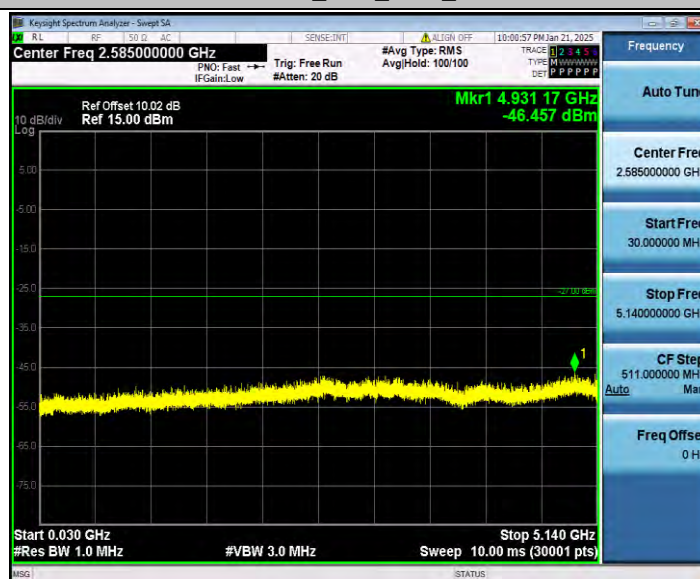
11AC40SISO_Ant0_5190_30~5140

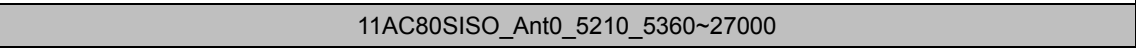
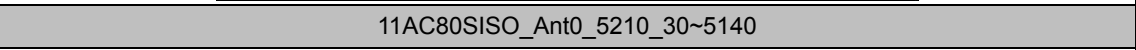


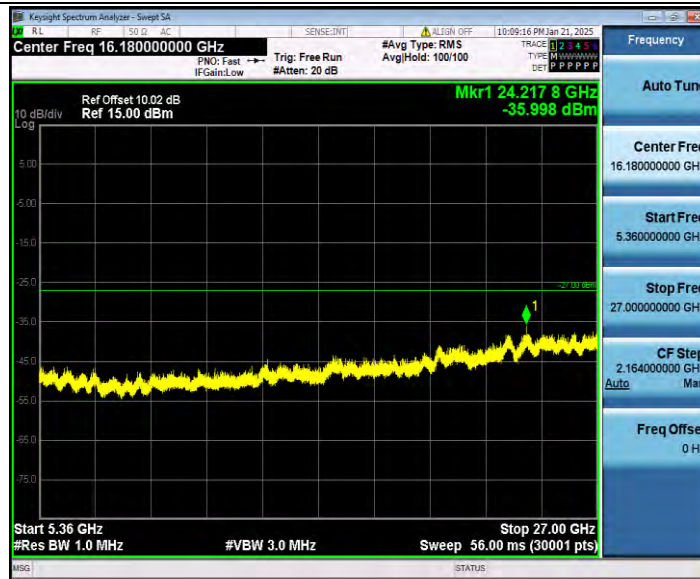
11AC40SISO_Ant0_5190_5360~27000



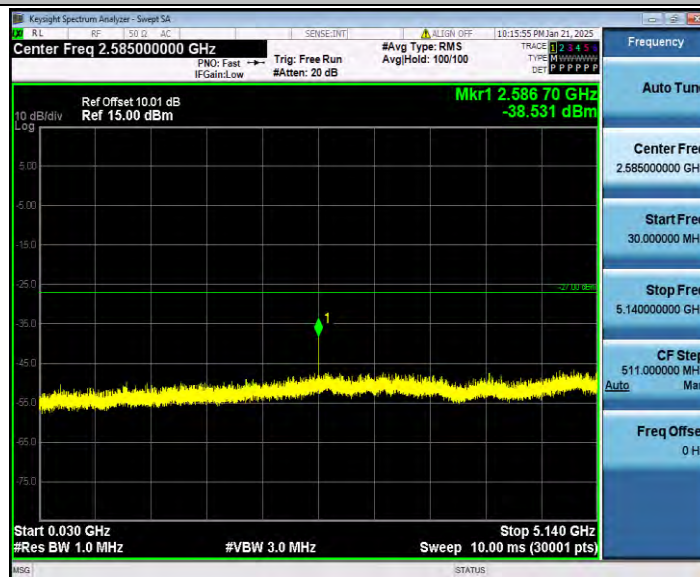
11AC40SISO_Ant0_5230_30~5140







11AX20SISO_Ant0_5180_30~5140



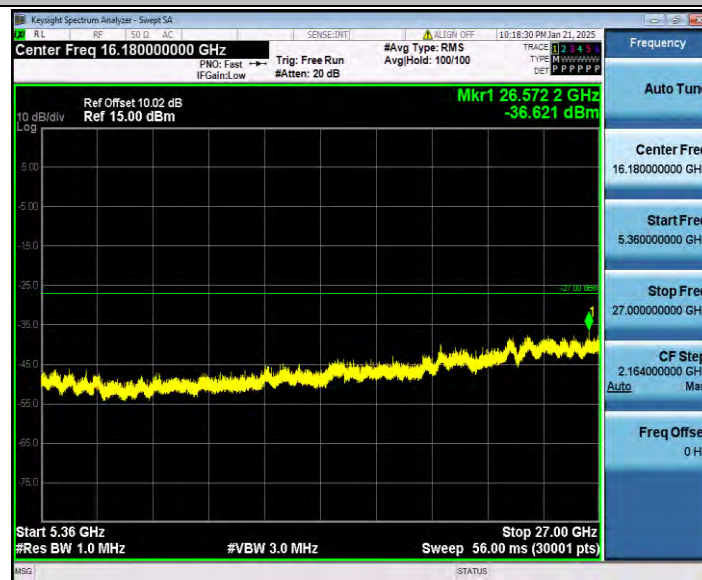
11AX20SISO_Ant0_5180_5360~27000



11AX20SISO_Ant0_5200_30~5140



11AX20SISO_Ant0_5200_5360~27000



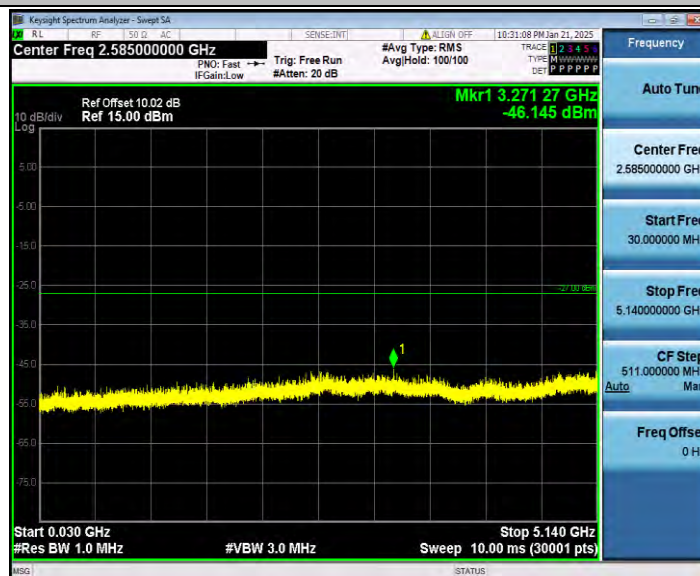
11AX20SISO_Ant0_5240_30~5140



11AX20SISO_Ant0_5240_5360~27000



11AX40SISO_Ant0_5190_30~5140



11AX40SISO_Ant0_5190_5360~27000



11AX40SISO_Ant0_5230_30~5140



11AX40SISO_Ant0_5230_5360~27000



11AX80SISO_Ant0_5210_30~5140



11AX80SISO_Ant0_5210_5360~27000

