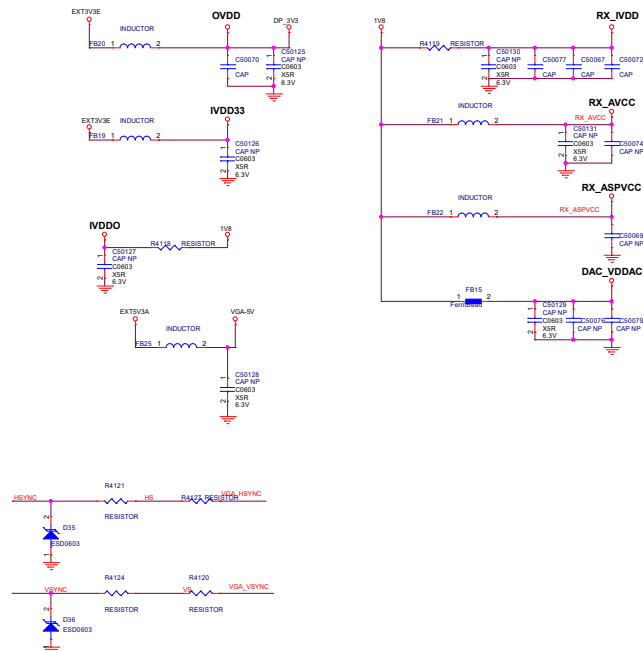
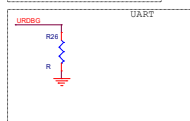
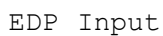
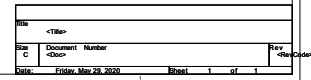
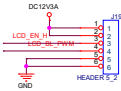


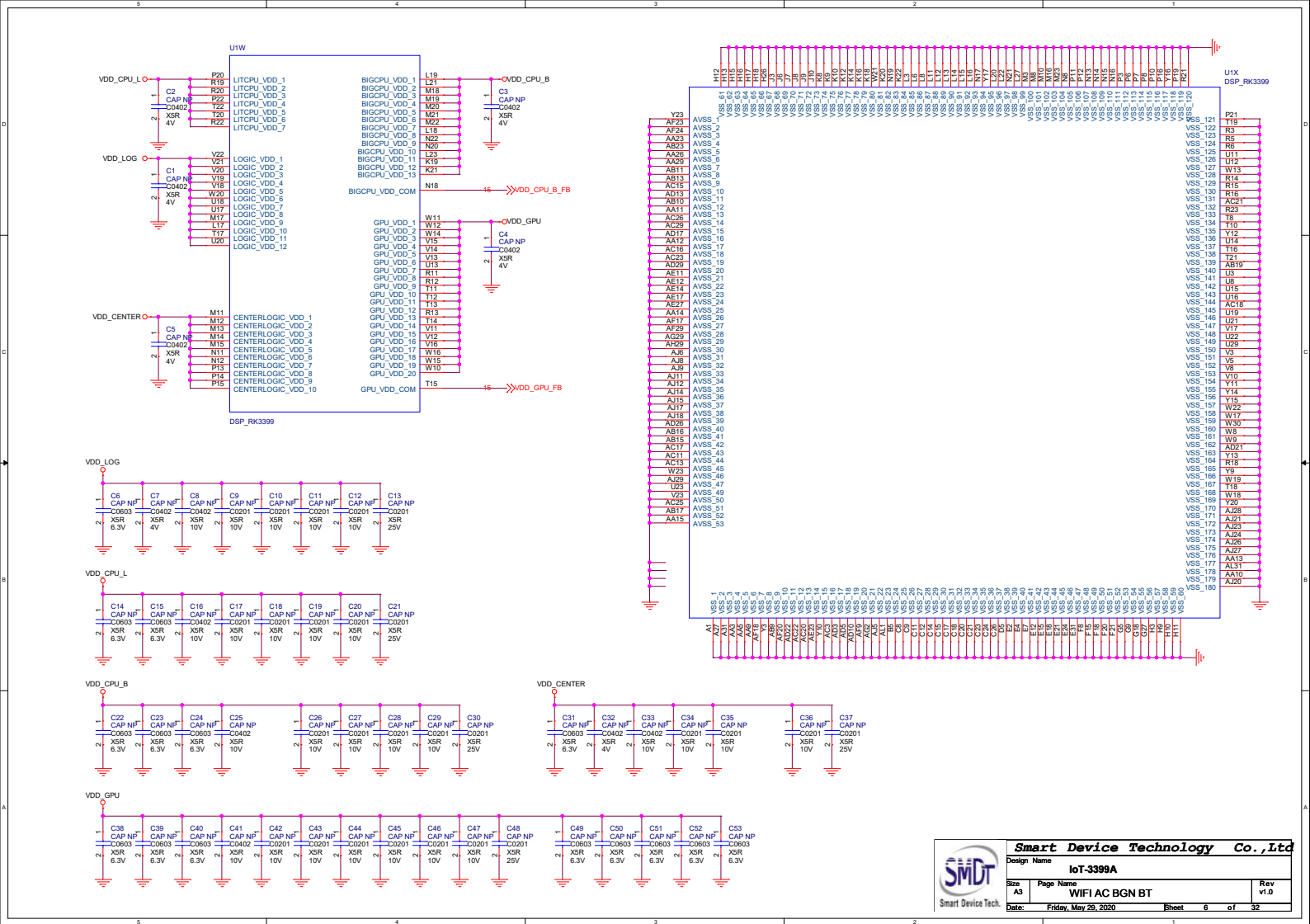
Title <Title>			
Size C	Document Number <Doc>		Rev <RevCode>
Date	Printed: May 29, 2020		Sheet 1 of 1

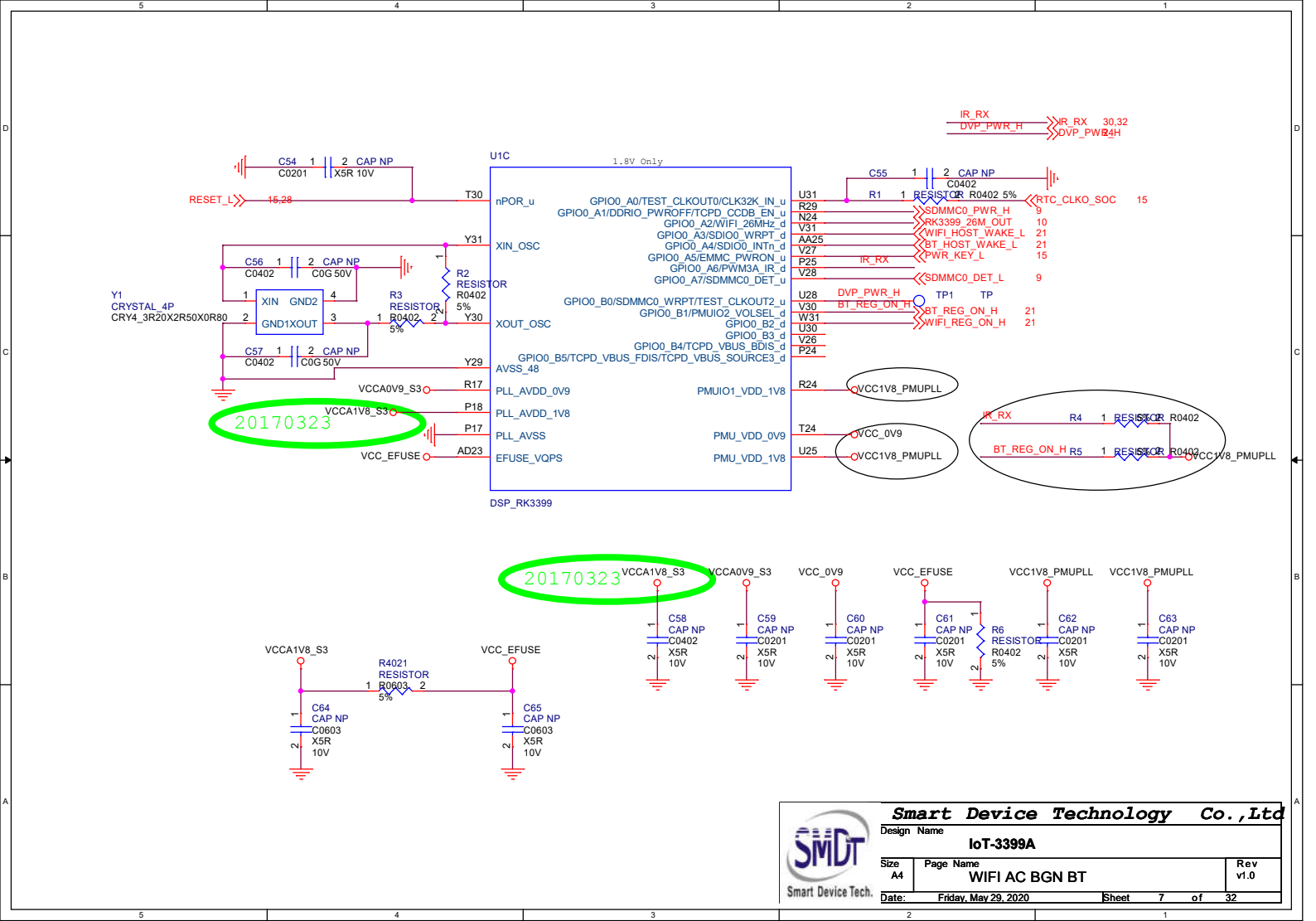


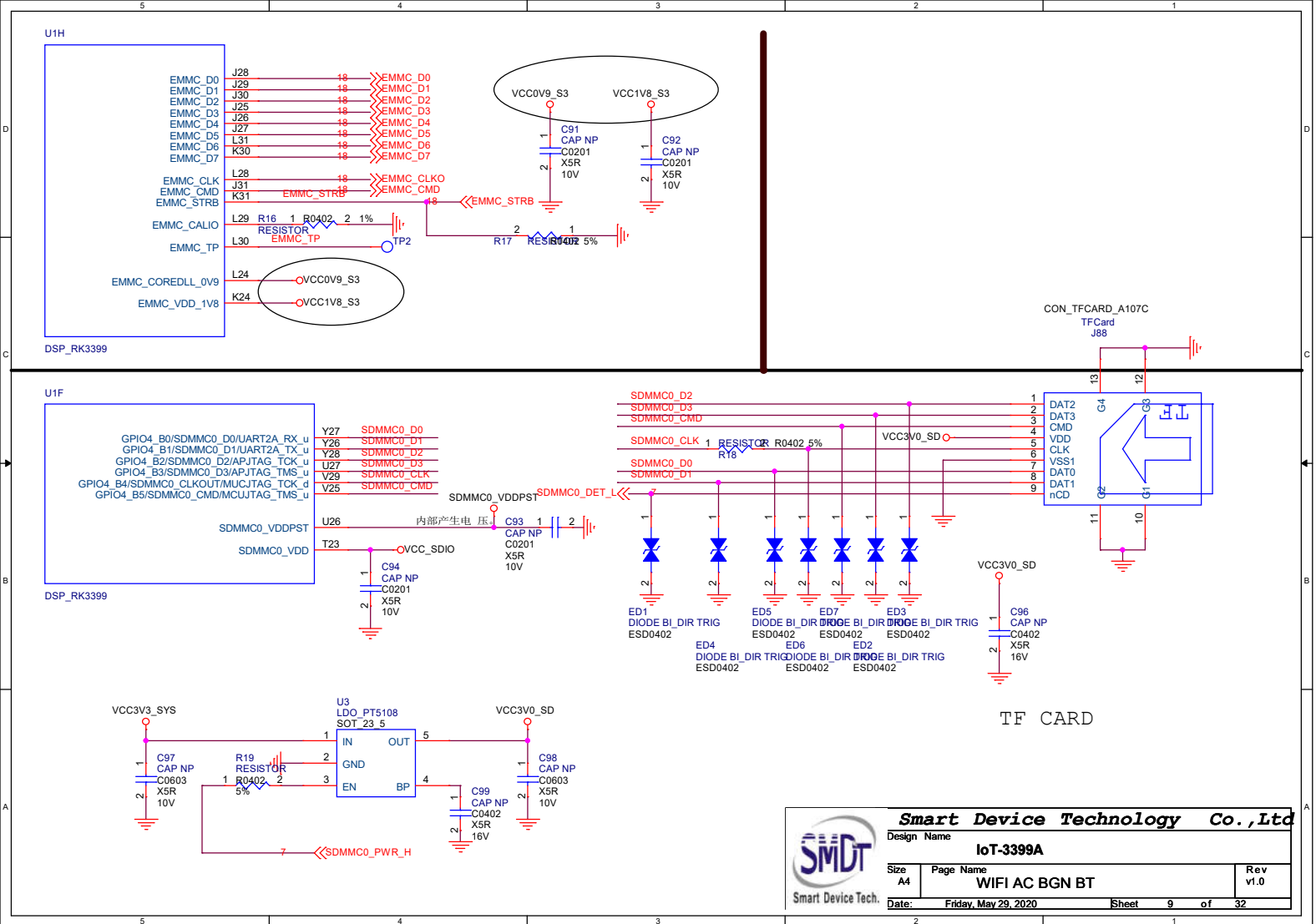


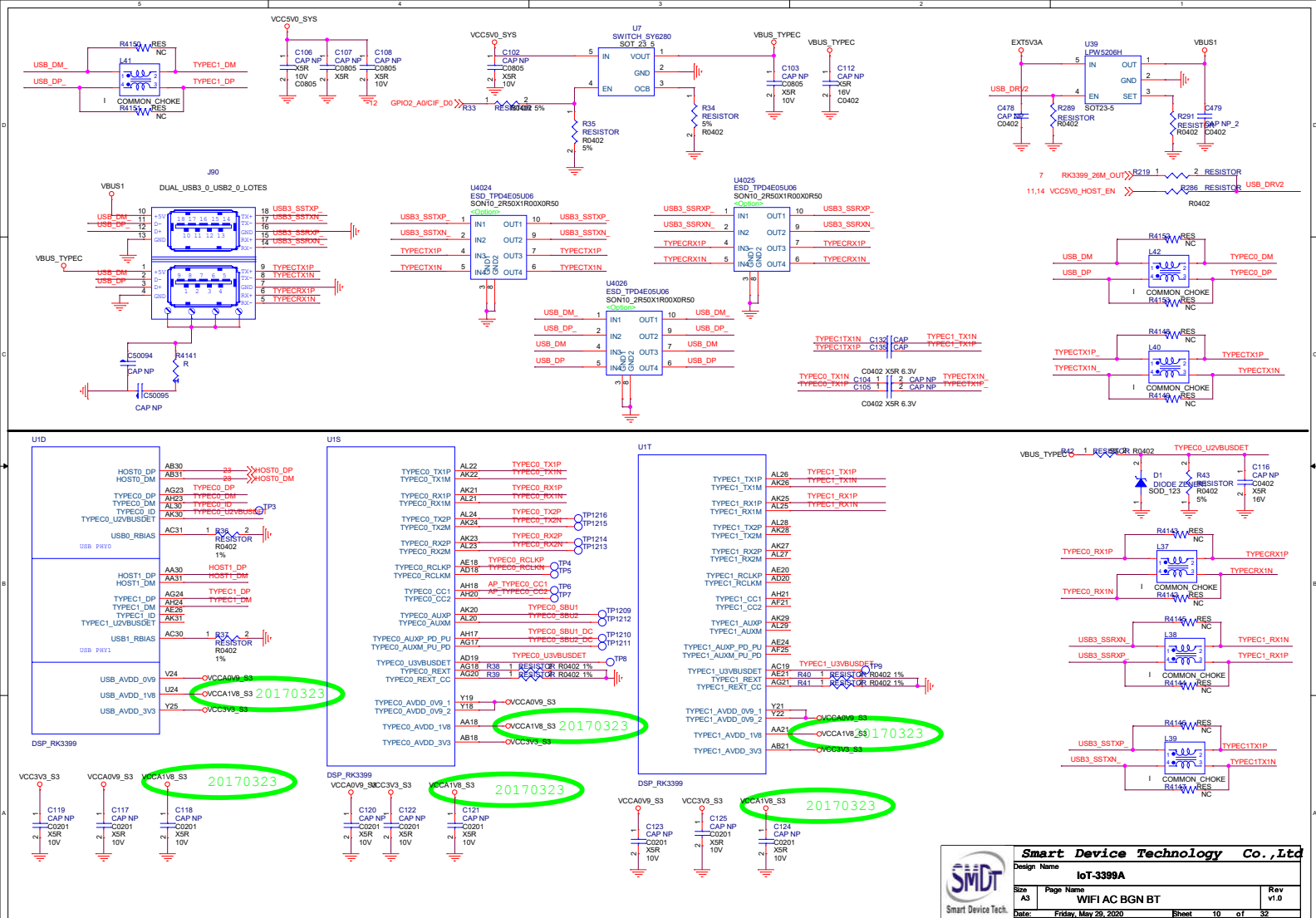
LCD_EN_H	»»»	CD_EN_H	24.27
LCD_EN_L	»»»	CD_EN_L	24.27

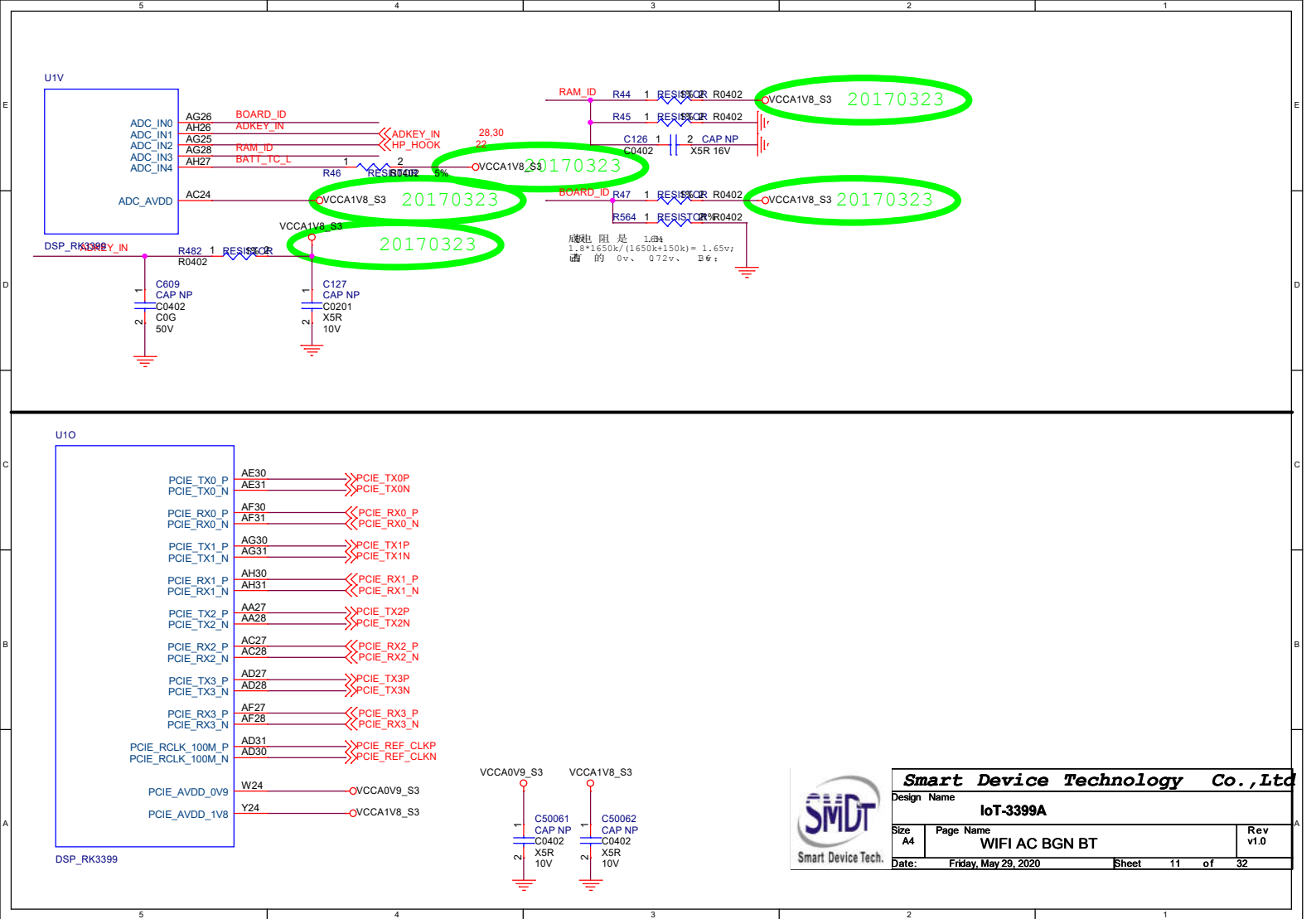
Rev			
<Title>			
Rev	Document	Number	Rev
C	2000		2000
Date: Friday, May 28, 2020 Sheet 1 of 1			

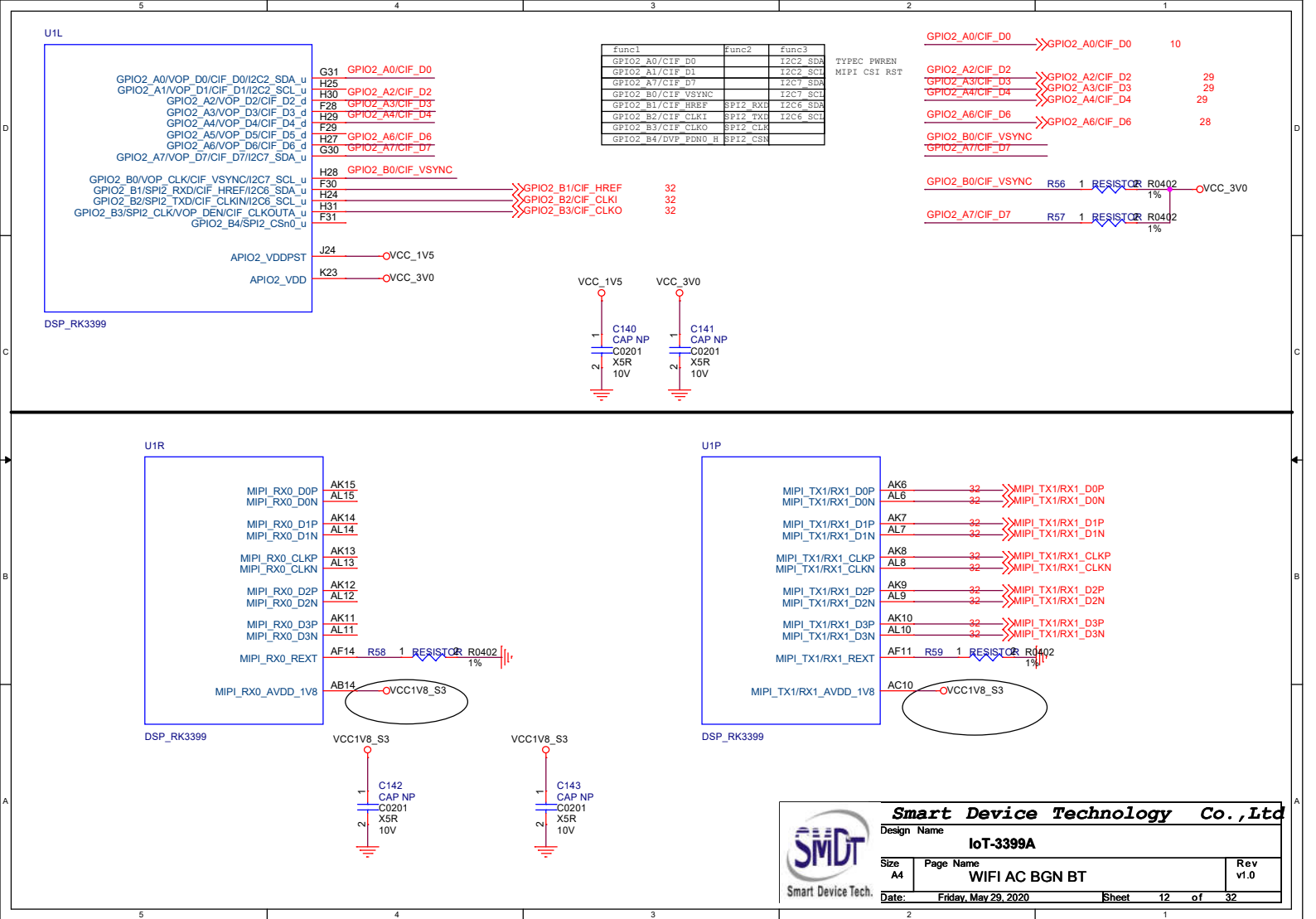


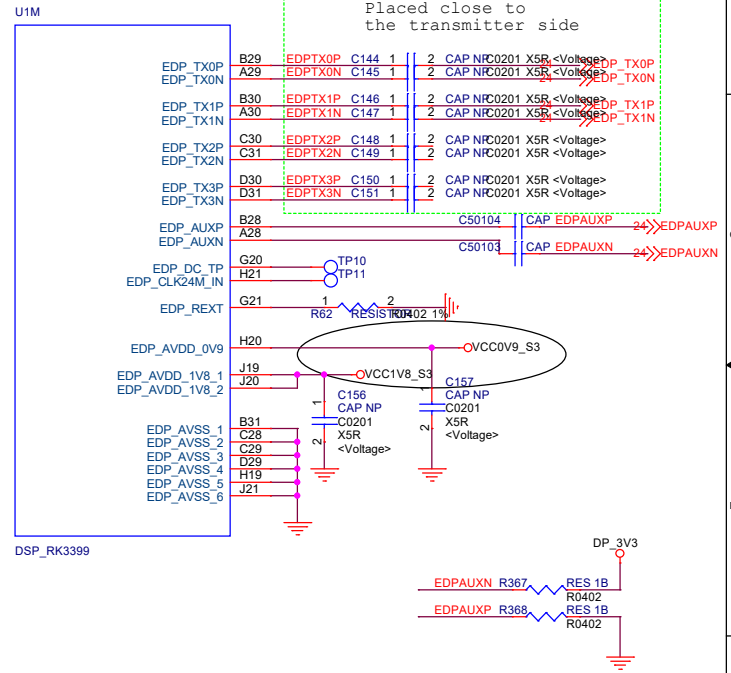
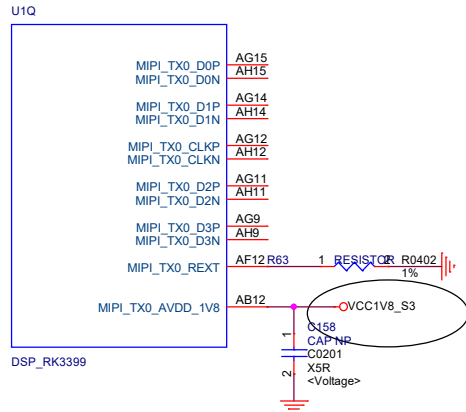
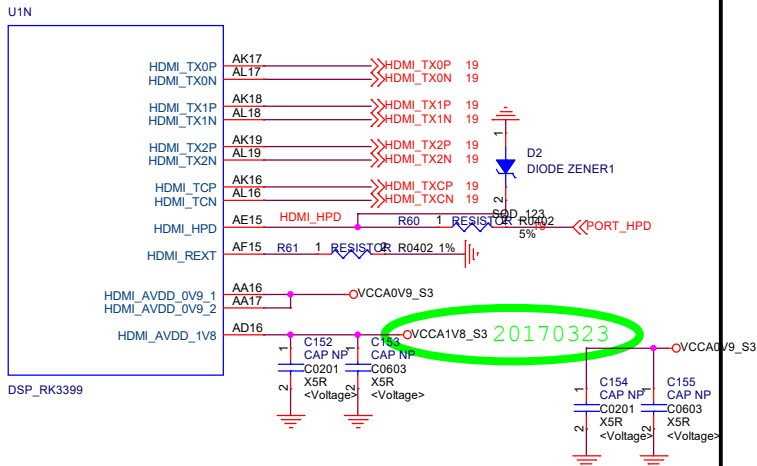


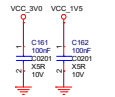
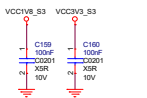










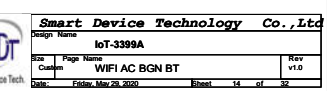
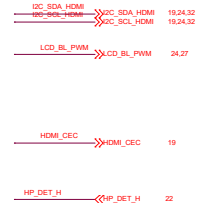
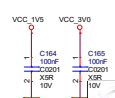
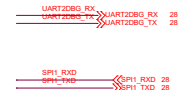


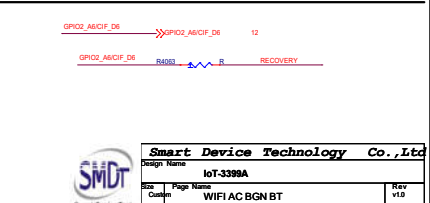
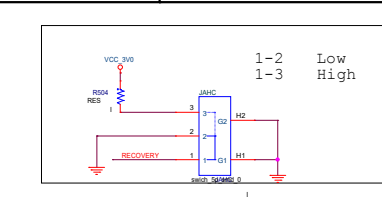
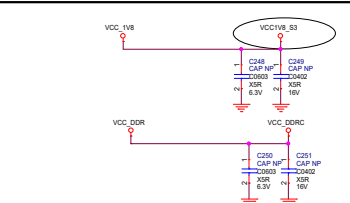
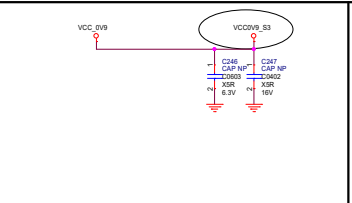
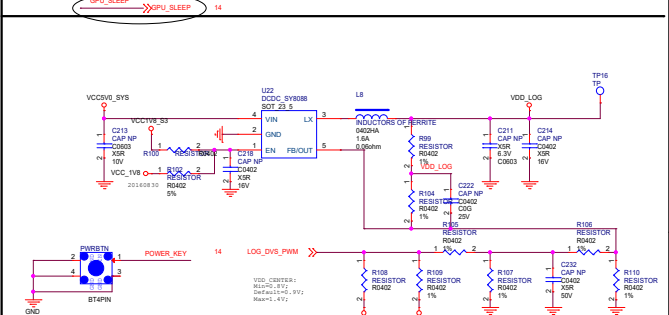
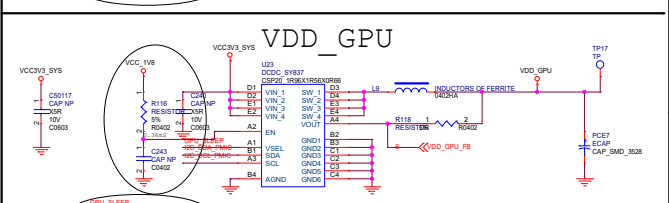
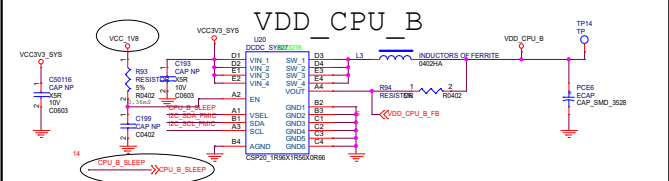
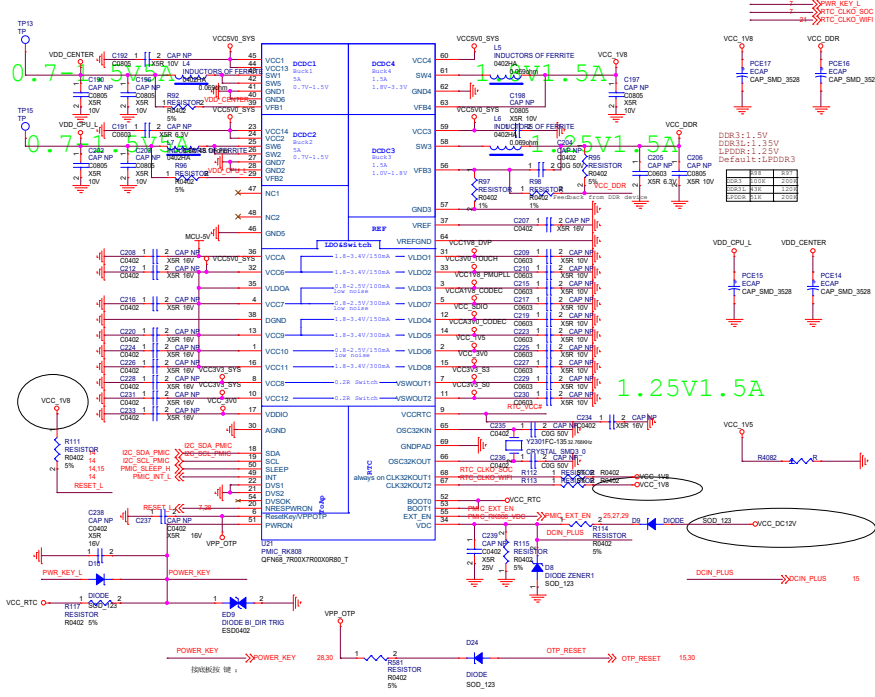
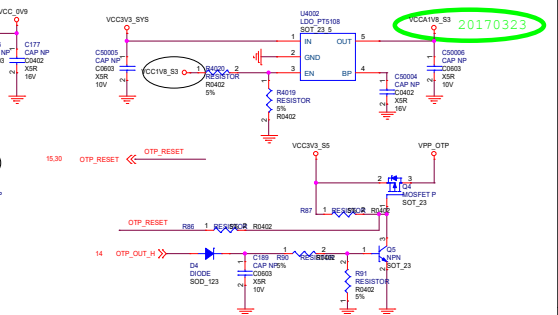
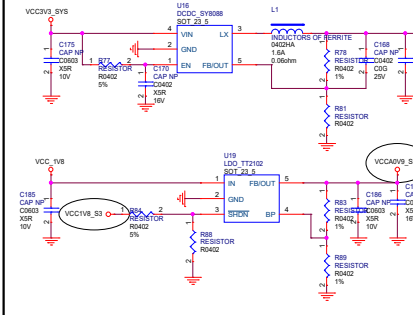
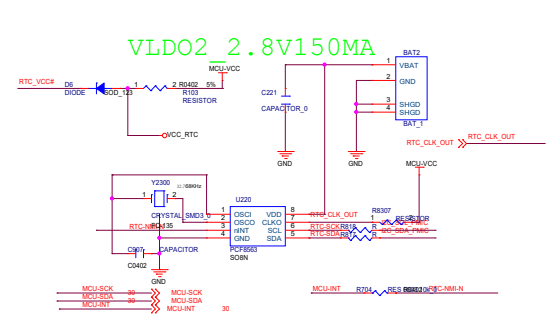
I2C_SDA_PMIC R66 1 2.7k 2 R6402 5% VCC_3V0

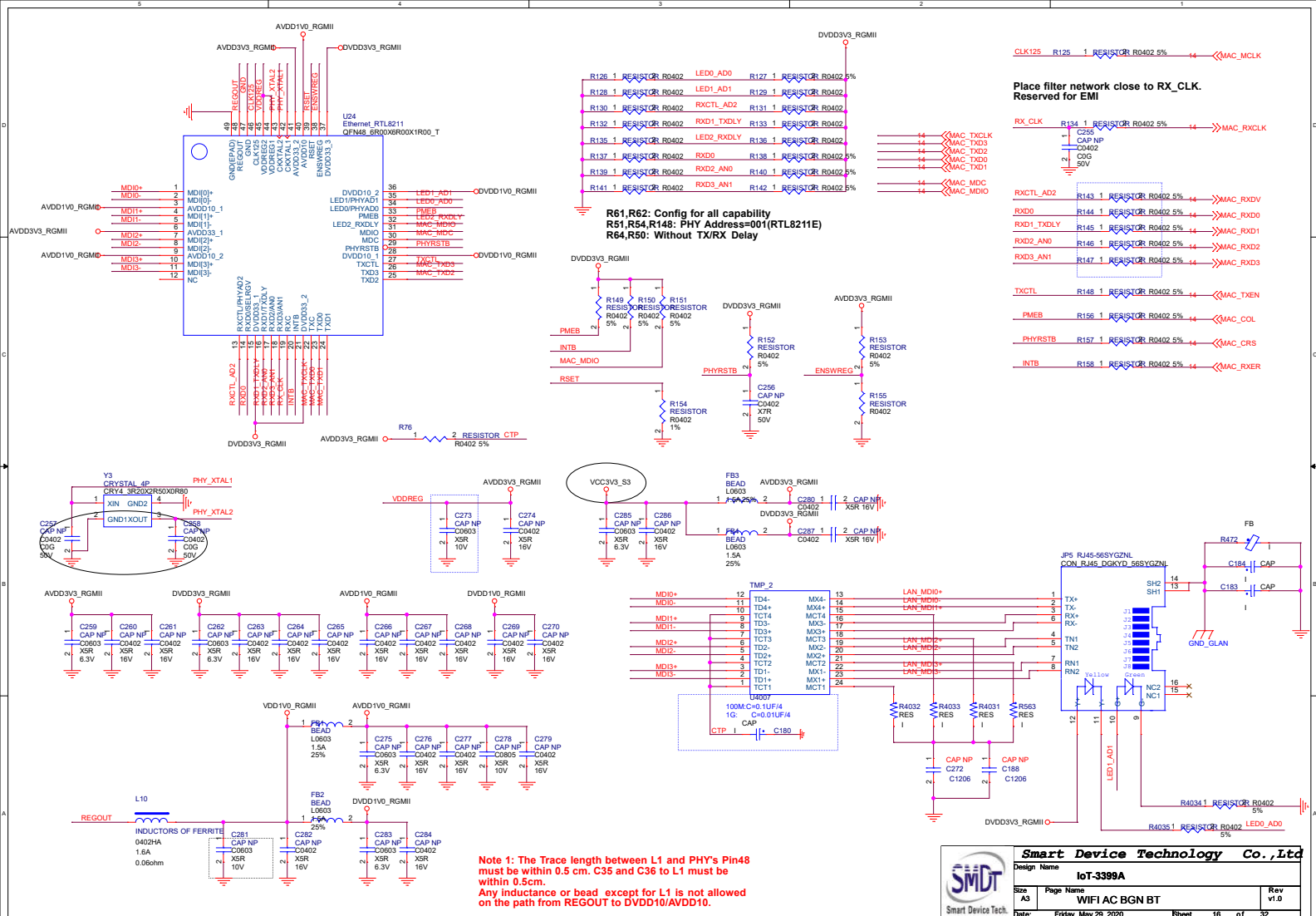
I2C_SCL_PMIC R67 1 2.7k 2 R6402 5% VCC_3V0

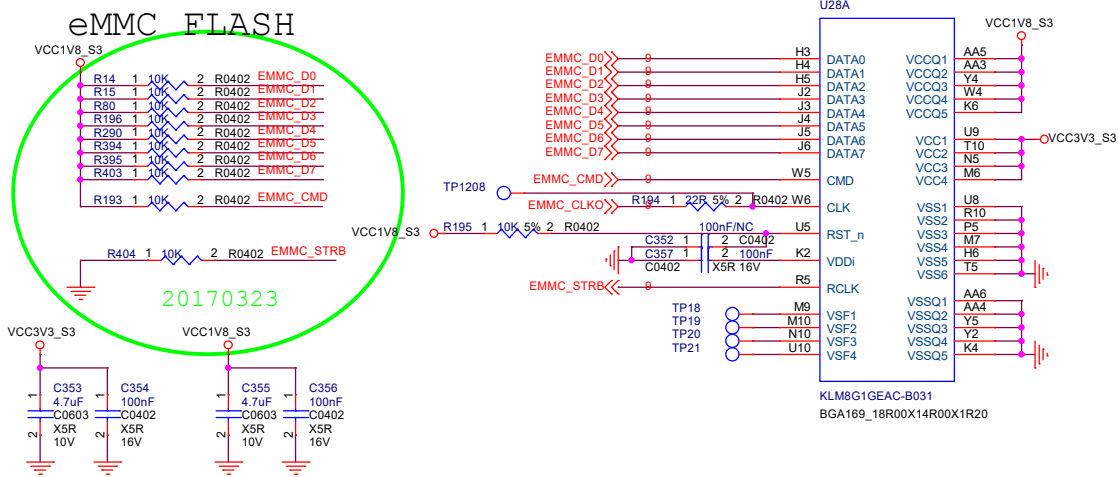
I2C_SDA_MEMS R68 1 2.7k 2 R6402 5% VCC_3V0

I2C_SCL_MEMS R69 1 2.7k 2 R6402 5% VCC_3V0

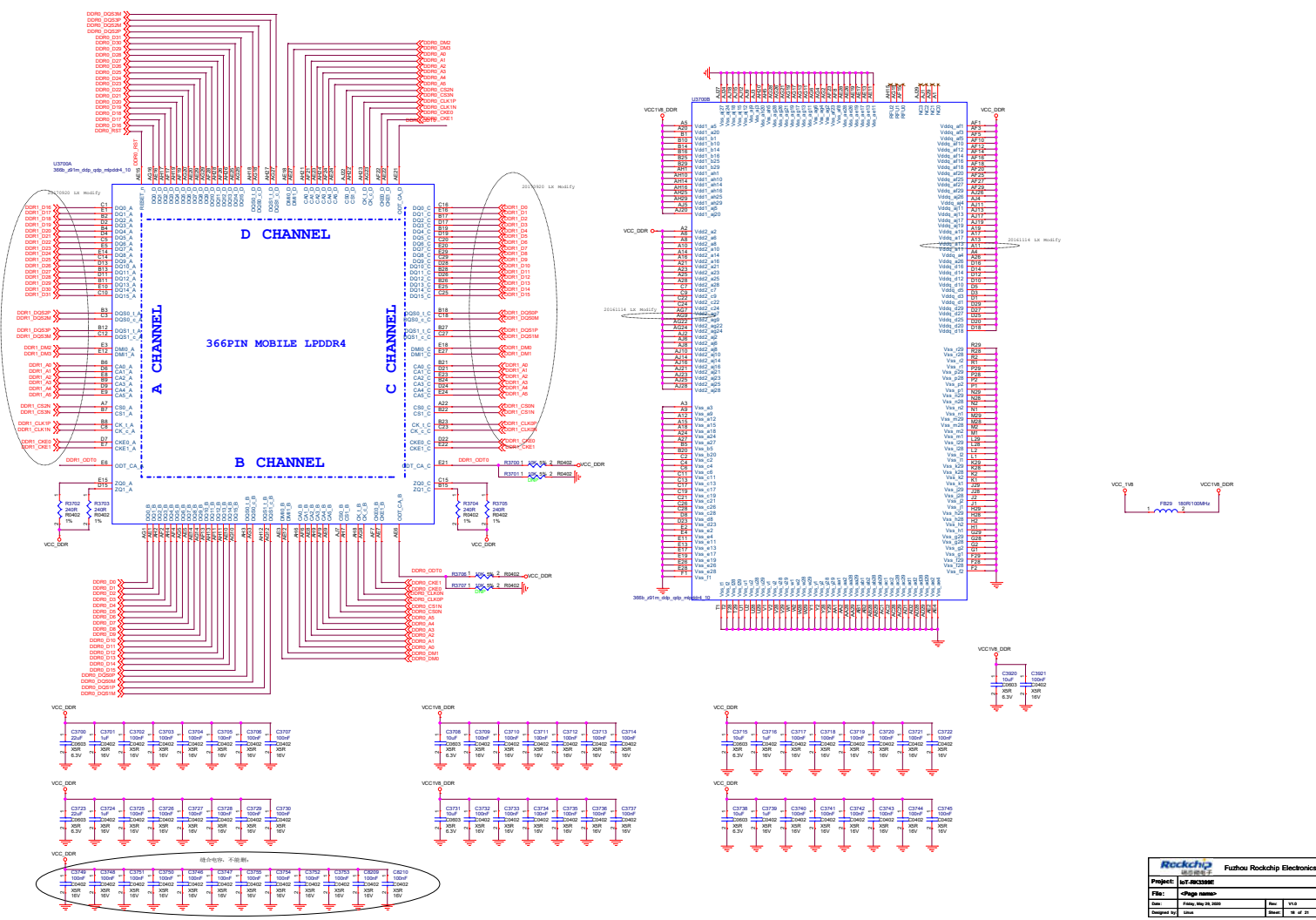




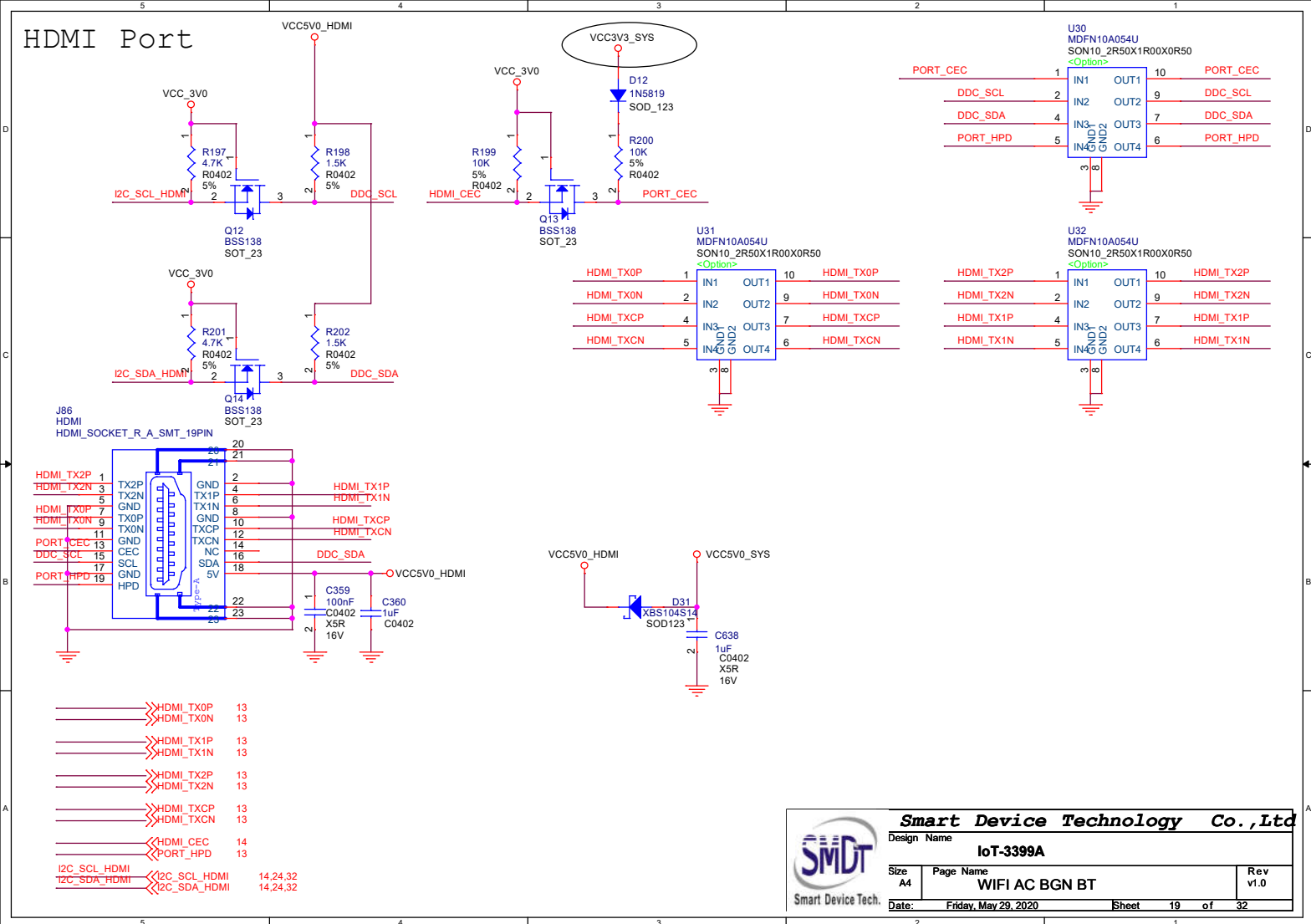


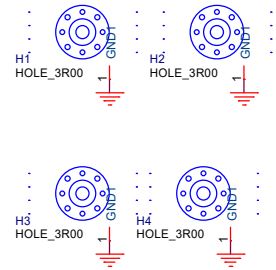
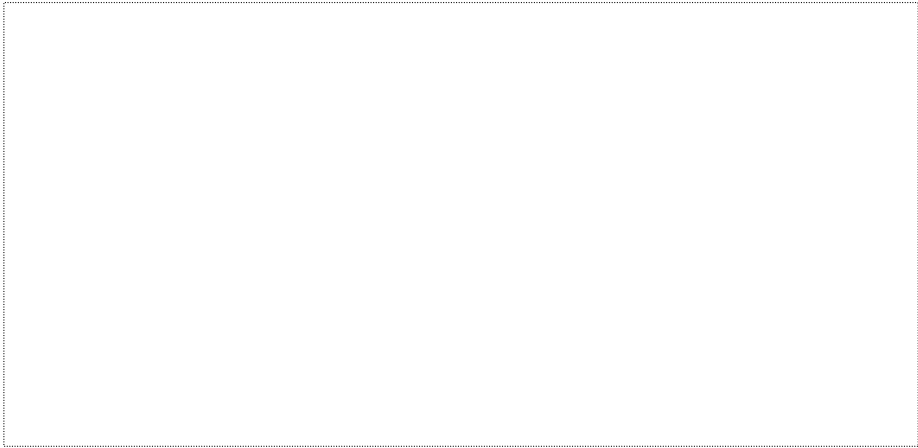


Smart Device Technology Co., Ltd			
Design Name		IoT-3399A	
Size A4	Page Name		Rev
	WIFI AC BGN BT		vi.0
Date:	Friday, May 29, 2020	Sheet	18 of 32



HDMI Port





PAGE28 4CH*UART
PAGE29 MINI PCI-E
PAGE30 MCU
PAGE31 DCIN DCDC8.6V,DCDC5V DCDC3.3V
PAGE32 PCI-E (M2) (V00 NO OPTION)



Smart Device Tech.

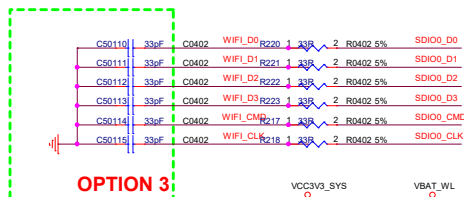
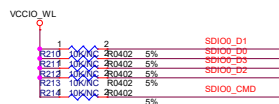
Smart Device Technology Co.,Ltd

IoT-3399A

Size A4	Page Name WIFI AC BGN BT	Rev v1.0
Date: Friday, May 29, 2020		Sheet 20 of 32

SDIO0_D0			
SDIO0_D1	SDIO0_D0	14	
SDIO0_D2	SDIO0_D1	15	
SDIO0_D3	SDIO0_D2	14	
SDIO0_D4	SDIO0_D3	15	
SDIO0_CMD	SDIO0_C14	14	
SDIO0_C0	SDIO0_C15	15	
SDIO0_C1	SDIO0_C14	14	
SDIO0_C2	SDIO0_C15	15	
UART0_RTS	UART0_RTS		
UART0_RXD	UART0_RXD		
UART0_TXD	UART0_TXD		
UART0_CTS	UART0_CTS		
BT_HOST_WAKE	BT_HOST_WAKE_L	7	
BT_HOST_WAKE	BT_HOST_WAKE_L	14	
BT_REG_ON_H	BT_REG_ON_H	7	
WIFI_REG_ON_H	WIFI_REG_ON_H	7	
WIFI_HOST_WAKE	WIFI_HOST_WAKE_L	7	
WIFI_HOST_WAKE	WIFI_HOST_WAKE_L	14	
DS1_LRCK_RX_BT_PCM	DS1_LRCK_RX_BT_PCM	14	
DS1_SDOCK_BT_PCM	DS1_SDOCK_BT_PCM	14	
DS1_SDOCK_BT_PCM	DS1_SDOCK_BT_PCM	14	
DS1_SCLK_BT_PCM	DS1_SCLK_BT_PCM	14	
DS1_SCLK_BT_PCM	DS1_SCLK_BT_PCM	14	
RTC_CLKO_WIFI	RTC_CLKO_WIFI	14	

AP6255:37.4MHz



VCC3V3_SYS

L74
220R/100mH2
L0603

VBAT_WL

3.0V~4.8V

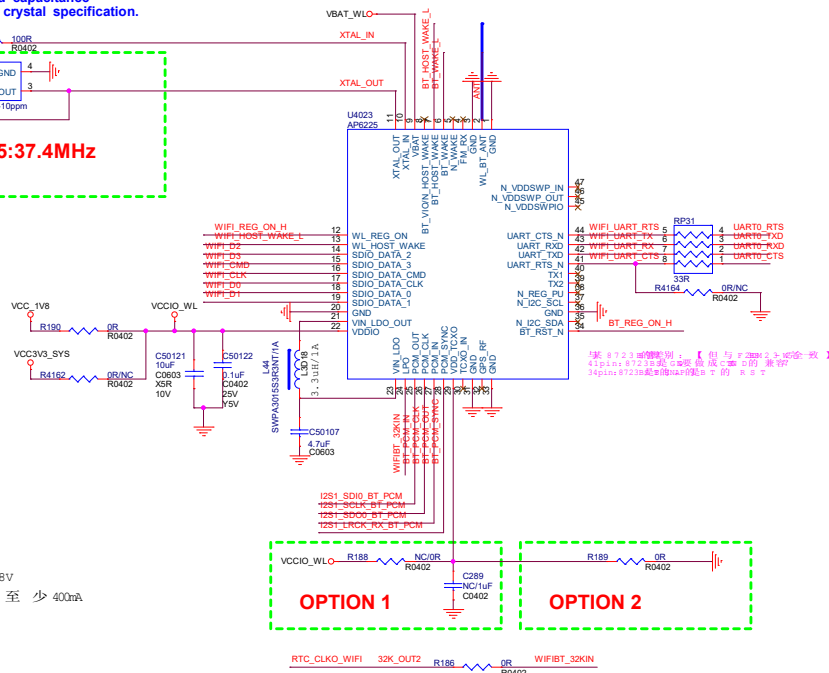
供电流 至少 400mA

C50109
10uF
C0603

XSR
10V

C288
3.1uF
D0402

25V
YSV



Smart Device Technology

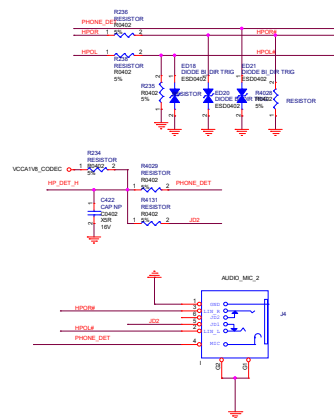
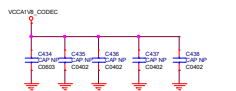
Smart Device Technology Co., Ltd

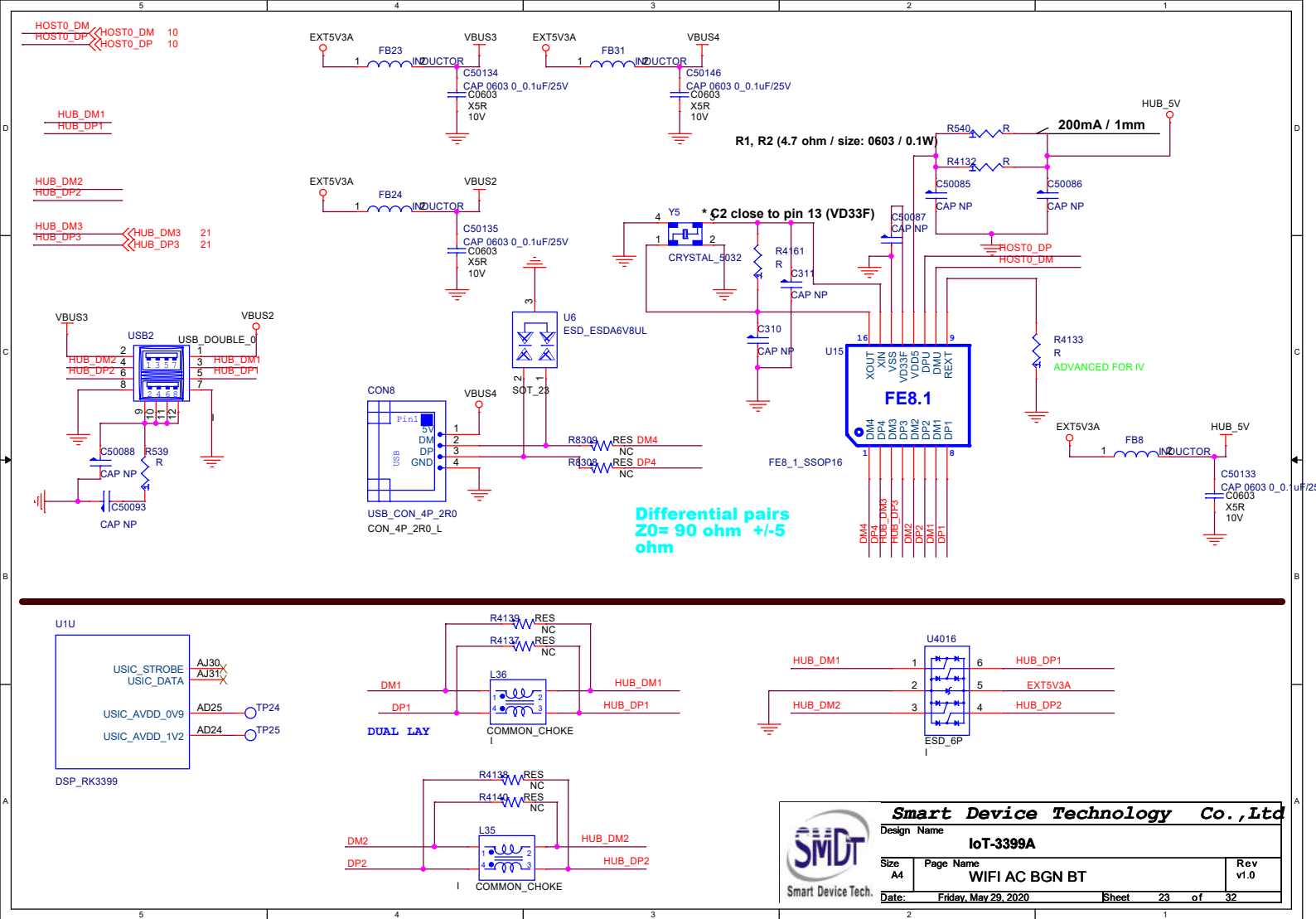
Design Name **IoT-3399A**

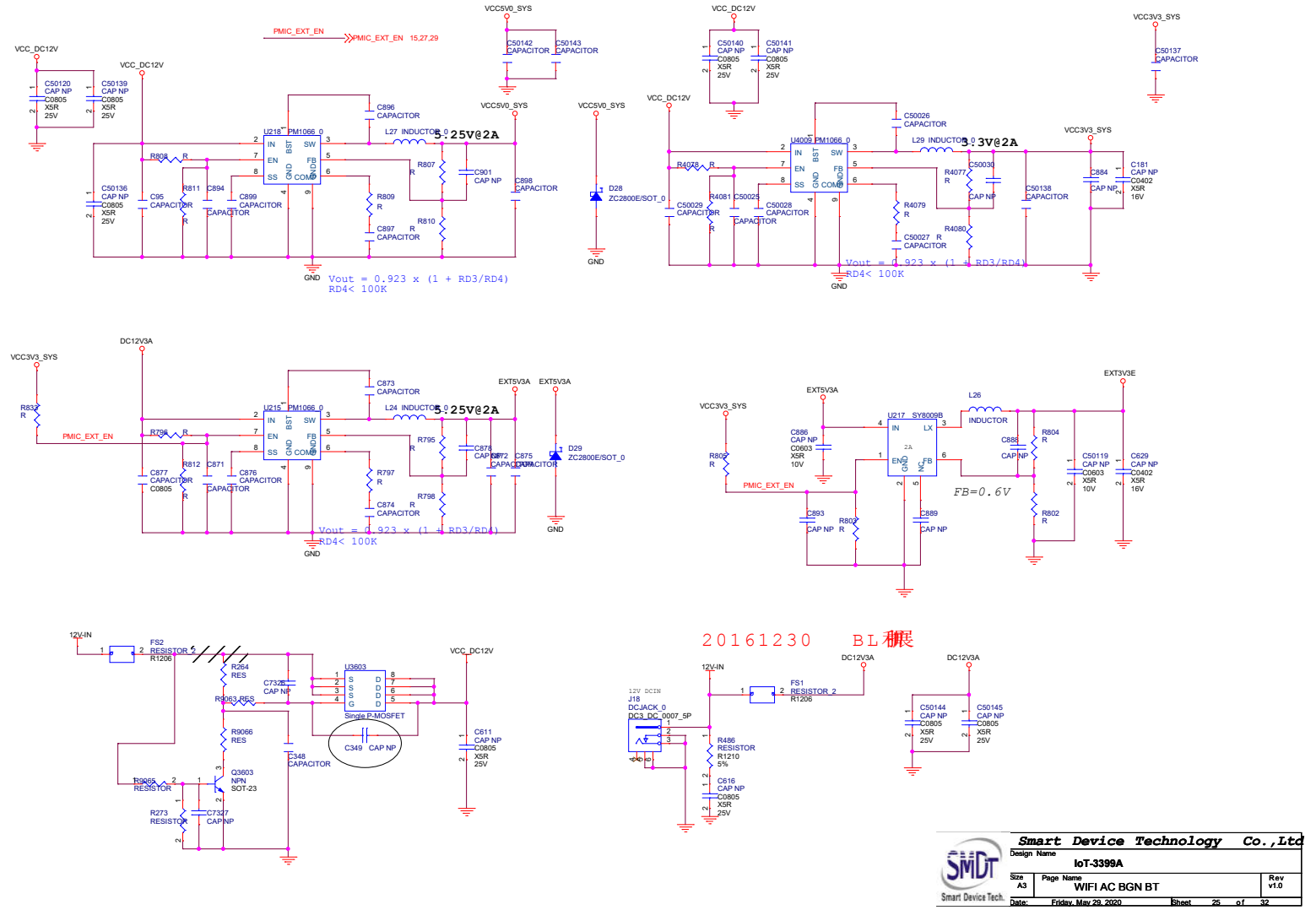
Size A3	Page Name WIFI AC BGN BT
------------	-----------------------------

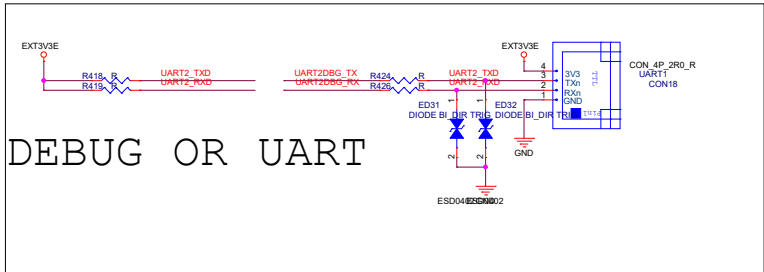
Date: Friday, May 29, 2020 Sheet 21 of 32

Date.	Friday, May 29, 2020	Sheet	21	01	32
-------	----------------------	-------	----	----	----

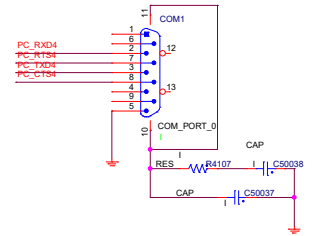
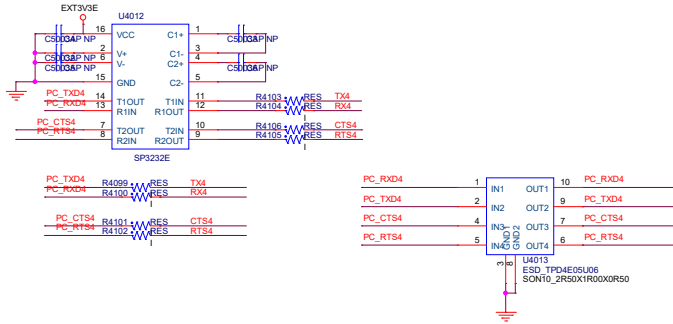
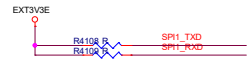




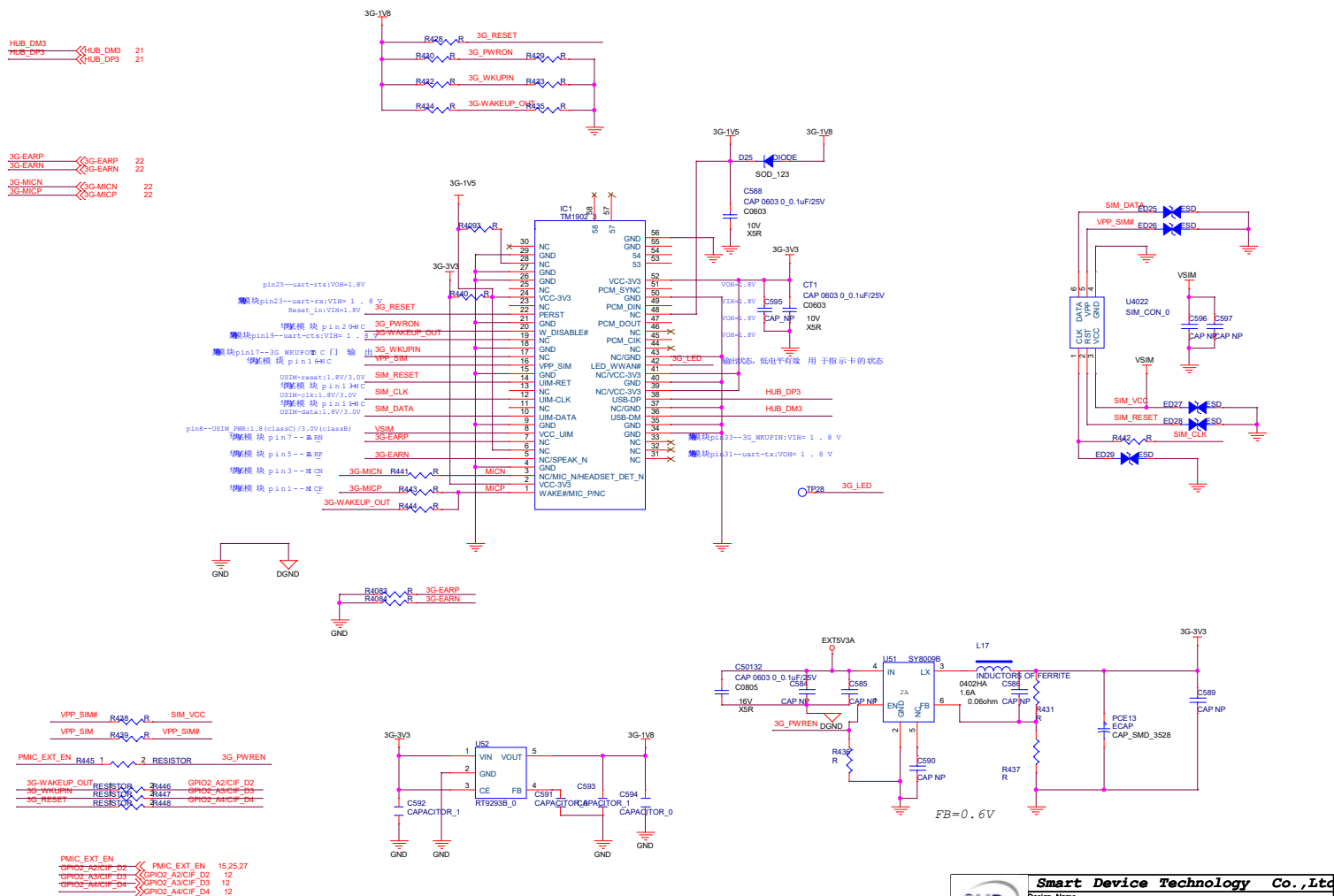


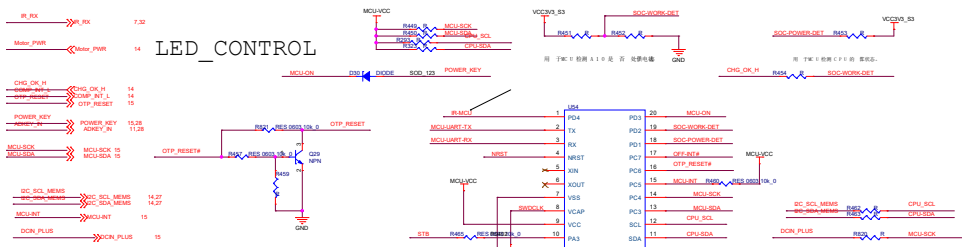


DEBUG OR UART

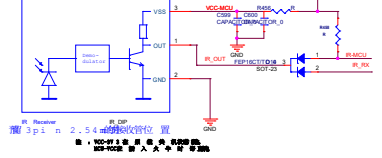


Smart Device Technology Co., Ltd			
Design Name		IoT-3399A	
Size	Page Name	Rev	
A3	WIFI AC BGN BT	v1.0	
Date:	Friday, May 25, 2020	Sheet	26 of 32

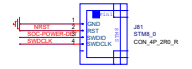
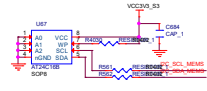
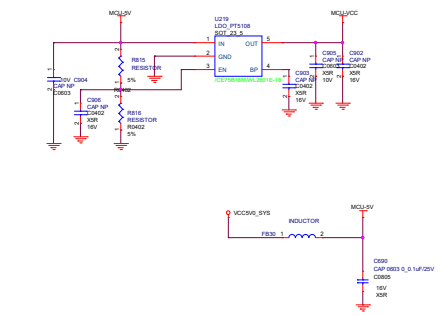
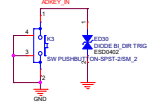
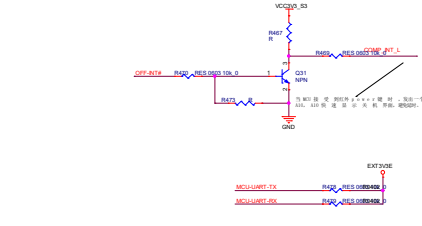
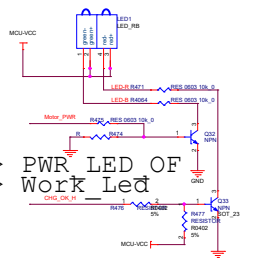


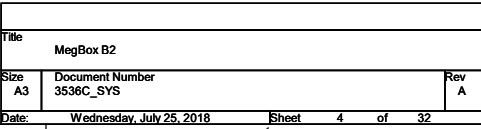


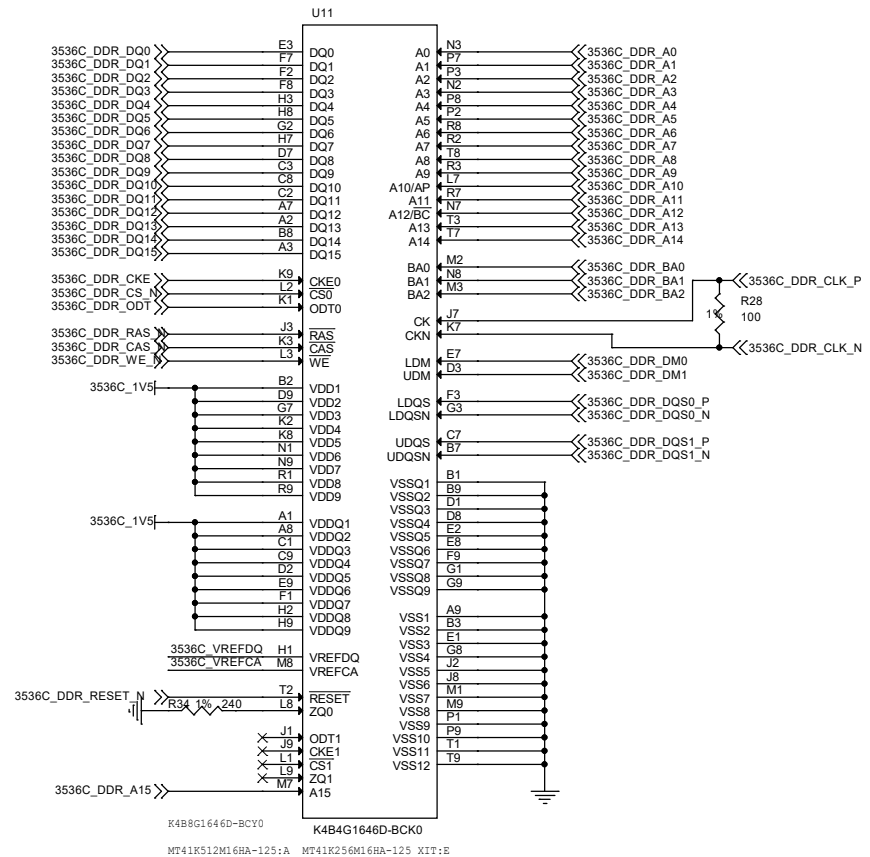
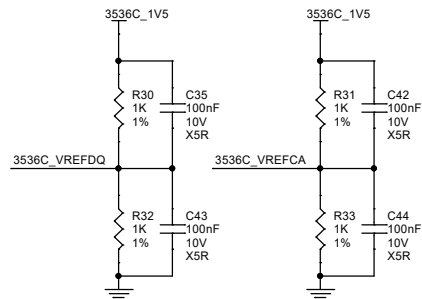
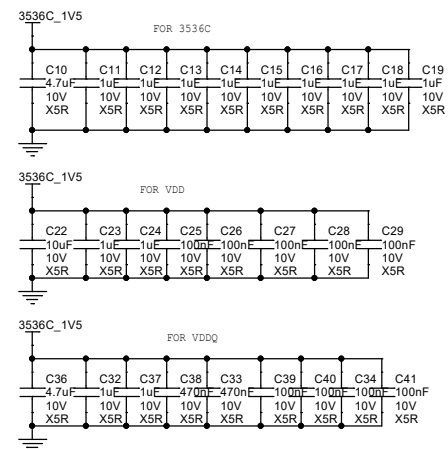
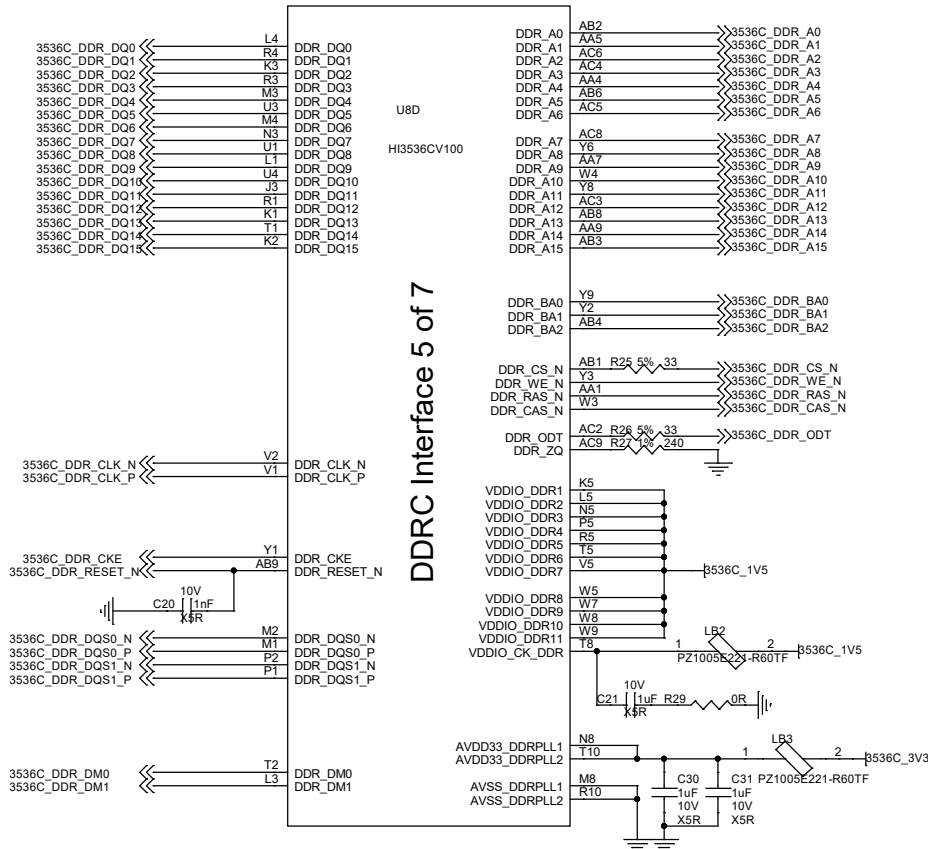
IR MODULE



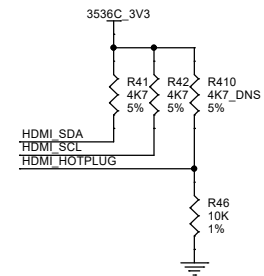
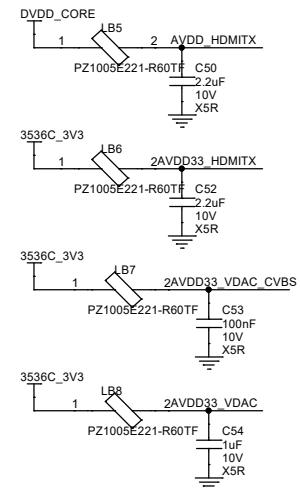
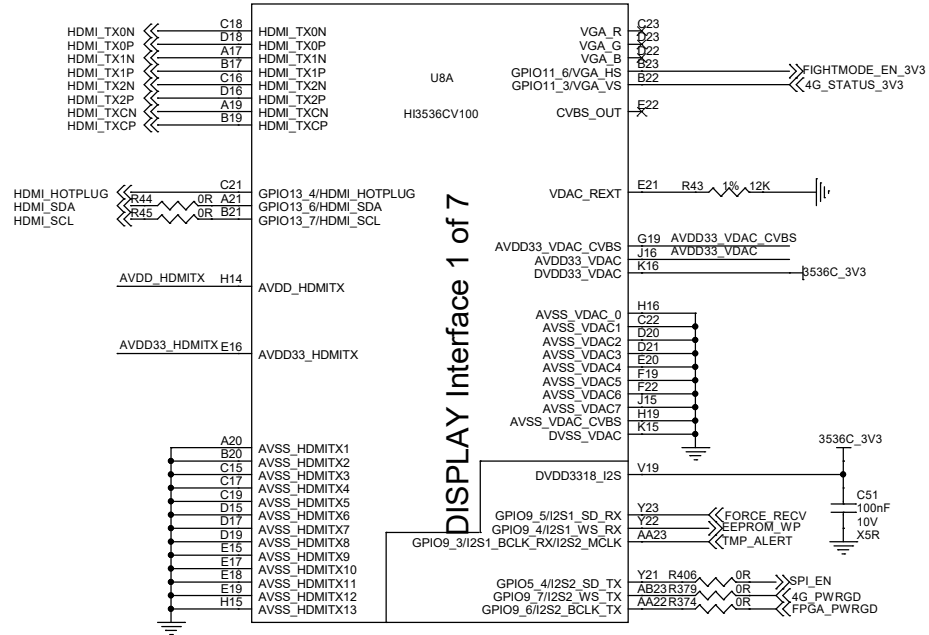
Motor -> PWR
Motor -> Work Led



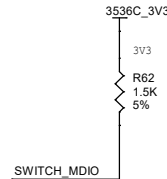
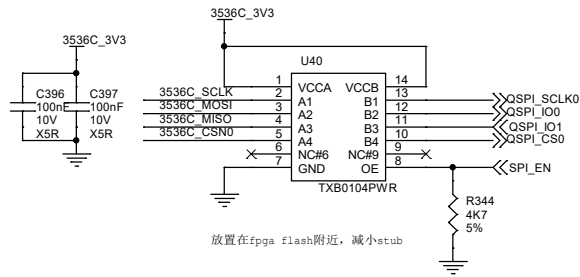
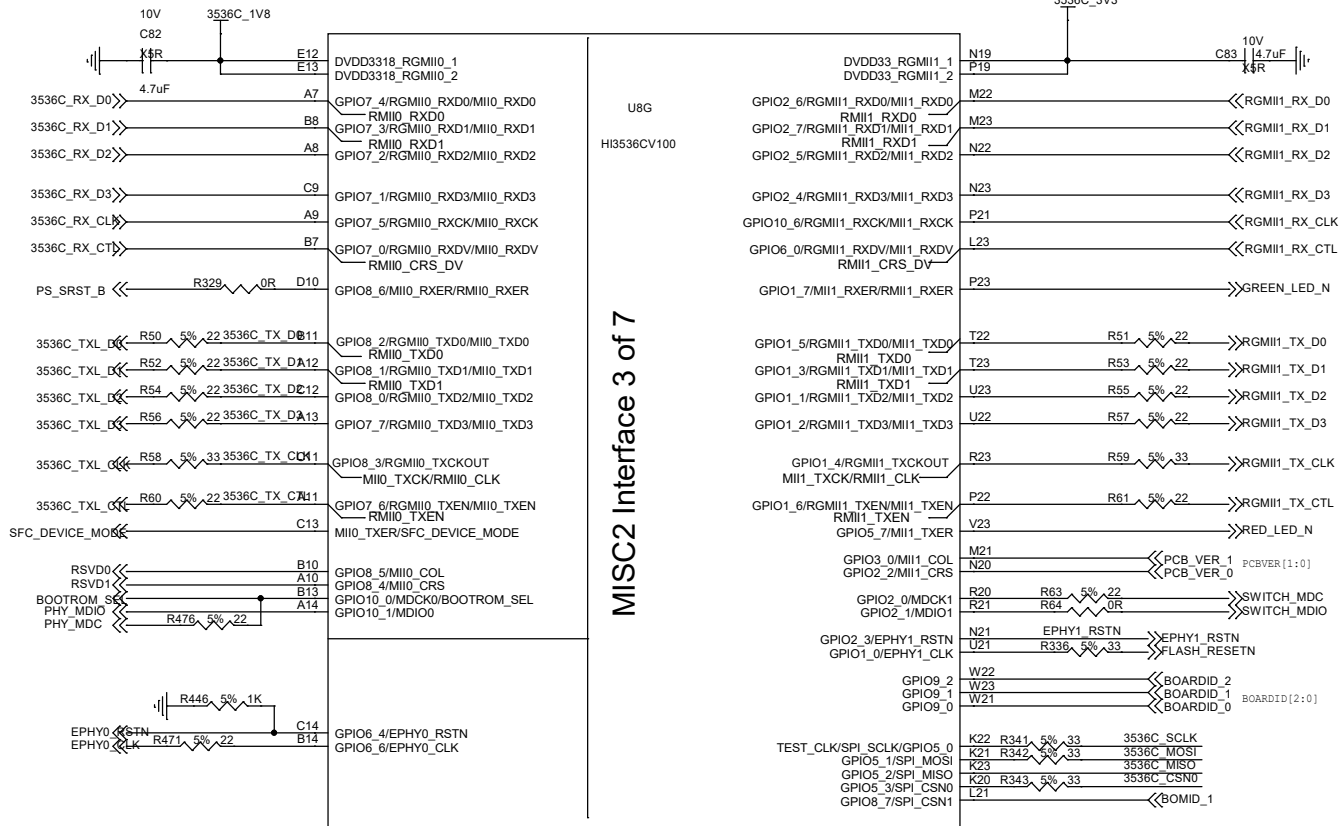


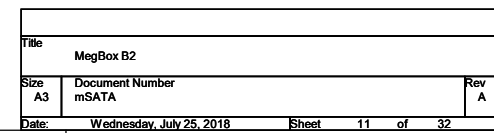
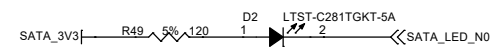


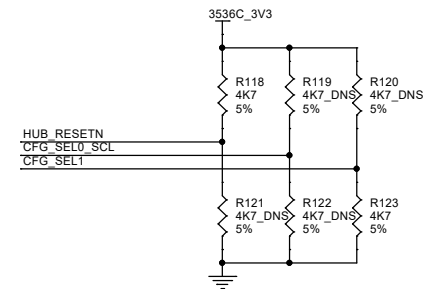
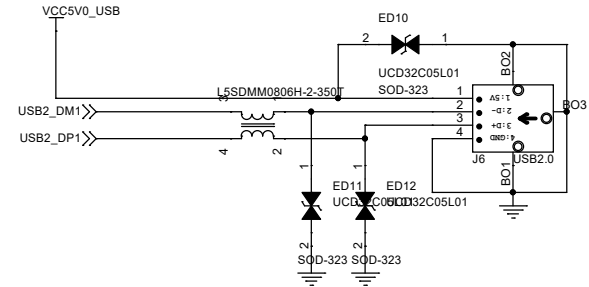
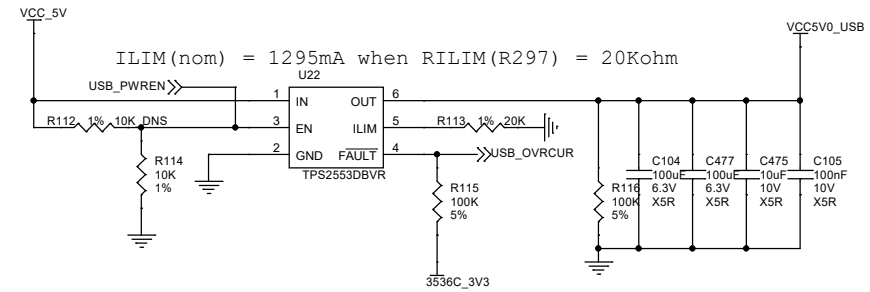
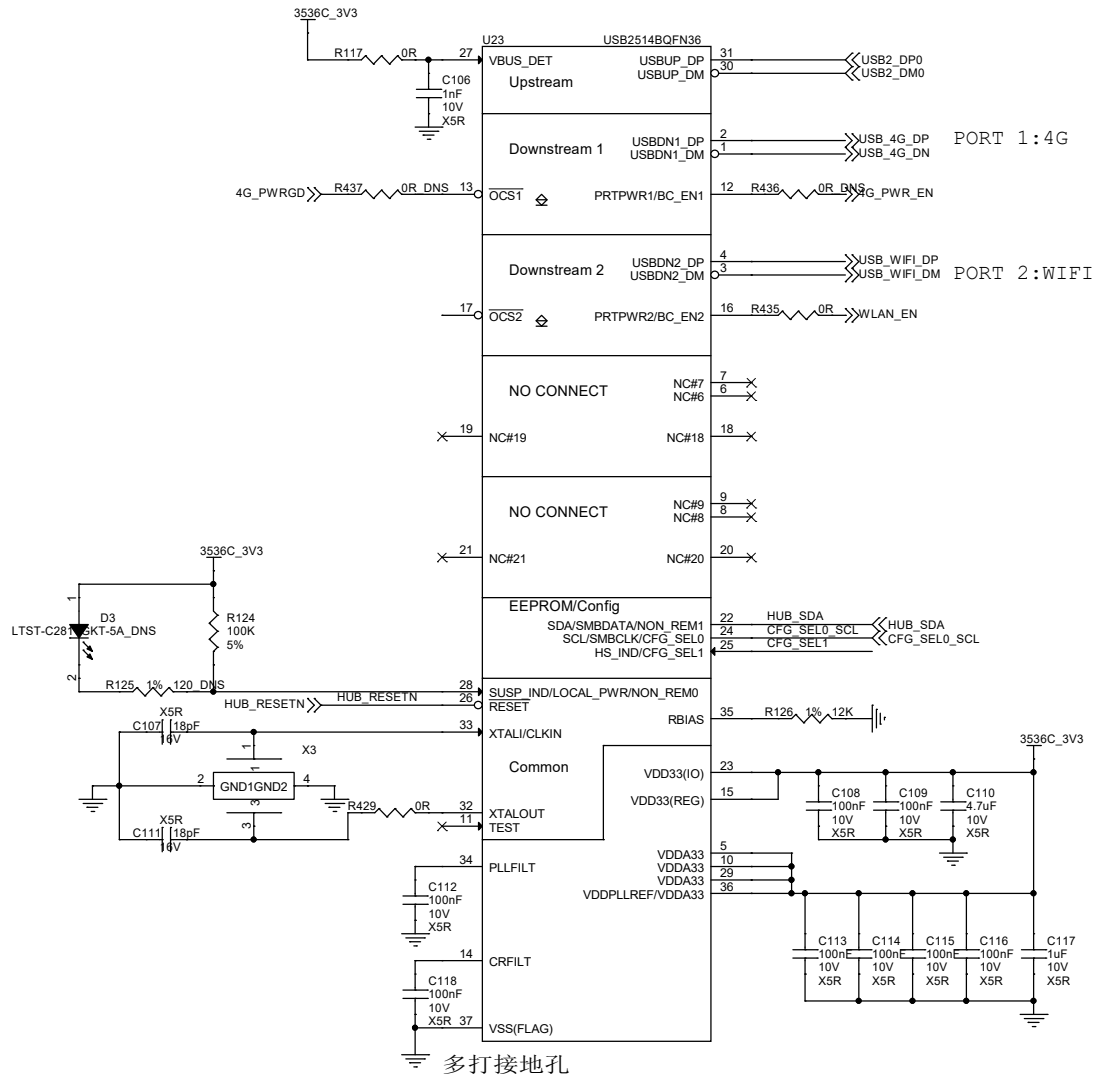
Title		
MegBox B2		
Size	Document Number	Rev
A3	3536C_DDR	A
Date:	Wednesday, July 25, 2018	Sheet 5 of 32

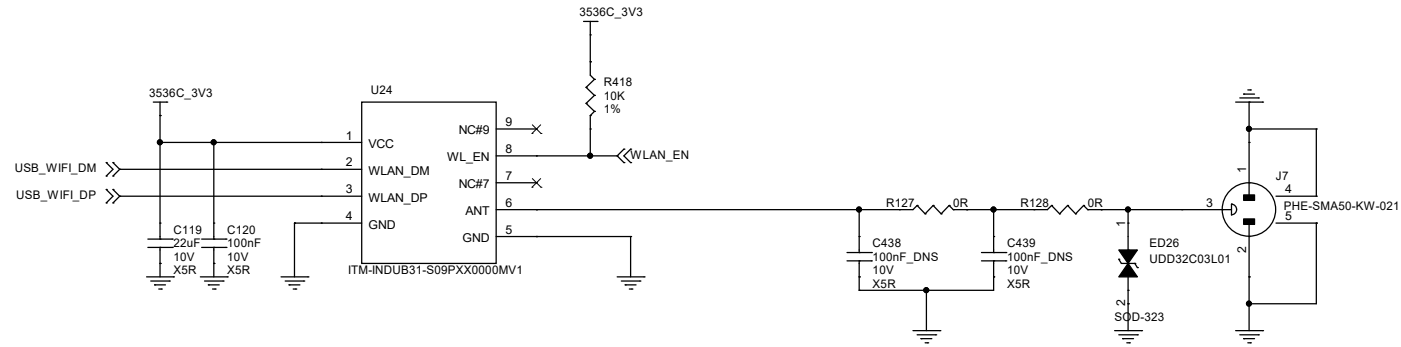


Title		
MegBox B2		
Size	Document Number	Rev
A3	3536C_HDMI	A
Date:	Wednesday, July 25, 2018	Sheet 6 of 32

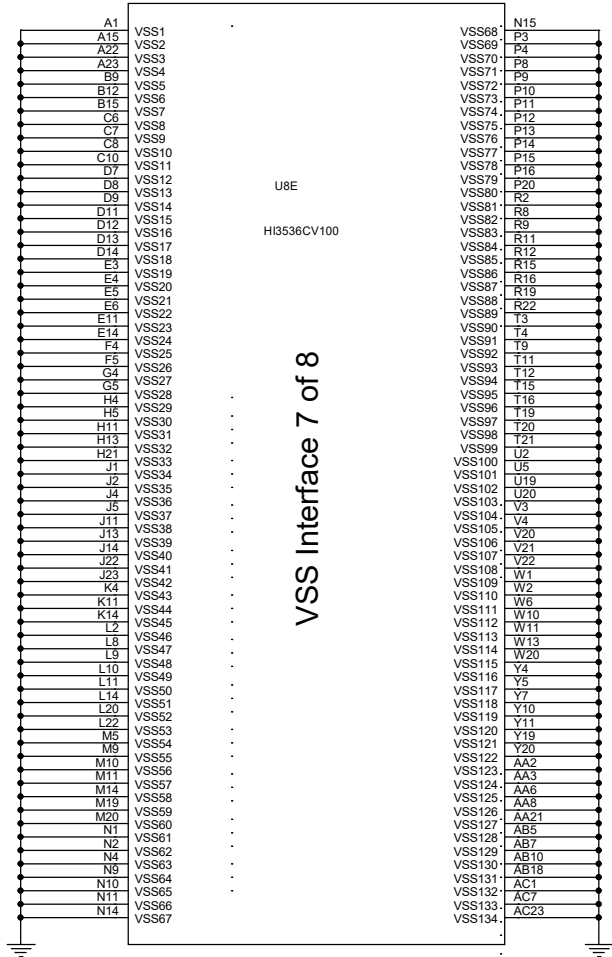




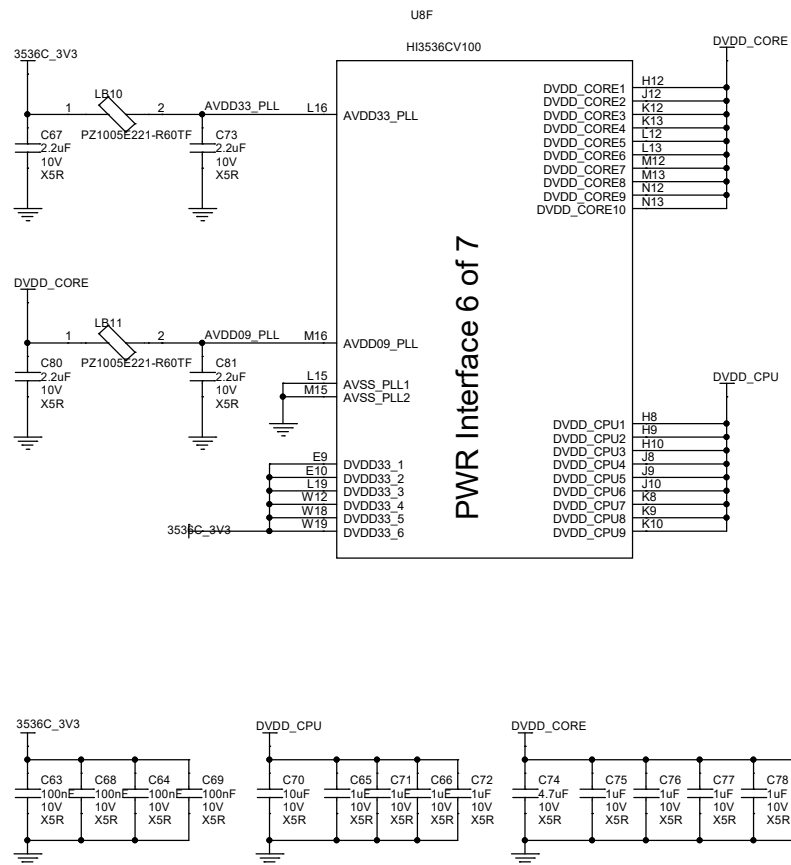




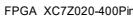
Title		
MegBox B2		
Size	Document Number	Rev
A3	WIFI MODULE	A
Date:	Wednesday, July 25, 2018	Sheet 13 of 32



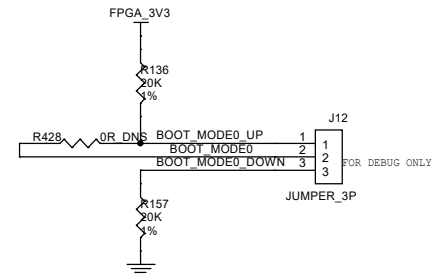
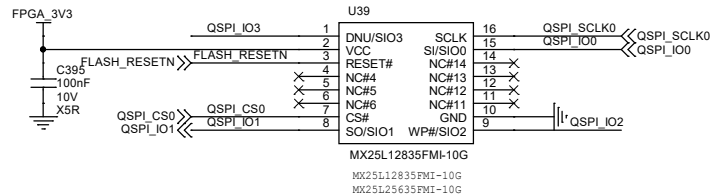
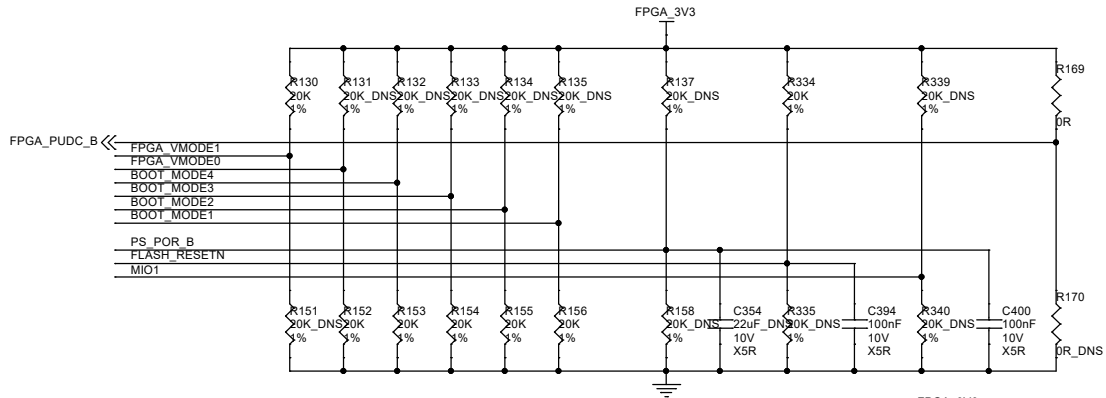
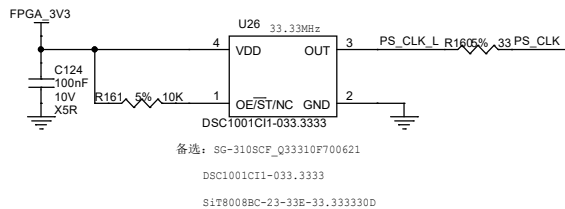
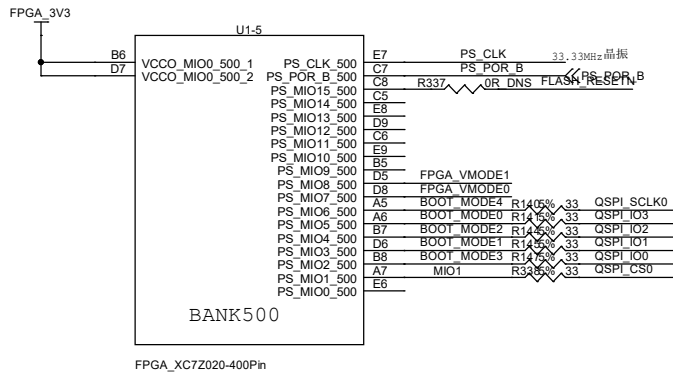
VSS Interface 7 of 8



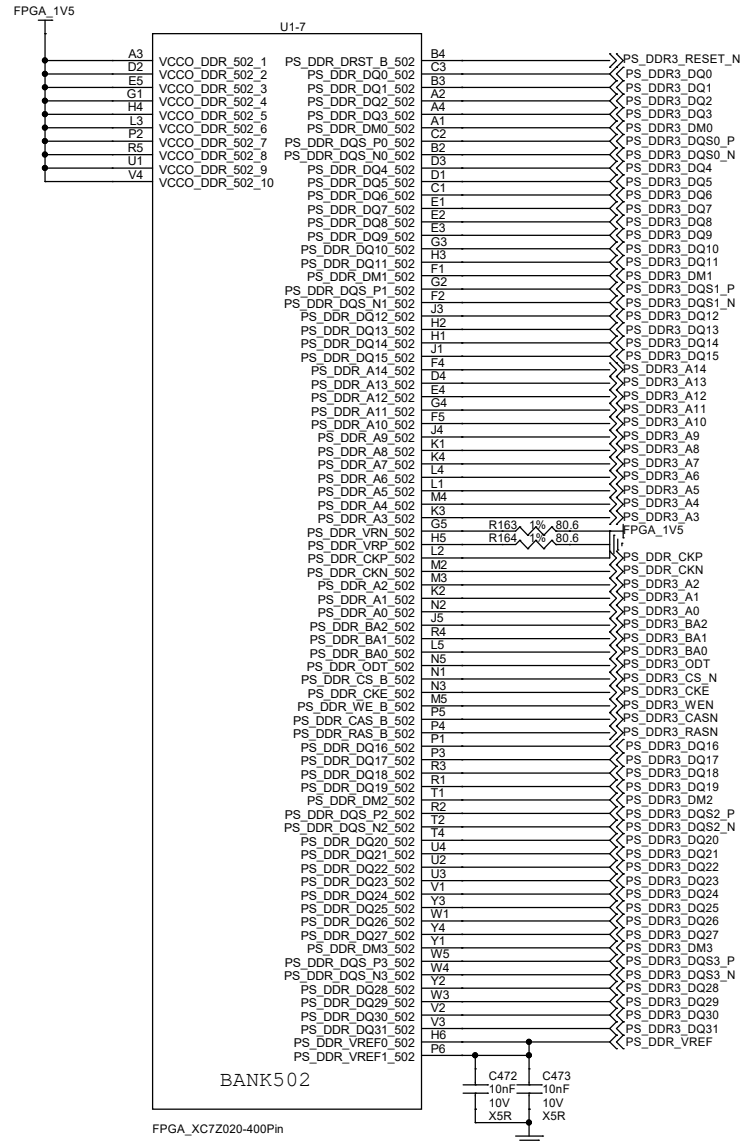
PWR Interface 6 of 7

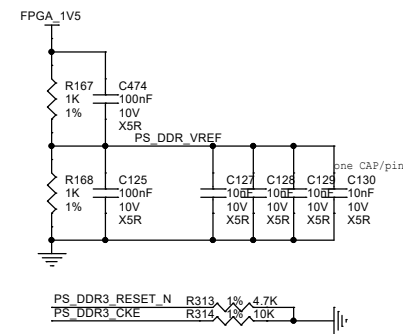
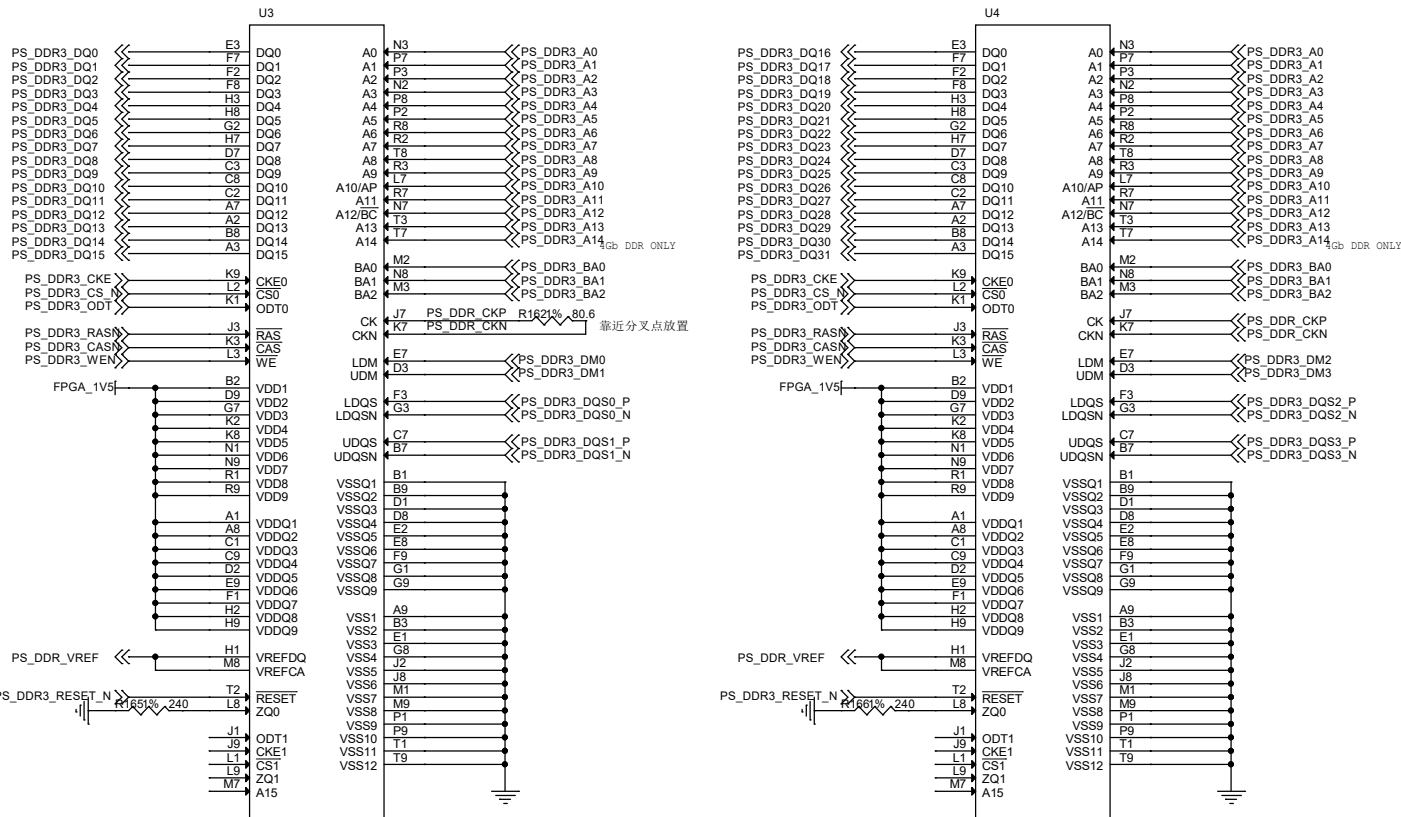


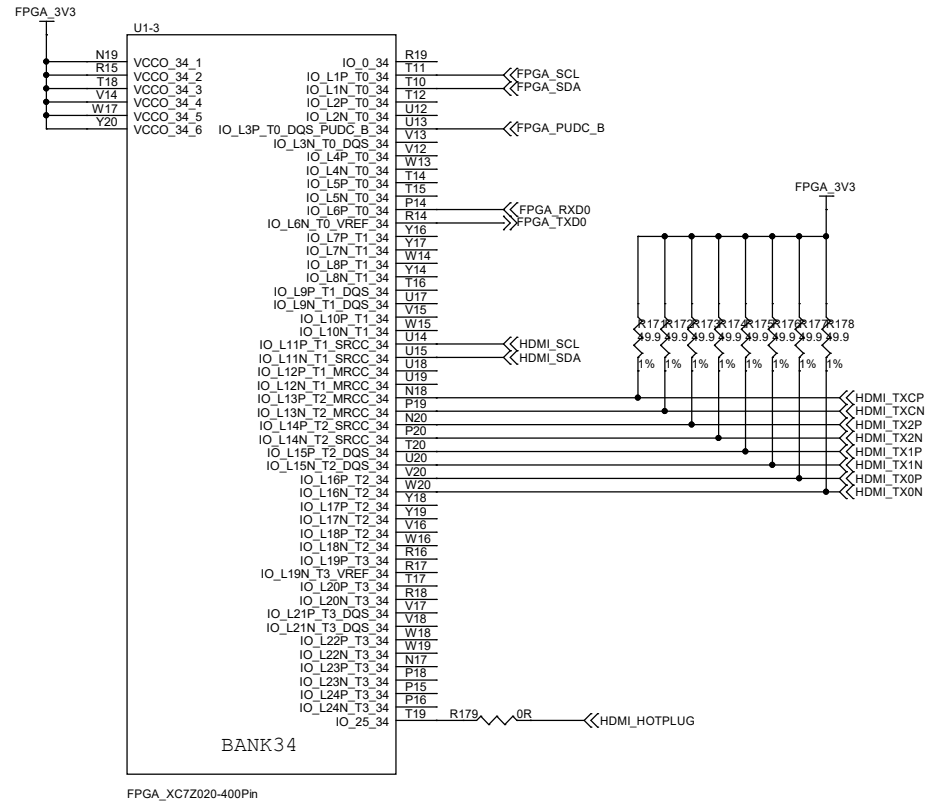
Title			
MegBox B2			
Size	Document Number	Rev	
A3	FPGA XADC&JTAG	A	
Date:	Wednesday, July 25, 2018	Sheet	15 of 32

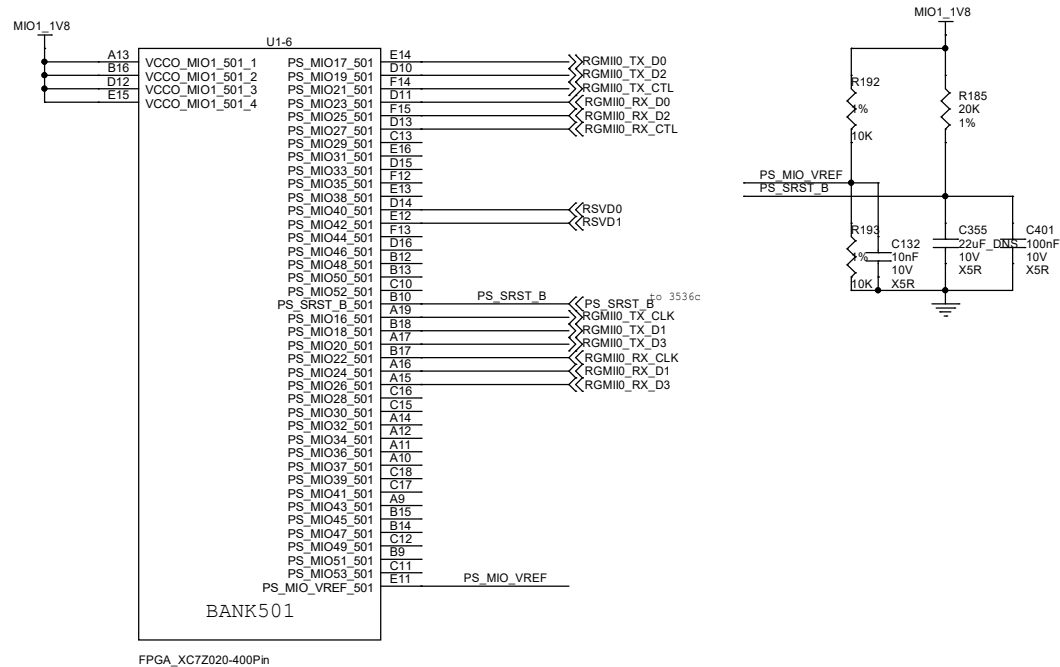


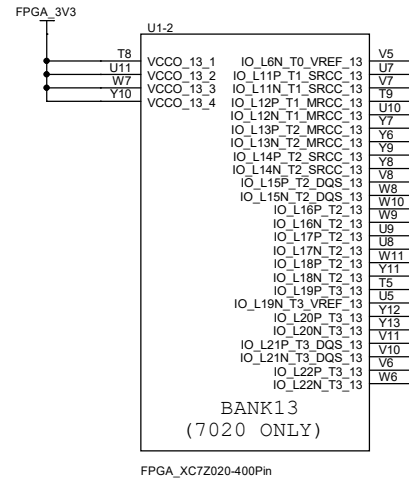
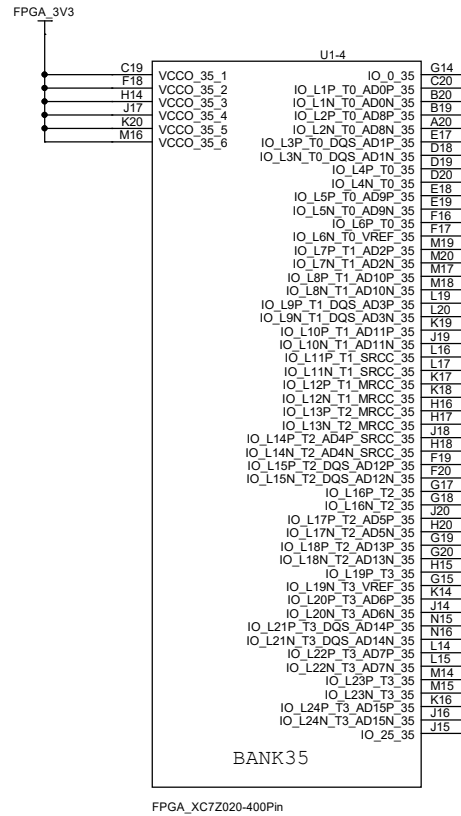
	VMODE1	VMODE0	BOOT MODE4
	Bank 1	Bank 0	FLL Enabled 0
			FLL Bypassed 1
2.5V, 3.3V	0	0	BOOT MODE3
1.8V	1	1	Cascade mode 0
			Independent mode 1
	BOOT MODE2	BOOT MODE1	BOOT MODE0
ITAG Boot Mode	0	0	0
NOR Boot	0	1	0
NAND	1	0	0
QUAD-SPI	0	0	1
SD Card	1	0	1

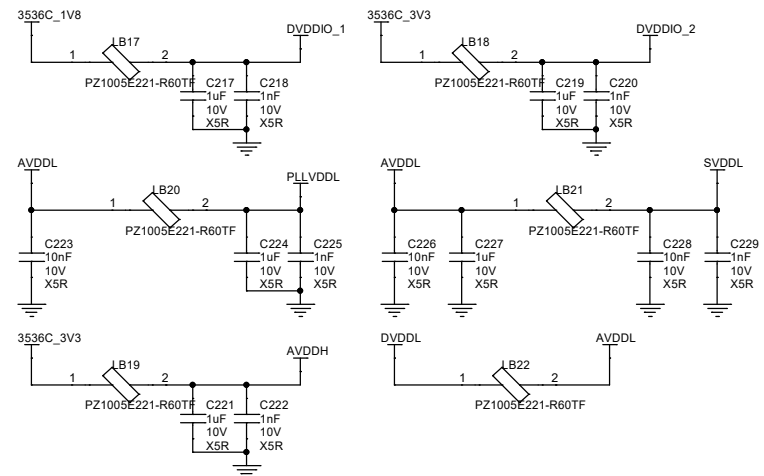
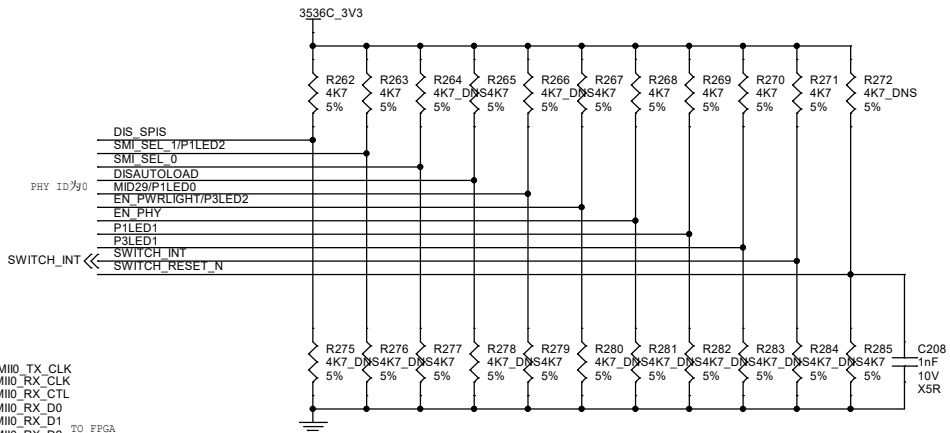




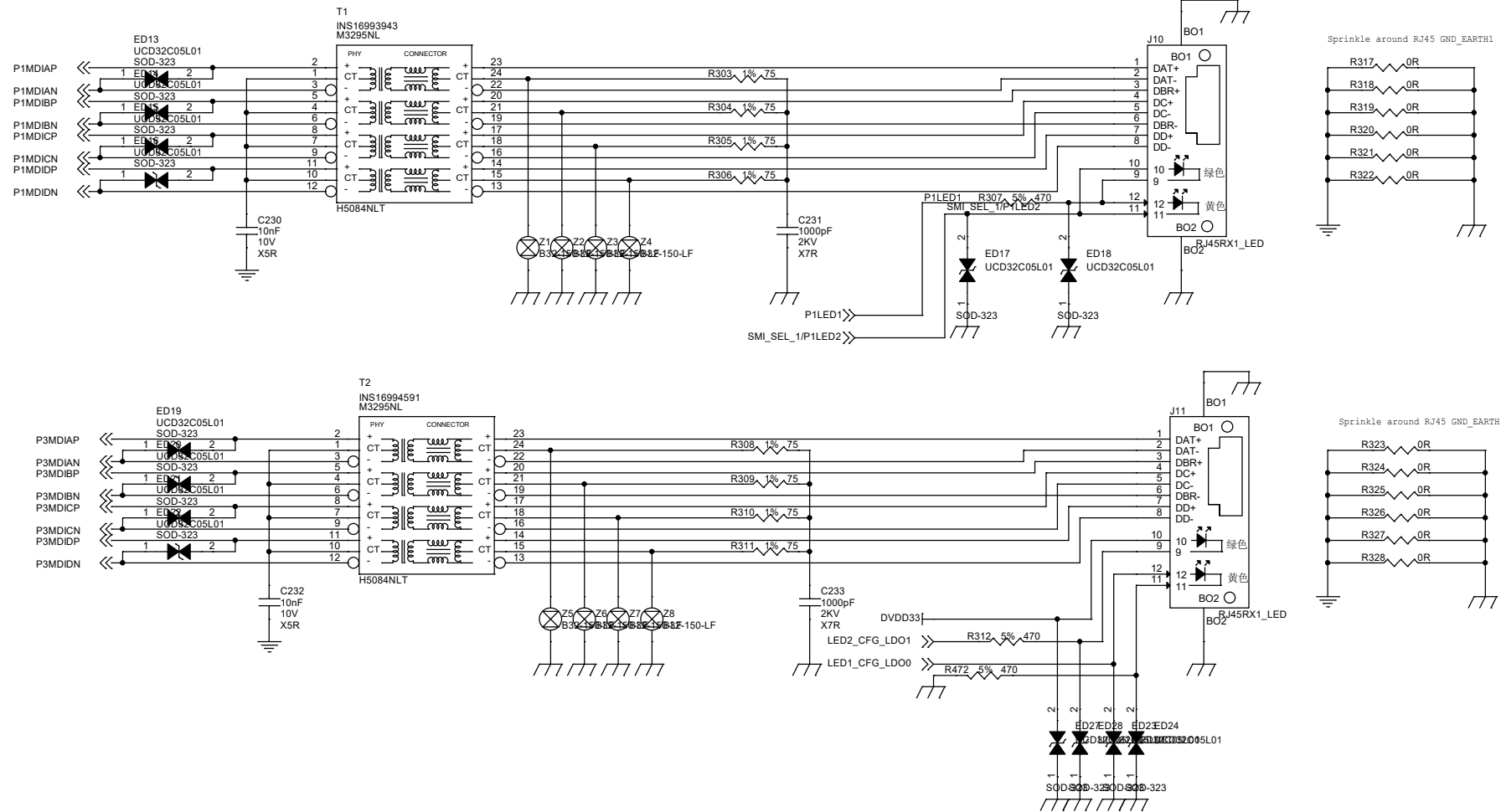




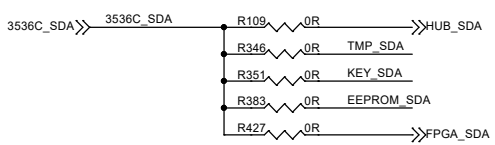
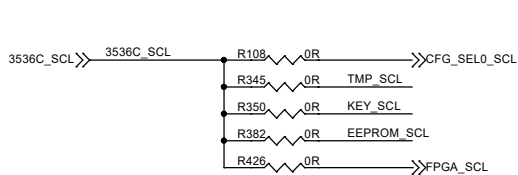




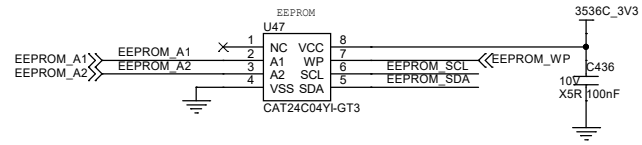
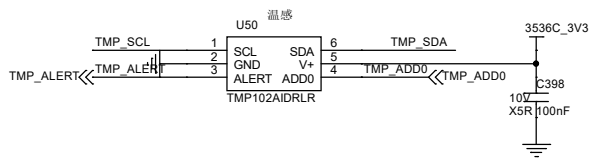
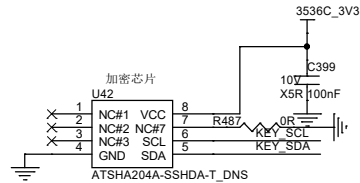
Title			
MegBox B2			
Size	Document Number		Rev
A3	LAN SWITCH		A
Date:	Wednesday, July 25, 2018	Sheet	23 of 32

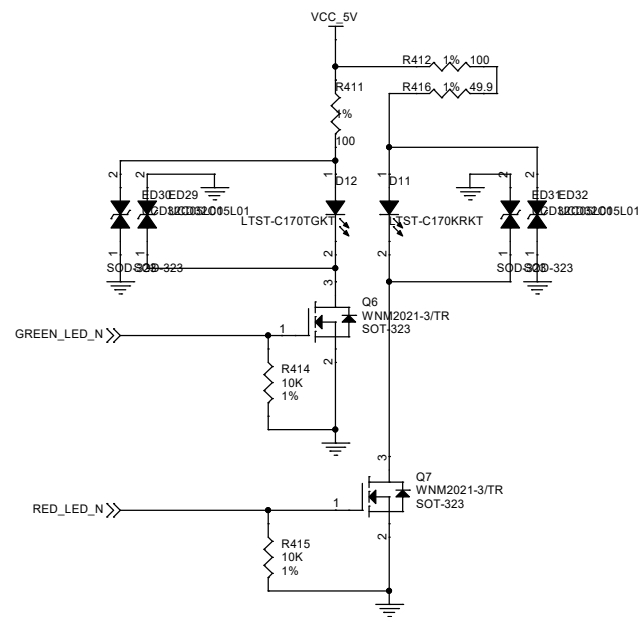
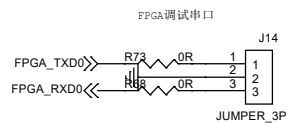
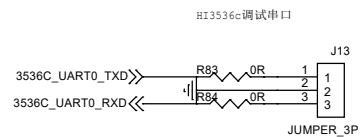
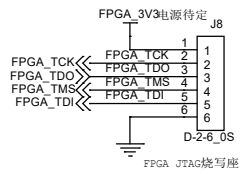


Title		
MegBox B2		
Size	Document Number	Rev
A3	1000BASE-T ETHERNET	A
Date:	Wednesday, July 25, 2018	Sheet 24 of 32



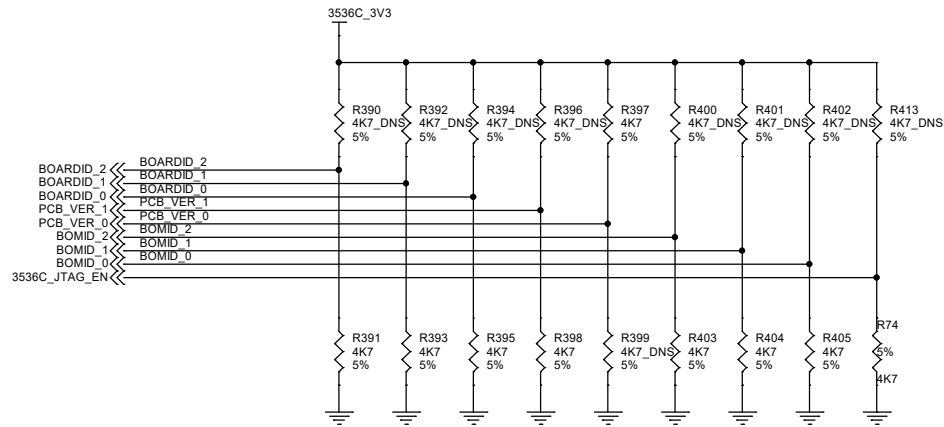
IIC devices	IIC ADDR
USB hsb	1010000b
温感	1001001b
加密芯片	1100100b
EEPROM	1010010b
FPGA	1010011b



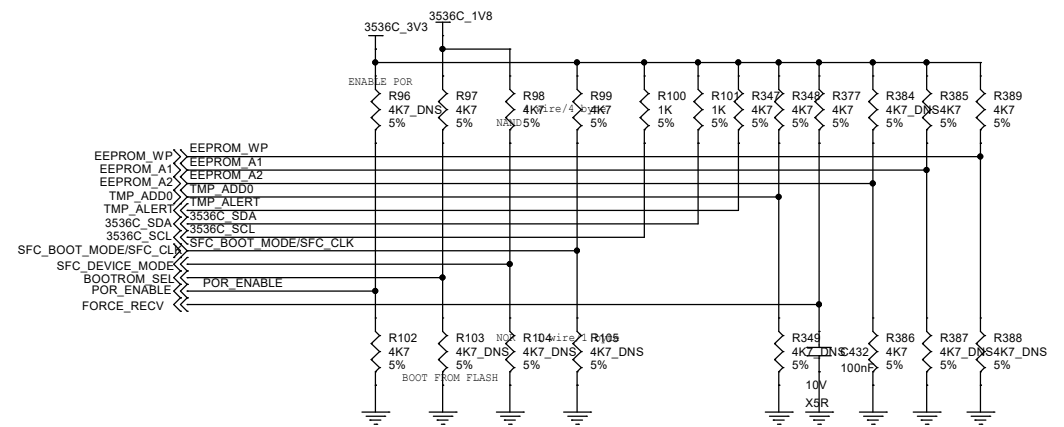


Title		
MegBox B2		
Size	Document Number	Rev
A3	DEBUG PORT	A
Date:	Wednesday, July 25, 2018	Sheet 26 of 32

BOARD STRAP PINS

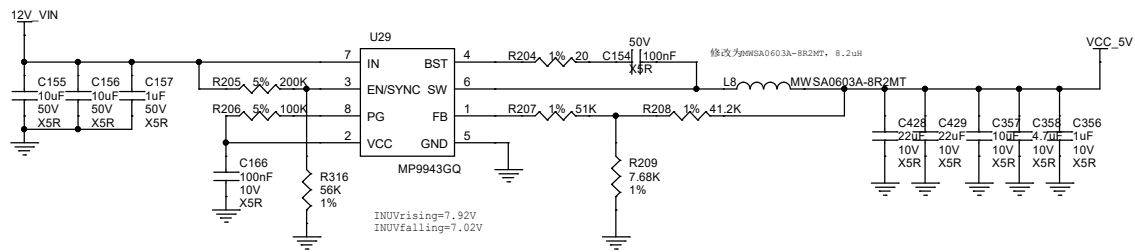
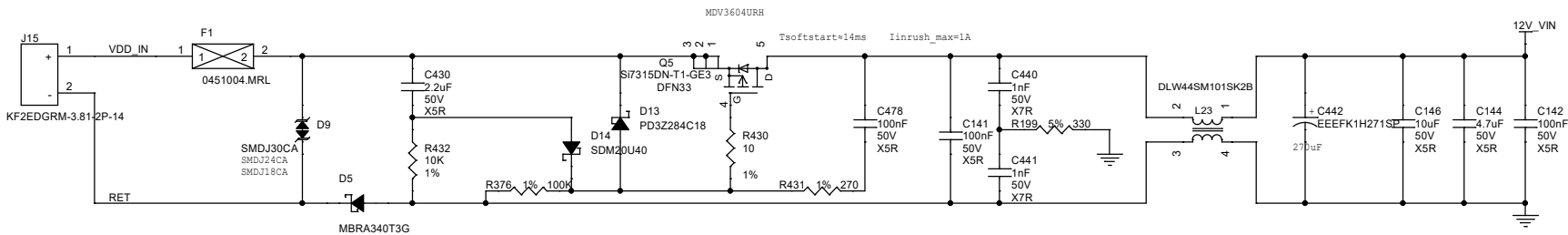


HI3536C STRAP PINS



Title		
MegBox B2		
Size	Document Number	Rev
A3	STRAP SIGNAL	A
Date:	Wednesday, July 25, 2018	Sheet 27 of 32

POWER INPUT RANGE: 8V~30V

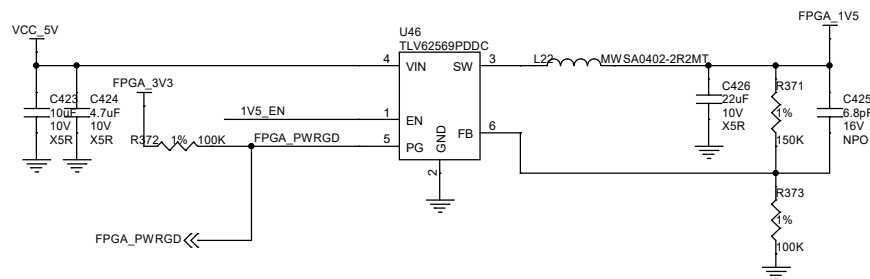
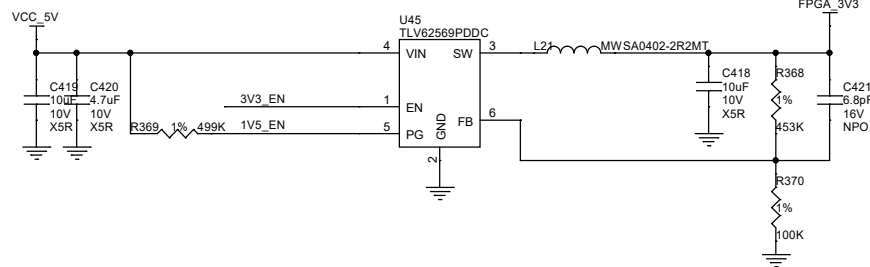
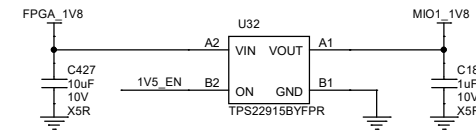
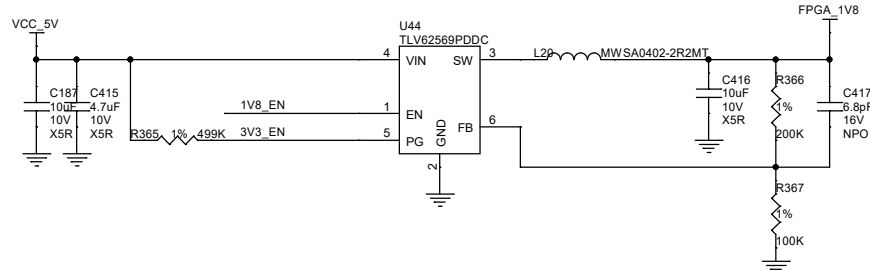
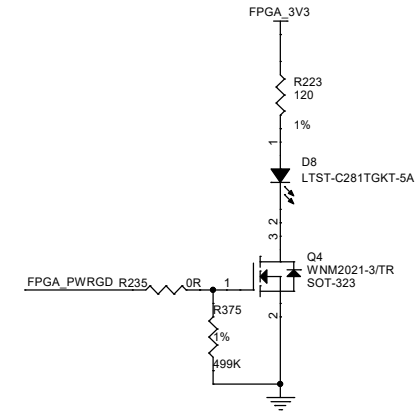
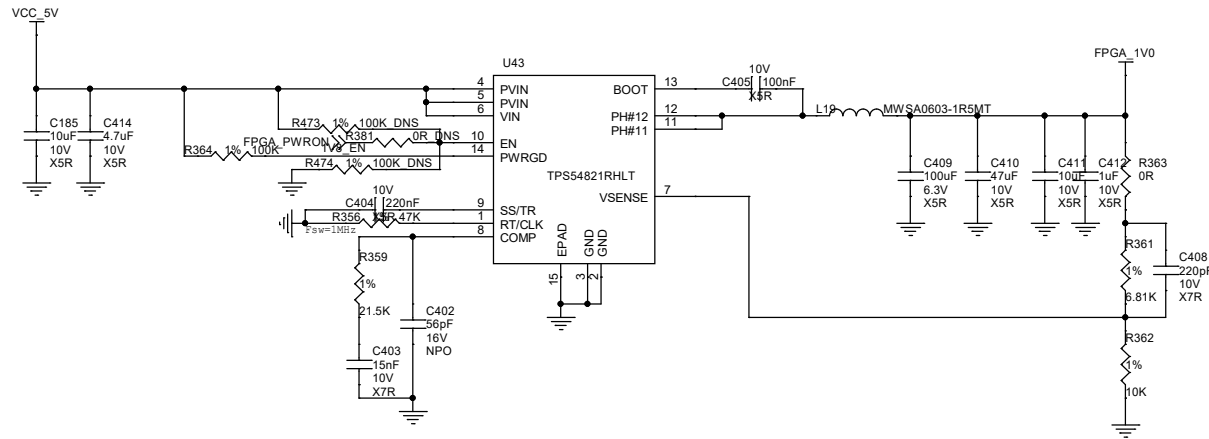


Hi3536C上电时序: 12V-->VCC_5V-->3536C_3V3/3536C_1V8-->DVDDL-->3536C_1V5-->DVDD_CORE/DVDD_CPU
Hi3536C下电时序: 12V-->VCC_5V-->3536C_3V3/3536C_1V8-->3536C_1V5/DVDD_CORE/DVDD_CPU

FPGA上电时序: FPGA_1V0-->FPGA_1V8-->FPGA_3V3-->FPGA_1V5/MIO1_1V8-->PS_POR_B
FPGA下电时序: 12V-->PS_POR_B-->.....

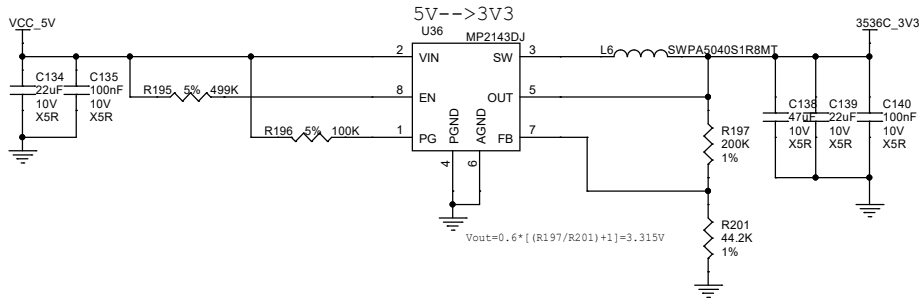
Title		
MegBox B2		
Size	Document Number	Rev
A3	POWER SUPPLY: INPUT	A
Date:	Friday, July 27, 2018	Sheet 28 of 32

FPGA POWER RAILS

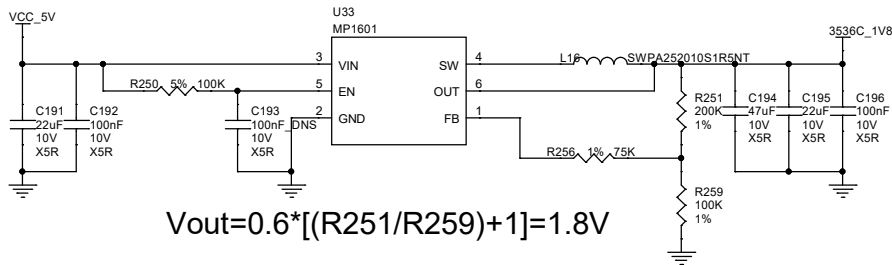


Title		
MegBox B2		
Size	Document Number	Rev
A3	POWER SUPPLY: FPGA	A
Date:	Wednesday, July 25, 2018	Sheet 29 of 32

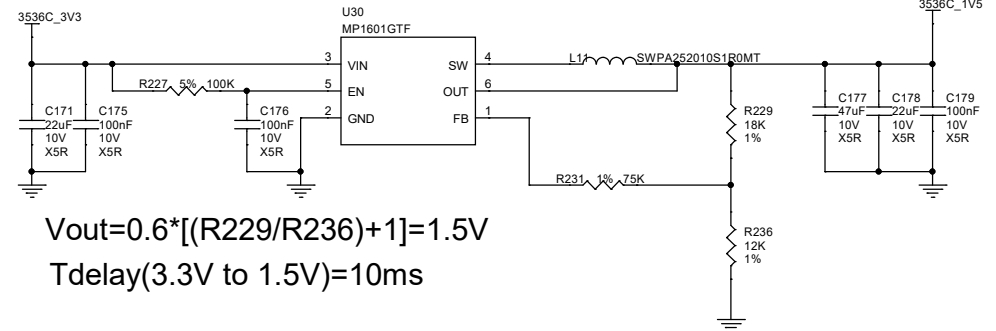
HI3536c POWER RAILS



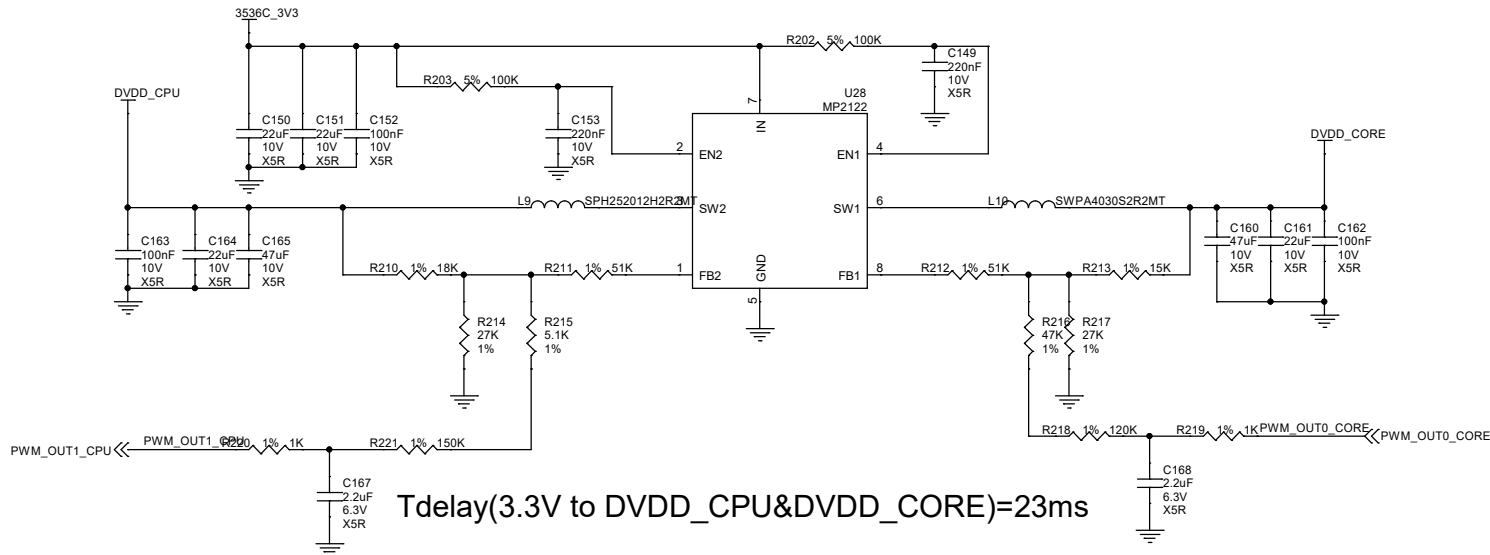
5V --> 1V8



3V3 --> 1V5

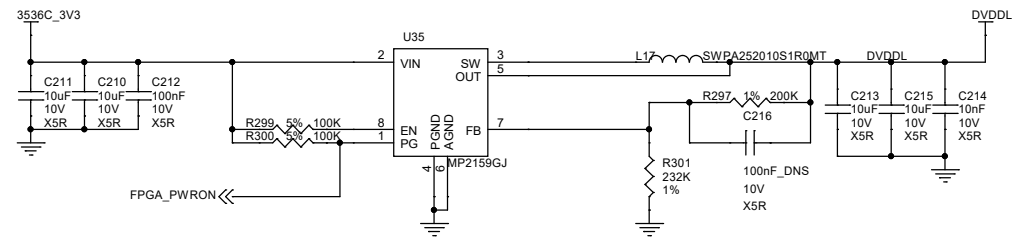
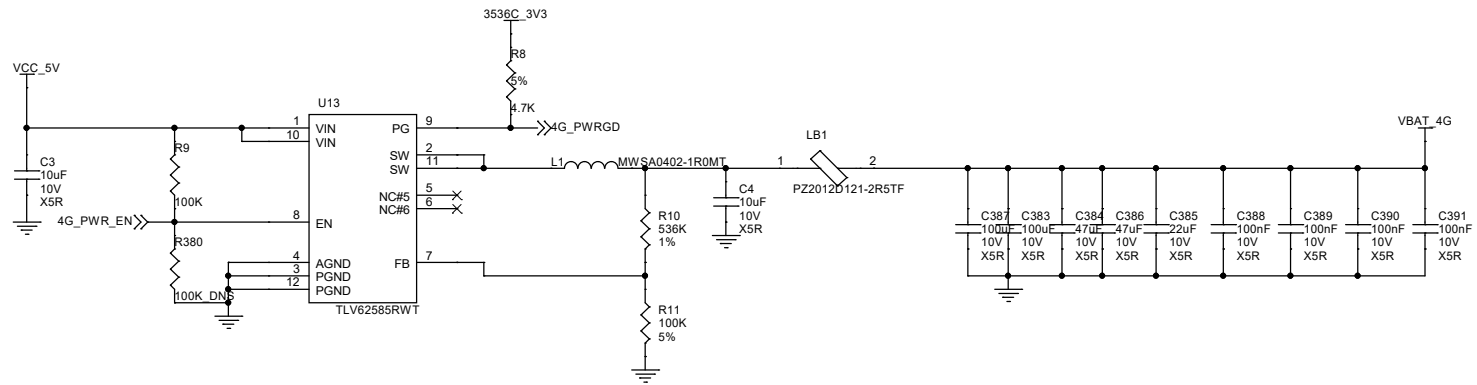


3V3 --> Hi3536C_Core/CPU

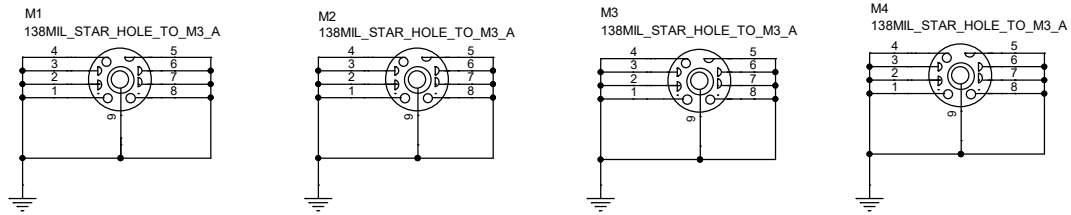


Title		
MegBox B2		
Size	Document Number	Rev
A3	POWER SUPPLY: 3536C	A
Date:	Wednesday, July 25, 2018	Sheet 30 of 32

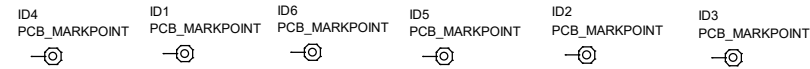
4G&SWITCH POWER RAILS



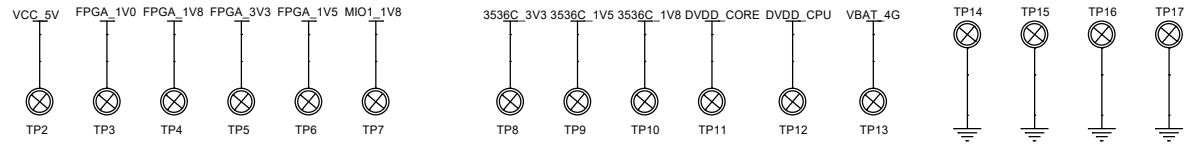
MECHANICAL



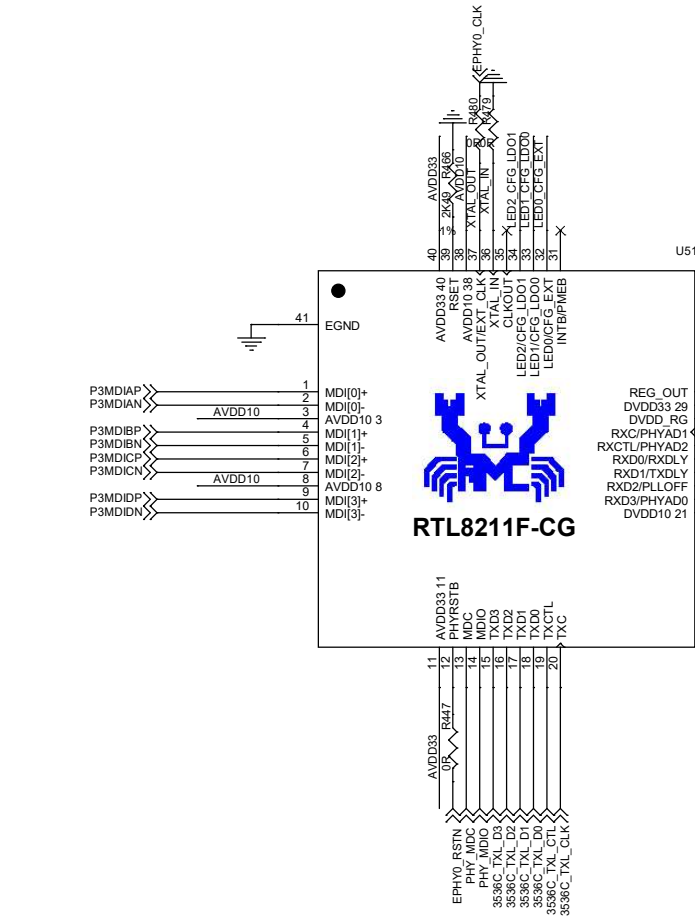
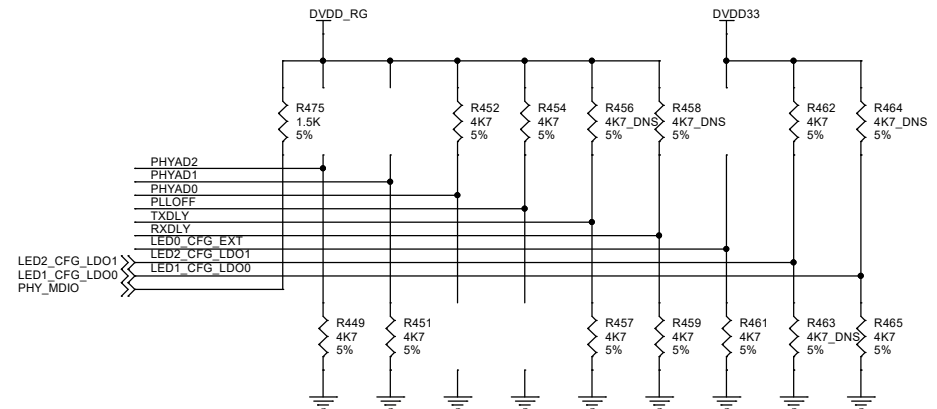
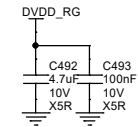
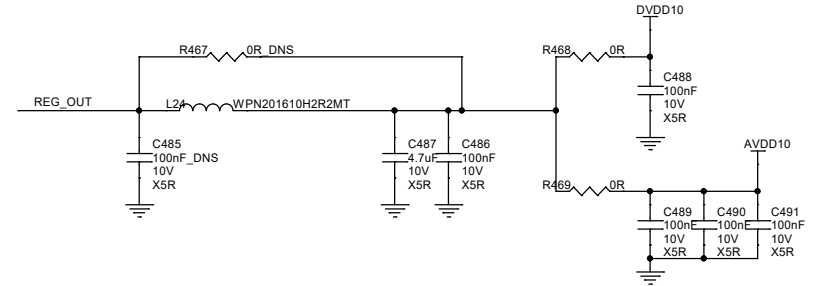
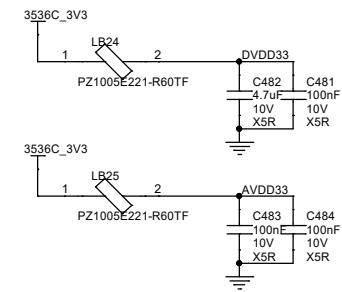
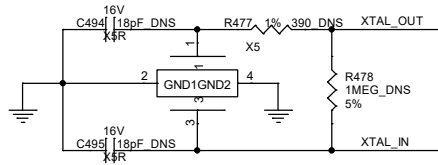
MARK POINTS



TEST POINTS

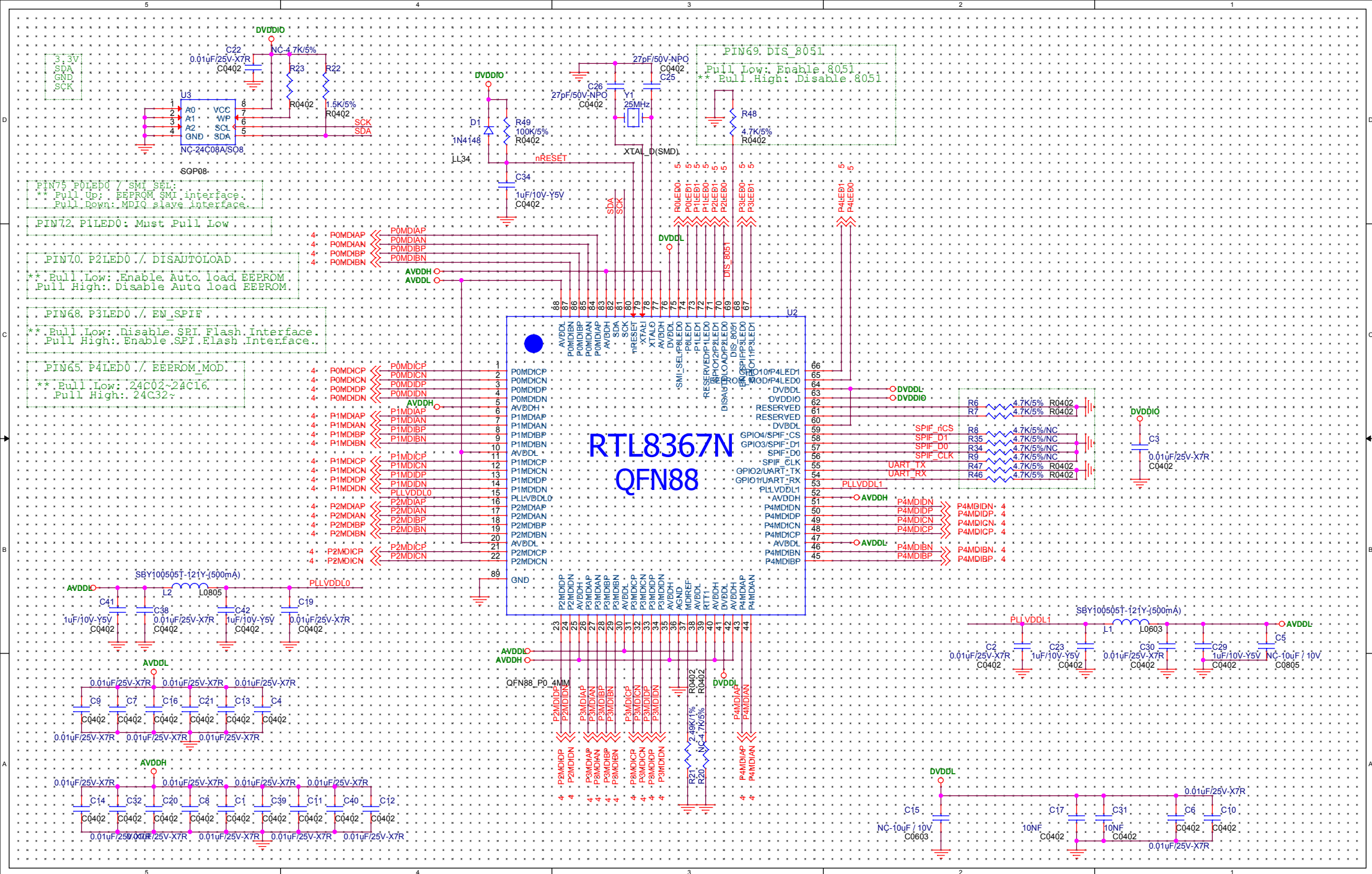


Title		
MegBox B2		
Size	Document Number	Rev
A3	MISC.	A
Date:	Wednesday, July 25, 2018	Sheet 32 of 32

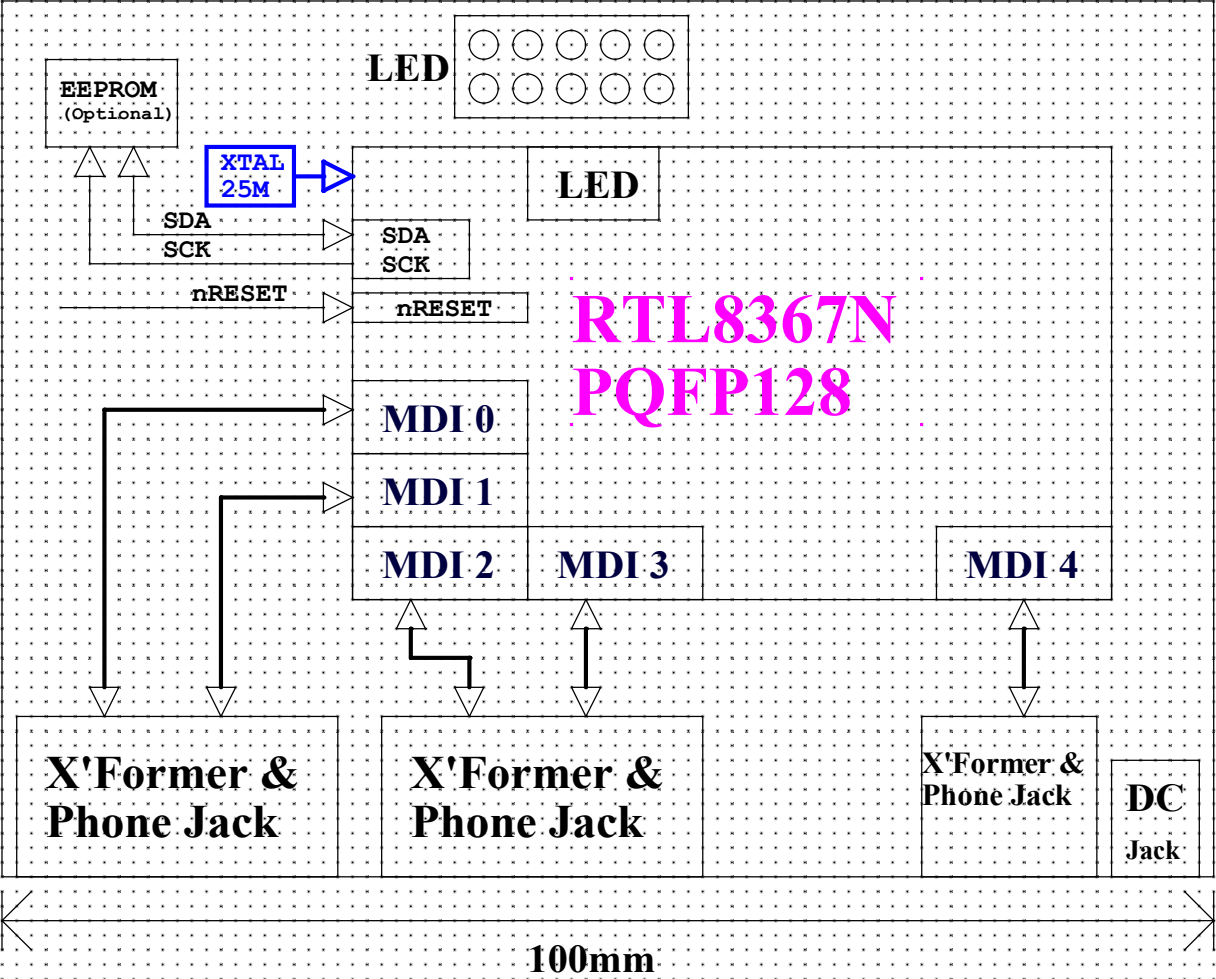


RGMII Power Source	CFG_EXT	CFG_LDO[1:0]
External 3.3V	1'b1	2'b00
External 2.5V	1'b1	2'b01
External 1.8V	1'b1	2'b10
External 1.5V	1'b1	2'b11
Internal 2.5V	1'b0	2'b01
Internal 1.8V (default)	1'b0	2'b10
Internal 1.5V	1'b0	2'b11

PHY Address	PHYAD[2:0]
0	3'b000
1 (default)	3'b001
2	3'b010
3	3'b011
4	3'b100
5	3'b101
6	3'b110
7	3'b111



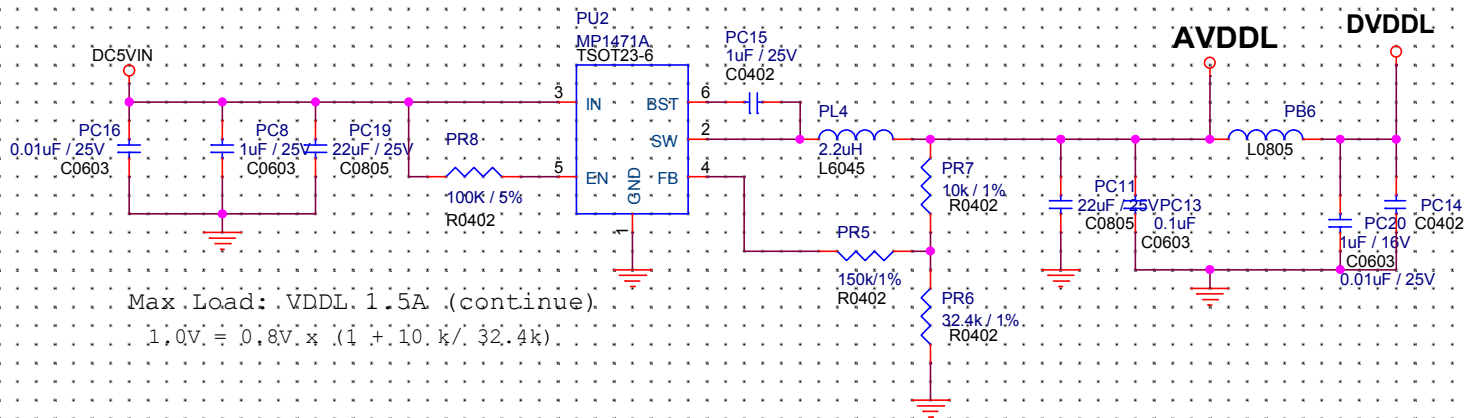
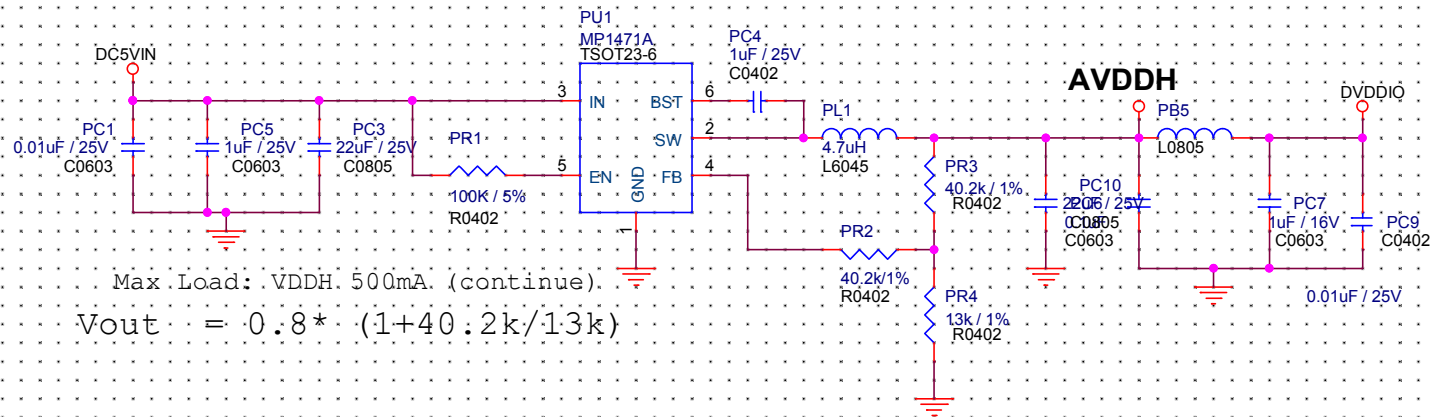
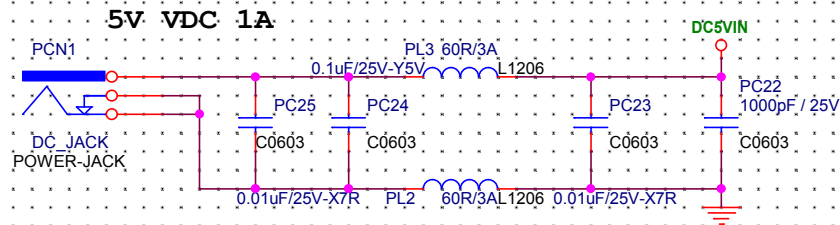
RTL8367 (PQFP128) Demo-Board Block Diagram



REVISION HISTORY

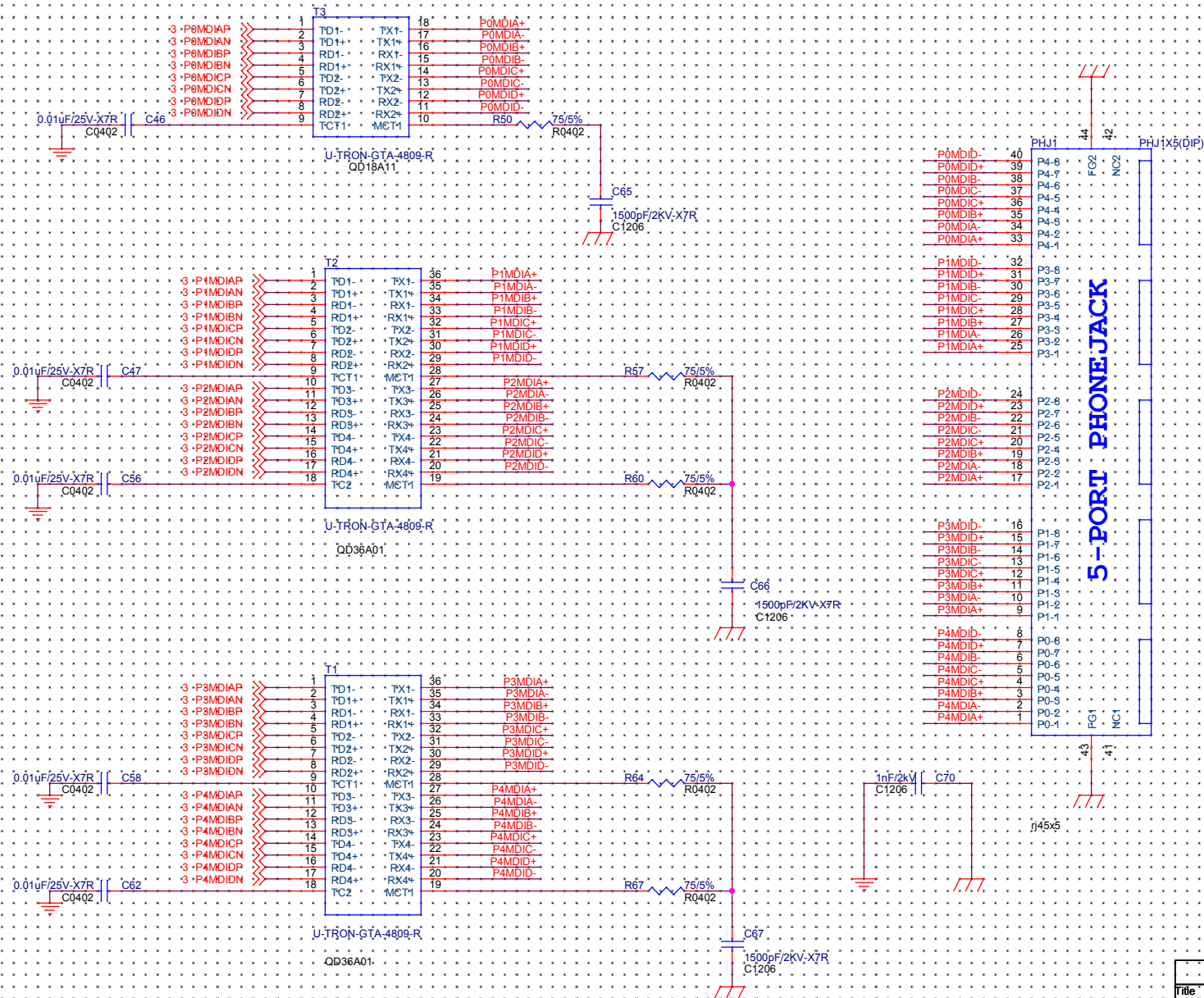
Revision	Release Date	Summary
v.3.0	2011/05/2	First release

Title: RTL8367 Demo-Board Block Diagram		
Size: B	Document Number: <Doc>	Rev: v.3.0
Date: Tuesday, March 28, 2017	Sheet: 1	of 5



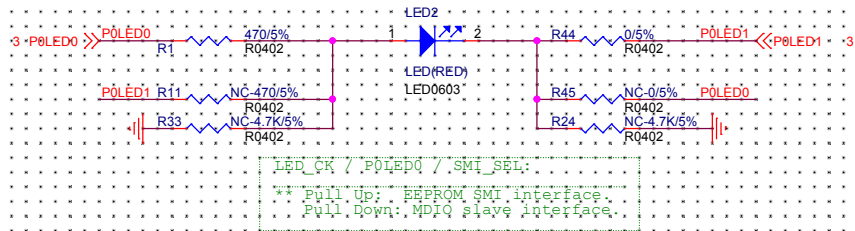
Title		
RTL8367 Power Regulator		
Size	Document Number	
Custom	<Doc>	
Date:	Tuesday, March 28, 2017	Sheet 2 of 5
		Rev v.3.0

X'Formre Central Tap at Primary
Side must tie with each other.

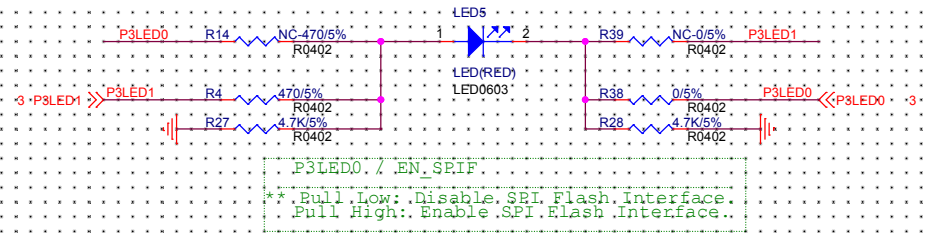


Title				
RTL8367 Transformer & Phonejack				
Size B	Document Number <Doc>			Rev v.3.0
Date:	Tuesday, March 28, 2017	Sheet	4	of 5

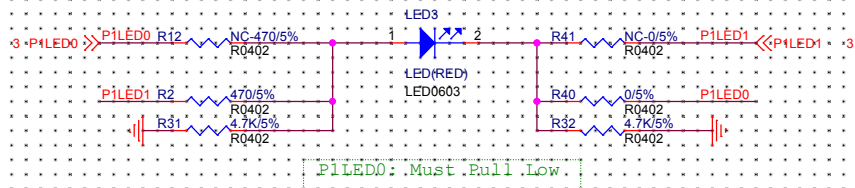
Port 0 LED



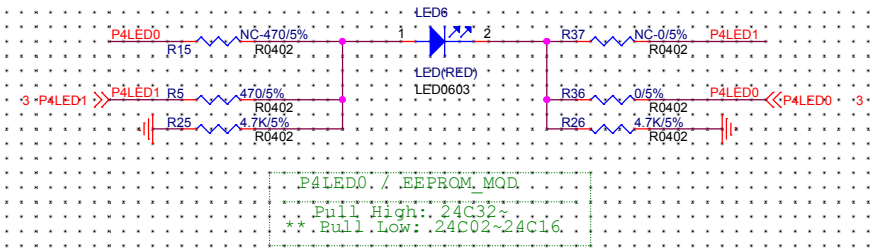
Port 3 LED



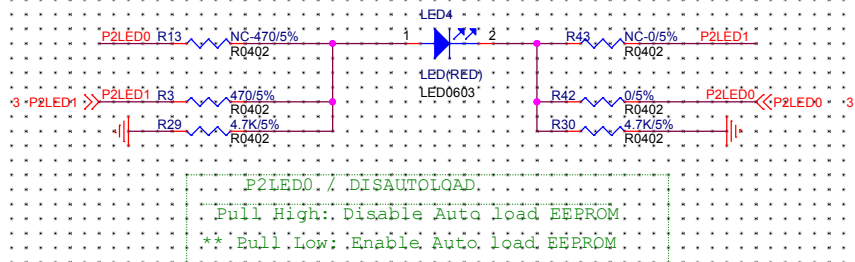
Port 1 LED



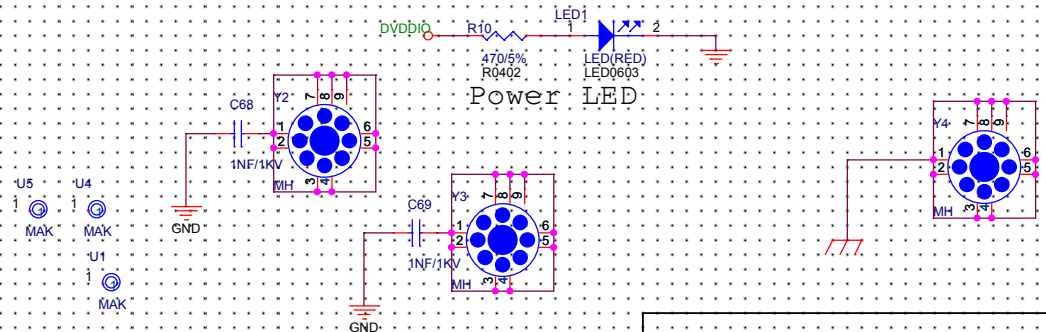
Port 4 LED



Port 2 LED



Power LED



Title: RTL8367 LED		
Size: B	Document Number: <Doc>	Rev: v3.0
Date: Tuesday, March 28, 2017	Sheet: 5	of 5