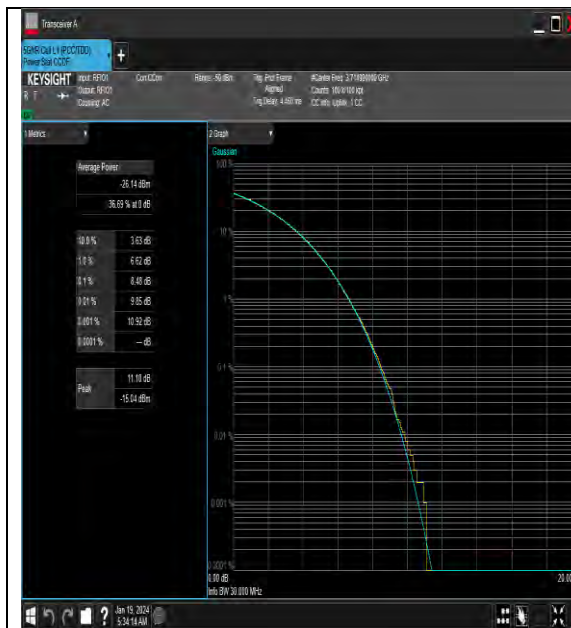
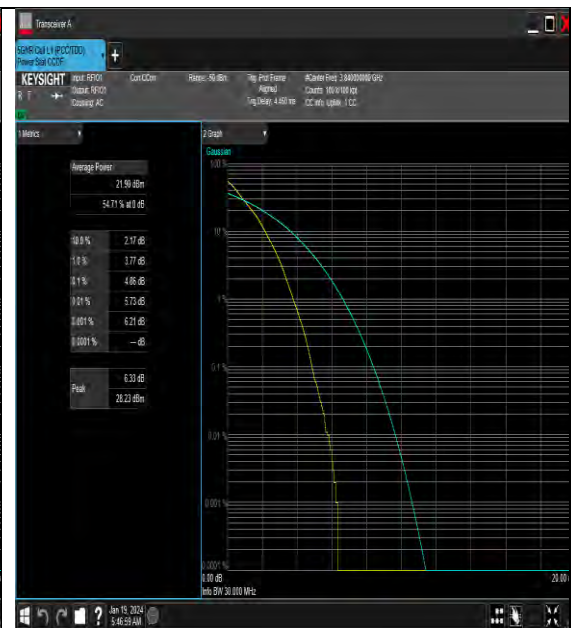
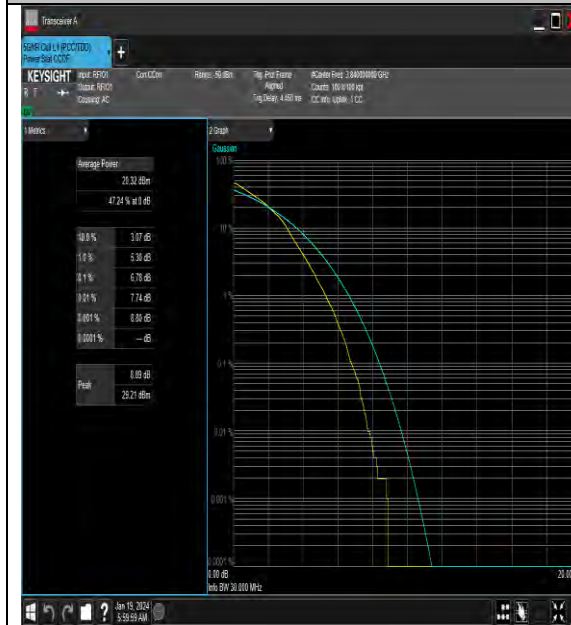




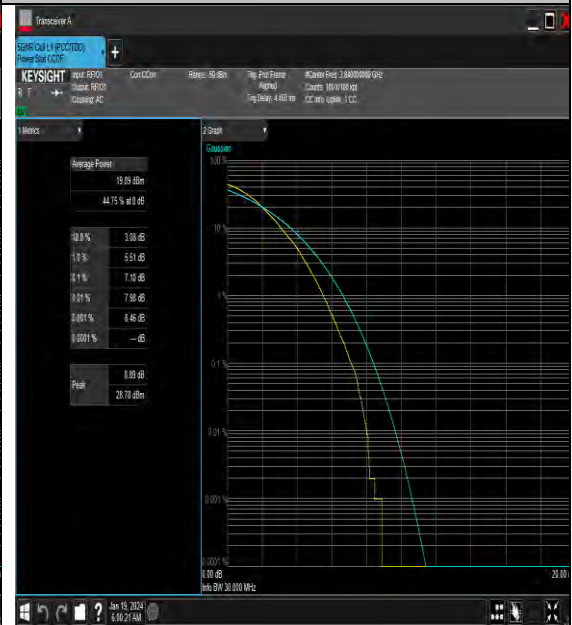
NTNV_N77-3700-3980_PC3_30_30_L_TID4_#1



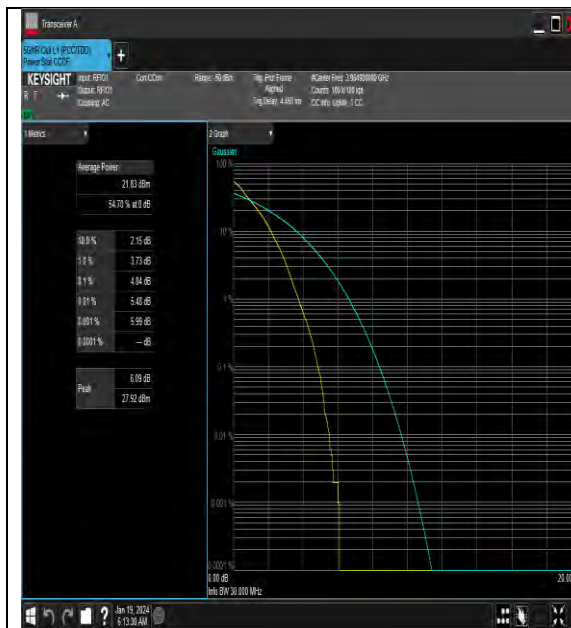
NTNV_N77-3700-3980_PC3_30_30_M_TID1_#1



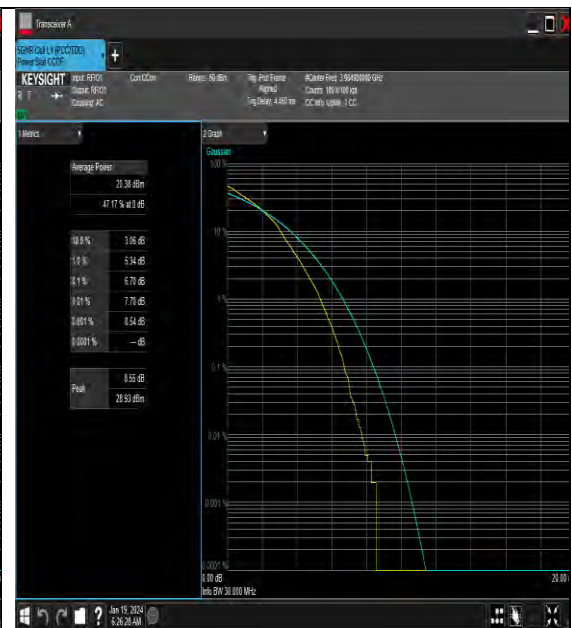
NTNV_N77-3700-3980_PC3_30_30_M_TID3_#1



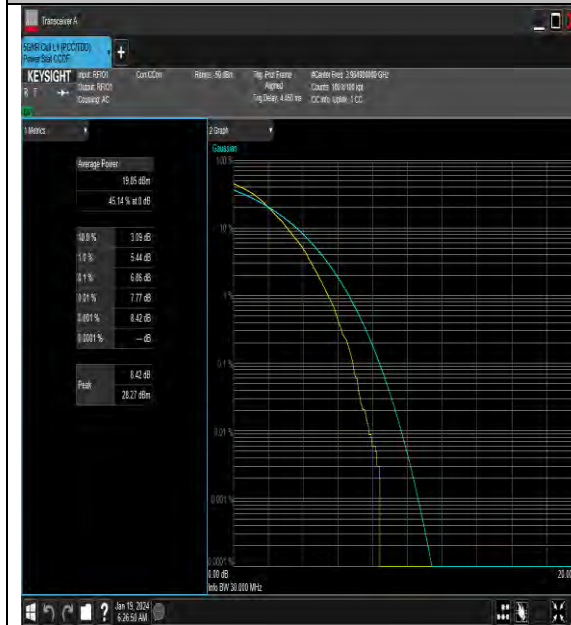
NTNV_N77-3700-3980_PC3_30_30_M_TID4_#1



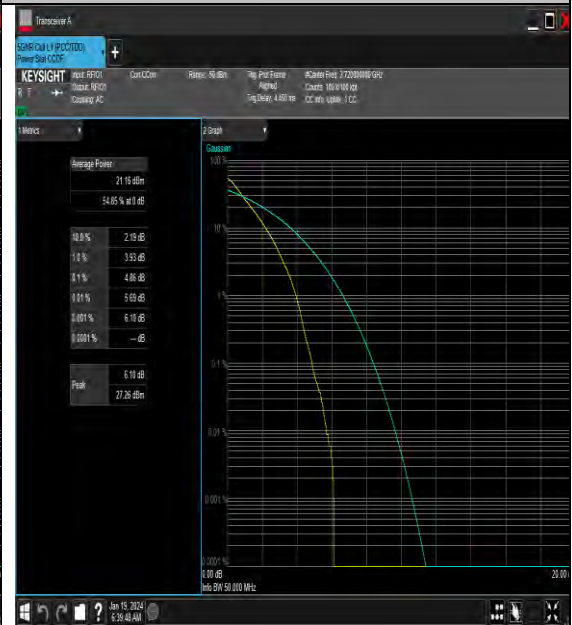
NTNV_N77-3700-3980_PC3_30_30_H_TID1_#1



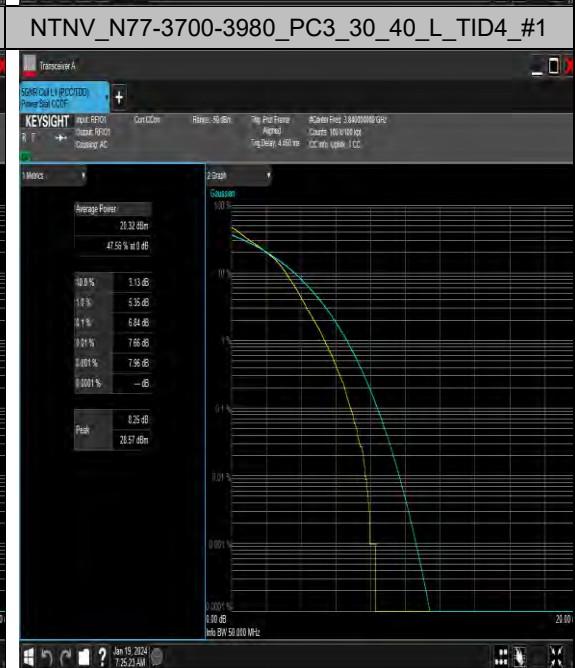
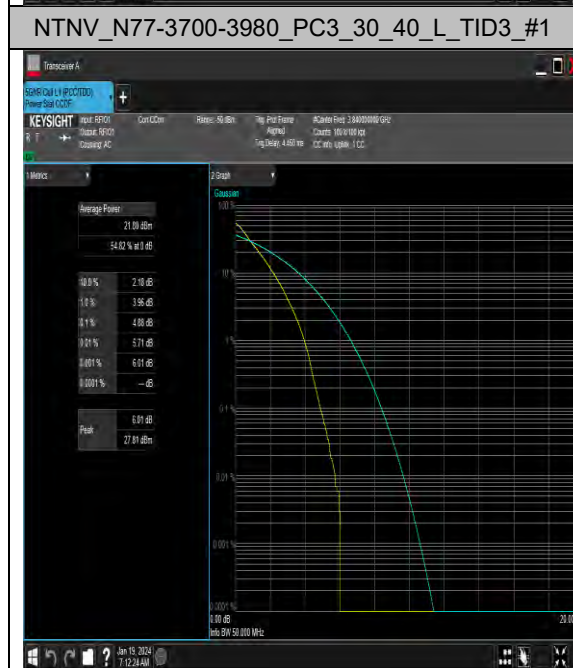
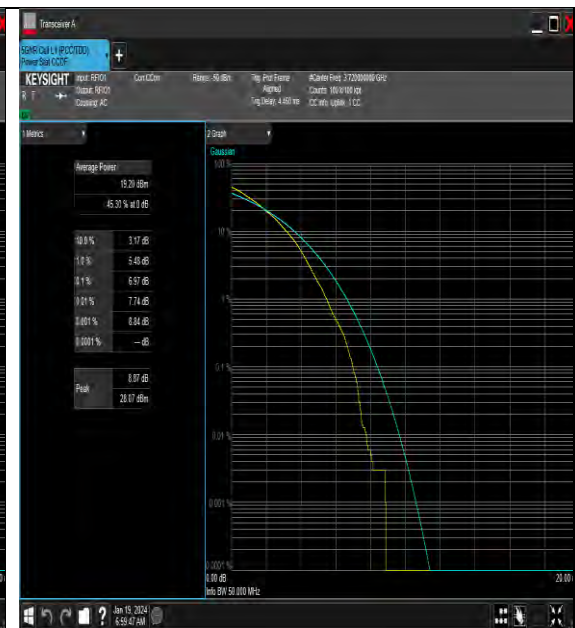
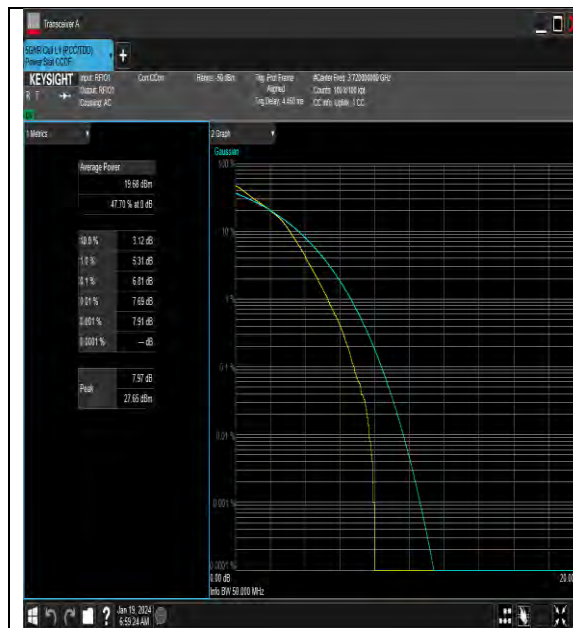
NTNV_N77-3700-3980_PC3_30_30_H_TID3_#1

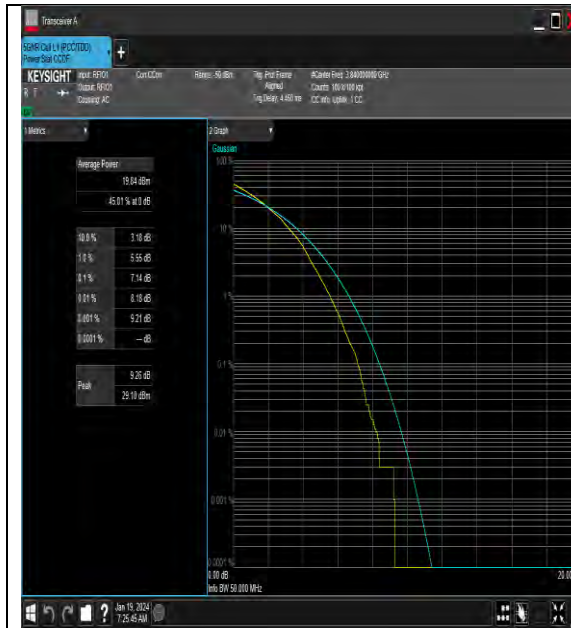


NTNV_N77-3700-3980_PC3_30_30_H_TID4_#1

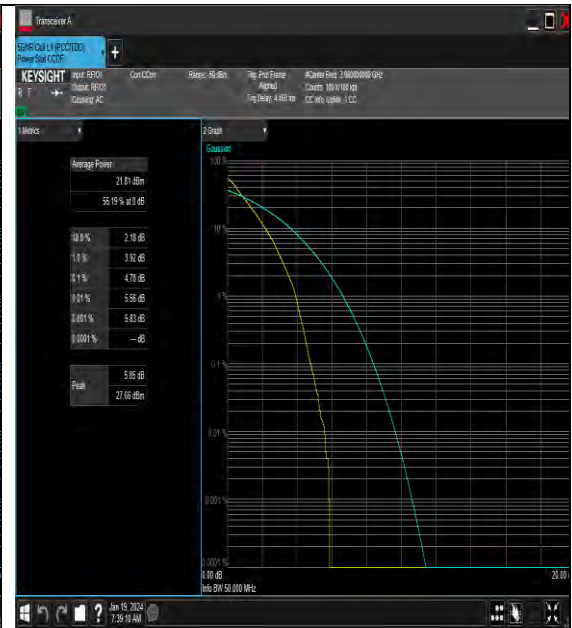


NTNV_N77-3700-3980_PC3_30_40_L_TID1_#1

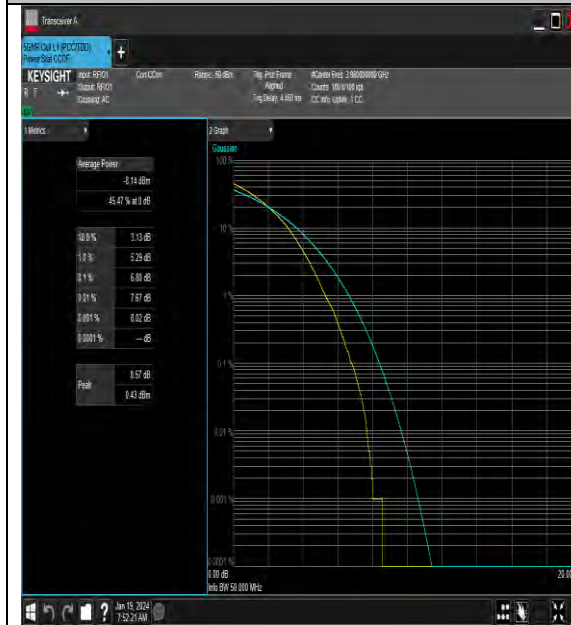




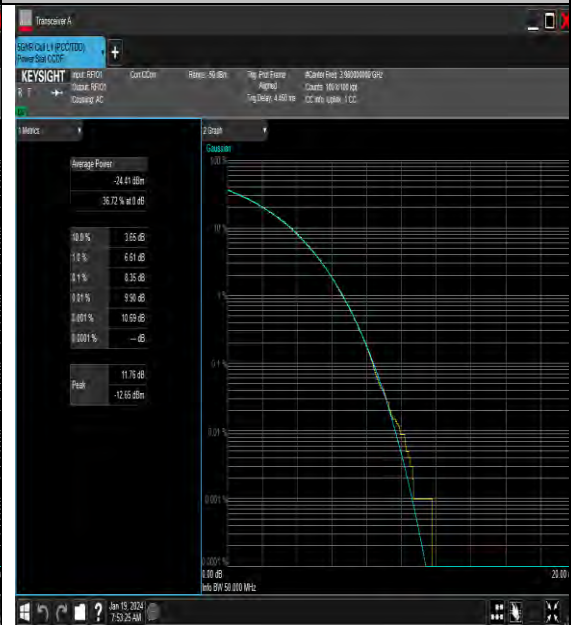
NTNV_N77-3700-3980_PC3_30_40_M_TID4_#1



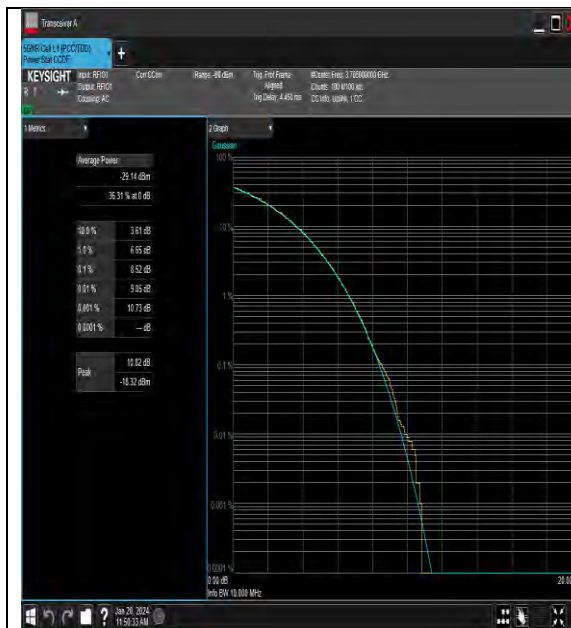
NTNV_N77-3700-3980_PC3_30_40_H_TID1_#1



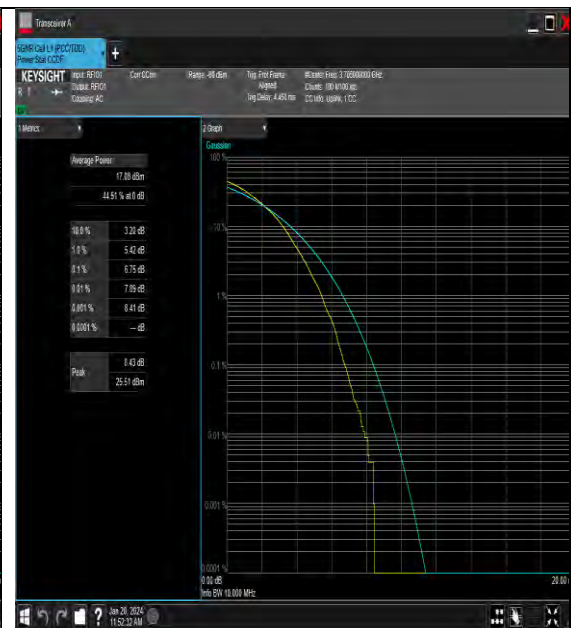
NTNV_N77-3700-3980_PC3_30_40_H_TID3_#1



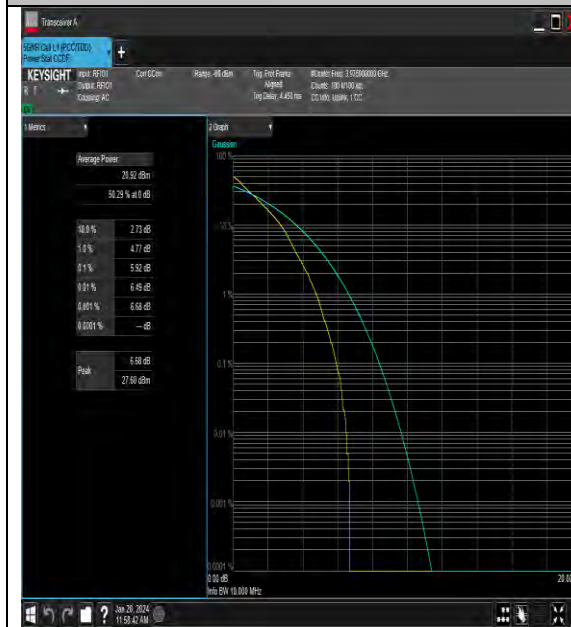
NTNV_N77-3700-3980_PC3_30_40_H_TID4_#1



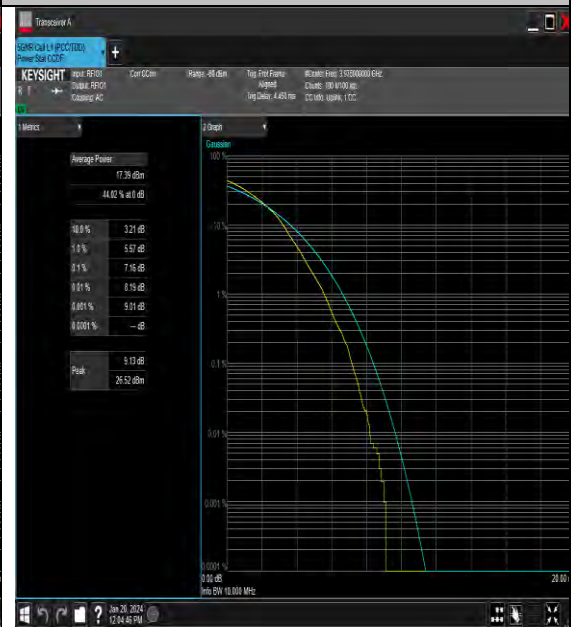
NTNV_N77-3700-3980_PC3_30_10_L_TID1_#1



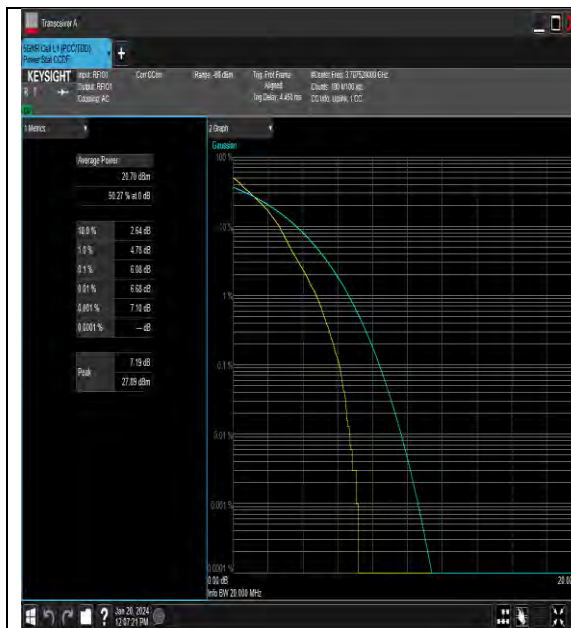
NTNV_N77-3700-3980_PC3_30_10_L_TID5_#1



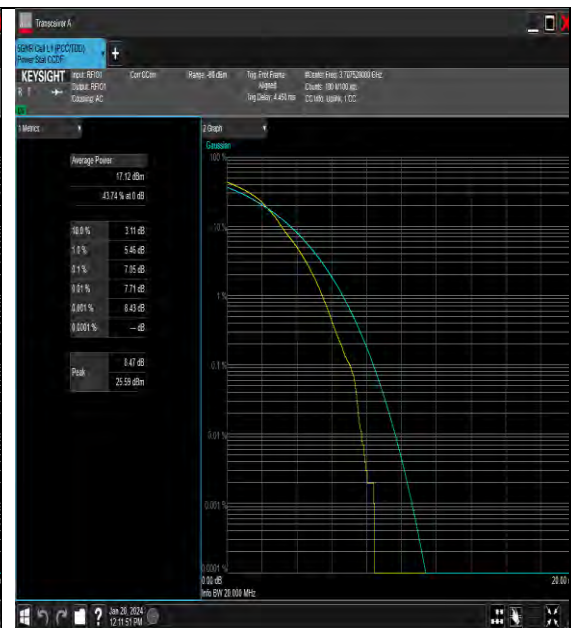
NTNV_N77-3700-3980_PC3_30_10_H_TID2_#1



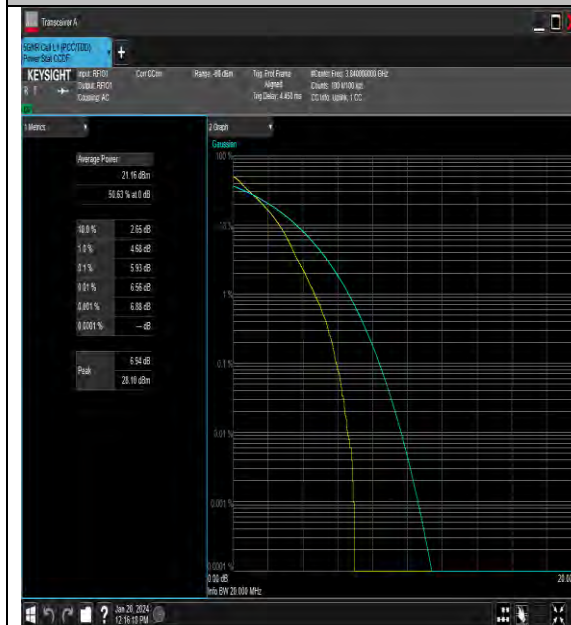
NTNV_N77-3700-3980_PC3_30_10_H_TID5_#1



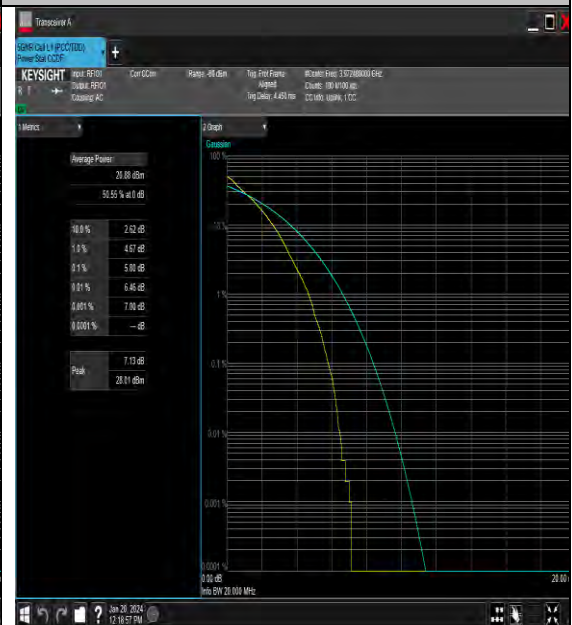
NTNV_N77-3700-3980_PC3_30_15_L_TID2_#1



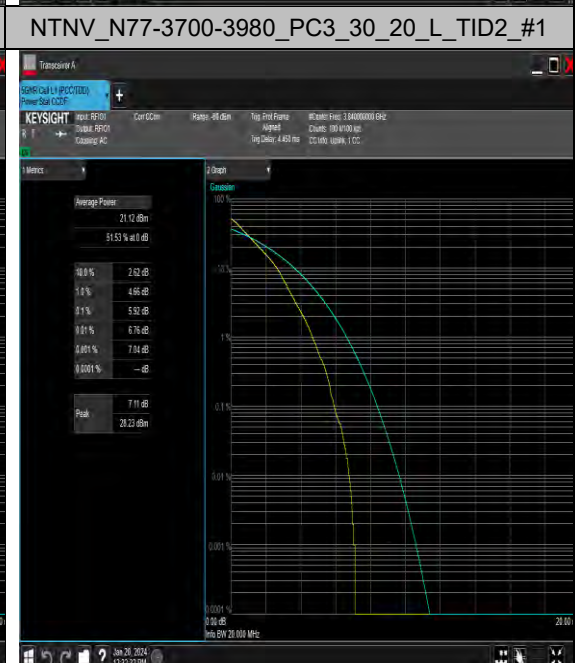
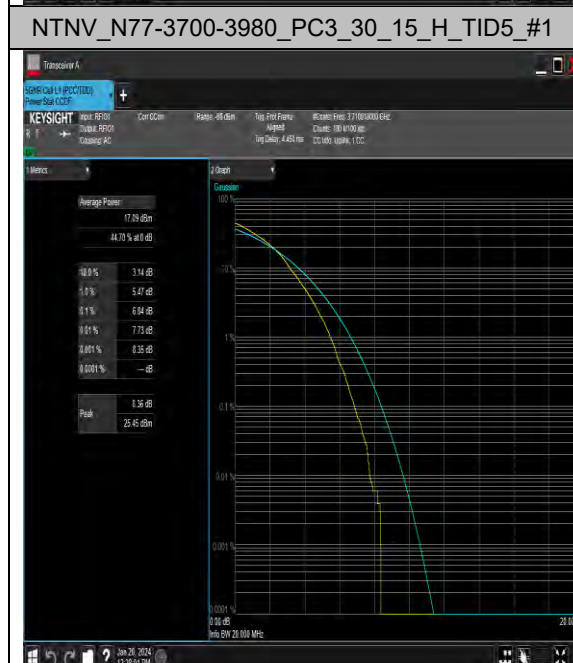
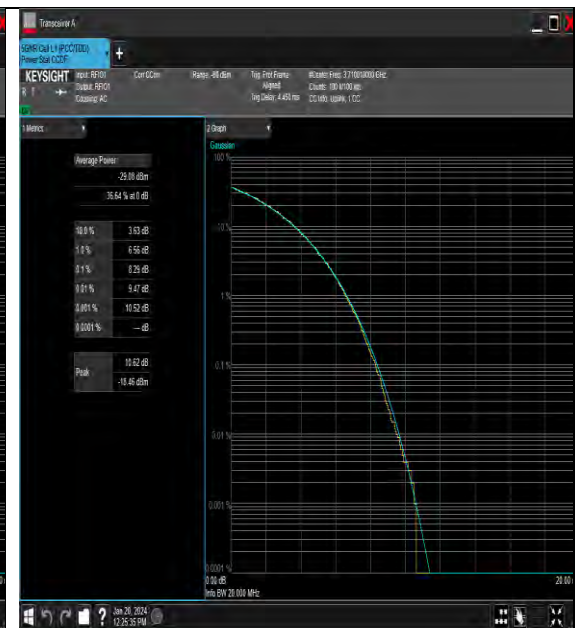
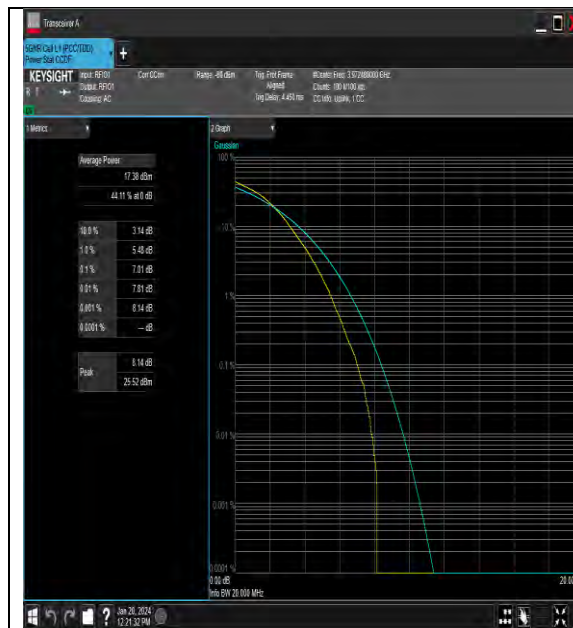
NTNV_N77-3700-3980_PC3_30_15_L_TID5_#1

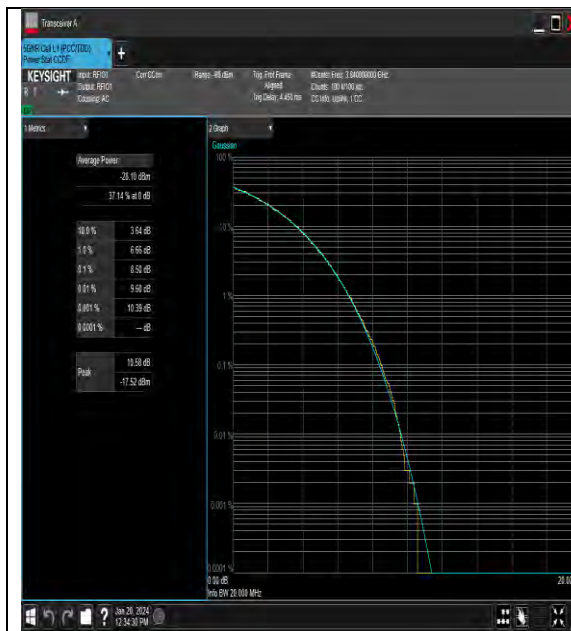


NTNV_N77-3700-3980_PC3_30_15_M_TID2_#1

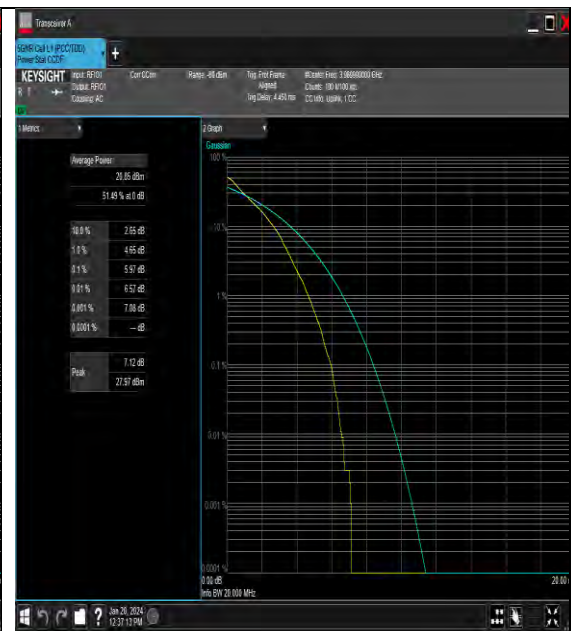


NTNV_N77-3700-3980_PC3_30_15_H_TID2_#1

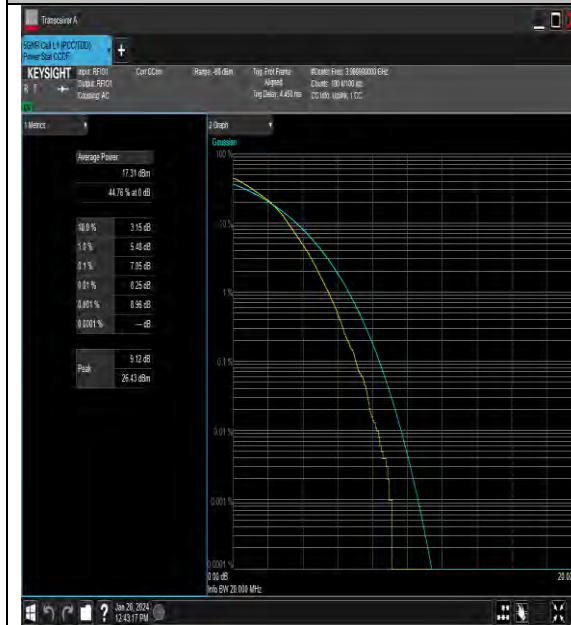




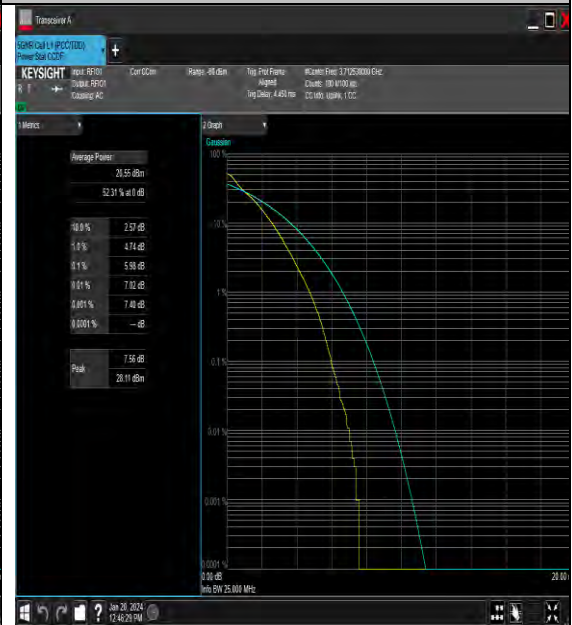
NTNV_N77-3700-3980_PC3_30_20_M_TID5_#1



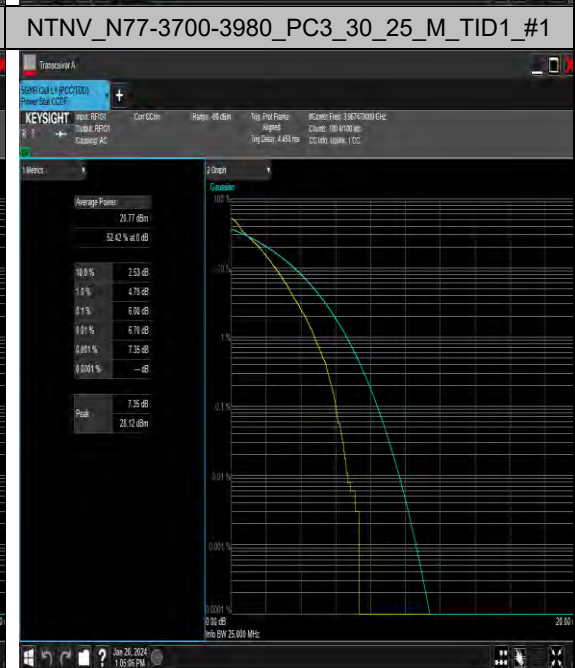
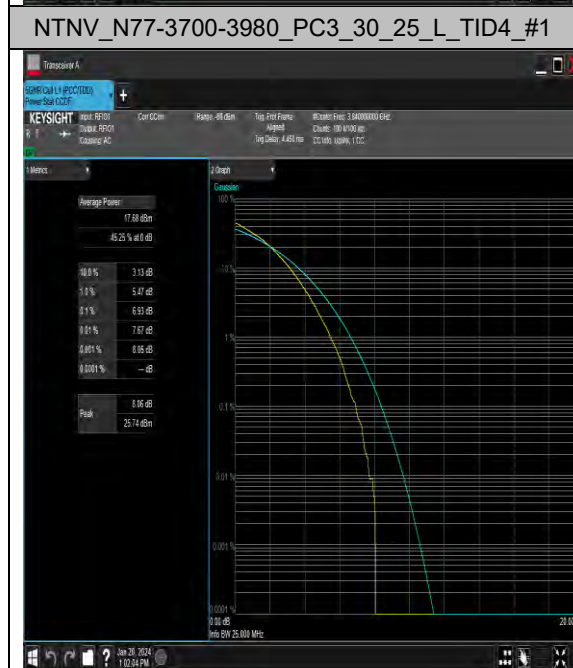
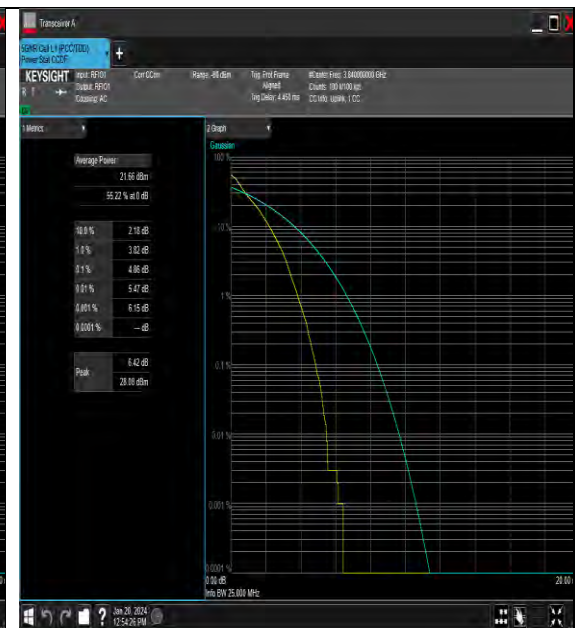
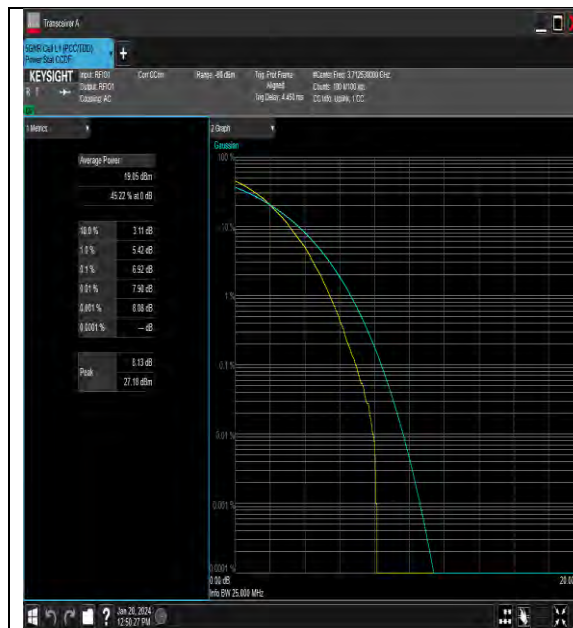
NTNV_N77-3700-3980_PC3_30_20_H_TID2_#1

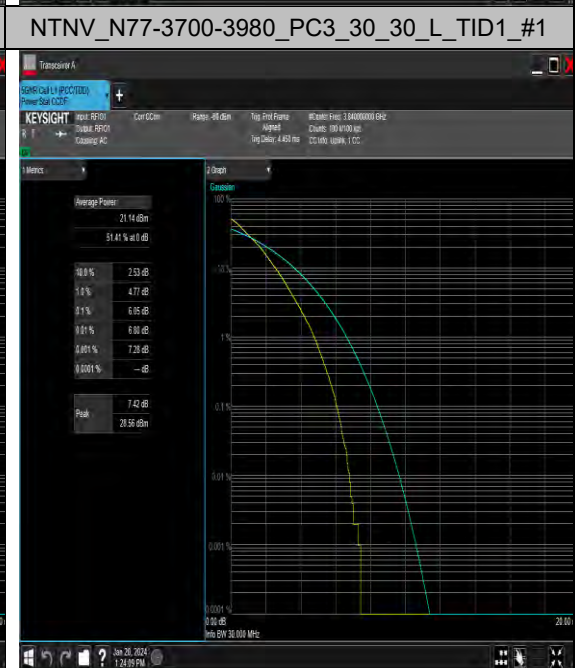
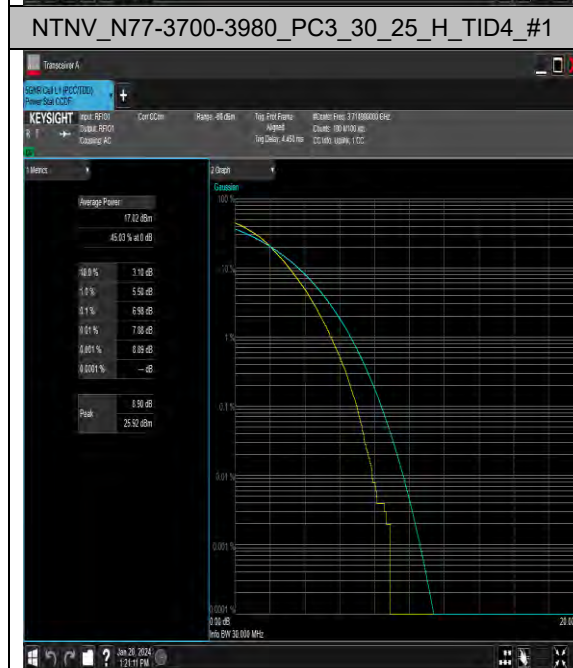
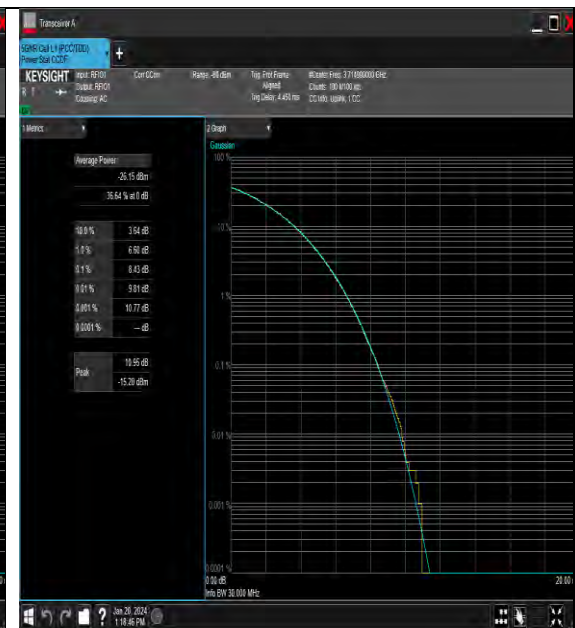
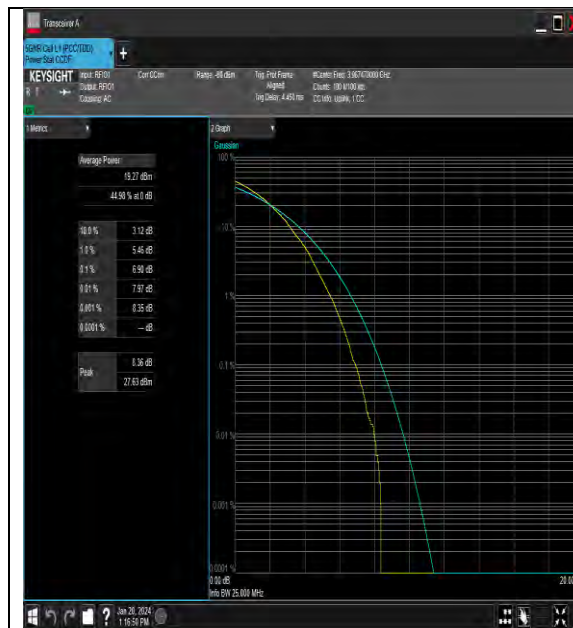


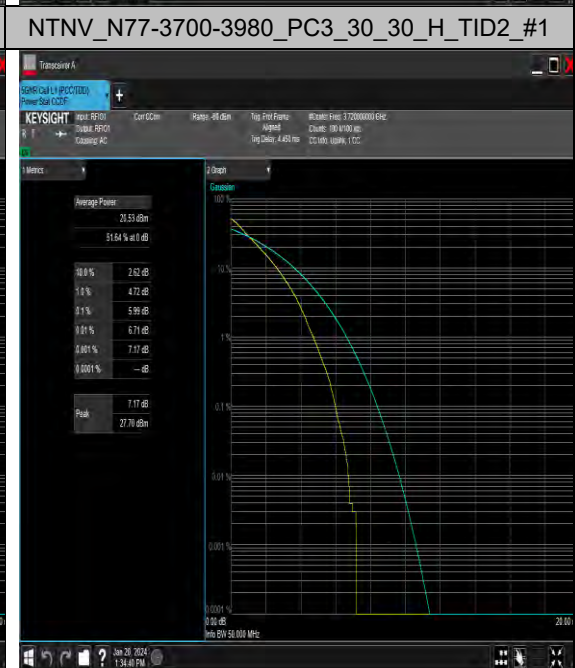
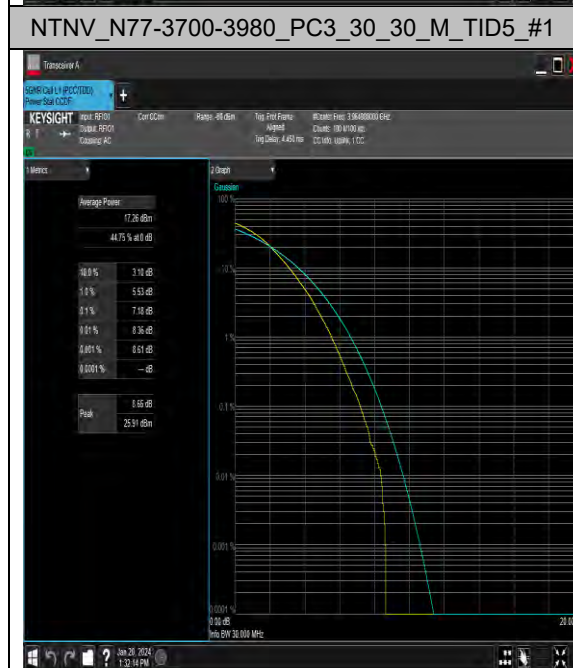
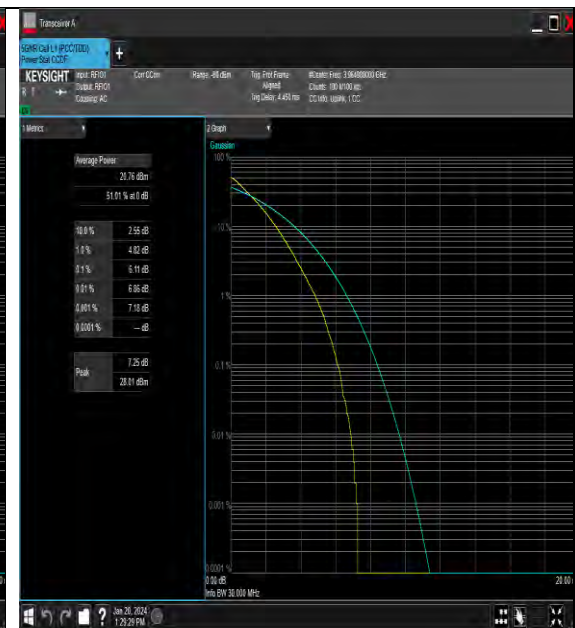
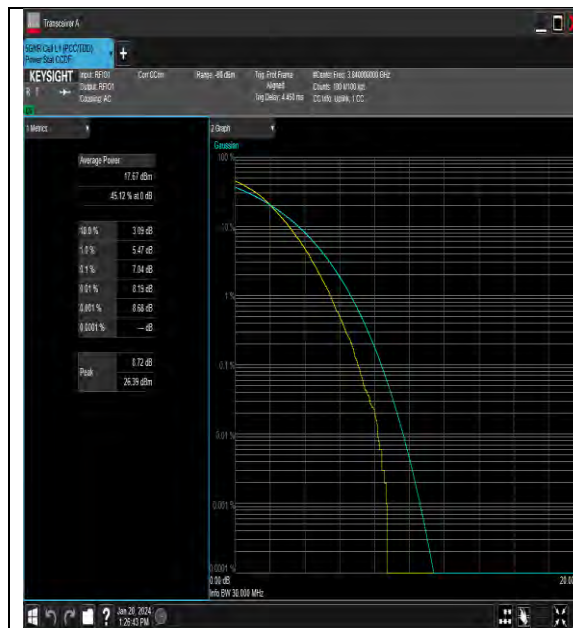
NTNV_N77-3700-3980_PC3_30_20_H_TID5_#1

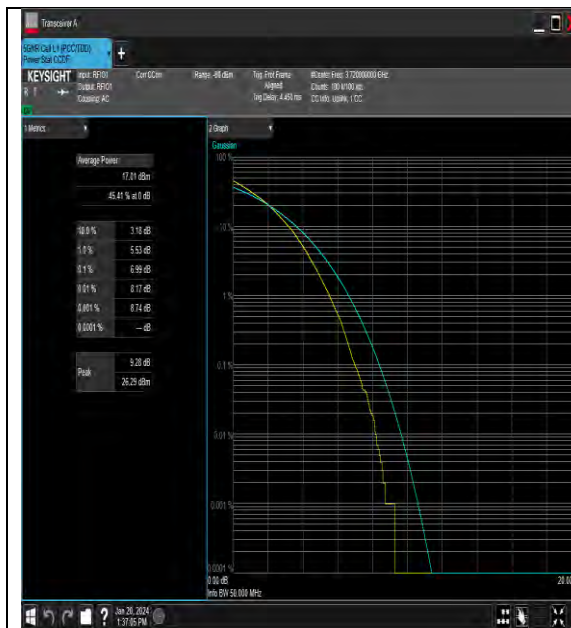


NTNV_N77-3700-3980_PC3_30_25_L_TID2_#1

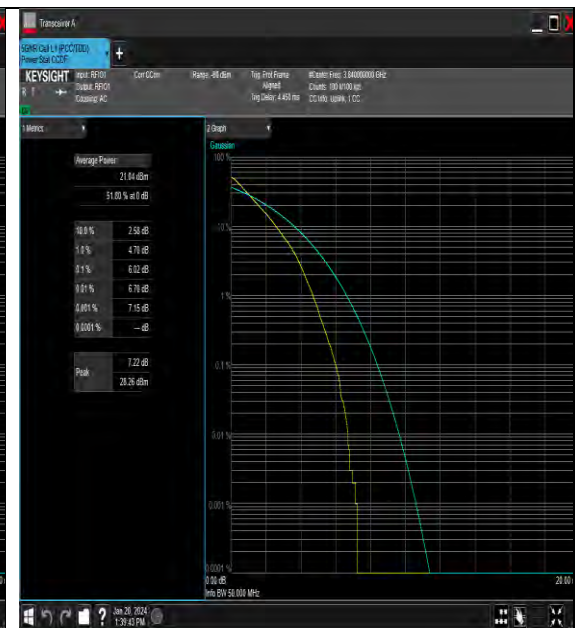




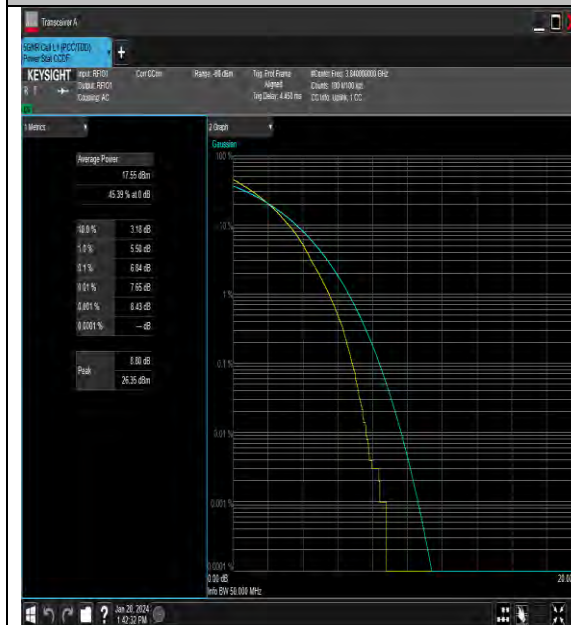




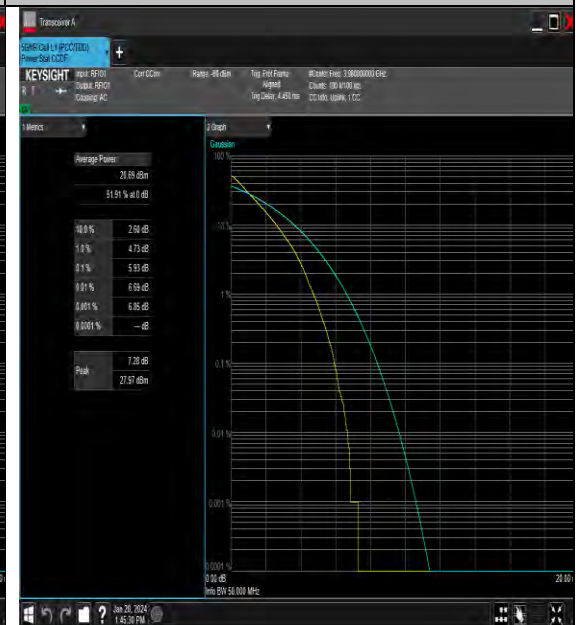
NTNV_N77-3700-3980_PC3_30_40_L_TID5_#1



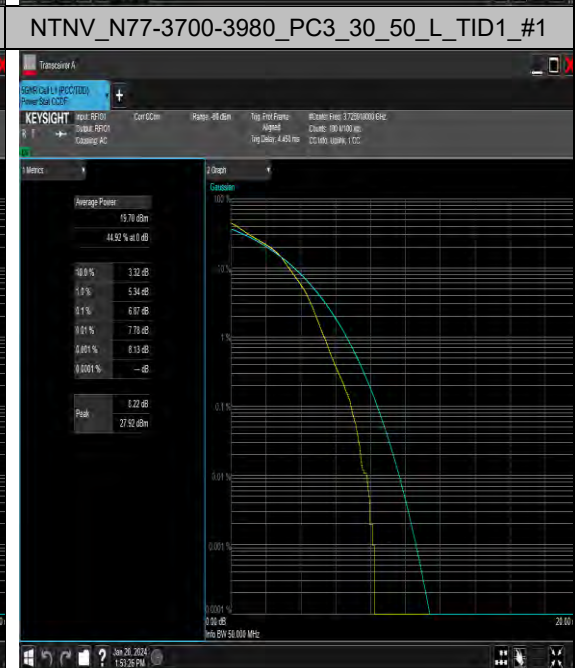
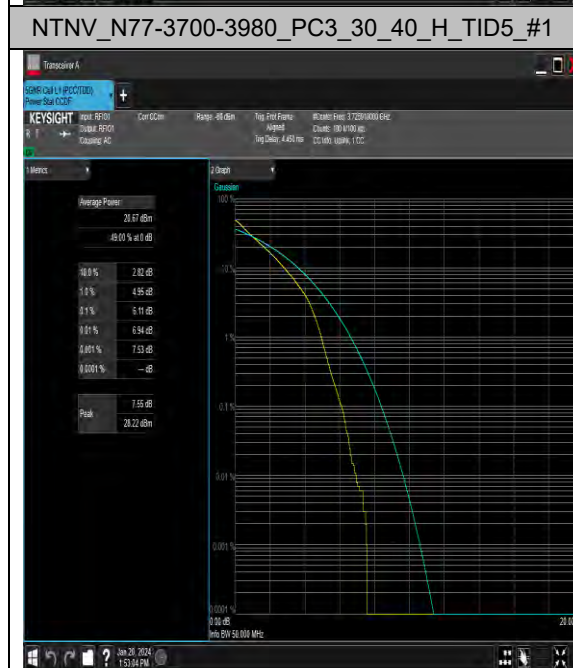
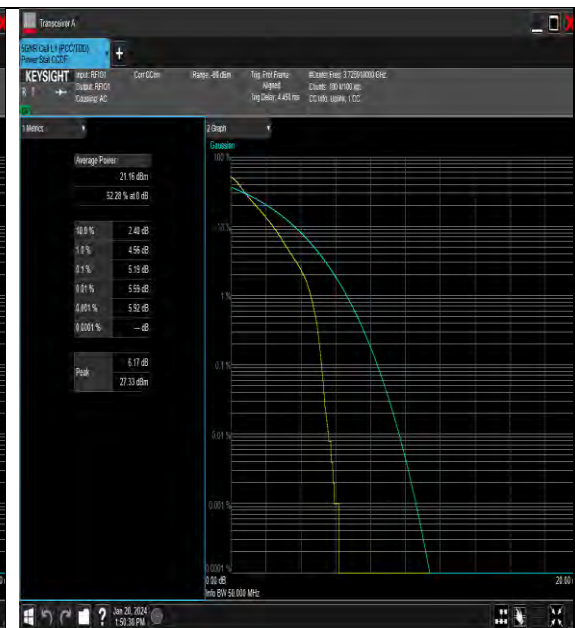
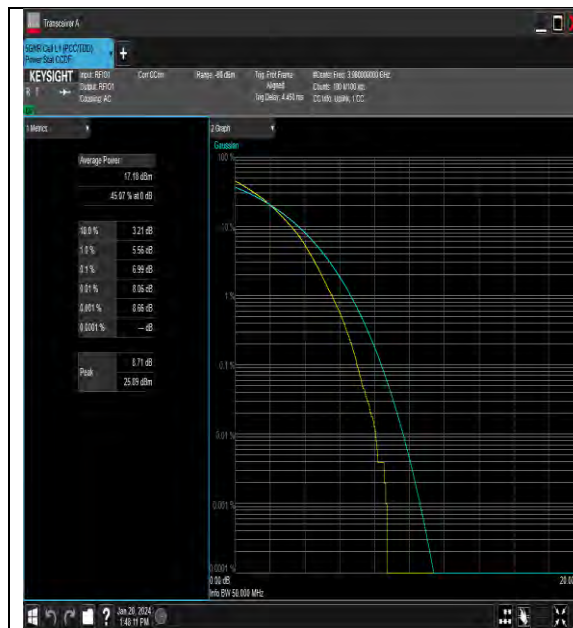
NTNV_N77-3700-3980_PC3_30_40_M_TID2_#1

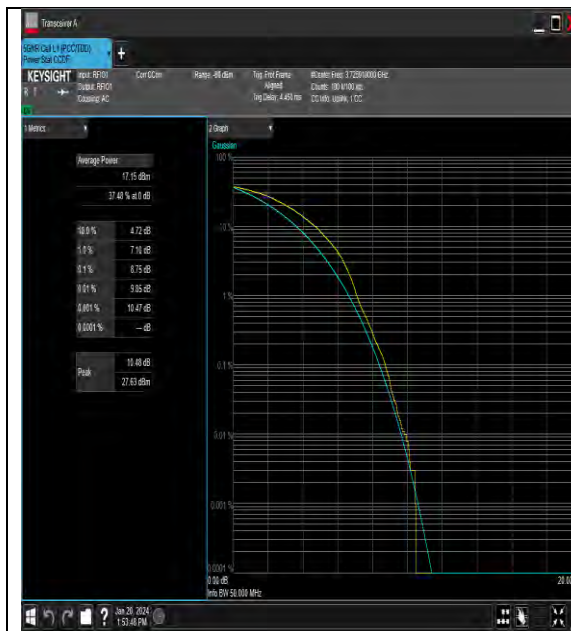


NTNV_N77-3700-3980_PC3_30_40_M_TID5_#1

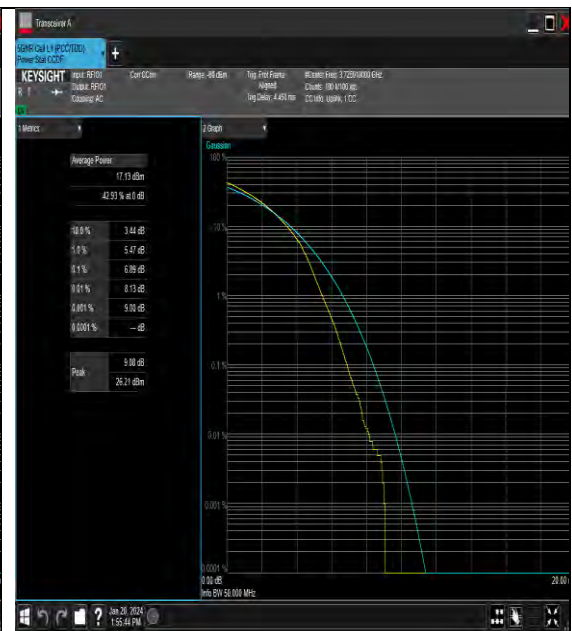


NTNV_N77-3700-3980_PC3_30_40_H_TID2_#1

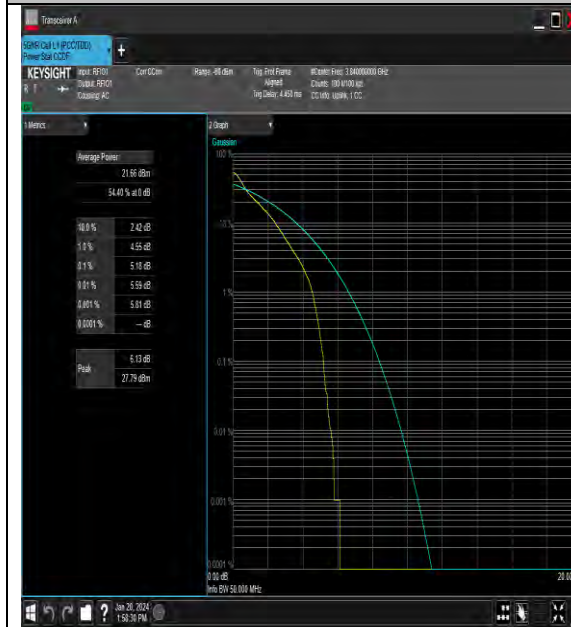




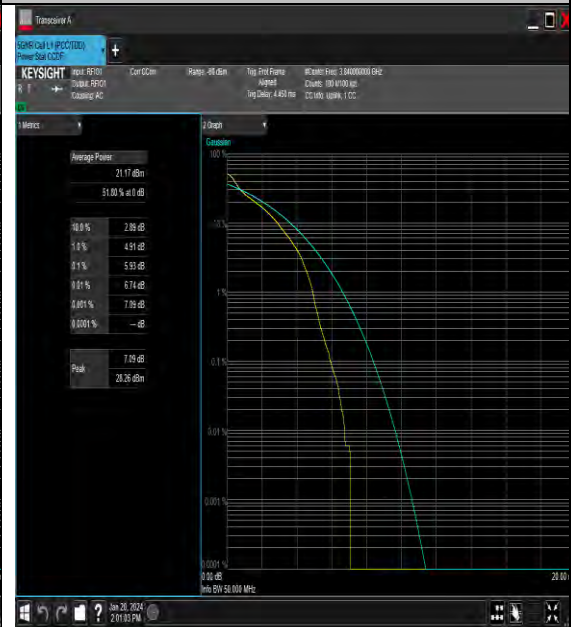
NTNV_N77-3700-3980_PC3_30_50_L_TID4_#1



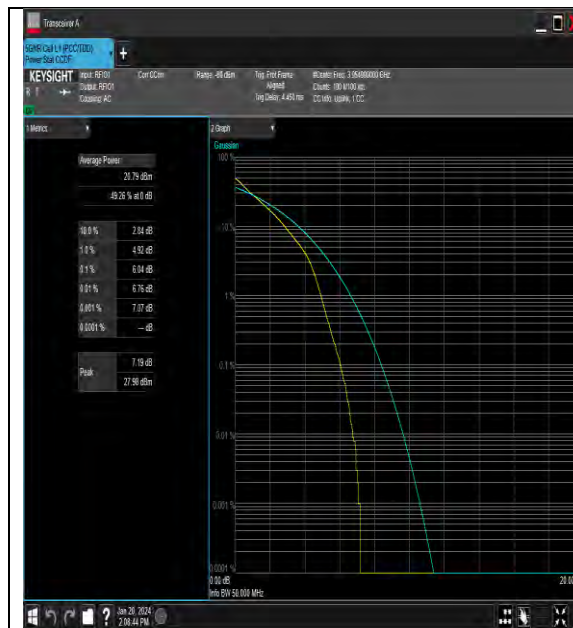
NTNV_N77-3700-3980_PC3_30_50_L_TID5_#1



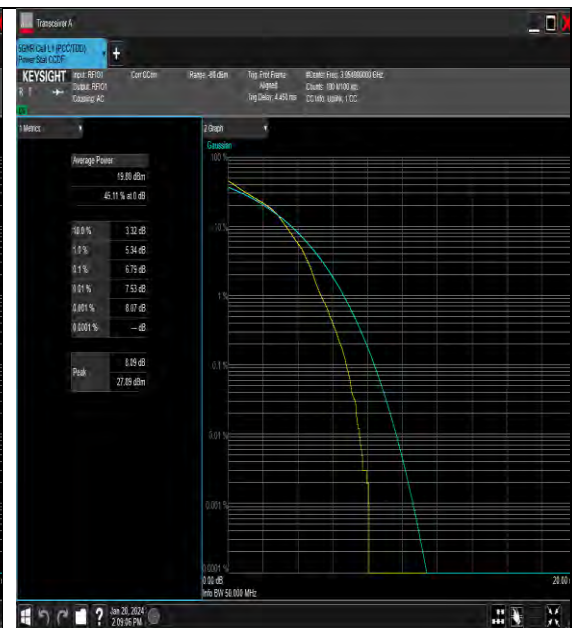
NTNV_N77-3700-3980_PC3_30_50_M_TID1_#1



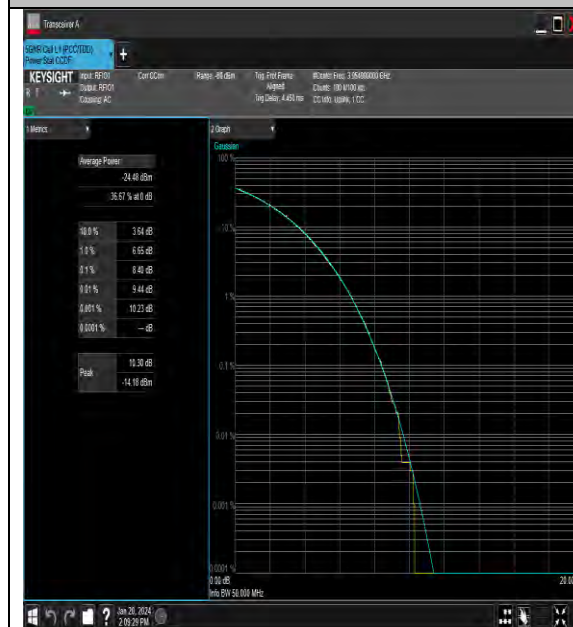
NTNV_N77-3700-3980_PC3_30_50_M_TID2_#1



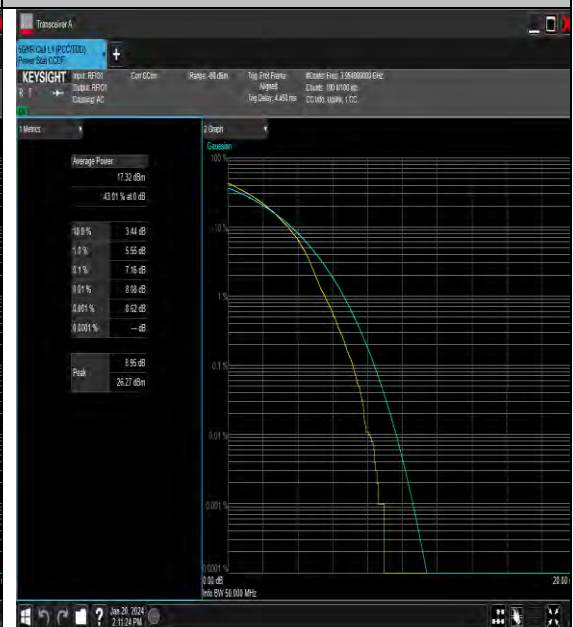
NTNV_N77-3700-3980_PC3_30_50_H_TID2_#1



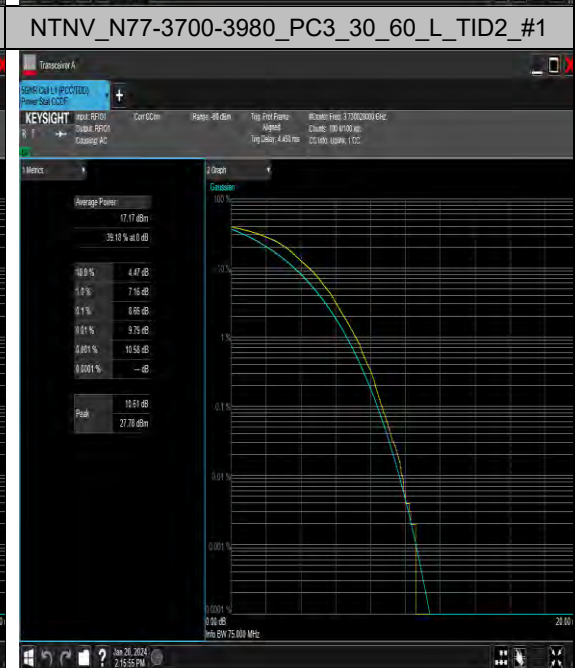
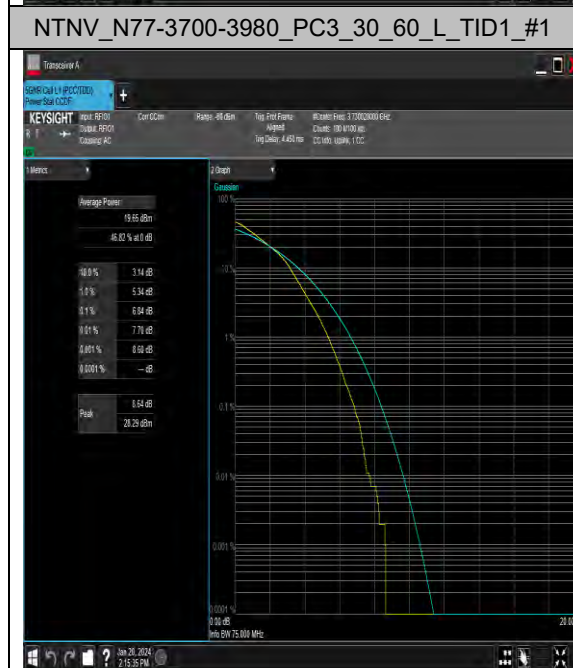
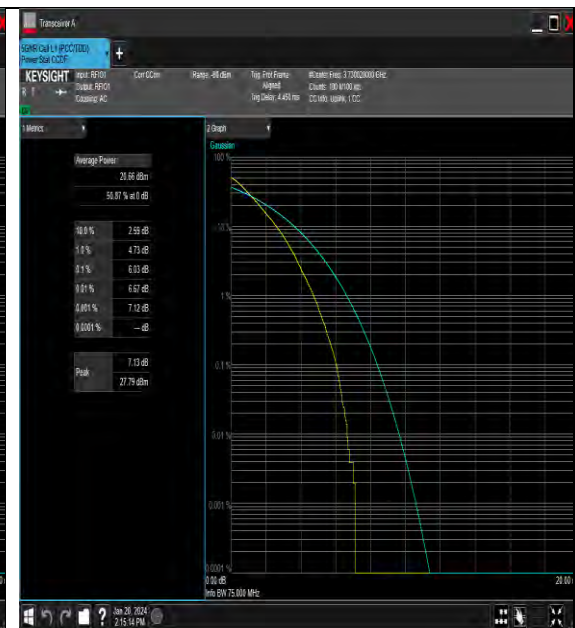
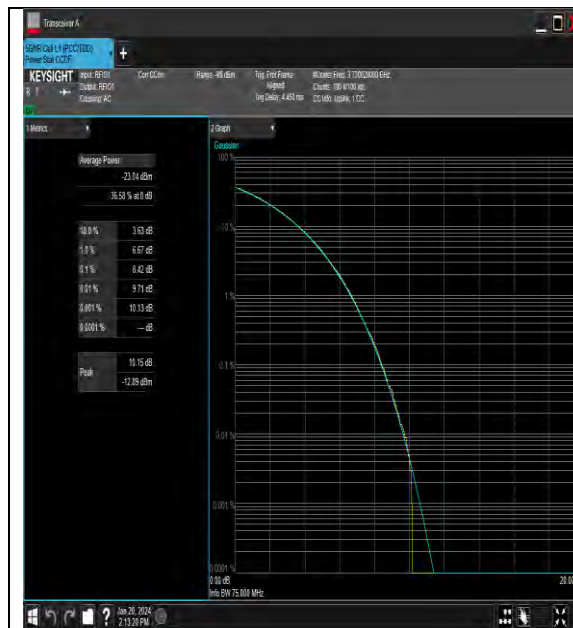
NTNV_N77-3700-3980_PC3_30_50_H_TID3_#1

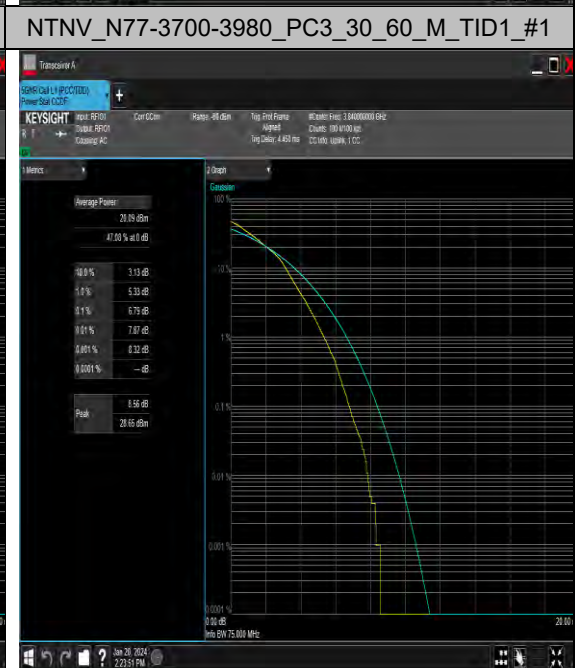
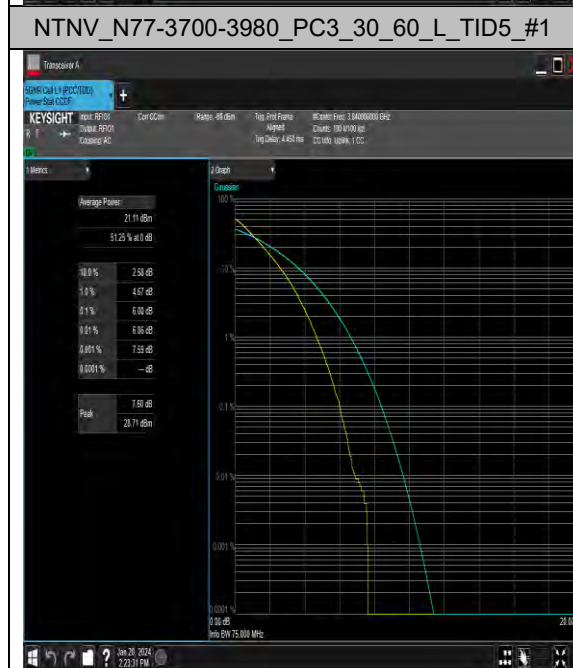
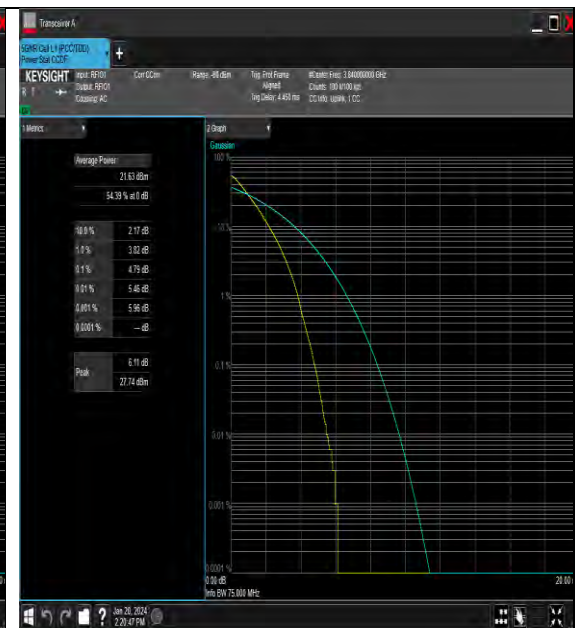
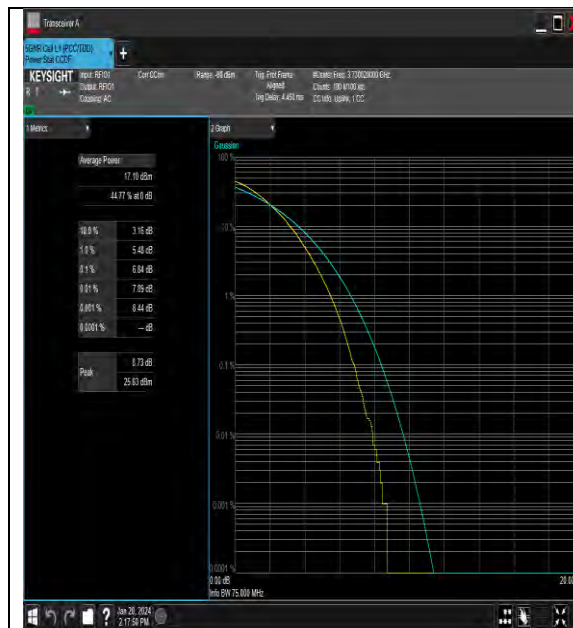


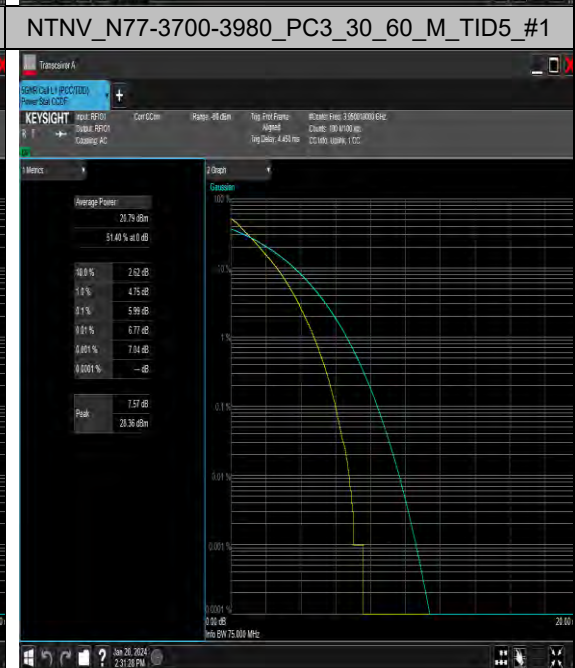
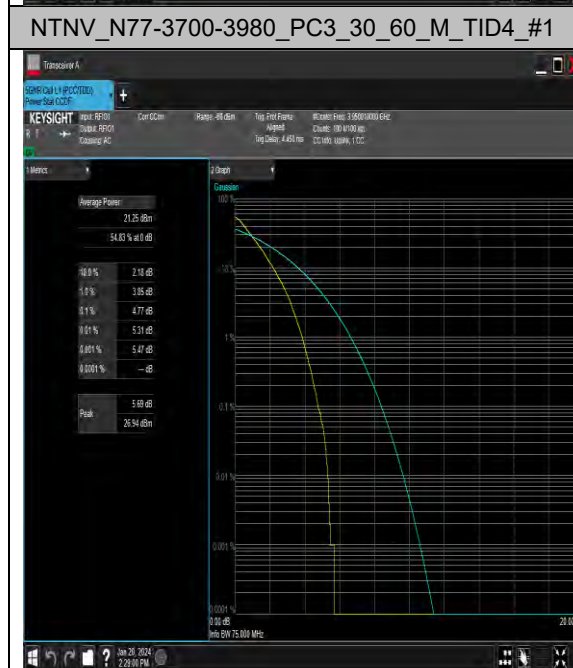
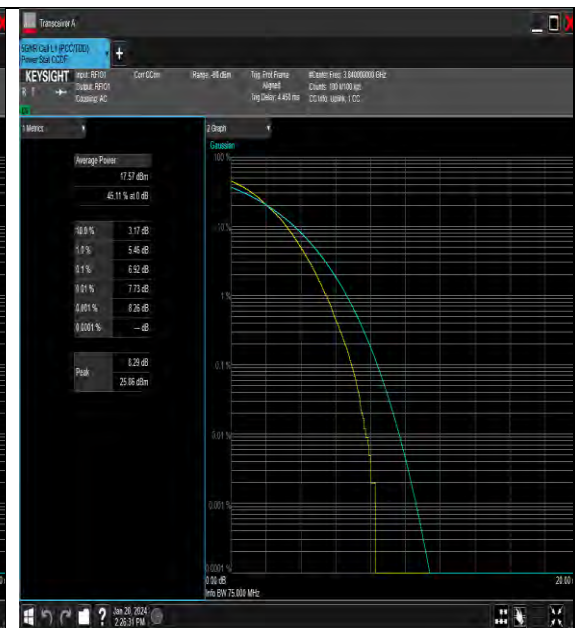
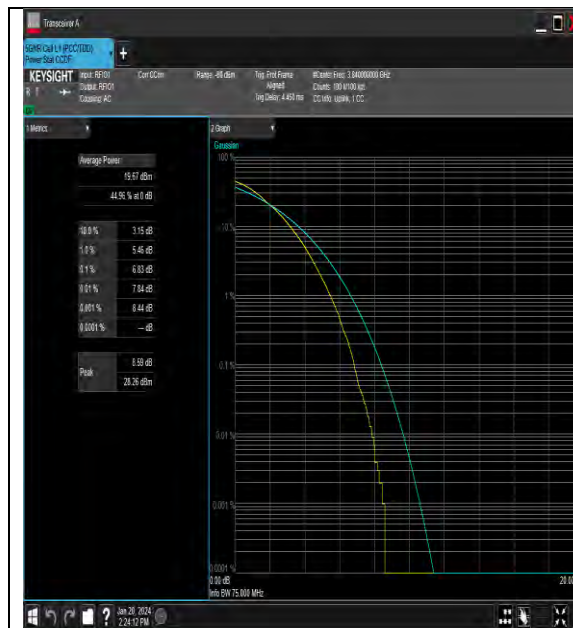
NTNV_N77-3700-3980_PC3_30_50_H_TID4_#1

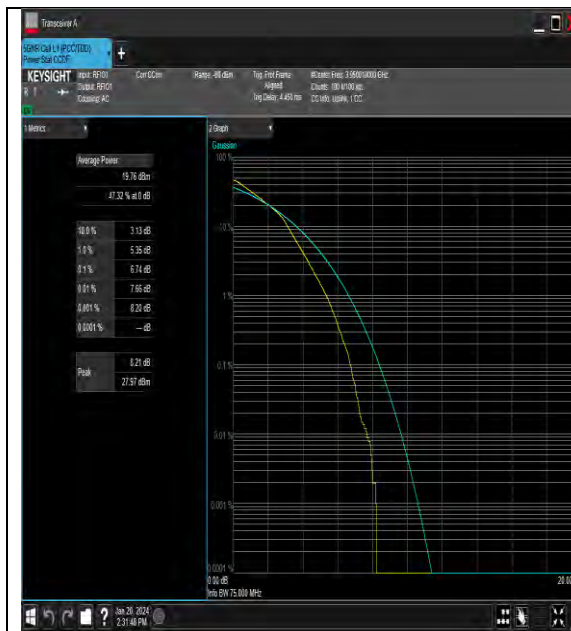


NTNV_N77-3700-3980_PC3_30_50_H_TID5_#1

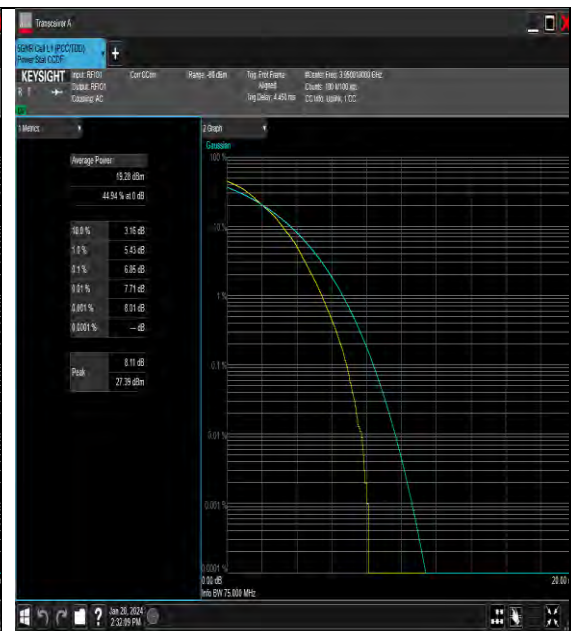




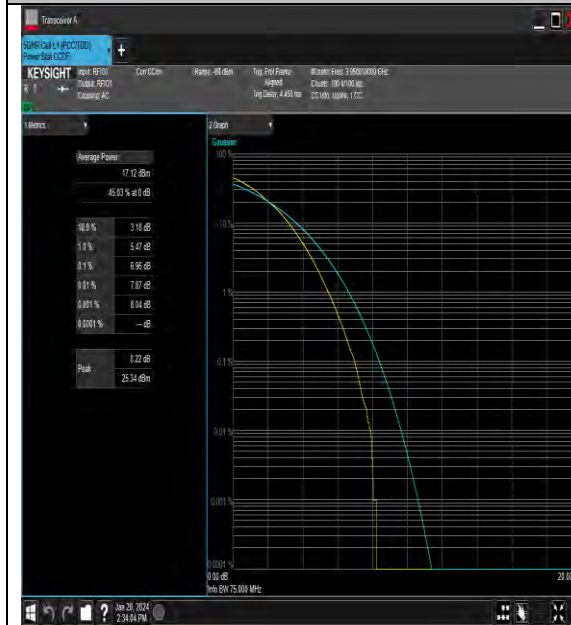




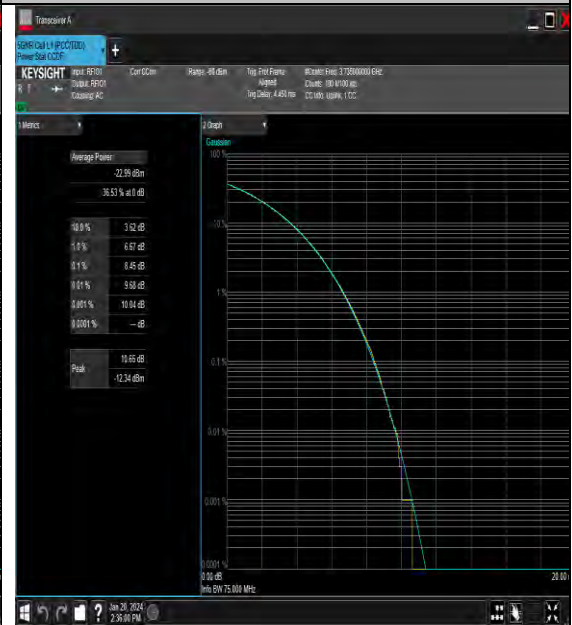
NTNV_N77-3700-3980_PC3_30_60_H_TID3_#1



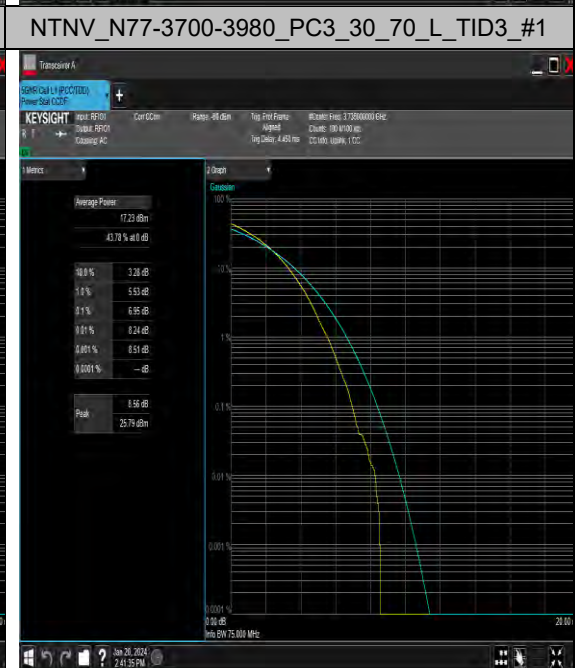
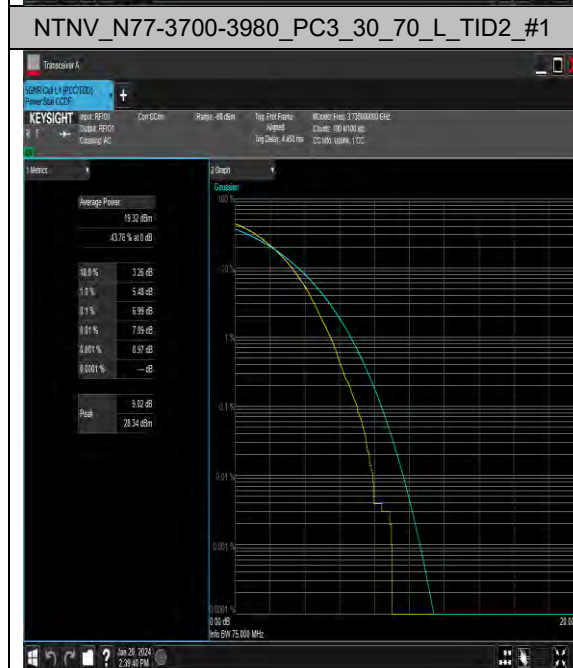
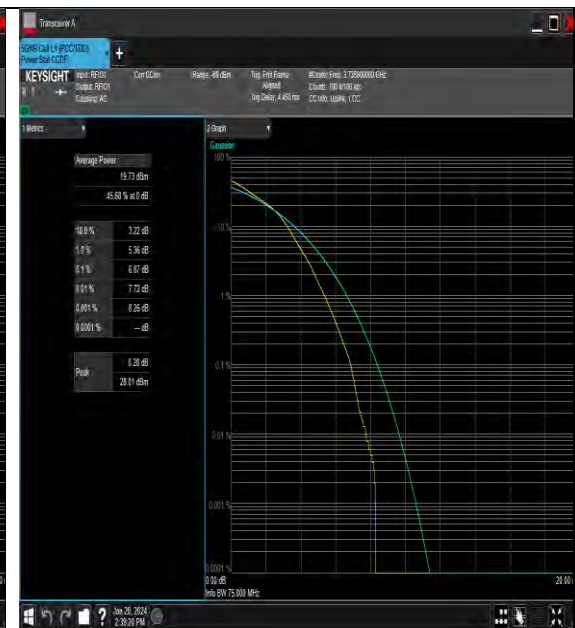
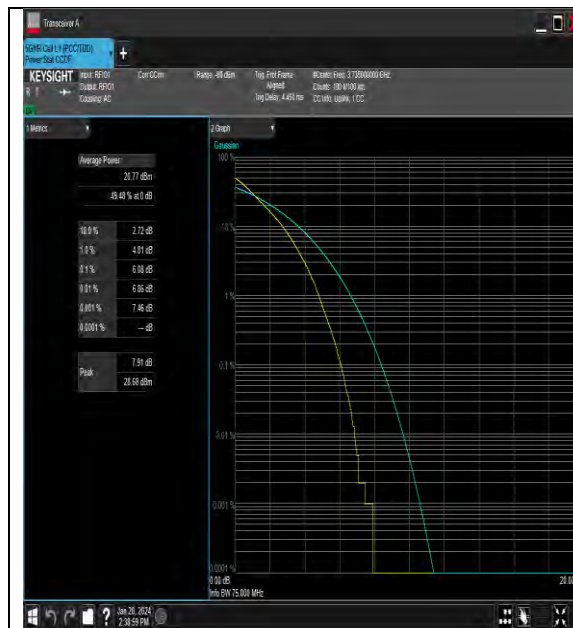
NTNV_N77-3700-3980_PC3_30_60_H_TID4_#1

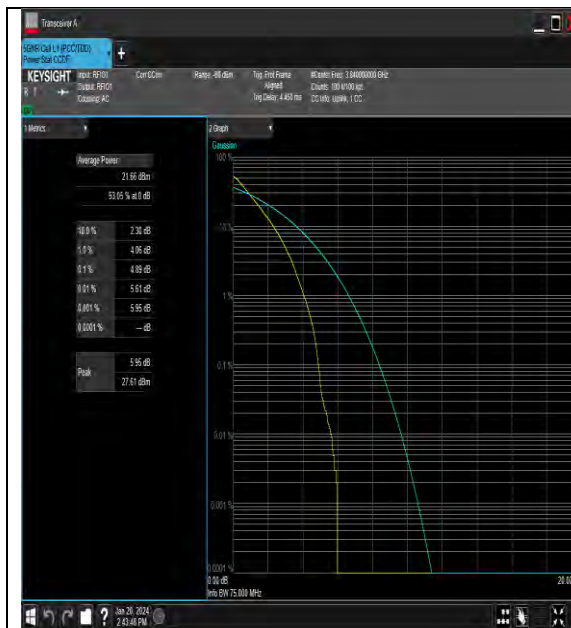


NTNV_N77-3700-3980_PC3_30_60_H_TID5_#1

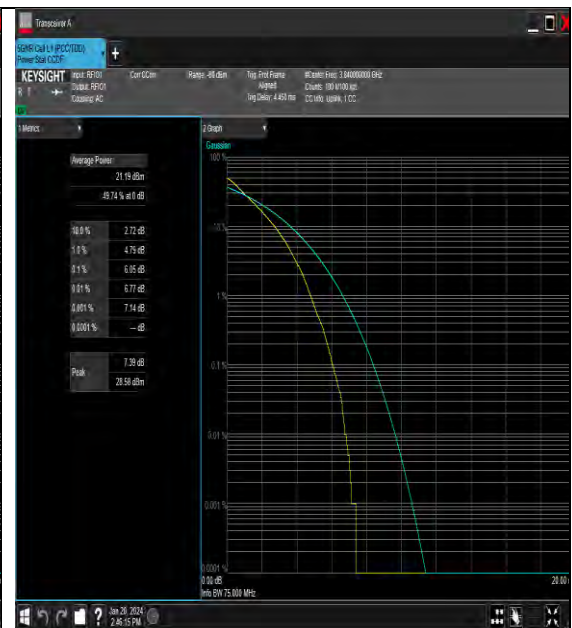


NTNV_N77-3700-3980_PC3_30_70_L_TID1_#1

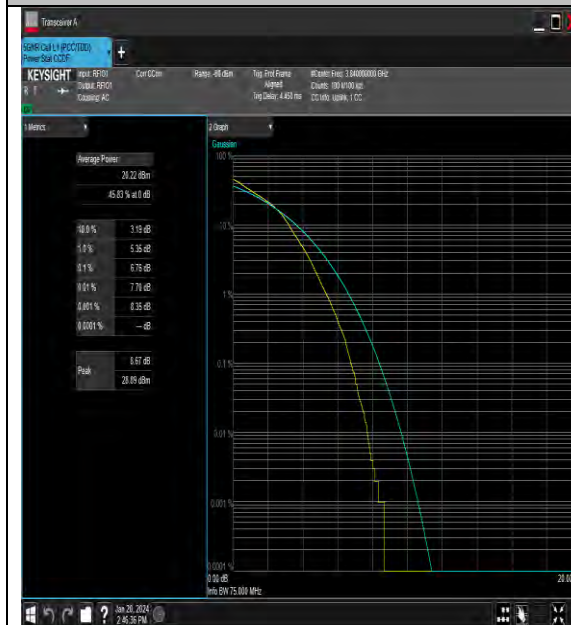




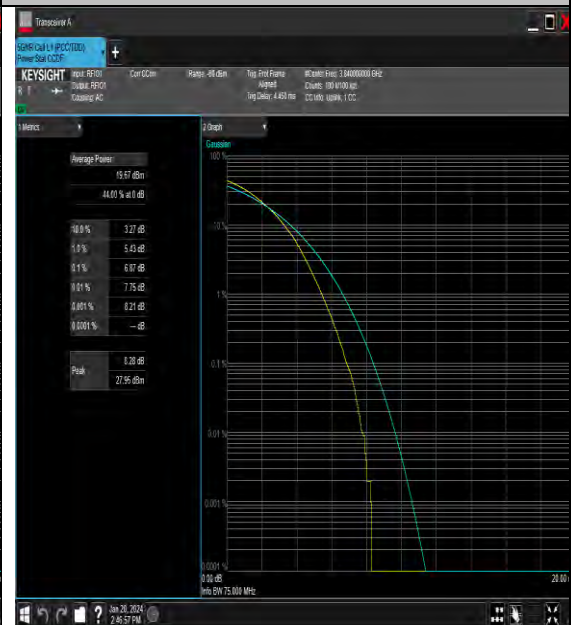
NTNV_N77-3700-3980_PC3_30_70_M_TID1_#1



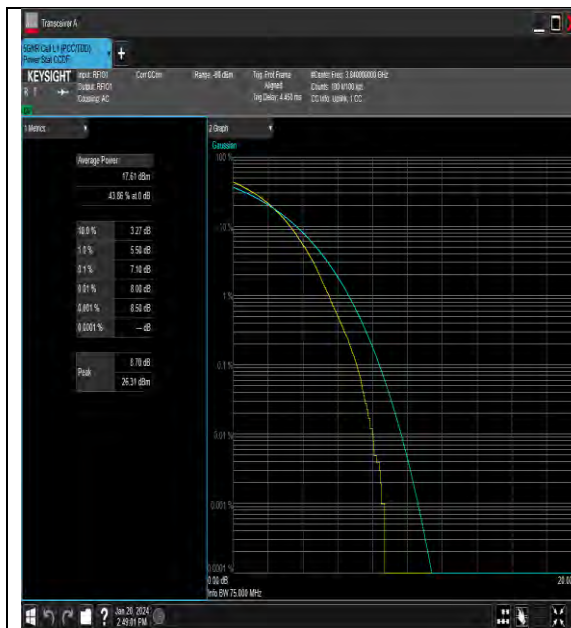
NTNV_N77-3700-3980_PC3_30_70_M_TID2_#1



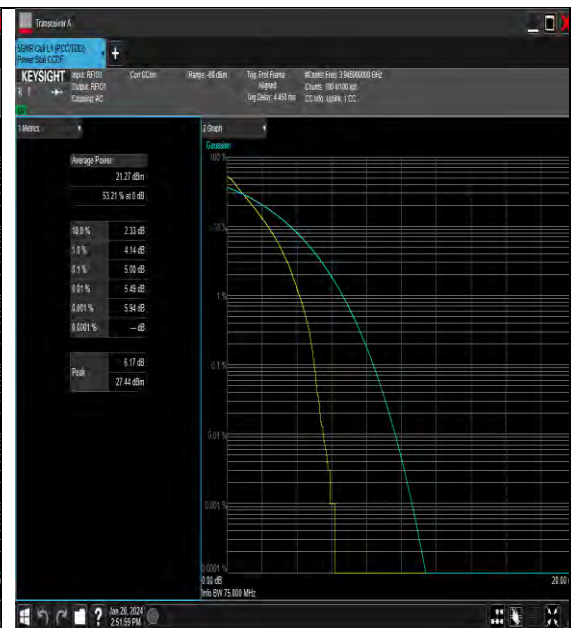
NTNV_N77-3700-3980_PC3_30_70_M_TID3_#1



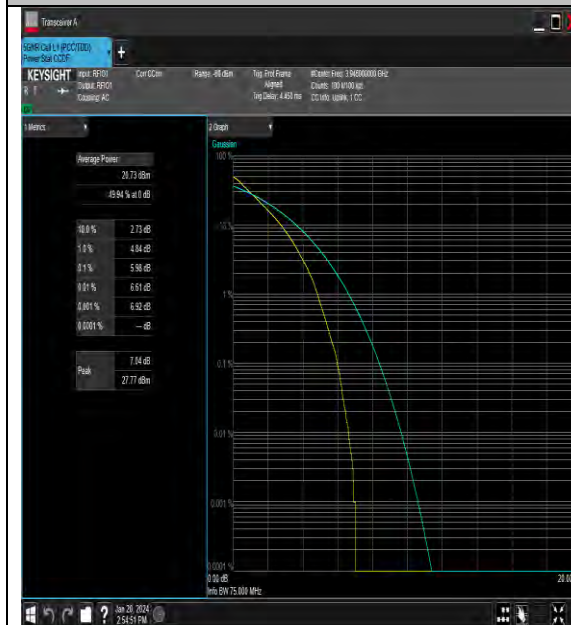
NTNV_N77-3700-3980_PC3_30_70_M_TID4_#1



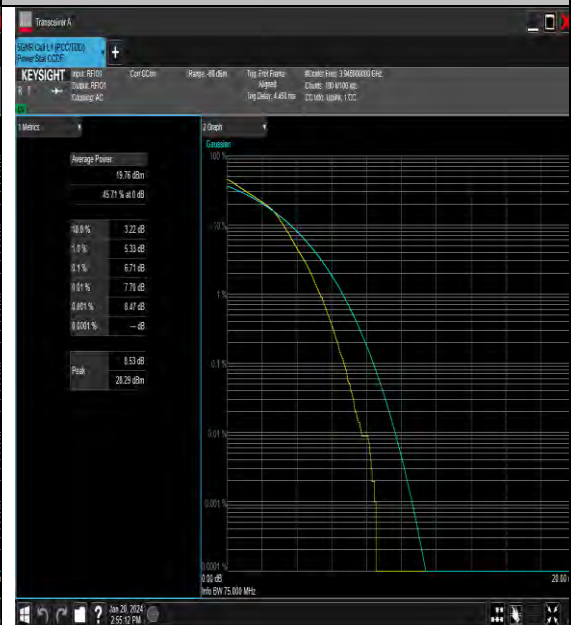
NTNV_N77-3700-3980_PC3_30_70_M_TID5_#1



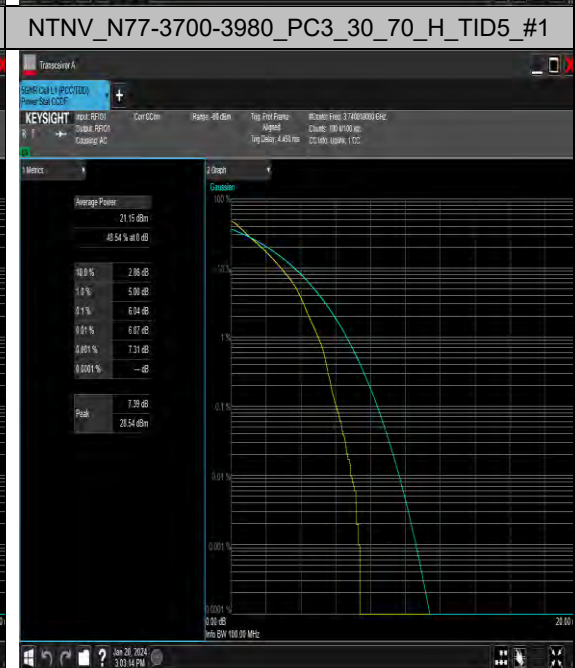
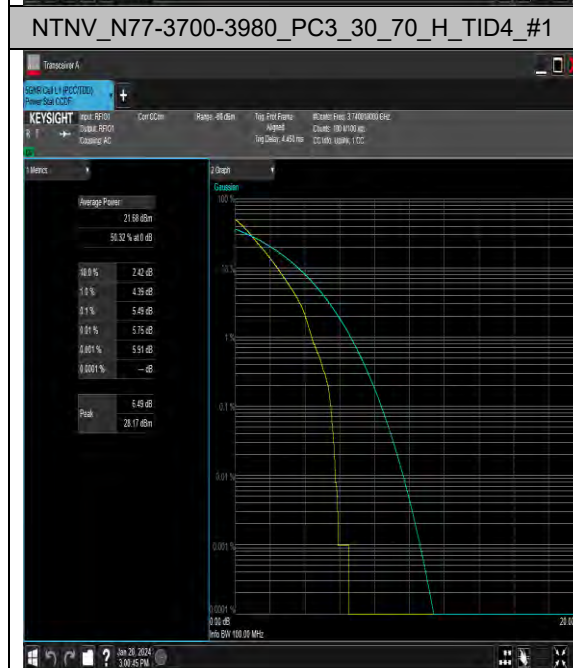
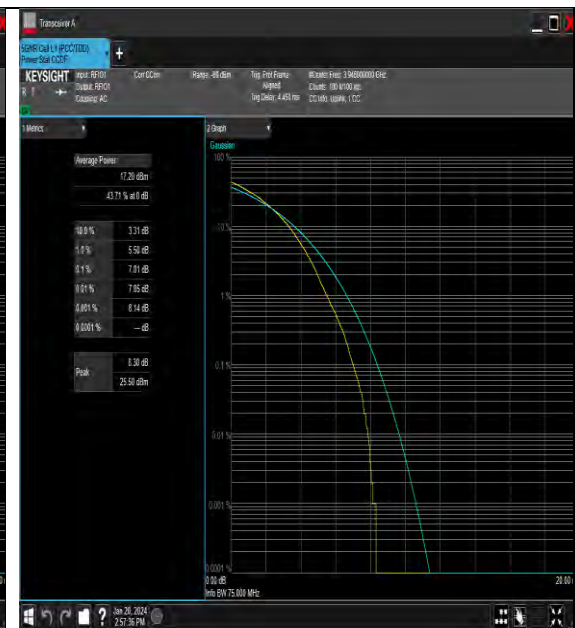
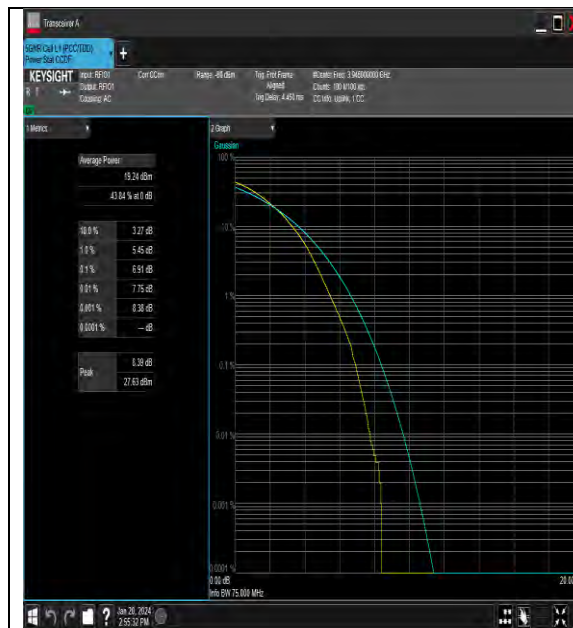
NTNV_N77-3700-3980_PC3_30_70_H_TID1_#1

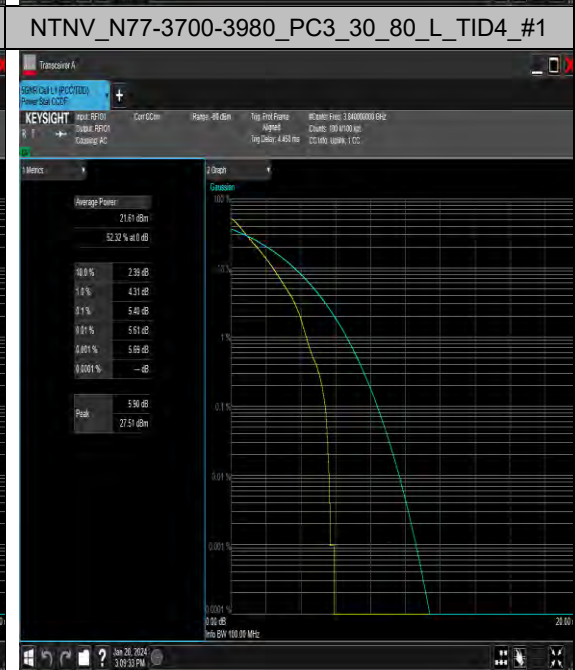
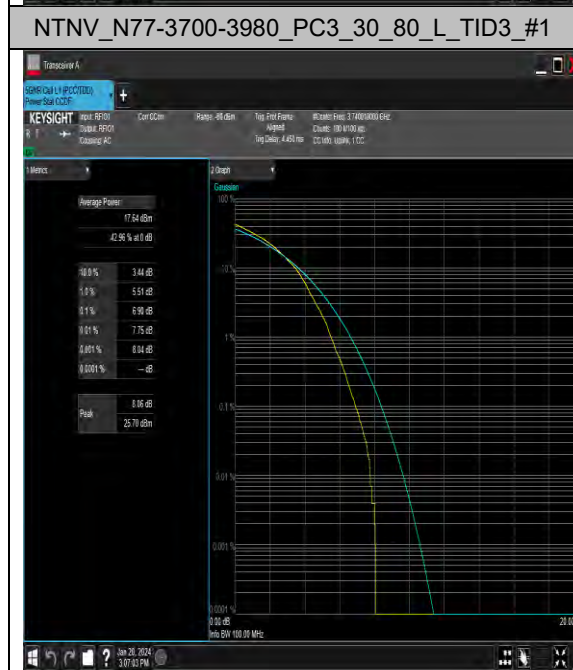
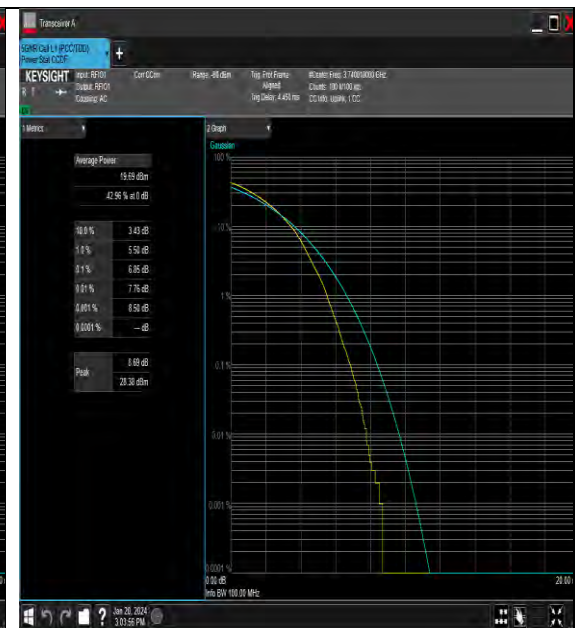
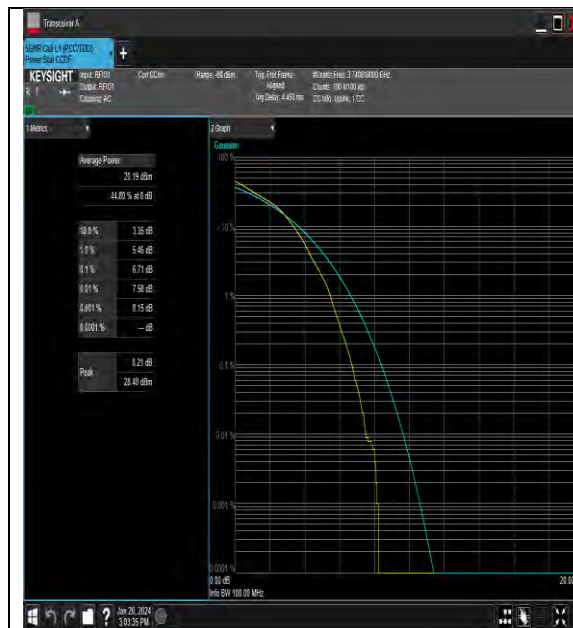


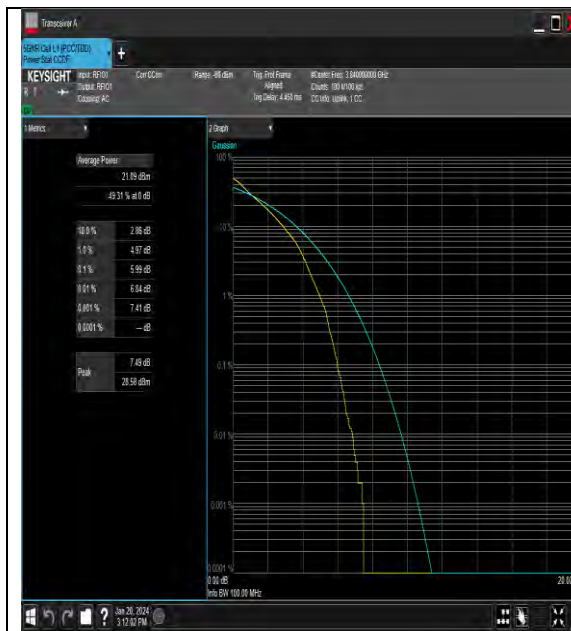
NTNV_N77-3700-3980_PC3_30_70_H_TID2_#1



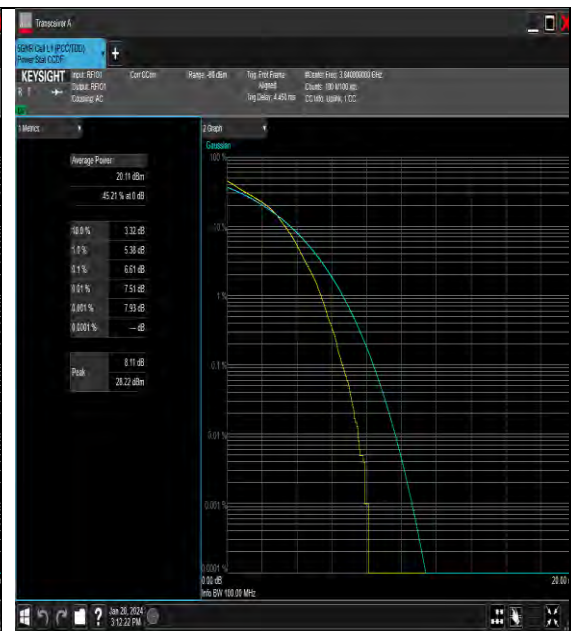
NTNV_N77-3700-3980_PC3_30_70_H_TID3_#1



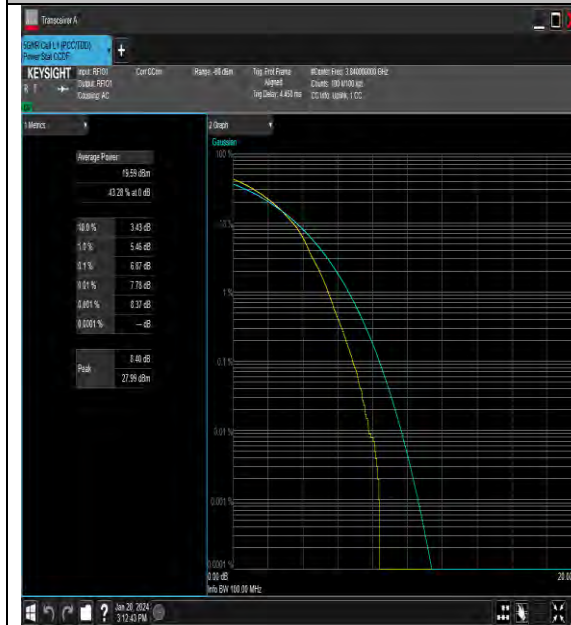




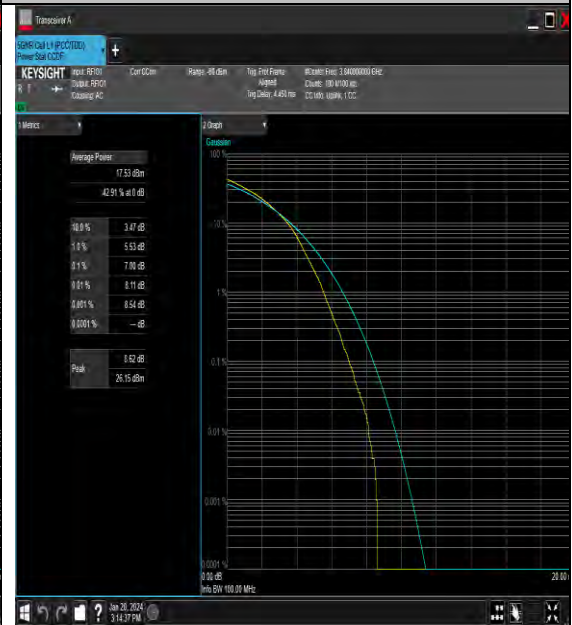
NTNV_N77-3700-3980_PC3_30_80_M_TID2_#1



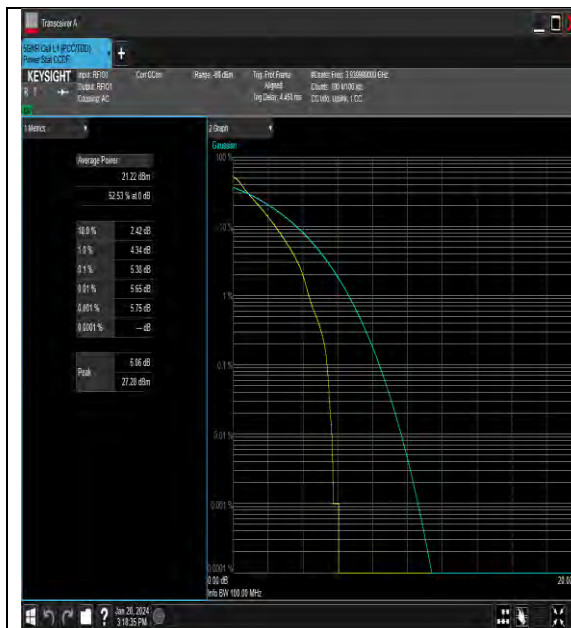
NTNV_N77-3700-3980_PC3_30_80_M_TID3_#1



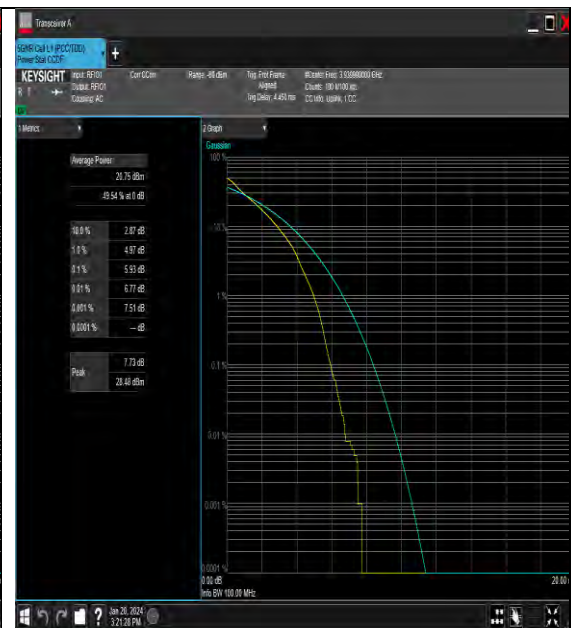
NTNV_N77-3700-3980_PC3_30_80_M_TID4_#1



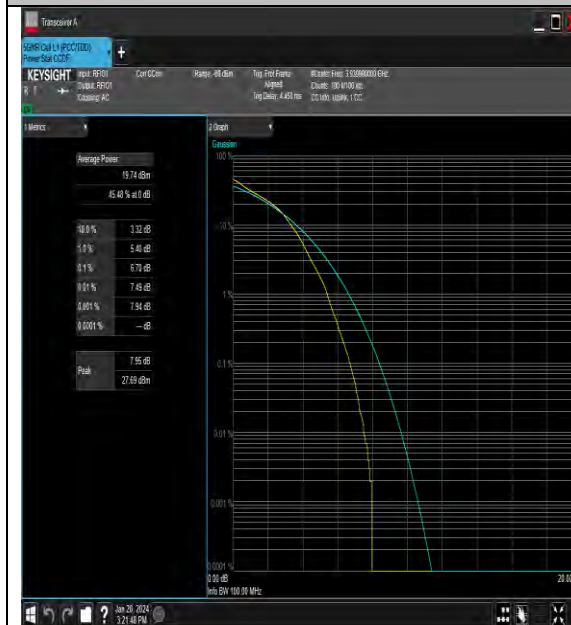
NTNV_N77-3700-3980_PC3_30_80_M_TID5_#1



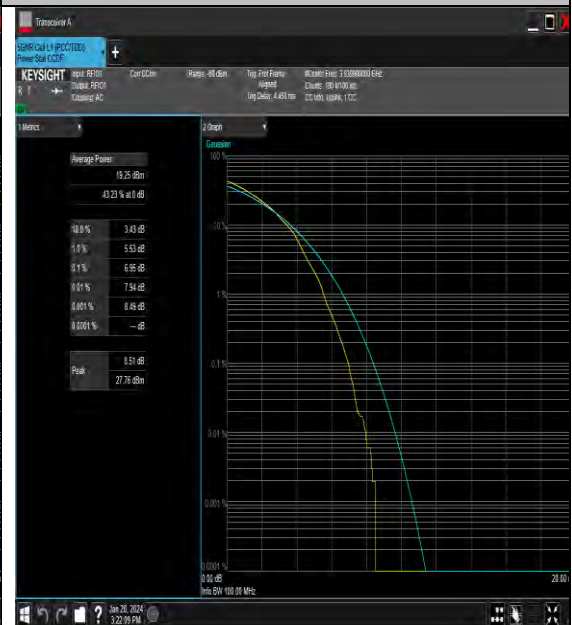
NTNV_N77-3700-3980_PC3_30_80_H_TID1_#1



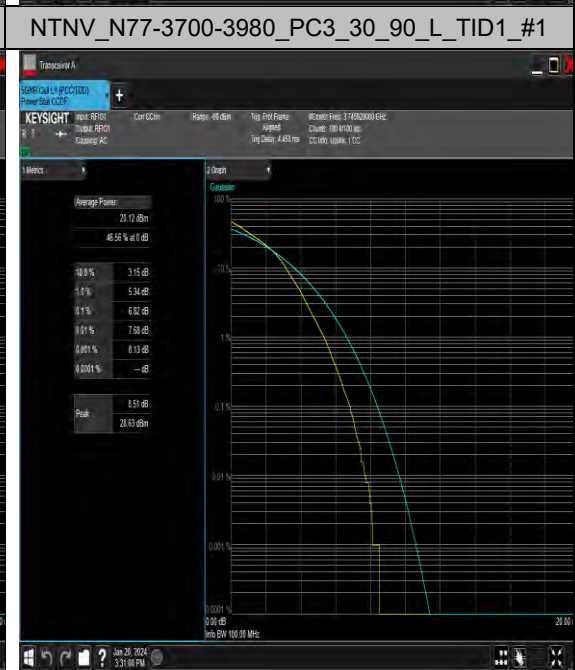
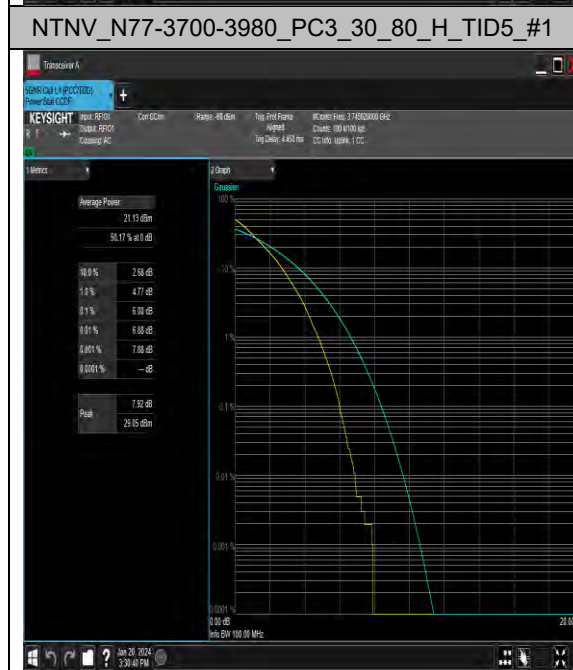
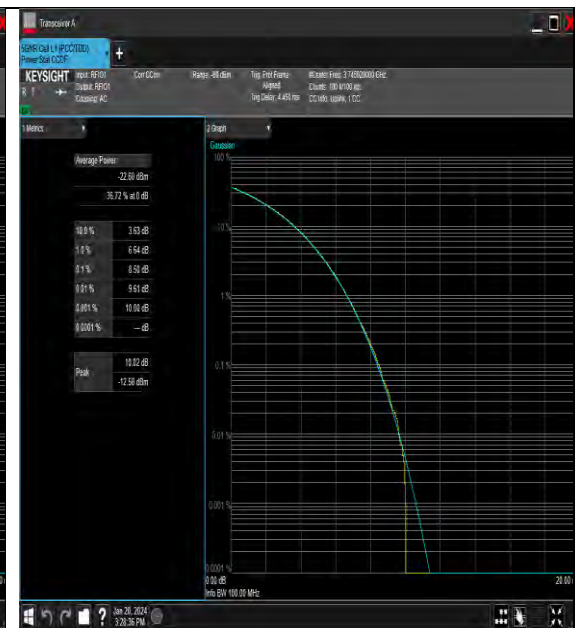
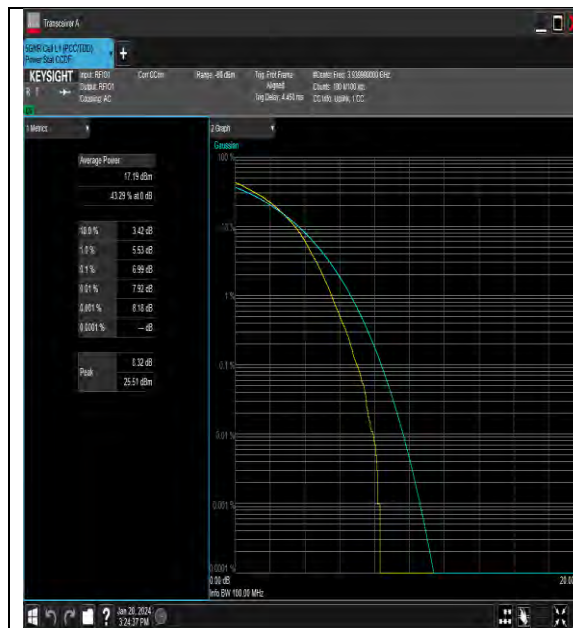
NTNV_N77-3700-3980_PC3_30_80_H_TID2_#1

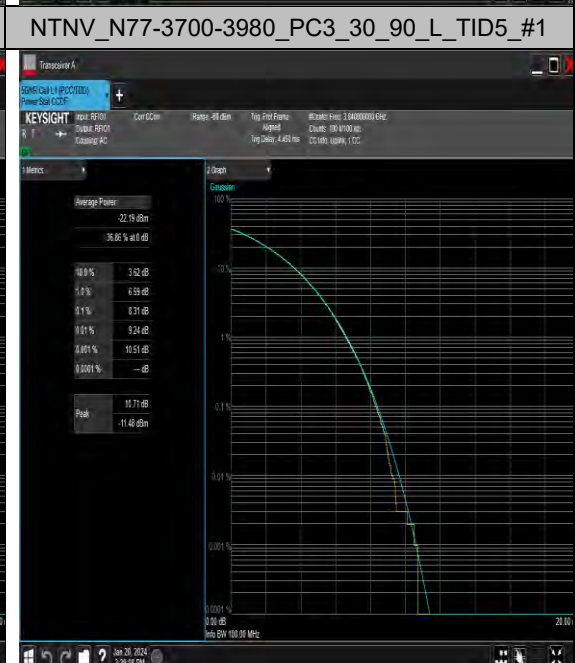
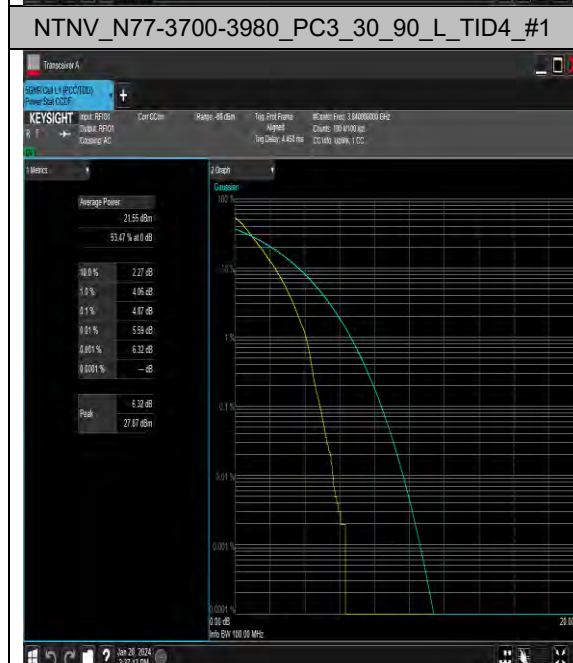
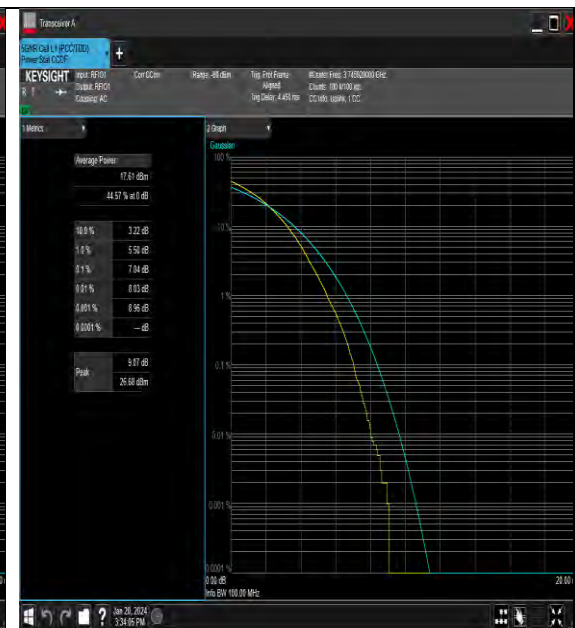
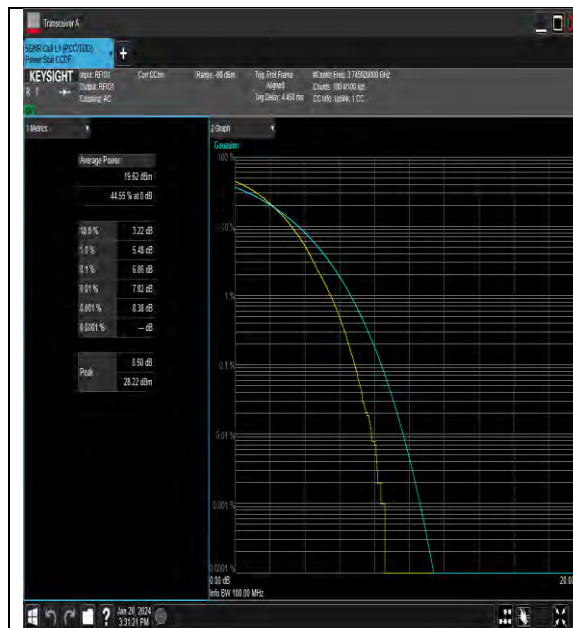


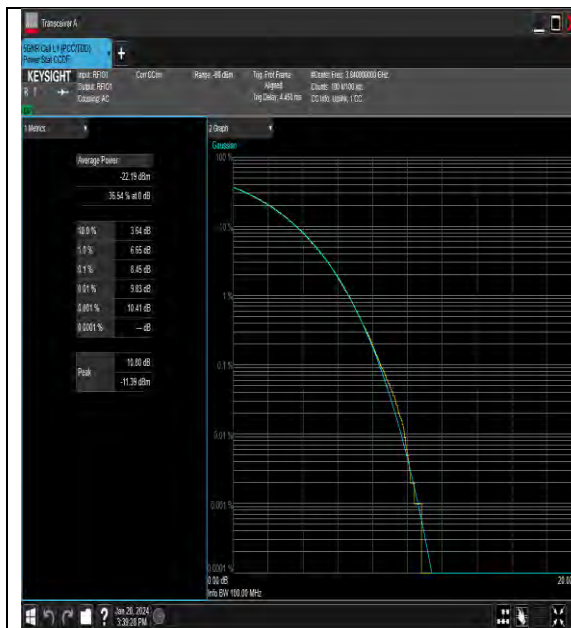
NTNV_N77-3700-3980_PC3_30_80_H_TID3_#1



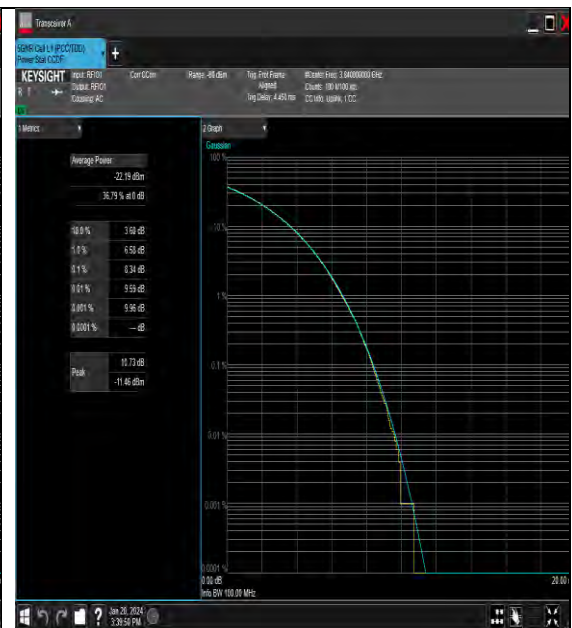
NTNV_N77-3700-3980_PC3_30_80_H_TID4_#1



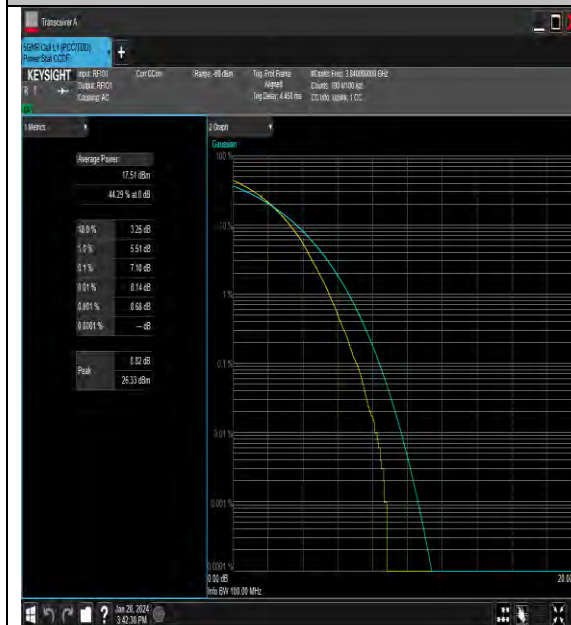




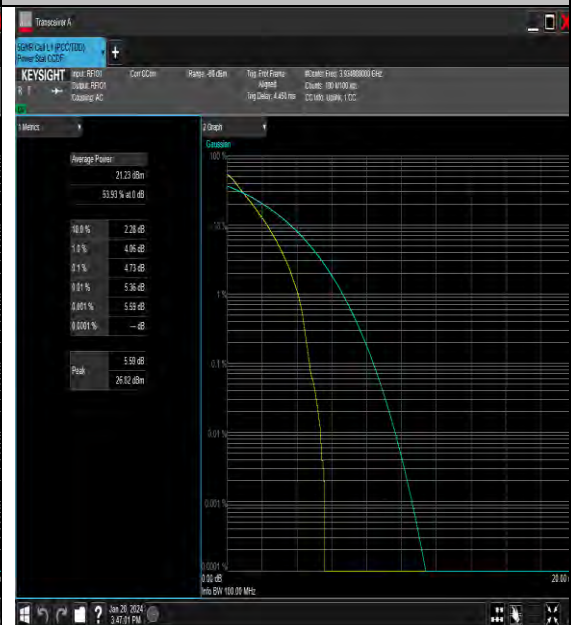
NTNV_N77-3700-3980_PC3_30_90_M_TID3_#1



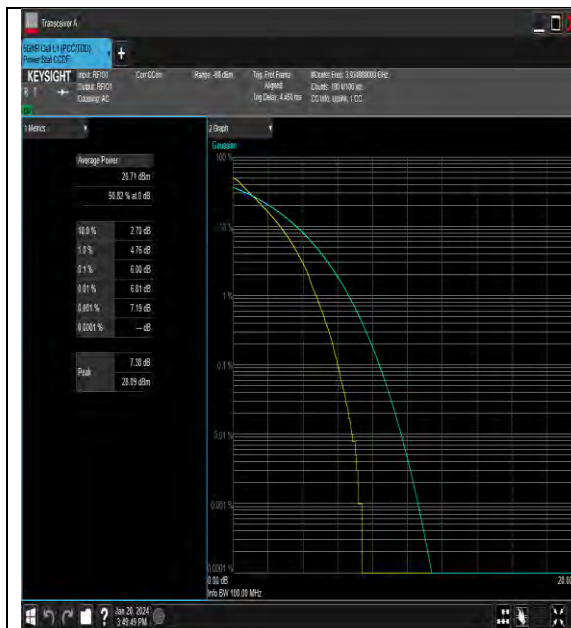
NTNV_N77-3700-3980_PC3_30_90_M_TID4_#1



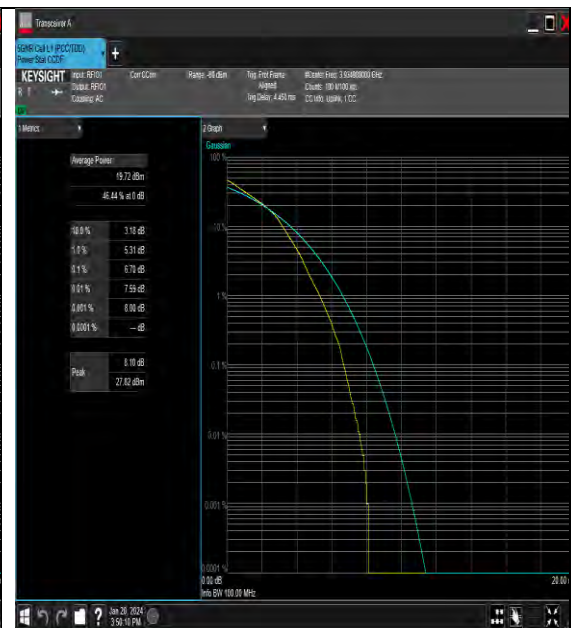
NTNV_N77-3700-3980_PC3_30_90_M_TID5_#1



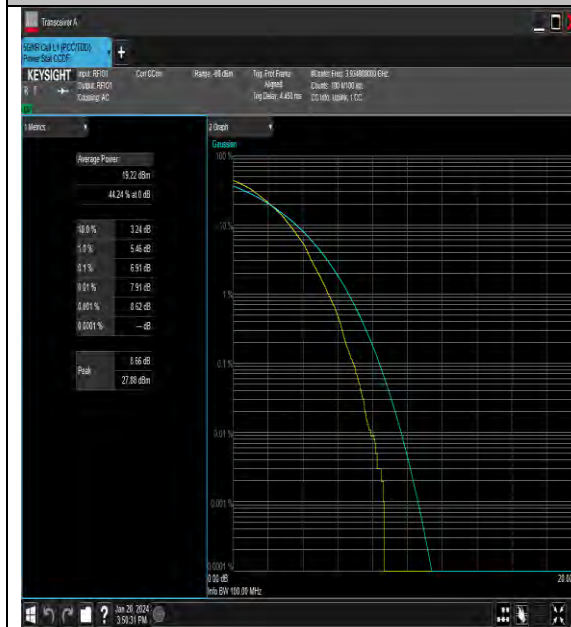
NTNV_N77-3700-3980_PC3_30_90_H_TID1_#1



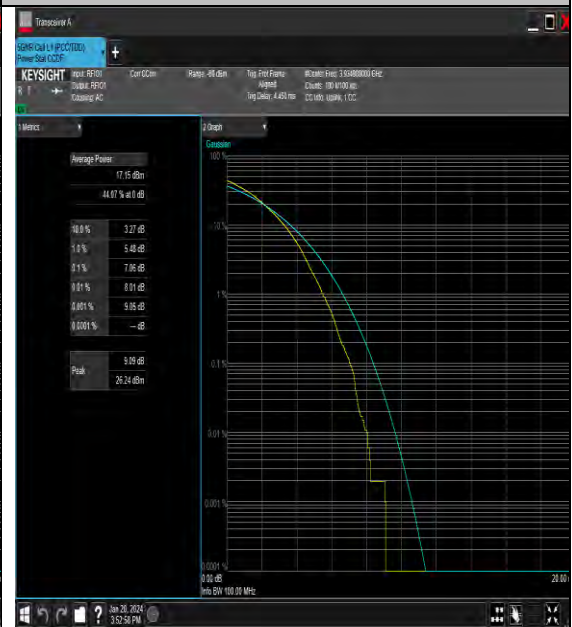
NTNV_N77-3700-3980_PC3_30_90_H_TID2_#1



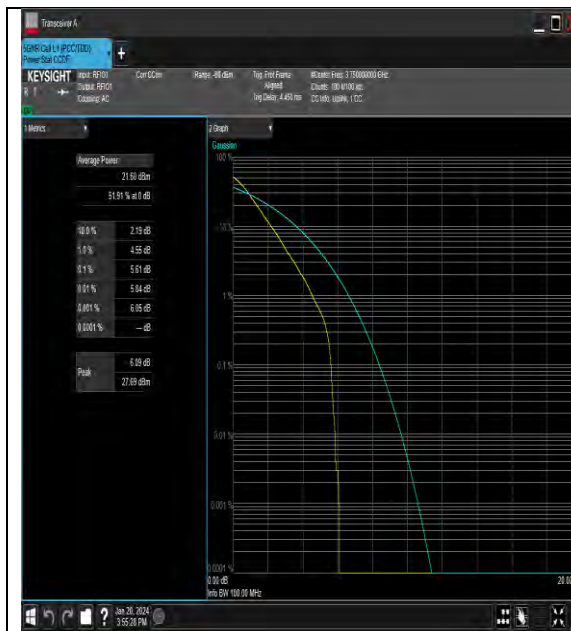
NTNV_N77-3700-3980_PC3_30_90_H_TID3_#1



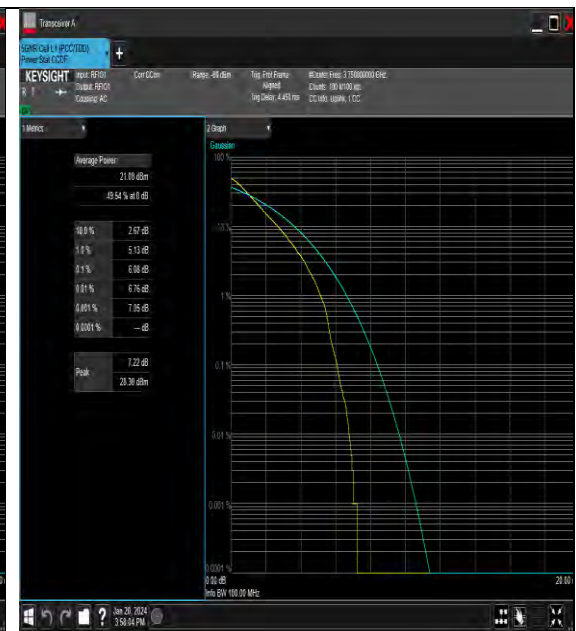
NTNV_N77-3700-3980_PC3_30_90_H_TID4_#1



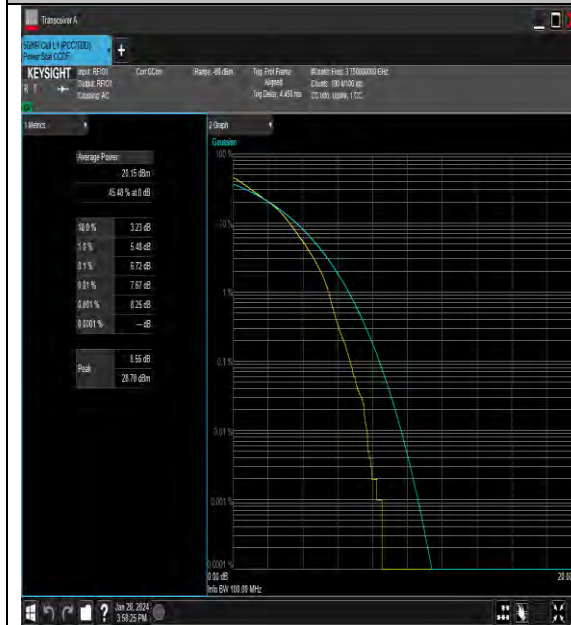
NTNV_N77-3700-3980_PC3_30_90_H_TID5_#1



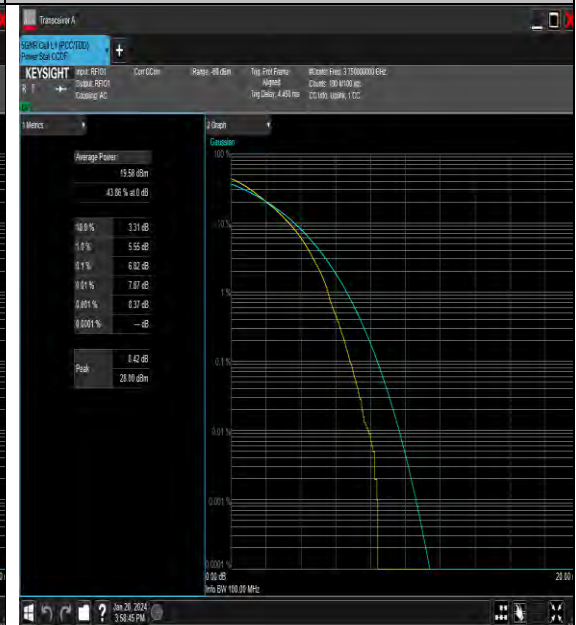
NTNV_N77-3700-3980_PC3_30_100_L_TID1_#1



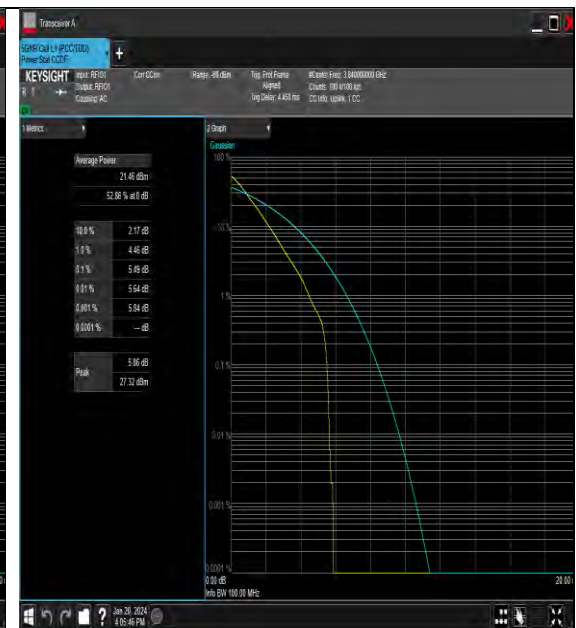
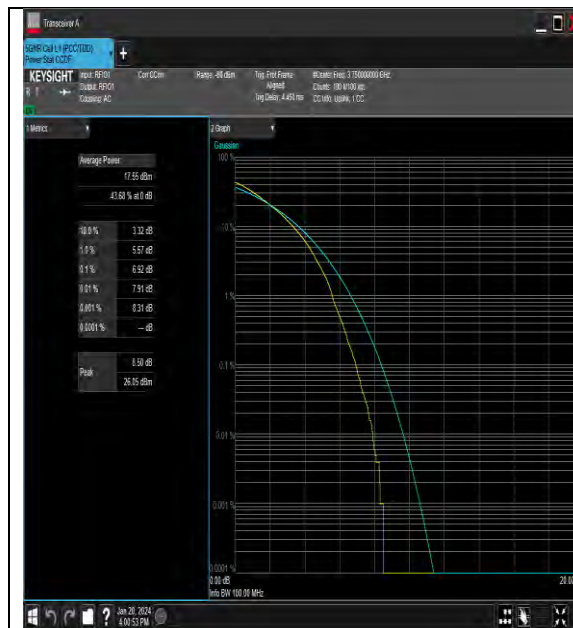
NTNV_N77-3700-3980_PC3_30_100_L_TID2_#1



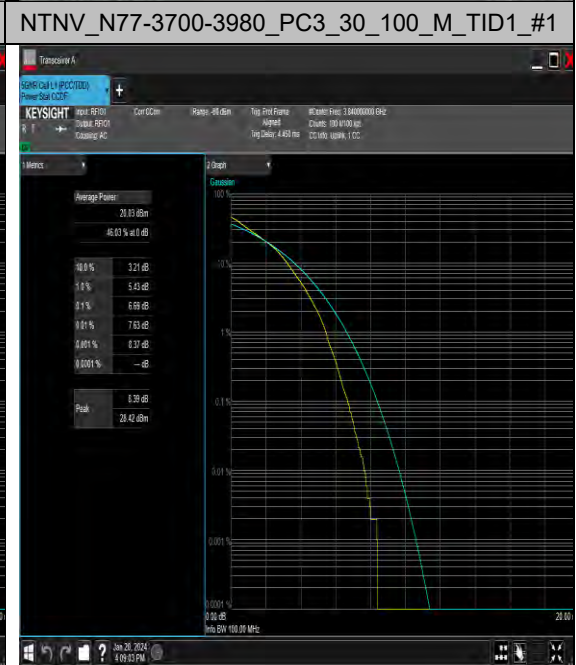
NTNV_N77-3700-3980_PC3_30_100_L_TID3_#1



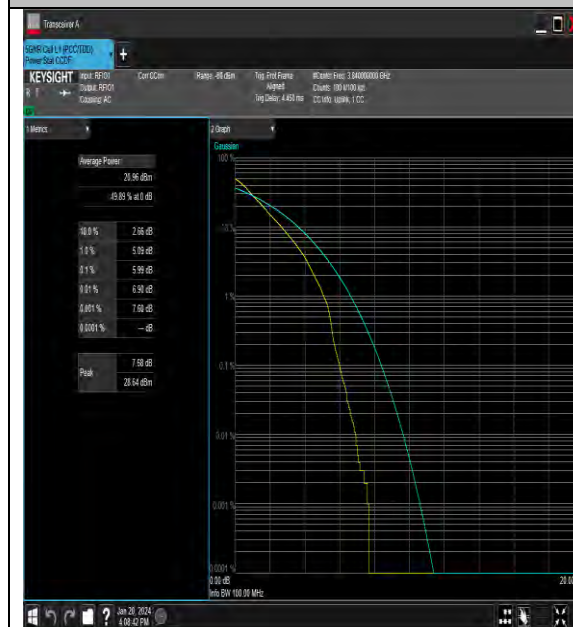
NTNV_N77-3700-3980_PC3_30_100_L_TID4_#1



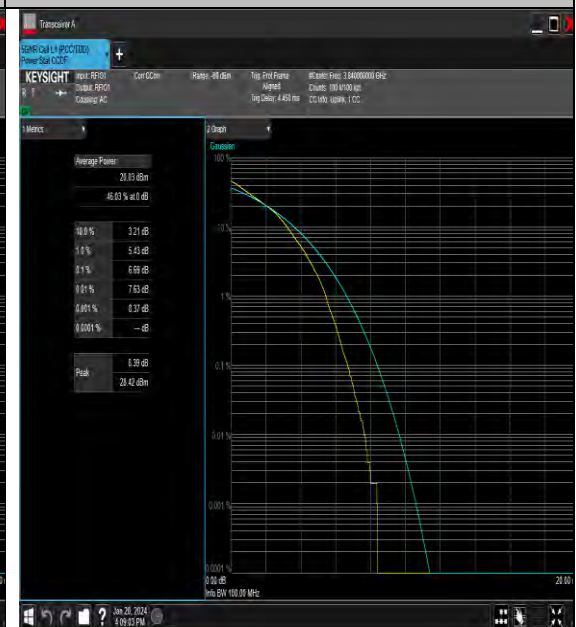
NTNV_N77-3700-3980_PC3_30_100_L_TID5_#1



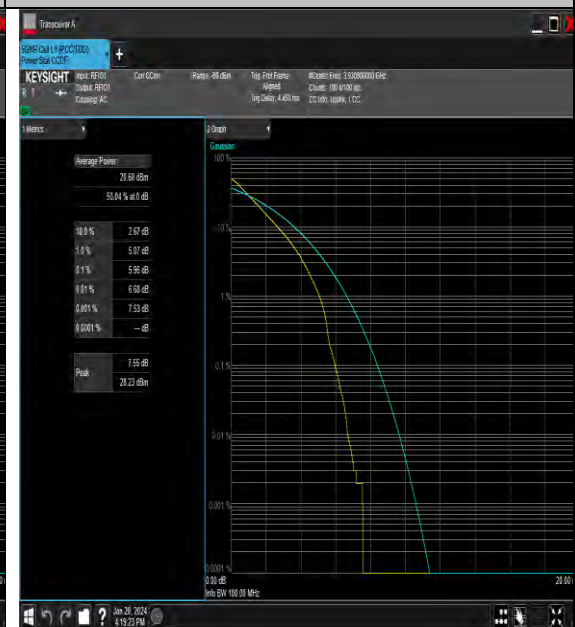
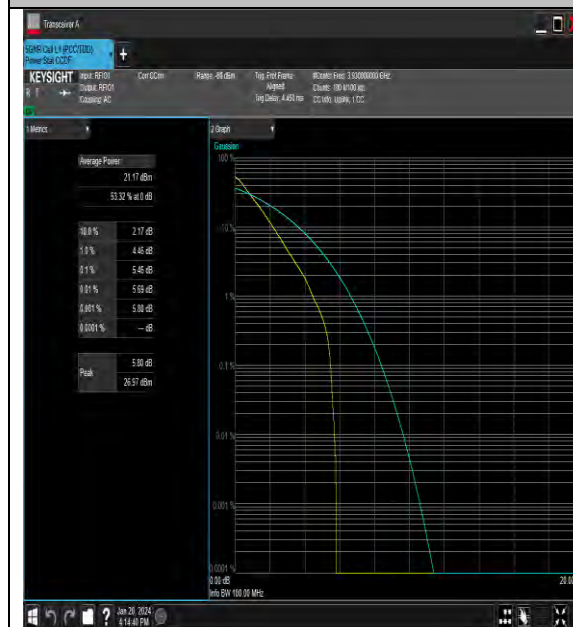
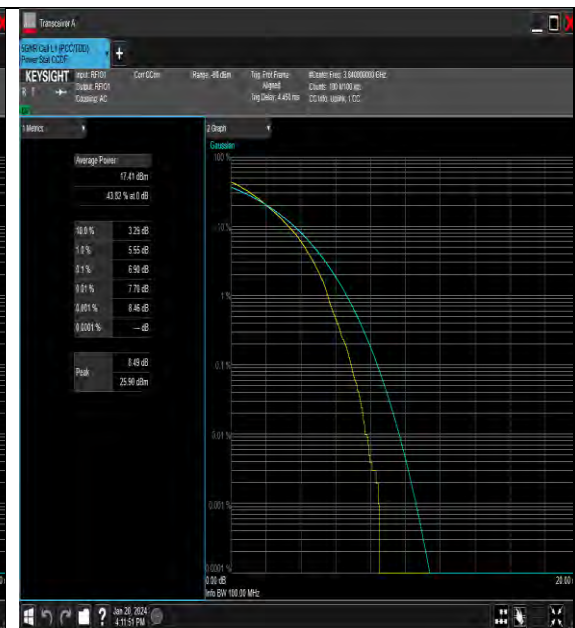
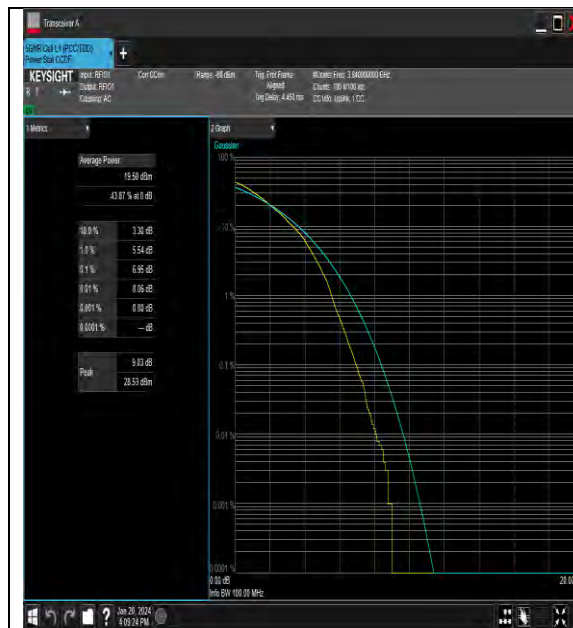
NTNV_N77-3700-3980_PC3_30_100_M_TID1_#1

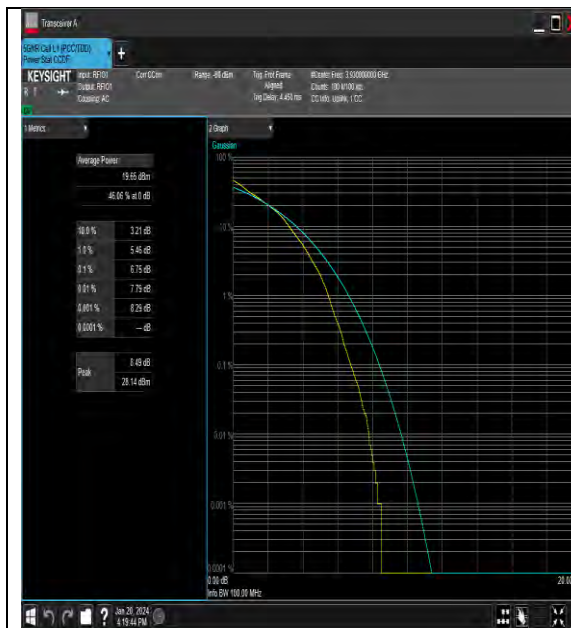


NTNV_N77-3700-3980_PC3_30_100_M_TID2_#1

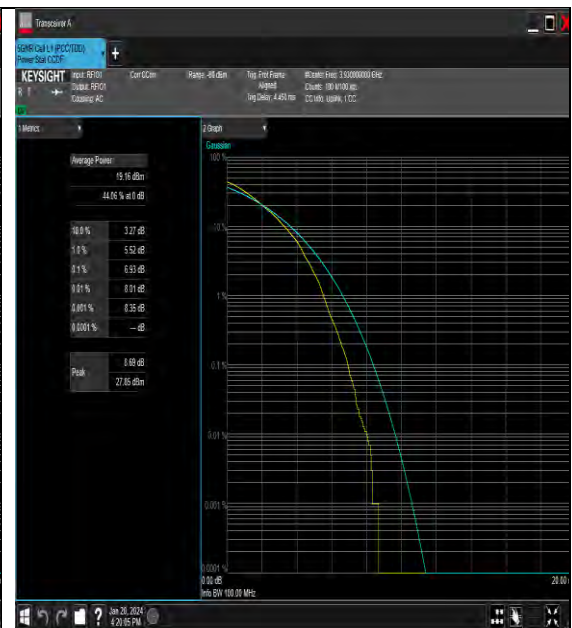


NTNV_N77-3700-3980_PC3_30_100_M_TID3_#1

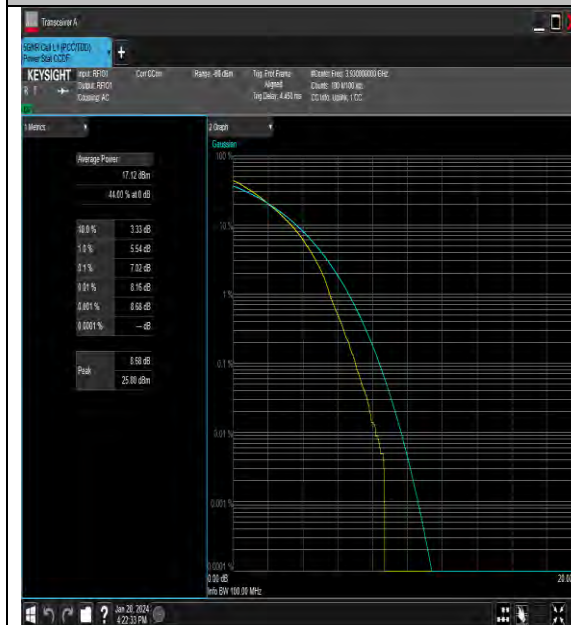




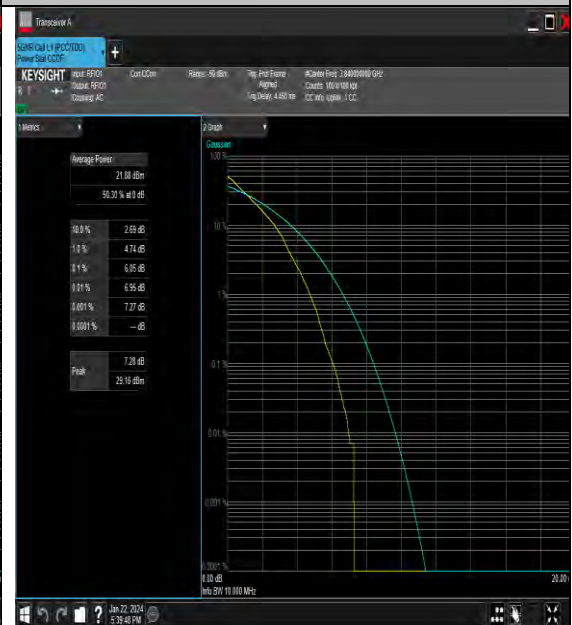
NTNV_N77-3700-3980_PC3_30_100_H_TID3_#1



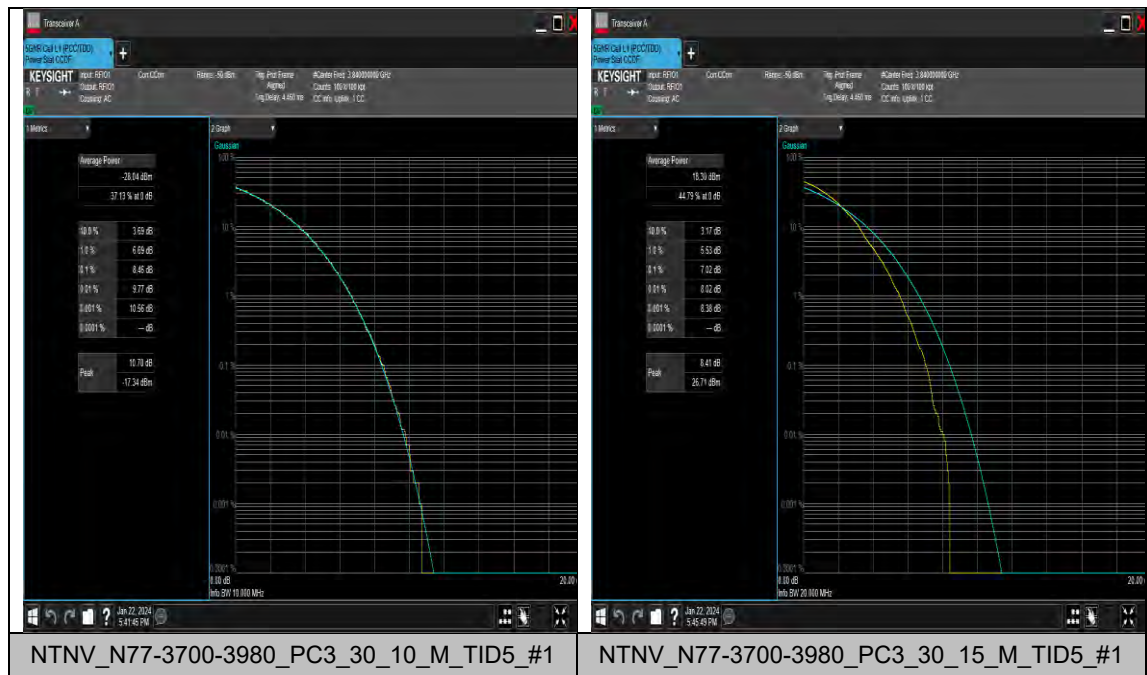
NTNV_N77-3700-3980_PC3_30_100_H_TID4_#1



NTNV_N77-3700-3980_PC3_30_100_H_TID5_#1



NTNV_N77-3700-3980_PC3_30_10_M_TID2_#1



NTNV_N77-3700-3980_PC3_30_15_M_TID5_#1

Appendix C: 26dB Bandwidth and Occupied Bandwidth for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
N77-3450-3550	30	10	DFT-PI2BPSK	L	Outer_Full	8.591	9.300	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Outer_Full	8.569	9.360	PASS
N77-3450-3550	30	10	DFT-16QAM	L	Outer_Full	8.558	9.400	PASS
N77-3450-3550	30	10	DFT-64QAM	L	Outer_Full	8.554	9.400	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Outer_Full	8.574	9.340	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Outer_Full	8.565	9.460	PASS
N77-3450-3550	30	10	DFT-16QAM	M	Outer_Full	8.613	9.480	PASS
N77-3450-3550	30	10	DFT-64QAM	M	Outer_Full	8.568	9.400	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Outer_Full	8.596	9.440	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Outer_Full	8.564	9.380	PASS
N77-3450-3550	30	10	DFT-16QAM	H	Outer_Full	8.609	9.400	PASS
N77-3450-3550	30	10	DFT-64QAM	H	Outer_Full	8.596	9.500	PASS
N77-3450-	30	15	DFT-PI2BPSK	L	Outer_Full	12.853	13.980	PASS

3550								
N77- 3450- 3550	30	15	DFT-QPSK	L	Outer_Full	12.88	14.250	PASS
N77- 3450- 3550	30	15	DFT-16QAM	L	Outer_Full	12.861	14.130	PASS
N77- 3450- 3550	30	15	DFT-64QAM	L	Outer_Full	12.909	14.220	PASS
N77- 3450- 3550	30	15	DFT-PI2BPSK	M	Outer_Full	12.941	14.280	PASS
N77- 3450- 3550	30	15	DFT-QPSK	M	Outer_Full	12.899	14.340	PASS
N77- 3450- 3550	30	15	DFT-16QAM	M	Outer_Full	12.888	14.160	PASS
N77- 3450- 3550	30	15	DFT-64QAM	M	Outer_Full	12.891	14.220	PASS
N77- 3450- 3550	30	15	DFT-PI2BPSK	H	Outer_Full	12.875	14.130	PASS
N77- 3450- 3550	30	15	DFT-QPSK	H	Outer_Full	12.921	14.250	PASS
N77- 3450- 3550	30	15	DFT-16QAM	H	Outer_Full	12.888	14.160	PASS
N77- 3450- 3550	30	15	DFT-64QAM	H	Outer_Full	12.736	14.040	PASS
N77- 3450- 3550	30	20	DFT-PI2BPSK	L	Outer_Full	17.822	19.040	PASS
N77- 3450- 3550	30	20	DFT-QPSK	L	Outer_Full	17.857	19.120	PASS
N77- 3450- 3550	30	20	DFT-16QAM	L	Outer_Full	17.791	19.120	PASS
N77- 3450- 3550	30	20	DFT-64QAM	L	Outer_Full	17.881	19.440	PASS

3450-3550								
N77-3450-3550	30	20	DFT-PI2BPSK	M	Outer_Full	17.838	18.920	PASS
N77-3450-3550	30	20	DFT-QPSK	M	Outer_Full	17.819	19.120	PASS
N77-3450-3550	30	20	DFT-16QAM	M	Outer_Full	17.874	19.200	PASS
N77-3450-3550	30	20	DFT-64QAM	M	Outer_Full	17.823	19.080	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	H	Outer_Full	17.838	19.360	PASS
N77-3450-3550	30	20	DFT-QPSK	H	Outer_Full	17.811	19.320	PASS
N77-3450-3550	30	20	DFT-16QAM	H	Outer_Full	17.838	19.240	PASS
N77-3450-3550	30	20	DFT-64QAM	H	Outer_Full	17.817	19.080	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	L	Outer_Full	22.914	25.000	PASS
N77-3450-3550	30	25	DFT-QPSK	L	Outer_Full	22.864	24.550	PASS
N77-3450-3550	30	25	DFT-16QAM	L	Outer_Full	22.889	24.550	PASS
N77-3450-3550	30	25	DFT-64QAM	L	Outer_Full	22.868	24.450	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	M	Outer_Full	22.886	24.400	PASS
N77-3450-3550	30	25	DFT-QPSK	M	Outer_Full	22.853	24.350	PASS

N77-3450-3550	30	25	DFT-16QAM	M	Outer_Full	22.861	24.450	PASS
N77-3450-3550	30	25	DFT-64QAM	M	Outer_Full	22.955	24.350	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	H	Outer_Full	22.875	24.350	PASS
N77-3450-3550	30	25	DFT-QPSK	H	Outer_Full	22.861	24.850	PASS
N77-3450-3550	30	25	DFT-16QAM	H	Outer_Full	22.942	24.600	PASS
N77-3450-3550	30	25	DFT-64QAM	H	Outer_Full	22.931	24.650	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	L	Outer_Full	26.801	28.560	PASS
N77-3450-3550	30	30	DFT-QPSK	L	Outer_Full	26.817	28.920	PASS
N77-3450-3550	30	30	DFT-16QAM	L	Outer_Full	26.75	28.980	PASS
N77-3450-3550	30	30	DFT-64QAM	L	Outer_Full	26.823	28.500	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	M	Outer_Full	26.79	28.800	PASS
N77-3450-3550	30	30	DFT-QPSK	M	Outer_Full	26.801	28.740	PASS
N77-3450-3550	30	30	DFT-16QAM	M	Outer_Full	26.795	28.680	PASS
N77-3450-3550	30	30	DFT-64QAM	M	Outer_Full	26.835	28.680	PASS
N77-3450-	30	30	DFT-PI2BPSK	H	Outer_Full	26.809	28.680	PASS

3550								
N77-3450-3550	30	30	DFT-QPSK	H	Outer_Full	26.785	28.620	PASS
N77-3450-3550	30	30	DFT-16QAM	H	Outer_Full	26.8	28.560	PASS
N77-3450-3550	30	30	DFT-64QAM	H	Outer_Full	26.829	28.740	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	L	Outer_Full	35.756	38.480	PASS
N77-3450-3550	30	40	DFT-QPSK	L	Outer_Full	35.711	37.760	PASS
N77-3450-3550	30	40	DFT-16QAM	L	Outer_Full	35.769	39.040	PASS
N77-3450-3550	30	40	DFT-64QAM	L	Outer_Full	35.786	38.320	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	M	Outer_Full	35.747	38.640	PASS
N77-3450-3550	30	40	DFT-QPSK	M	Outer_Full	35.654	37.920	PASS
N77-3450-3550	30	40	DFT-16QAM	M	Outer_Full	35.859	38.000	PASS
N77-3450-3550	30	40	DFT-64QAM	M	Outer_Full	35.702	38.080	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	H	Outer_Full	35.773	38.000	PASS
N77-3450-3550	30	40	DFT-QPSK	H	Outer_Full	35.791	38.240	PASS
N77-3450-3550	30	40	DFT-16QAM	H	Outer_Full	35.694	37.920	PASS
N77-	30	40	DFT-64QAM	H	Outer_Full	35.726	38.000	PASS

3450-3550								
N77-3450-3550	30	50	DFT-PI2BPSK	L	Outer_Full	45.693	48.600	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Outer_Full	45.822	48.500	PASS
N77-3450-3550	30	50	DFT-16QAM	L	Outer_Full	45.856	48.700	PASS
N77-3450-3550	30	50	DFT-64QAM	L	Outer_Full	45.632	48.600	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	M	Outer_Full	45.65	48.500	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Outer_Full	45.627	48.400	PASS
N77-3450-3550	30	50	DFT-16QAM	M	Outer_Full	45.586	48.500	PASS
N77-3450-3550	30	50	DFT-64QAM	M	Outer_Full	45.596	48.300	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Outer_Full	45.662	48.300	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Outer_Full	45.703	48.500	PASS
N77-3450-3550	30	50	DFT-16QAM	H	Outer_Full	45.632	48.400	PASS
N77-3450-3550	30	50	DFT-64QAM	H	Outer_Full	45.77	48.700	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	L	Outer_Full	57.648	60.120	PASS
N77-3450-3550	30	60	DFT-QPSK	L	Outer_Full	57.763	60.240	PASS

N77-3450-3550	30	60	DFT-16QAM	L	Outer_Full	57.778	60.240	PASS
N77-3450-3550	30	60	DFT-64QAM	L	Outer_Full	57.732	60.360	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	M	Outer_Full	57.736	60.000	PASS
N77-3450-3550	30	60	DFT-QPSK	M	Outer_Full	57.666	60.240	PASS
N77-3450-3550	30	60	DFT-16QAM	M	Outer_Full	57.633	60.120	PASS
N77-3450-3550	30	60	DFT-64QAM	M	Outer_Full	57.669	60.240	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	H	Outer_Full	57.782	60.480	PASS
N77-3450-3550	30	60	DFT-QPSK	H	Outer_Full	57.758	60.240	PASS
N77-3450-3550	30	60	DFT-16QAM	H	Outer_Full	57.879	60.480	PASS
N77-3450-3550	30	60	DFT-64QAM	H	Outer_Full	57.74	60.480	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	L	Outer_Full	64.261	67.200	PASS
N77-3450-3550	30	70	DFT-QPSK	L	Outer_Full	64.143	67.620	PASS
N77-3450-3550	30	70	DFT-16QAM	L	Outer_Full	64.204	67.200	PASS
N77-3450-3550	30	70	DFT-64QAM	L	Outer_Full	64.31	67.620	PASS
N77-3450-	30	70	DFT-PI2BPSK	M	Outer_Full	64.251	67.200	PASS

3550								
N77-3450-3550	30	70	DFT-QPSK	M	Outer_Full	64.171	67.060	PASS
N77-3450-3550	30	70	DFT-16QAM	M	Outer_Full	64.255	67.620	PASS
N77-3450-3550	30	70	DFT-64QAM	M	Outer_Full	64.193	67.760	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	H	Outer_Full	64.139	67.060	PASS
N77-3450-3550	30	70	DFT-QPSK	H	Outer_Full	64.172	67.340	PASS
N77-3450-3550	30	70	DFT-16QAM	H	Outer_Full	64.251	67.200	PASS
N77-3450-3550	30	70	DFT-64QAM	H	Outer_Full	64.184	67.060	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	L	Outer_Full	77.231	80.800	PASS
N77-3450-3550	30	80	DFT-QPSK	L	Outer_Full	77.008	80.800	PASS
N77-3450-3550	30	80	DFT-16QAM	L	Outer_Full	76.982	82.080	PASS
N77-3450-3550	30	80	DFT-64QAM	L	Outer_Full	76.826	80.800	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	M	Outer_Full	76.983	80.960	PASS
N77-3450-3550	30	80	DFT-QPSK	M	Outer_Full	76.977	80.640	PASS
N77-3450-3550	30	80	DFT-16QAM	M	Outer_Full	77.028	80.480	PASS
N77-3450-3550	30	80	DFT-64QAM	M	Outer_Full	77.136	80.320	PASS

3450-3550								
N77-3450-3550	30	80	DFT-PI2BPSK	H	Outer_Full	77.03	81.760	PASS
N77-3450-3550	30	80	DFT-QPSK	H	Outer_Full	76.915	80.960	PASS
N77-3450-3550	30	80	DFT-16QAM	H	Outer_Full	76.766	81.120	PASS
N77-3450-3550	30	80	DFT-64QAM	H	Outer_Full	76.897	81.600	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	L	Outer_Full	86.658	90.180	PASS
N77-3450-3550	30	90	DFT-QPSK	L	Outer_Full	86.504	90.540	PASS
N77-3450-3550	30	90	DFT-16QAM	L	Outer_Full	86.545	90.360	PASS
N77-3450-3550	30	90	DFT-64QAM	L	Outer_Full	86.377	90.180	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	M	Outer_Full	86.533	90.360	PASS
N77-3450-3550	30	90	DFT-QPSK	M	Outer_Full	86.508	90.180	PASS
N77-3450-3550	30	90	DFT-16QAM	M	Outer_Full	86.473	90.540	PASS
N77-3450-3550	30	90	DFT-64QAM	M	Outer_Full	86.473	90.360	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	H	Outer_Full	86.619	90.540	PASS
N77-3450-3550	30	90	DFT-QPSK	H	Outer_Full	86.665	90.540	PASS

N77-3450-3550	30	90	DFT-16QAM	H	Outer_Full	86.615	90.180	PASS
N77-3450-3550	30	90	DFT-64QAM	H	Outer_Full	86.546	90.360	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	95.916	100.200	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	95.912	100.200	PASS
N77-3450-3550	30	100	DFT-16QAM	M	Outer_Full	96.022	99.800	PASS
N77-3450-3550	30	100	DFT-64QAM	M	Outer_Full	96.016	100.200	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	L	Outer_Full	8.616	9.540	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Outer_Full	8.624	9.540	PASS
N77-3700-3980	30	10	DFT-16QAM	L	Outer_Full	8.577	9.360	PASS
N77-3700-3980	30	10	DFT-64QAM	L	Outer_Full	8.566	9.440	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	M	Outer_Full	8.592	9.400	PASS
N77-3700-3980	30	10	DFT-QPSK	M	Outer_Full	8.565	9.360	PASS
N77-3700-3980	30	10	DFT-16QAM	M	Outer_Full	8.586	9.520	PASS
N77-3700-3980	30	10	DFT-64QAM	M	Outer_Full	8.58	9.540	PASS
N77-3700-	30	10	DFT-PI2BPSK	H	Outer_Full	8.554	9.360	PASS

3980								
N77-3700-3980	30	10	DFT-QPSK	H	Outer_Full	8.565	9.380	PASS
N77-3700-3980	30	10	DFT-16QAM	H	Outer_Full	8.555	9.320	PASS
N77-3700-3980	30	10	DFT-64QAM	H	Outer_Full	8.593	9.440	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	L	Outer_Full	12.919	14.070	PASS
N77-3700-3980	30	15	DFT-QPSK	L	Outer_Full	12.919	14.310	PASS
N77-3700-3980	30	15	DFT-16QAM	L	Outer_Full	12.902	14.040	PASS
N77-3700-3980	30	15	DFT-64QAM	L	Outer_Full	12.906	14.130	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	M	Outer_Full	12.933	14.310	PASS
N77-3700-3980	30	15	DFT-QPSK	M	Outer_Full	12.924	14.130	PASS
N77-3700-3980	30	15	DFT-16QAM	M	Outer_Full	12.95	14.250	PASS
N77-3700-3980	30	15	DFT-64QAM	M	Outer_Full	12.902	14.070	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	H	Outer_Full	12.894	13.980	PASS
N77-3700-3980	30	15	DFT-QPSK	H	Outer_Full	12.876	14.100	PASS
N77-3700-3980	30	15	DFT-16QAM	H	Outer_Full	12.919	14.160	PASS
N77-3700-3980	30	15	DFT-64QAM	H	Outer_Full	12.891	14.250	PASS

3700-3980								
N77-3700-3980	30	20	DFT-PI2BPSK	L	Outer_Full	17.883	19.240	PASS
N77-3700-3980	30	20	DFT-QPSK	L	Outer_Full	17.854	19.200	PASS
N77-3700-3980	30	20	DFT-16QAM	L	Outer_Full	17.851	19.240	PASS
N77-3700-3980	30	20	DFT-64QAM	L	Outer_Full	17.82	19.280	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	M	Outer_Full	17.837	19.080	PASS
N77-3700-3980	30	20	DFT-QPSK	M	Outer_Full	17.898	19.200	PASS
N77-3700-3980	30	20	DFT-16QAM	M	Outer_Full	17.877	19.240	PASS
N77-3700-3980	30	20	DFT-64QAM	M	Outer_Full	17.834	19.040	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	H	Outer_Full	17.812	18.960	PASS
N77-3700-3980	30	20	DFT-QPSK	H	Outer_Full	17.918	19.320	PASS
N77-3700-3980	30	20	DFT-16QAM	H	Outer_Full	17.878	19.160	PASS
N77-3700-3980	30	20	DFT-64QAM	H	Outer_Full	17.833	19.000	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	L	Outer_Full	22.928	24.550	PASS
N77-3700-3980	30	25	DFT-QPSK	L	Outer_Full	22.858	24.550	PASS

N77-3700-3980	30	25	DFT-16QAM	L	Outer_Full	22.896	24.500	PASS
N77-3700-3980	30	25	DFT-64QAM	L	Outer_Full	22.847	24.900	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	M	Outer_Full	23.01	24.450	PASS
N77-3700-3980	30	25	DFT-QPSK	M	Outer_Full	22.88	24.550	PASS
N77-3700-3980	30	25	DFT-16QAM	M	Outer_Full	22.89	24.500	PASS
N77-3700-3980	30	25	DFT-64QAM	M	Outer_Full	22.885	24.400	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	H	Outer_Full	22.9	24.450	PASS
N77-3700-3980	30	25	DFT-QPSK	H	Outer_Full	22.862	24.450	PASS
N77-3700-3980	30	25	DFT-16QAM	H	Outer_Full	22.838	24.400	PASS
N77-3700-3980	30	25	DFT-64QAM	H	Outer_Full	22.901	24.550	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	L	Outer_Full	26.76	28.560	PASS
N77-3700-3980	30	30	DFT-QPSK	L	Outer_Full	26.762	28.680	PASS
N77-3700-3980	30	30	DFT-16QAM	L	Outer_Full	26.822	28.680	PASS
N77-3700-3980	30	30	DFT-64QAM	L	Outer_Full	26.798	29.040	PASS
N77-3700-	30	30	DFT-PI2BPSK	M	Outer_Full	26.882	28.680	PASS

3980								
N77-3700-3980	30	30	DFT-QPSK	M	Outer_Full	26.841	28.680	PASS
N77-3700-3980	30	30	DFT-16QAM	M	Outer_Full	26.821	28.620	PASS
N77-3700-3980	30	30	DFT-64QAM	M	Outer_Full	26.809	28.680	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	H	Outer_Full	26.854	28.680	PASS
N77-3700-3980	30	30	DFT-QPSK	H	Outer_Full	26.774	28.620	PASS
N77-3700-3980	30	30	DFT-16QAM	H	Outer_Full	26.797	28.740	PASS
N77-3700-3980	30	30	DFT-64QAM	H	Outer_Full	26.807	28.800	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	L	Outer_Full	35.703	38.080	PASS
N77-3700-3980	30	40	DFT-QPSK	L	Outer_Full	35.729	38.560	PASS
N77-3700-3980	30	40	DFT-16QAM	L	Outer_Full	35.709	38.240	PASS
N77-3700-3980	30	40	DFT-64QAM	L	Outer_Full	35.73	37.920	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	M	Outer_Full	35.771	37.840	PASS
N77-3700-3980	30	40	DFT-QPSK	M	Outer_Full	35.81	38.320	PASS
N77-3700-3980	30	40	DFT-16QAM	M	Outer_Full	35.886	38.320	PASS
N77-3700-3980	30	40	DFT-64QAM	M	Outer_Full	35.796	37.920	PASS

3700-3980								
N77-3700-3980	30	40	DFT-PI2BPSK	H	Outer_Full	35.747	38.400	PASS
N77-3700-3980	30	40	DFT-QPSK	H	Outer_Full	35.704	38.240	PASS
N77-3700-3980	30	40	DFT-16QAM	H	Outer_Full	35.723	38.560	PASS
N77-3700-3980	30	40	DFT-64QAM	H	Outer_Full	35.722	38.320	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Outer_Full	45.693	48.500	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Outer_Full	45.807	48.600	PASS
N77-3700-3980	30	50	DFT-16QAM	L	Outer_Full	45.733	48.200	PASS
N77-3700-3980	30	50	DFT-64QAM	L	Outer_Full	45.67	48.700	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	M	Outer_Full	45.723	48.300	PASS
N77-3700-3980	30	50	DFT-QPSK	M	Outer_Full	45.689	48.600	PASS
N77-3700-3980	30	50	DFT-16QAM	M	Outer_Full	45.683	48.700	PASS
N77-3700-3980	30	50	DFT-64QAM	M	Outer_Full	45.9	48.500	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Outer_Full	45.657	48.400	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Outer_Full	45.748	48.300	PASS

N77-3700-3980	30	50	DFT-16QAM	H	Outer_Full	45.707	48.700	PASS
N77-3700-3980	30	50	DFT-64QAM	H	Outer_Full	45.703	48.500	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	L	Outer_Full	57.906	60.240	PASS
N77-3700-3980	30	60	DFT-QPSK	L	Outer_Full	57.742	61.320	PASS
N77-3700-3980	30	60	DFT-16QAM	L	Outer_Full	57.892	60.240	PASS
N77-3700-3980	30	60	DFT-64QAM	L	Outer_Full	57.665	60.240	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	M	Outer_Full	57.86	60.480	PASS
N77-3700-3980	30	60	DFT-QPSK	M	Outer_Full	57.827	60.240	PASS
N77-3700-3980	30	60	DFT-16QAM	M	Outer_Full	57.738	60.120	PASS
N77-3700-3980	30	60	DFT-64QAM	M	Outer_Full	57.802	60.240	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	H	Outer_Full	57.785	60.240	PASS
N77-3700-3980	30	60	DFT-QPSK	H	Outer_Full	57.803	60.120	PASS
N77-3700-3980	30	60	DFT-16QAM	H	Outer_Full	57.657	60.240	PASS
N77-3700-3980	30	60	DFT-64QAM	H	Outer_Full	57.67	60.240	PASS
N77-3700-	30	70	DFT-PI2BPSK	L	Outer_Full	64.382	67.340	PASS

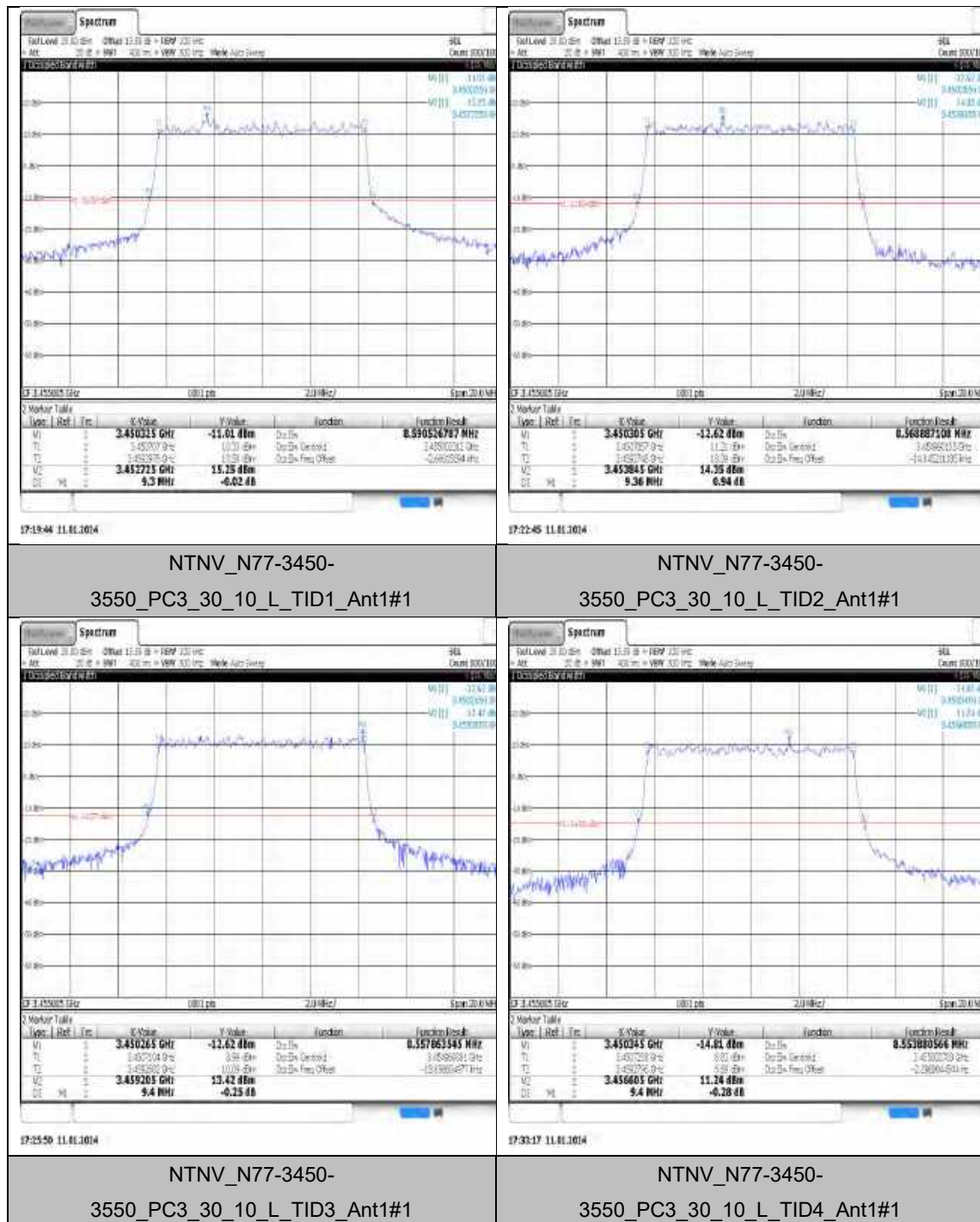
3980								
N77-3700-3980	30	70	DFT-QPSK	L	Outer_Full	64.233	67.200	PASS
N77-3700-3980	30	70	DFT-16QAM	L	Outer_Full	64.335	67.340	PASS
N77-3700-3980	30	70	DFT-64QAM	L	Outer_Full	64.241	67.480	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	M	Outer_Full	64.291	67.760	PASS
N77-3700-3980	30	70	DFT-QPSK	M	Outer_Full	64.354	67.200	PASS
N77-3700-3980	30	70	DFT-16QAM	M	Outer_Full	64.323	67.200	PASS
N77-3700-3980	30	70	DFT-64QAM	M	Outer_Full	64.347	67.480	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	H	Outer_Full	64.314	68.040	PASS
N77-3700-3980	30	70	DFT-QPSK	H	Outer_Full	64.228	67.480	PASS
N77-3700-3980	30	70	DFT-16QAM	H	Outer_Full	64.171	67.060	PASS
N77-3700-3980	30	70	DFT-64QAM	H	Outer_Full	64.136	67.340	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	L	Outer_Full	77.337	83.520	PASS
N77-3700-3980	30	80	DFT-16QAM	L	Outer_Full	77.141	80.960	PASS
N77-3700-3980	30	80	DFT-64QAM	L	Outer_Full	76.959	80.800	PASS
N77-	30	80	DFT-PI2BPSK	M	Outer_Full	77.089	80.480	PASS

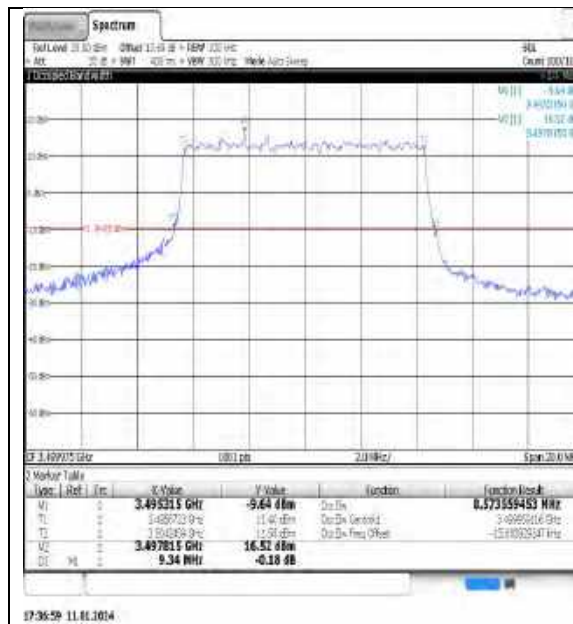
3700-3980								
N77-3700-3980	30	80	DFT-QPSK	M	Outer_Full	77.252	81.760	PASS
N77-3700-3980	30	80	DFT-16QAM	M	Outer_Full	77.208	80.480	PASS
N77-3700-3980	30	80	DFT-64QAM	M	Outer_Full	77.052	83.040	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	H	Outer_Full	77.381	81.760	PASS
N77-3700-3980	30	80	DFT-QPSK	H	Outer_Full	77.271	80.640	PASS
N77-3700-3980	30	80	DFT-16QAM	H	Outer_Full	77.046	80.800	PASS
N77-3700-3980	30	80	DFT-64QAM	H	Outer_Full	76.959	80.480	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	L	Outer_Full	86.904	90.720	PASS
N77-3700-3980	30	90	DFT-QPSK	L	Outer_Full	86.841	90.720	PASS
N77-3700-3980	30	90	DFT-16QAM	L	Outer_Full	86.806	90.540	PASS
N77-3700-3980	30	90	DFT-64QAM	L	Outer_Full	86.598	90.180	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	M	Outer_Full	86.659	90.360	PASS
N77-3700-3980	30	90	DFT-QPSK	M	Outer_Full	86.702	90.720	PASS
N77-3700-3980	30	90	DFT-16QAM	M	Outer_Full	86.828	90.540	PASS

N77-3700-3980	30	90	DFT-64QAM	M	Outer_Full	86.929	90.360	PASS
N77-3700-3980	30	90	DFT-QPSK	H	Outer_Full	86.953	89.640	PASS
N77-3700-3980	30	90	DFT-16QAM	H	Outer_Full	86.82	90.540	PASS
N77-3700-3980	30	90	DFT-64QAM	H	Outer_Full	86.674	90.540	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	96.384	100.400	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	96.055	100.400	PASS
N77-3700-3980	30	100	DFT-16QAM	L	Outer_Full	96.416	100.000	PASS
N77-3700-3980	30	100	DFT-64QAM	L	Outer_Full	96.163	100.000	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	96.348	101.000	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	96.462	100.200	PASS
N77-3700-3980	30	100	DFT-16QAM	M	Outer_Full	96.225	100.400	PASS
N77-3700-3980	30	100	DFT-64QAM	M	Outer_Full	96.392	100.600	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	96.241	100.000	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	96.086	100.800	PASS
N77-3700-	30	100	DFT-16QAM	H	Outer_Full	96.343	101.600	PASS

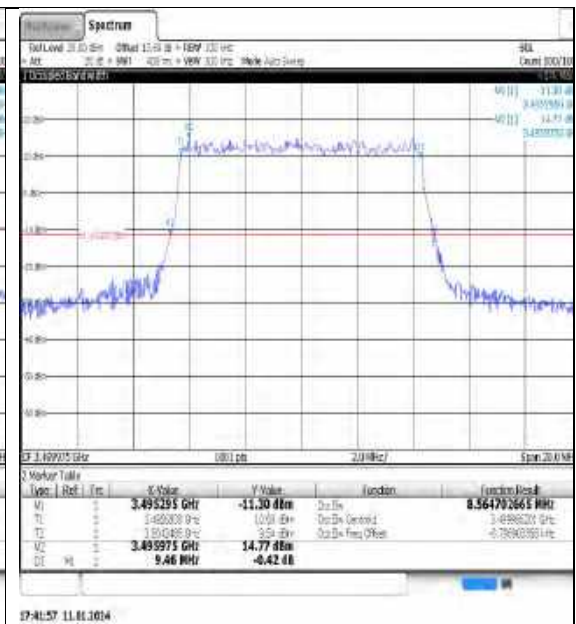
3980								
N77- 3700- 3980	30	100	DFT-64QAM	H	Outer_Full	96.474	100.200	PASS
N77- 3700- 3980	30	80	DFT-QPSK	L	Outer_Full	77.181	80.800	PASS
N77- 3700- 3980	30	90	DFT-PI2BPSK	H	Outer_Full	86.474	90.180	PASS

Test Graphs

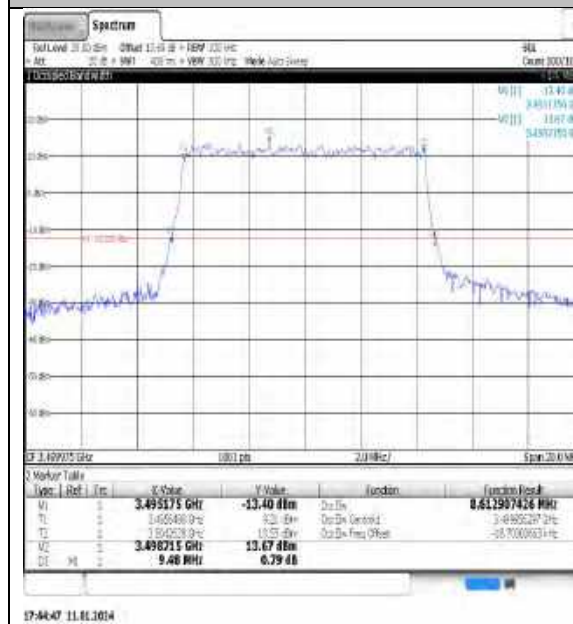




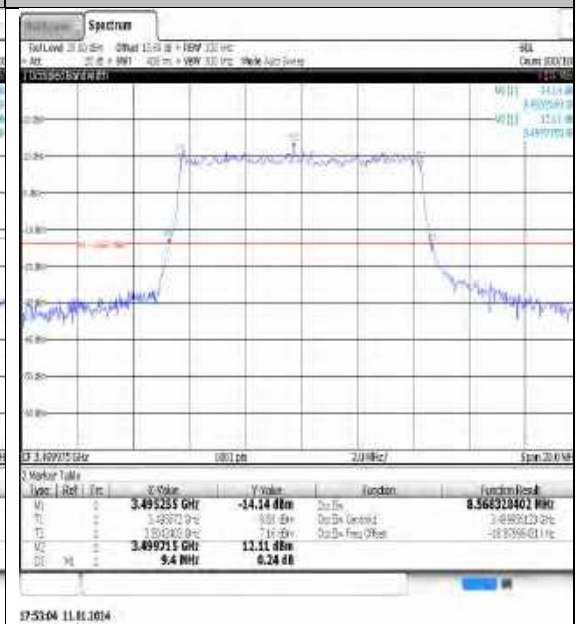
NTNV_N77-3450-
3550_PC3_30_10_M_TID1_Ant1#1



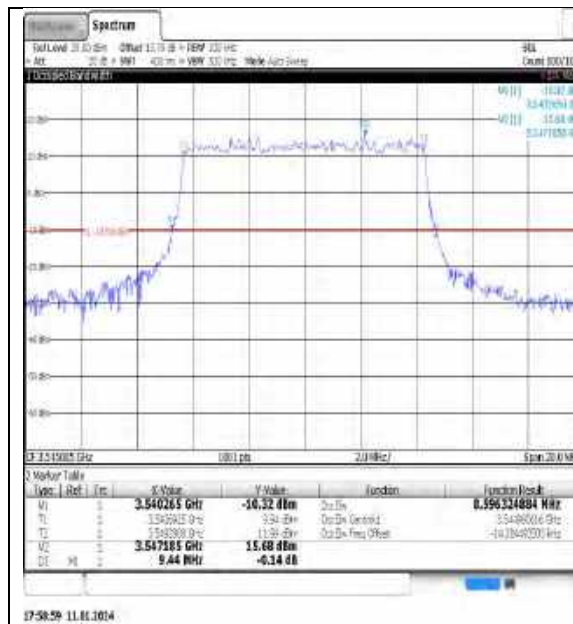
NTNV_N77-3450-
3550_PC3_30_10_M_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_10_M_TID3_Ant1#1



NTNV_N77-3450-
3550_PC3_30_10_M_TID4_Ant1#1



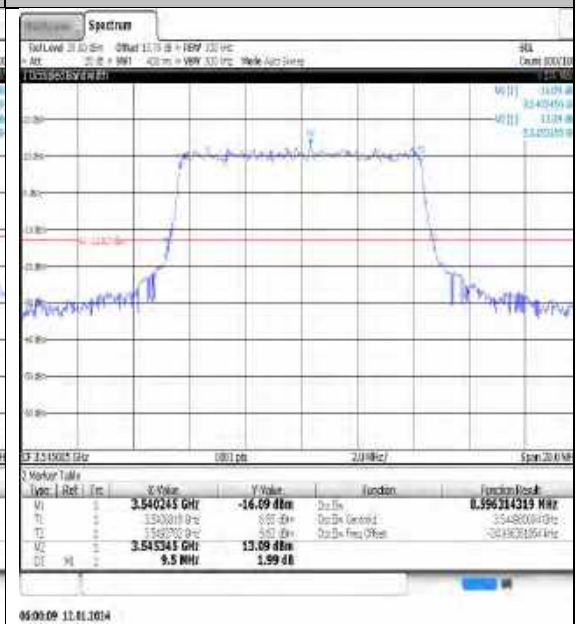
NTNV_N77-3450-
3550_PC3_30_10_H_TID1_Ant1#1



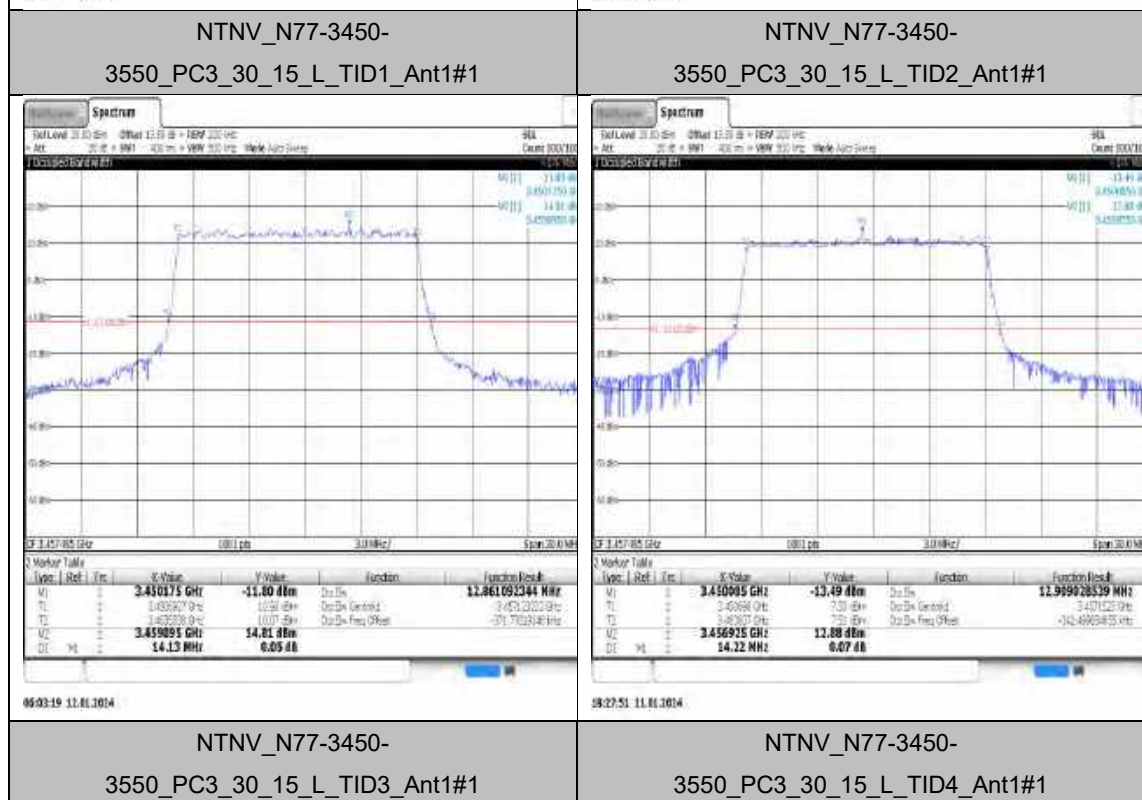
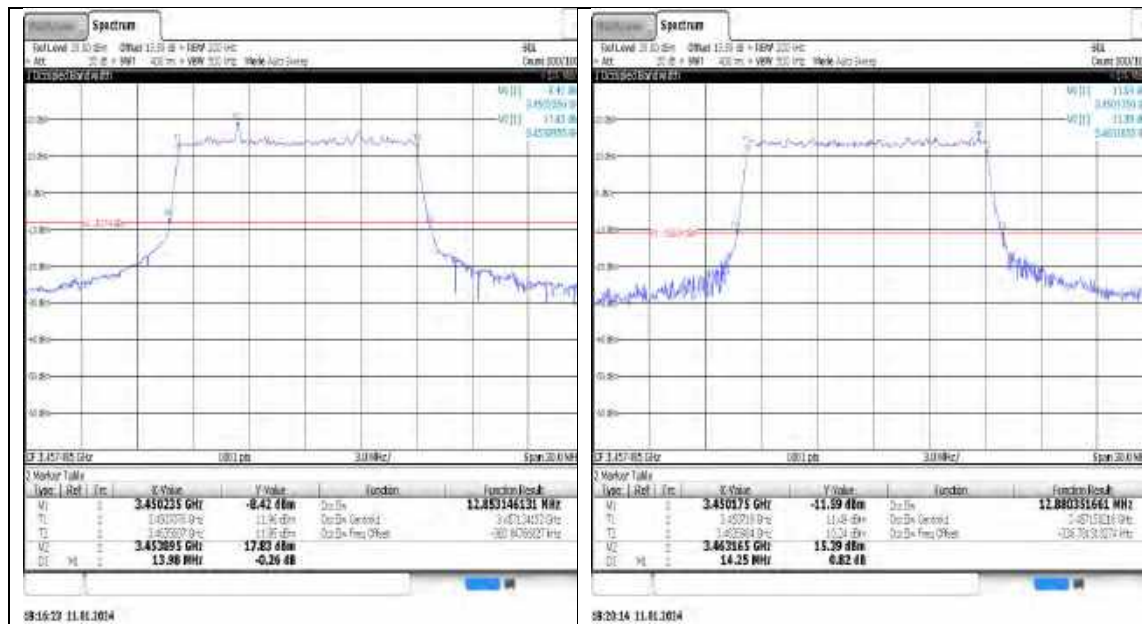
NTNV_N77-3450-
3550_PC3_30_10_H_TID2_Ant1#1

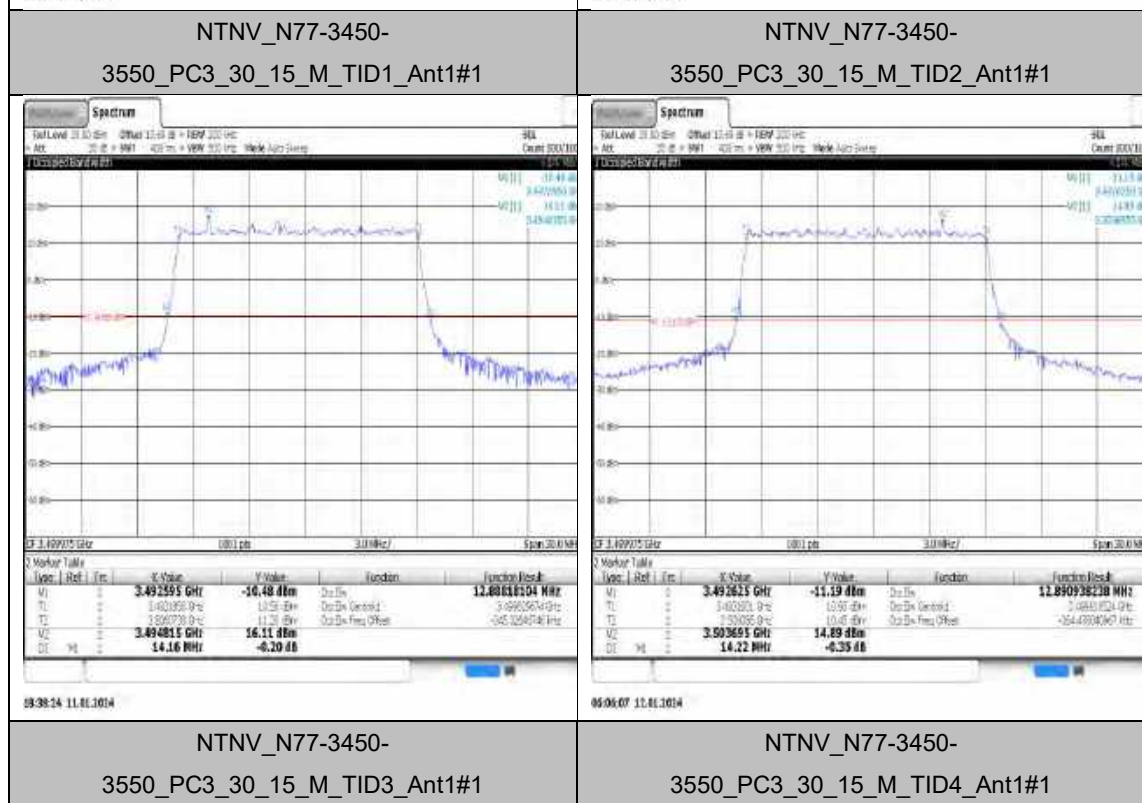
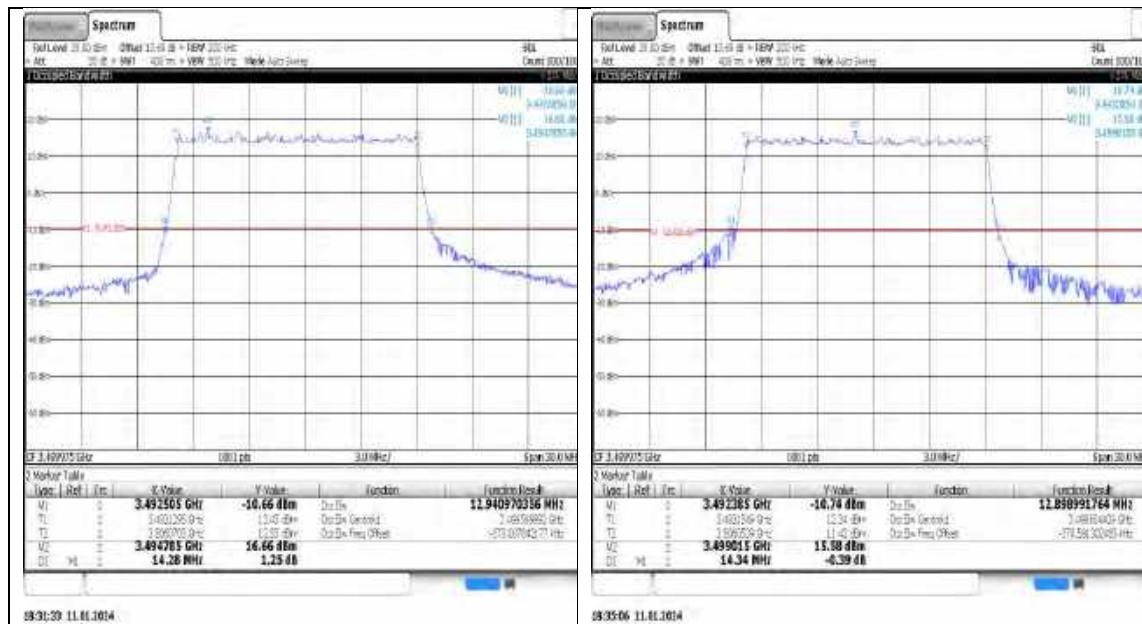


NTNV_N77-3450-
3550_PC3_30_10_H_TID3_Ant1#1



NTNV_N77-3450-
3550_PC3_30_10_H_TID4_Ant1#1



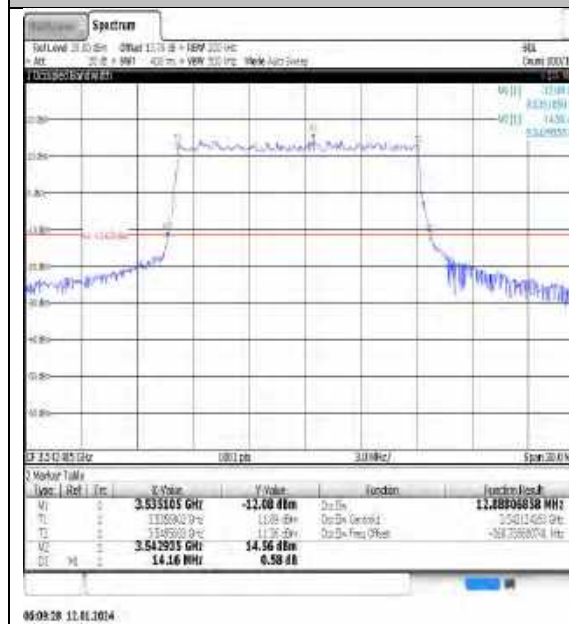




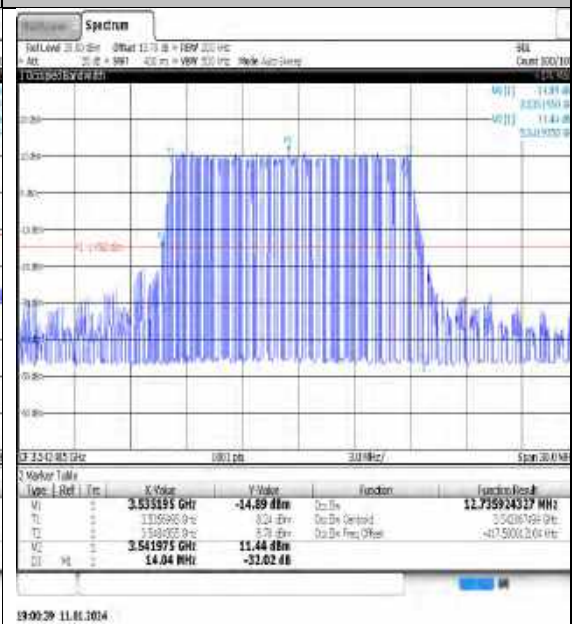
NTNV_N77-3450-
3550_PC3_30_15_H_TID1_Ant1#1



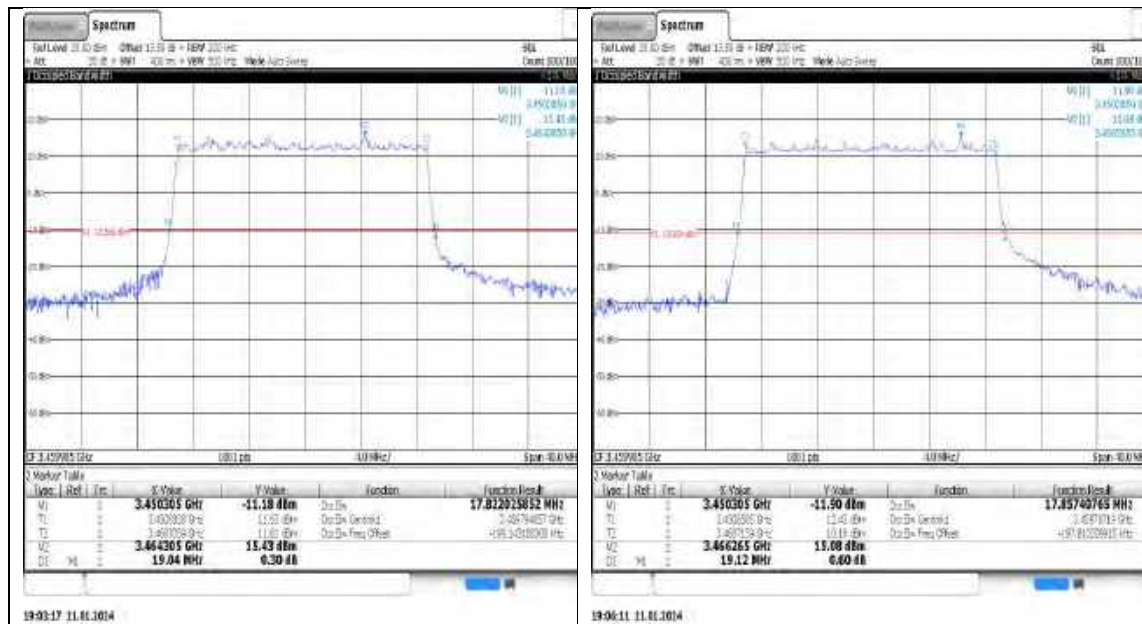
NTNV_N77-3450-
3550_PC3_30_15_H_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_15_H_TID3_Ant1#1

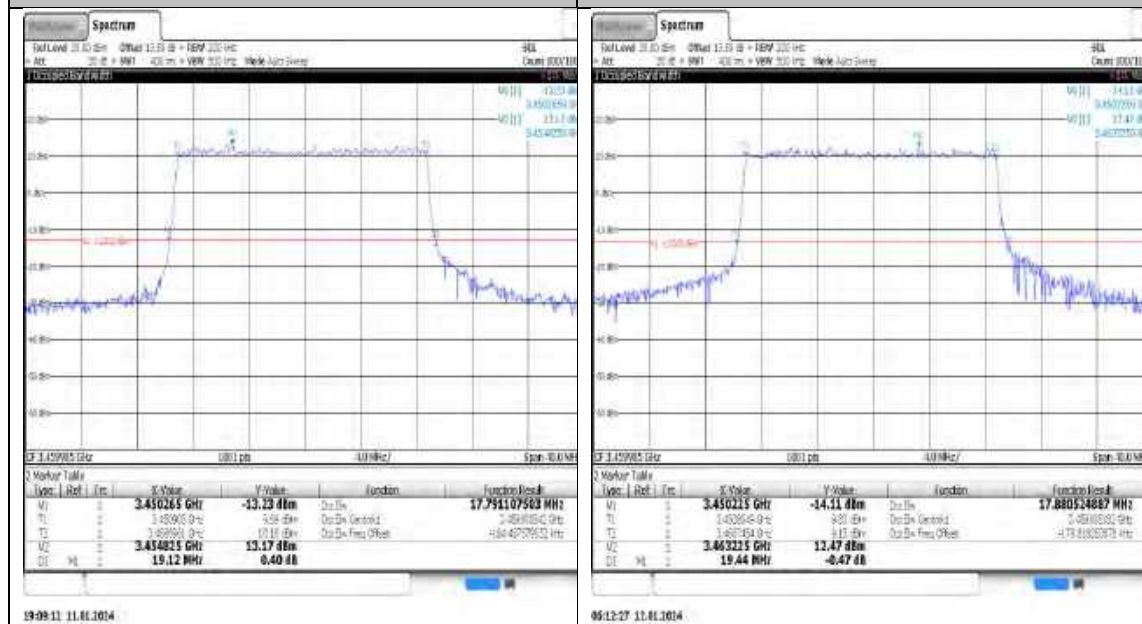


NTNV_N77-3450-
3550_PC3_30_15_H_TID4_Ant1#1



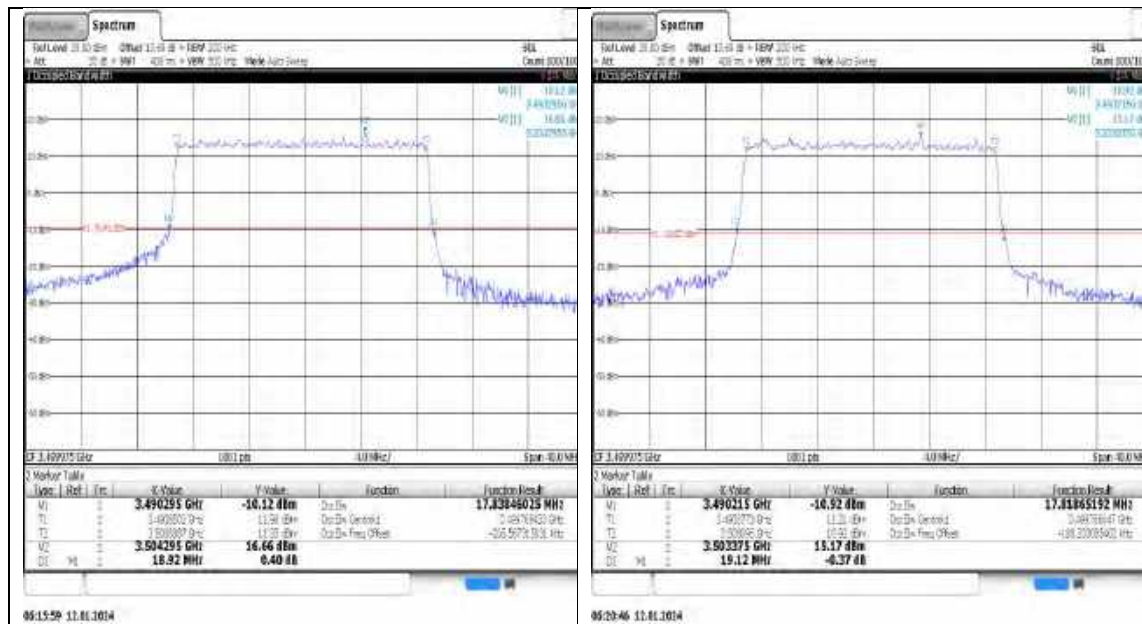
NTNV_N77-3450-
3550_PC3_30_20_L_TID1_Ant1#1

NTNV_N77-3450-
3550_PC3_30_20_L_TID2_Ant1#1



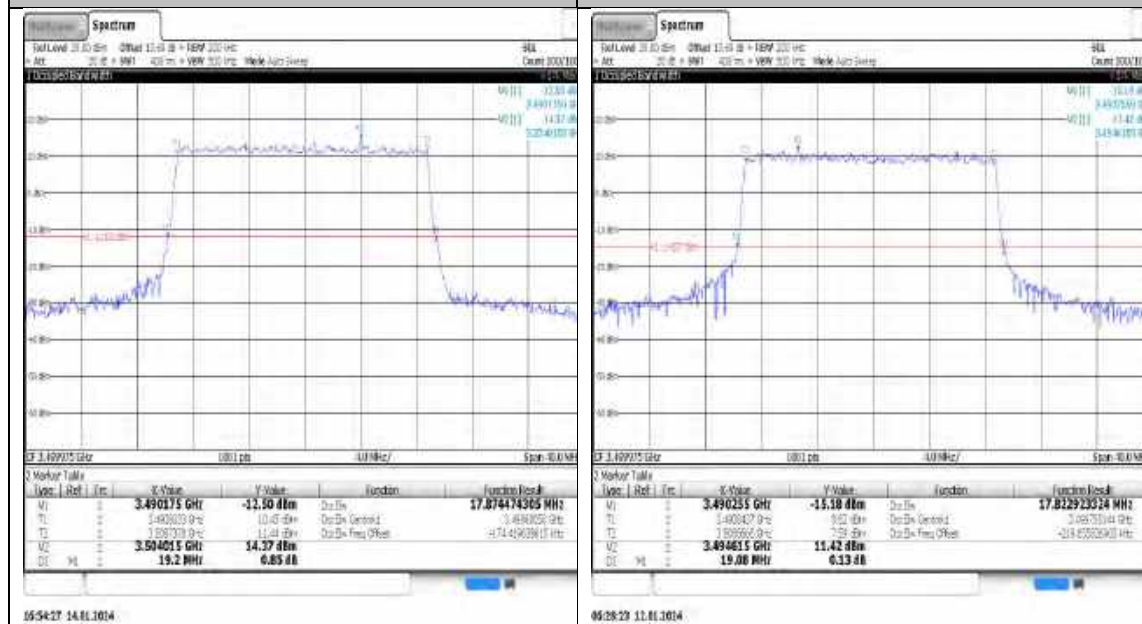
NTNV_N77-3450-
3550_PC3_30_20_L_TID3_Ant1#1

NTNV_N77-3450-
3550_PC3_30_20_L_TID4_Ant1#1



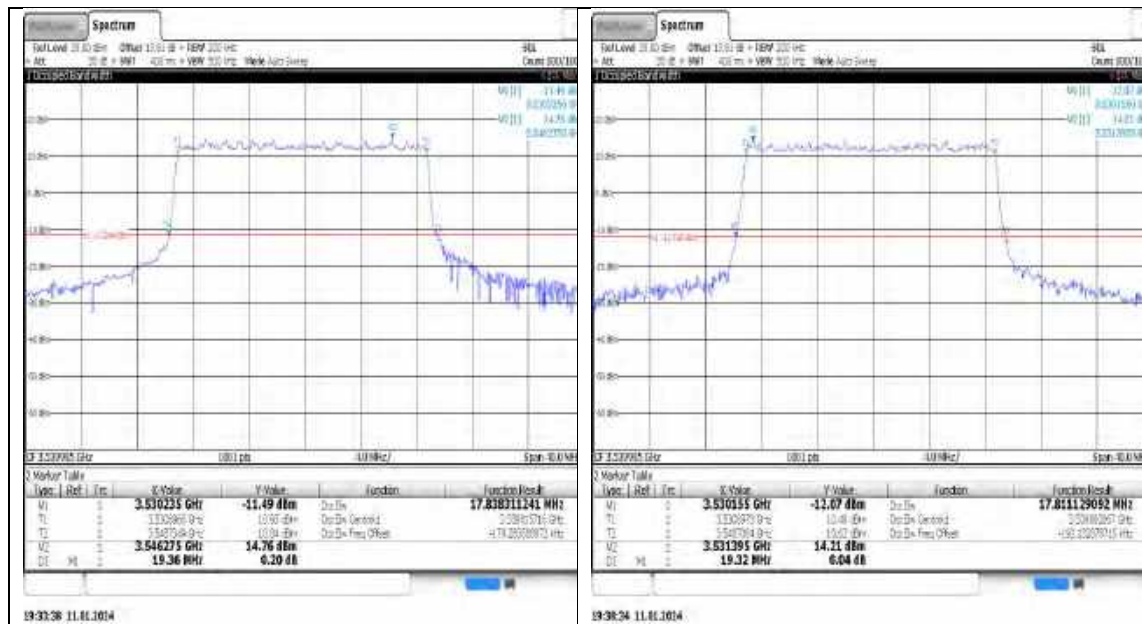
NTNV_N77-3450-
3550_PC3_30_20_M_TID1_Ant1#1

NTNV_N77-3450-
3550_PC3_30_20_M_TID2_Ant1#1



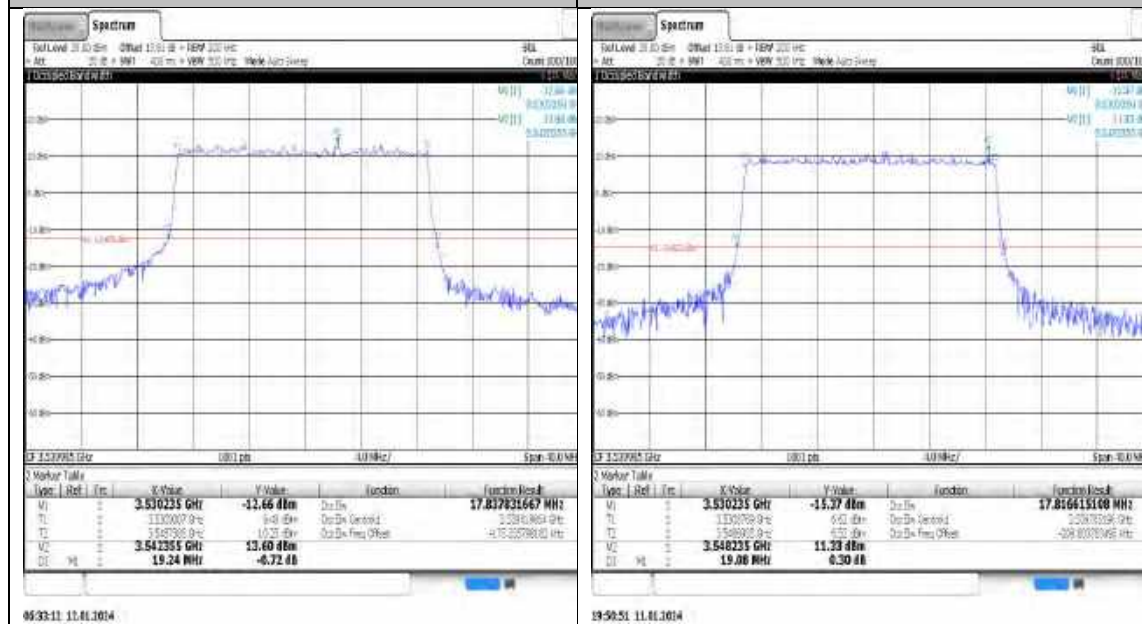
NTNV_N77-3450-
3550_PC3_30_20_M_TID3_Ant1#1

NTNV_N77-3450-
3550_PC3_30_20_M_TID4_Ant1#1



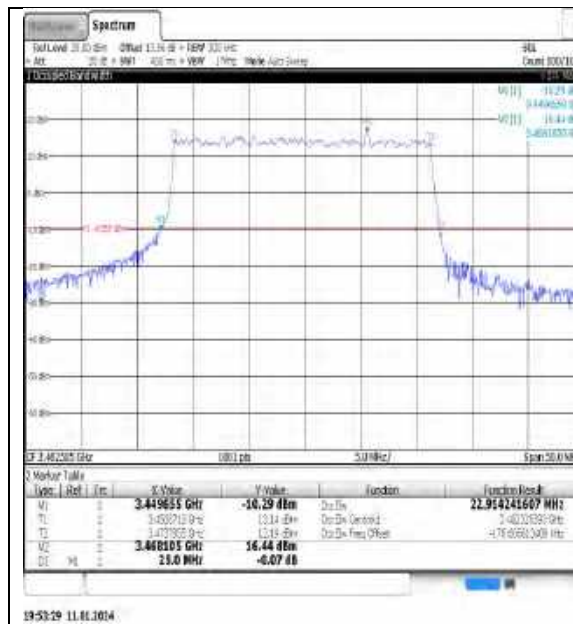
NTNV_N77-3450-
3550_PC3_30_20_H_TID1_Ant1#1

NTNV_N77-3450-
3550_PC3_30_20_H_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_20_H_TID3_Ant1#1

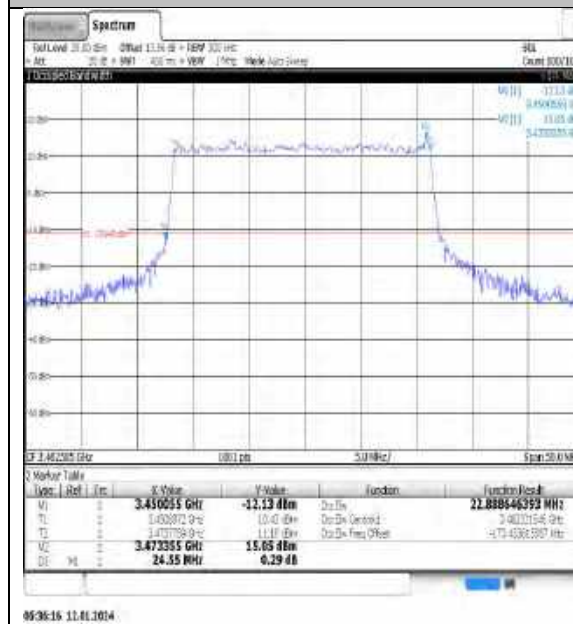
NTNV_N77-3450-
3550_PC3_30_20_H_TID4_Ant1#1



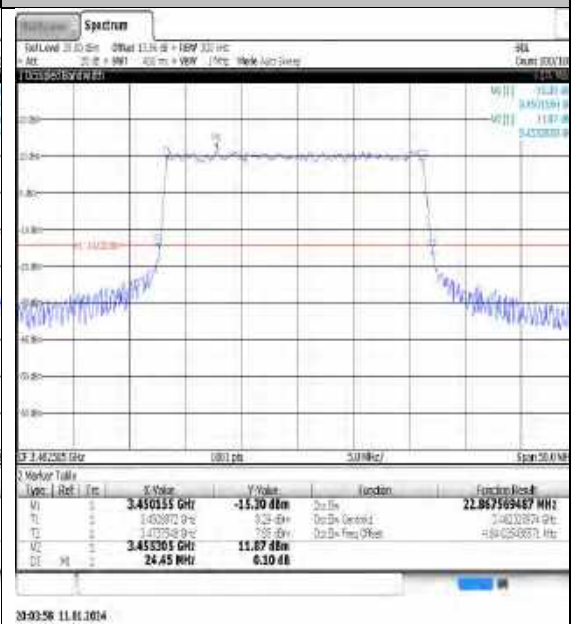
NTNV_N77-3450-
3550_PC3_30_25_L_TID1_Ant1#1



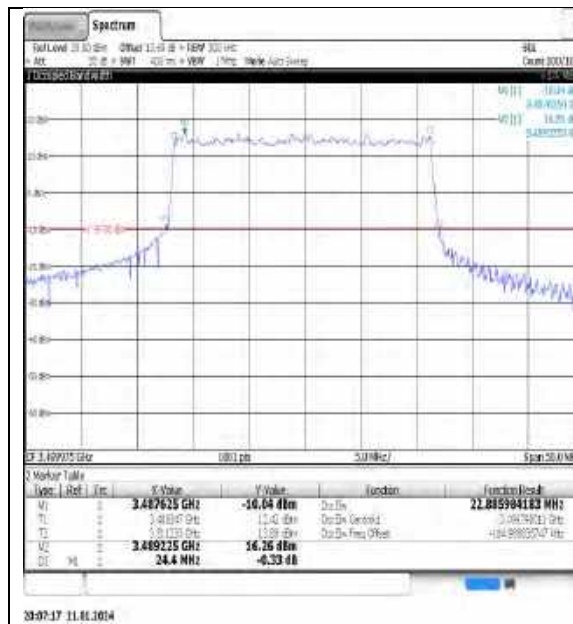
NTNV_N77-3450-
3550_PC3_30_25_L_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_25_L_TID3_Ant1#1



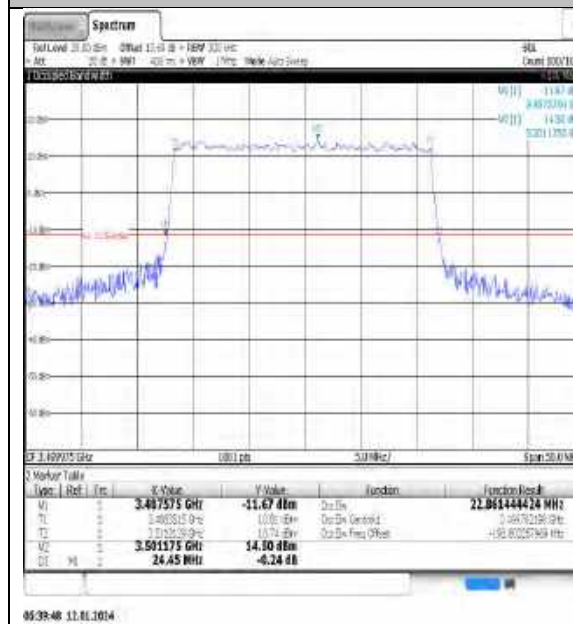
NTNV_N77-3450-
3550_PC3_30_25_L_TID4_Ant1#1



NTNV_N77-3450-
3550_PC3_30_25_M_TID1_Ant1#1



NTNV_N77-3450-
3550_PC3_30_25_M_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_25_M_TID3_Ant1#1



NTNV_N77-3450-
3550_PC3_30_25_M_TID4_Ant1#1



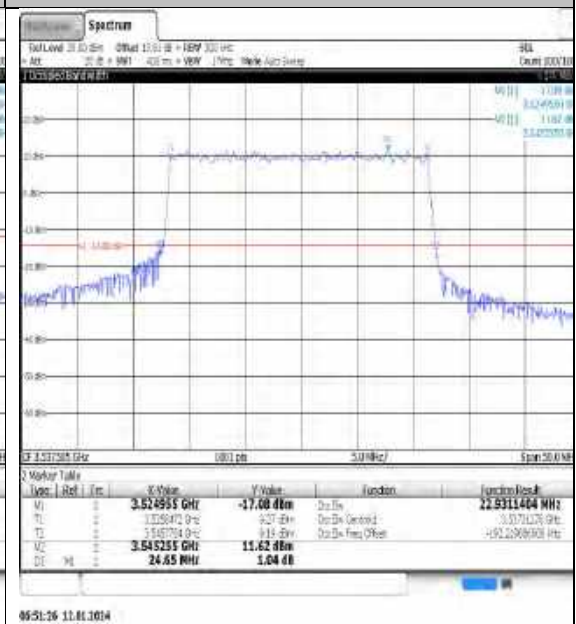
NTNV_N77-3450-
3550_PC3_30_25_H_TID1_Ant1#1



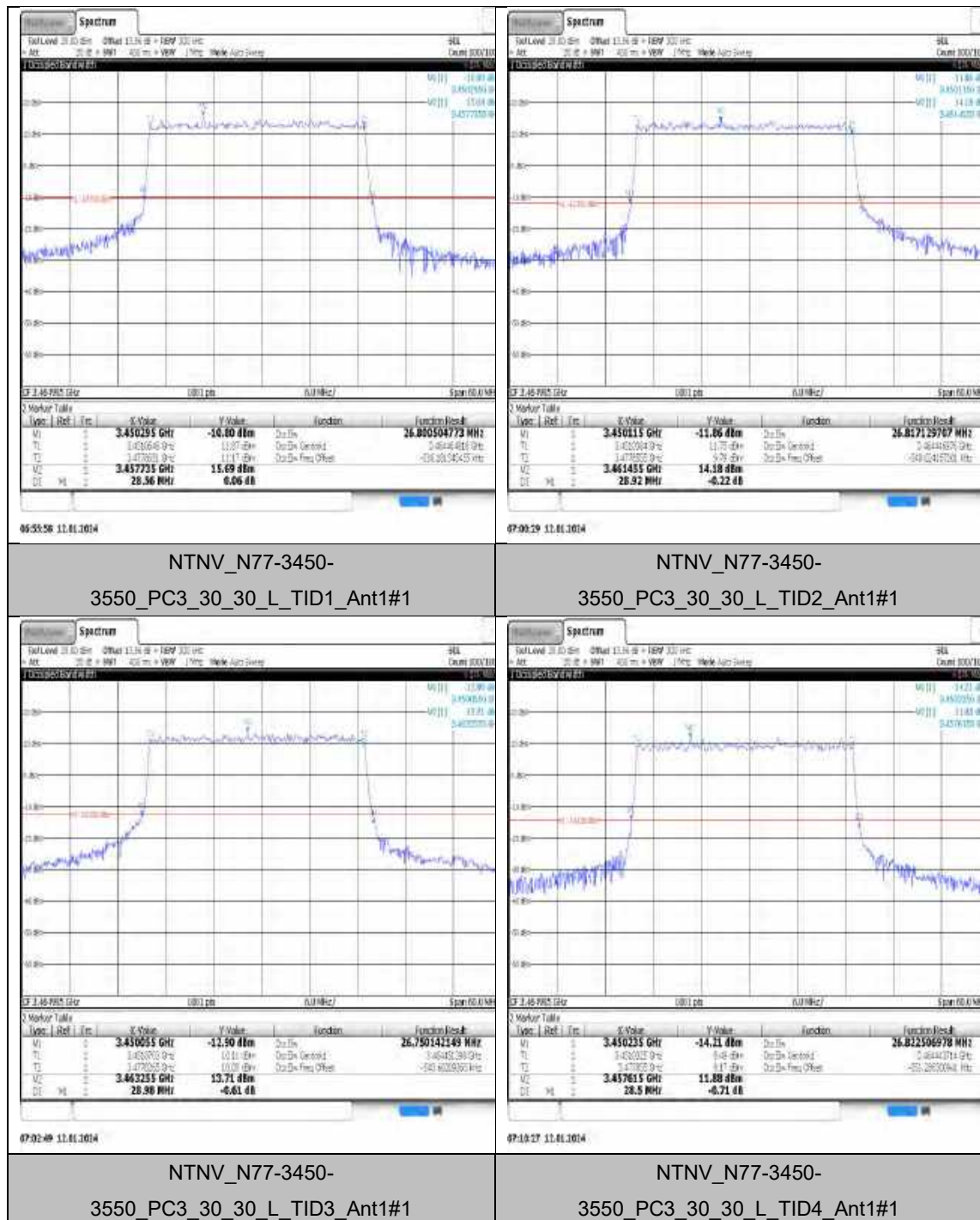
NTNV_N77-3450-
3550_PC3_30_25_H_TID2_Ant1#1

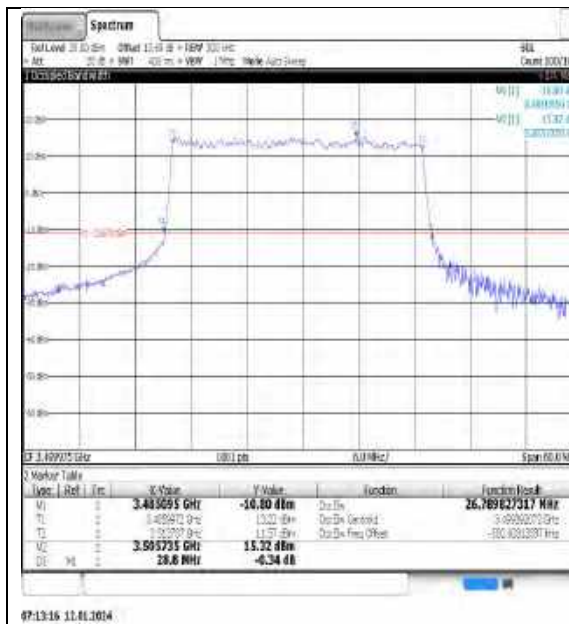


NTNV_N77-3450-
3550_PC3_30_25_H_TID3_Ant1#1



NTNV_N77-3450-
3550_PC3_30_25_H_TID4_Ant1#1

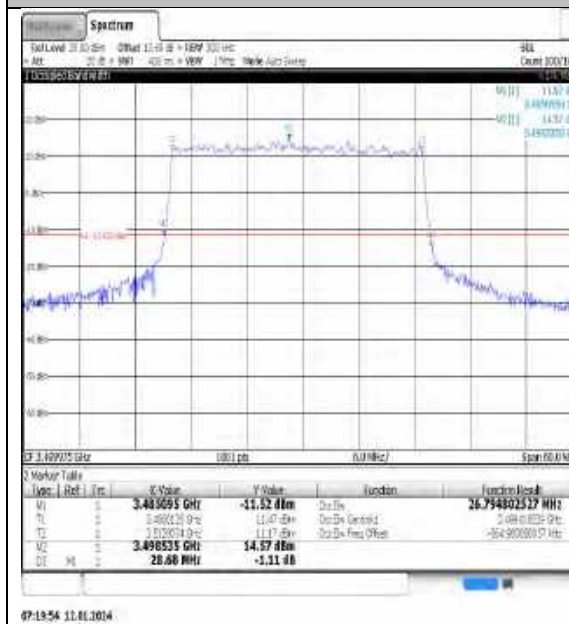




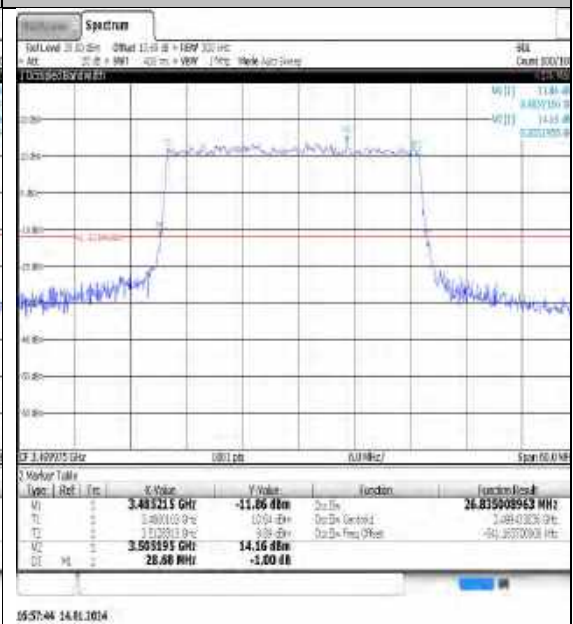
NTNV_N77-3450-
3550_PC3_30_30_M_TID1_Ant1#1



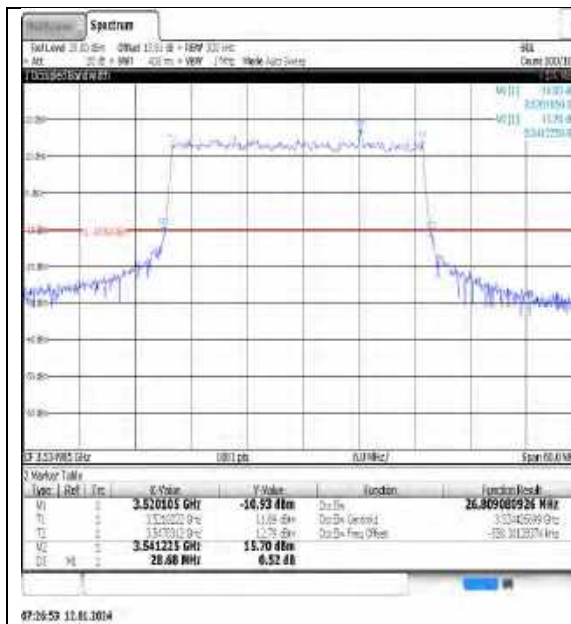
NTNV_N77-3450-
3550_PC3_30_30_M_TID2_Ant1#1



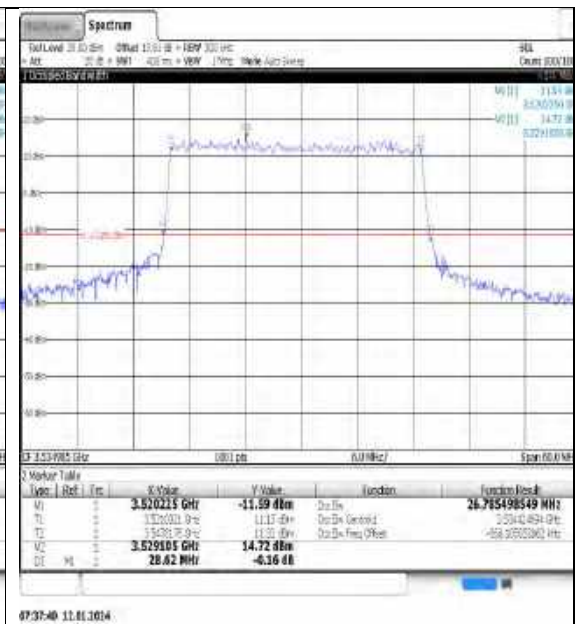
NTNV_N77-3450-
3550_PC3_30_30_M_TID3_Ant1#1



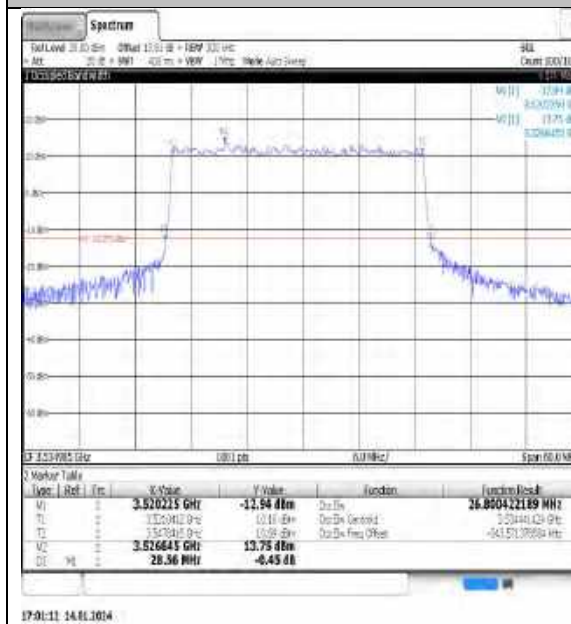
NTNV_N77-3450-
3550_PC3_30_30_M_TID4_Ant1#1



NTNV_N77-3450-
3550_PC3_30_30_H_TID1_Ant1#1



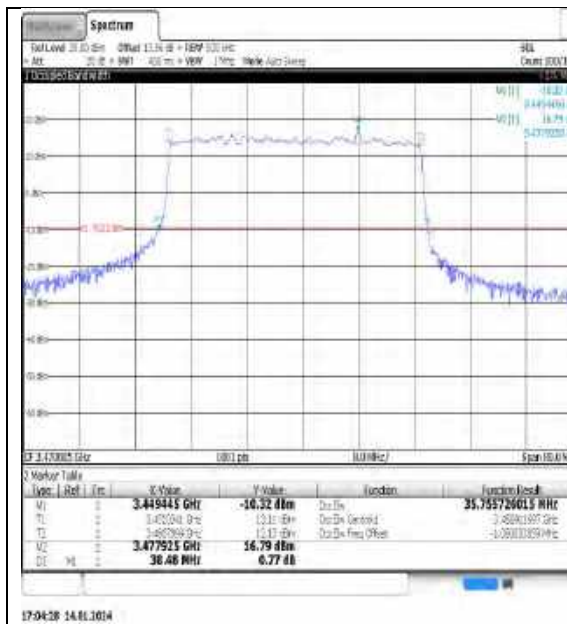
NTNV_N77-3450-
3550_PC3_30_30_H_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_30_H_TID3_Ant1#1



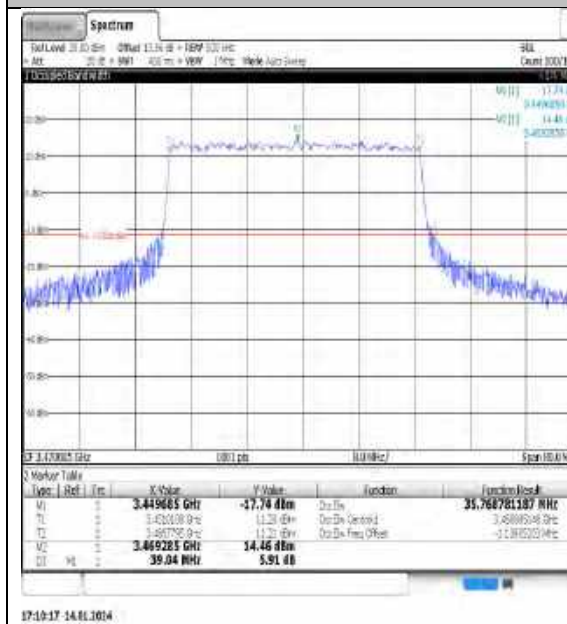
NTNV_N77-3450-
3550_PC3_30_30_H_TID4_Ant1#1



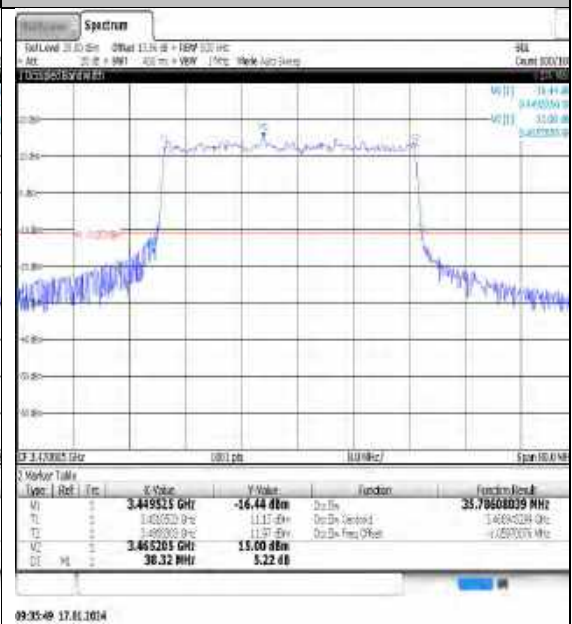
NTNV_N77-3450-
3550_PC3_30_40_L_TID1_Ant1#1



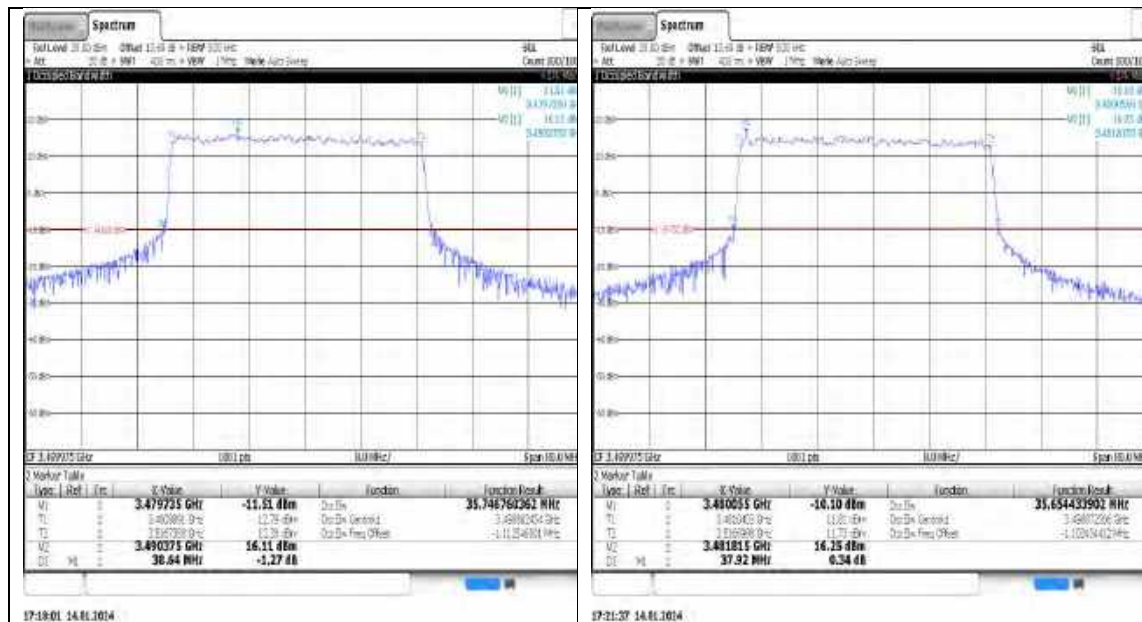
NTNV_N77-3450-
3550_PC3_30_40_L_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_40_L_TID3_Ant1#1

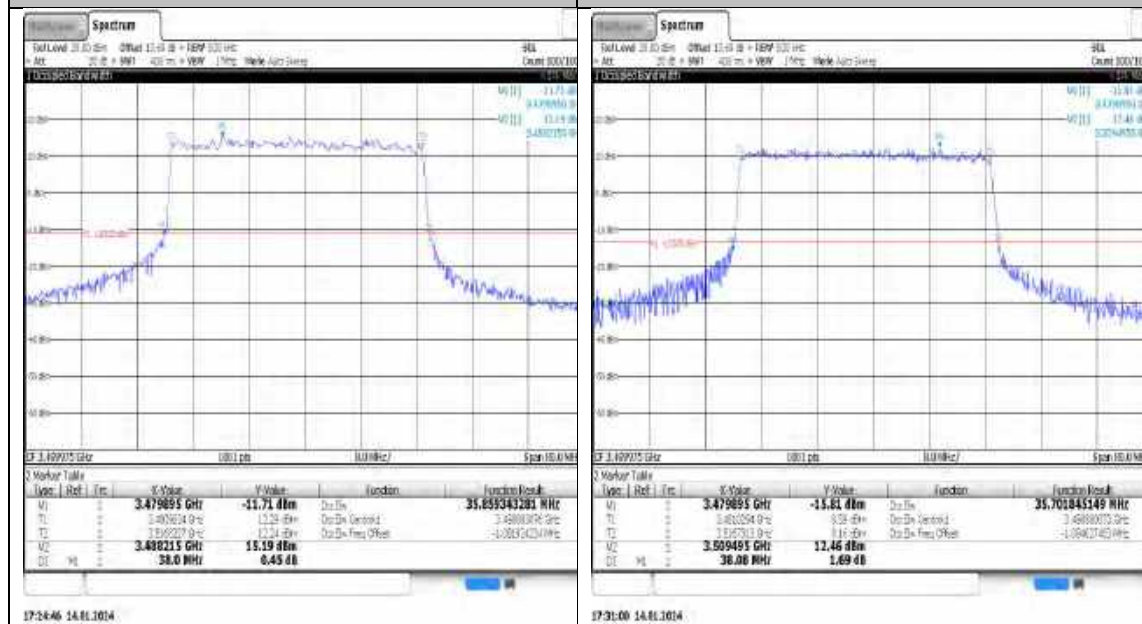


NTNV_N77-3450-
3550_PC3_30_40_L_TID4_Ant1#1



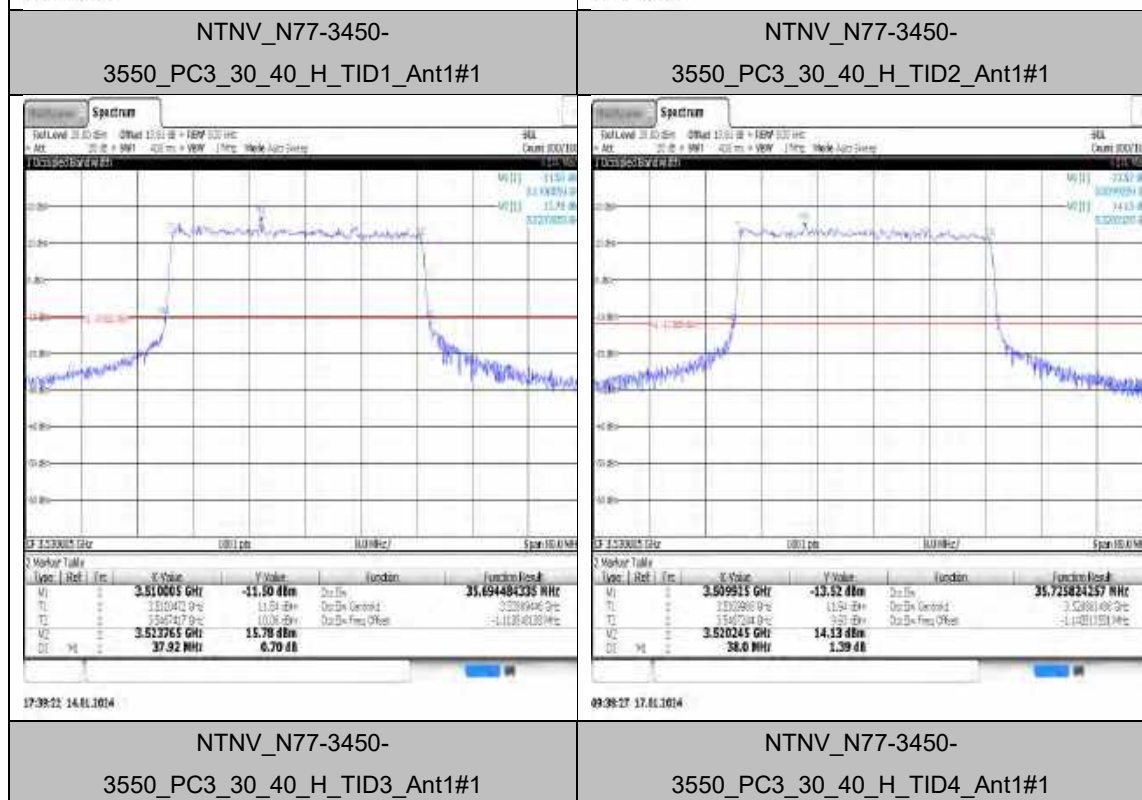
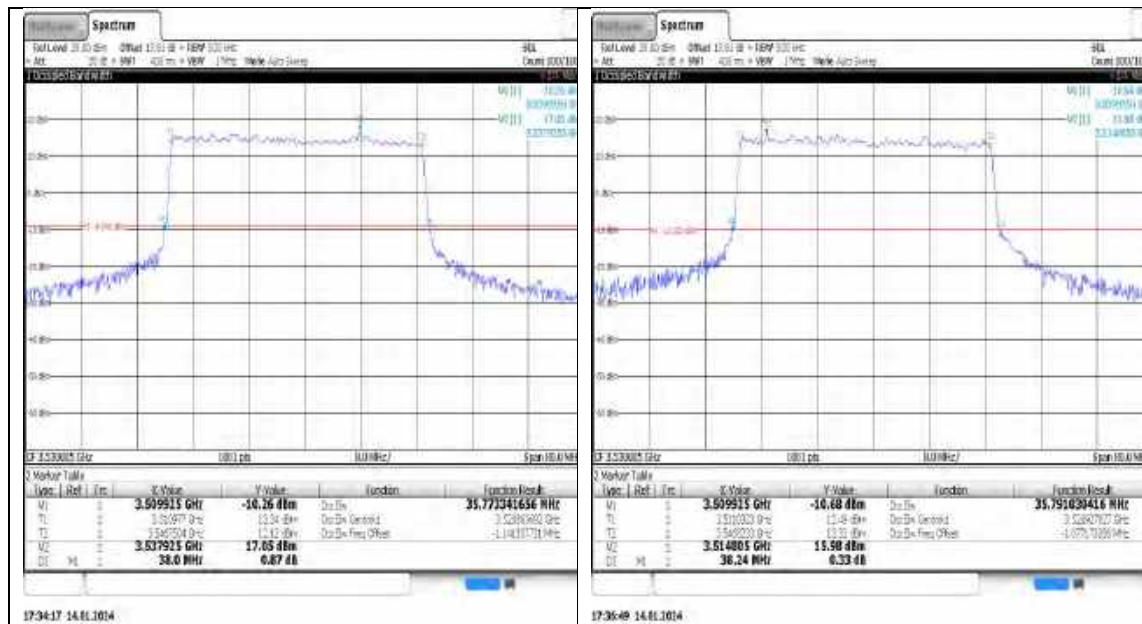
NTNV_N77-3450-
3550_PC3_30_40_M_TID1_Ant1#1

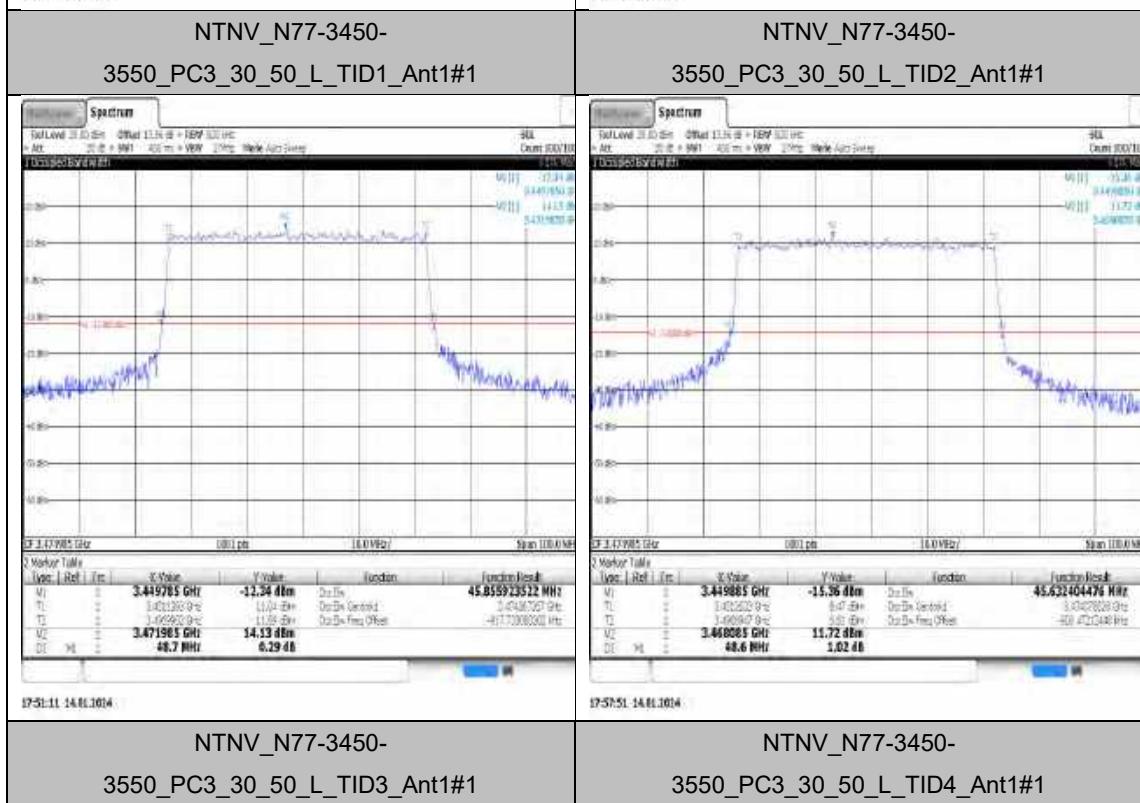
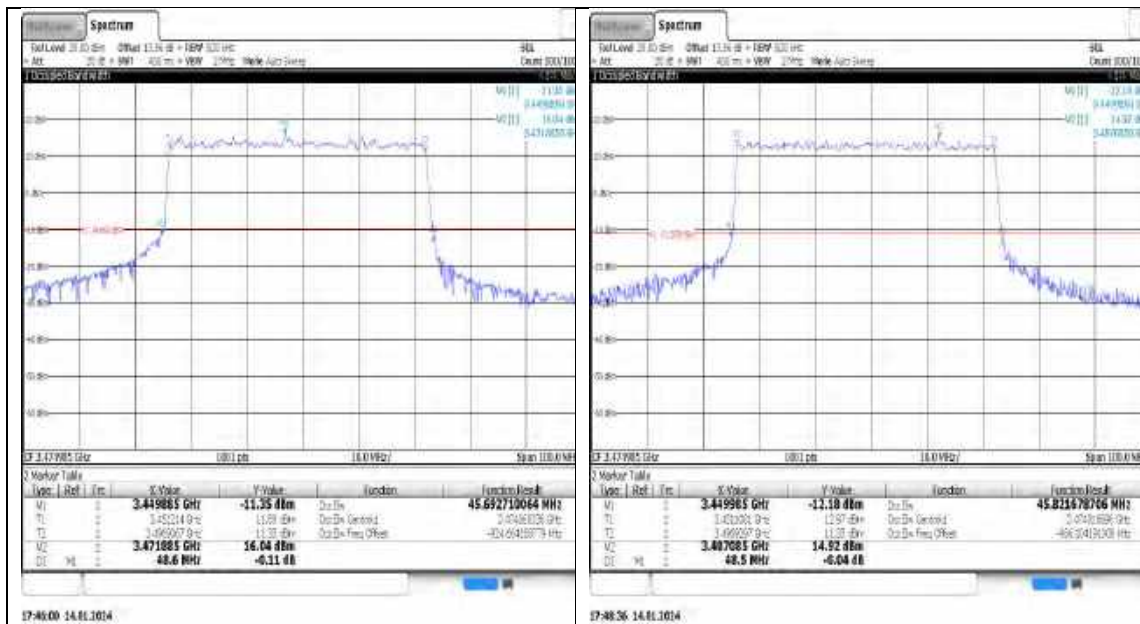
NTNV_N77-3450-
3550_PC3_30_40_M_TID2_Ant1#1

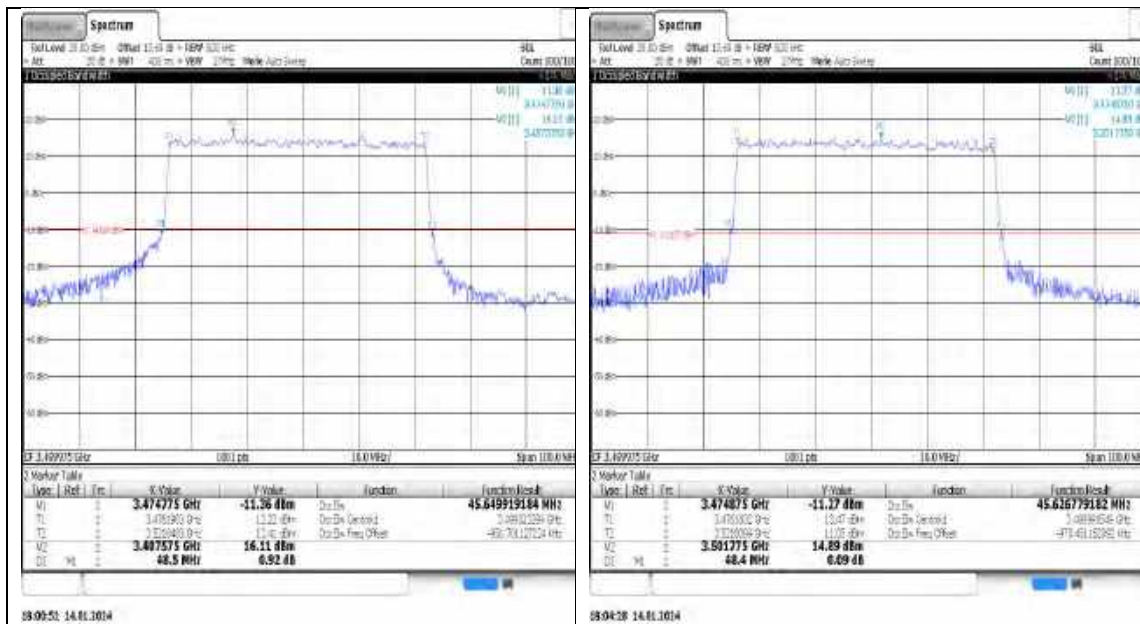


NTNV_N77-3450-
3550_PC3_30_40_M_TID3_Ant1#1

NTNV_N77-3450-
3550_PC3_30_40_M_TID4_Ant1#1

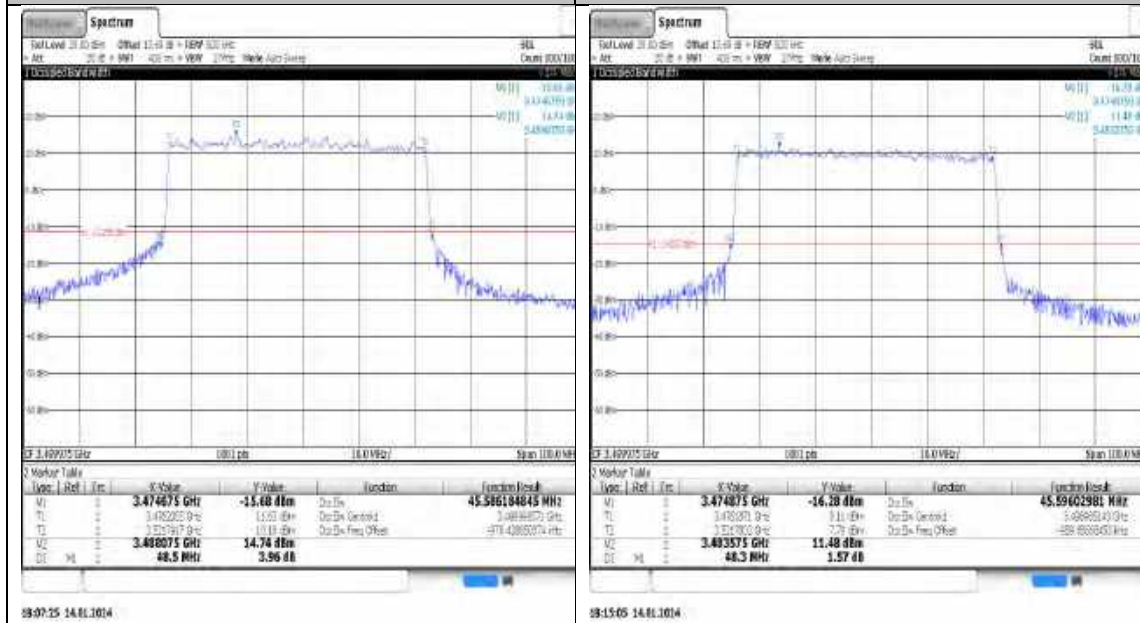






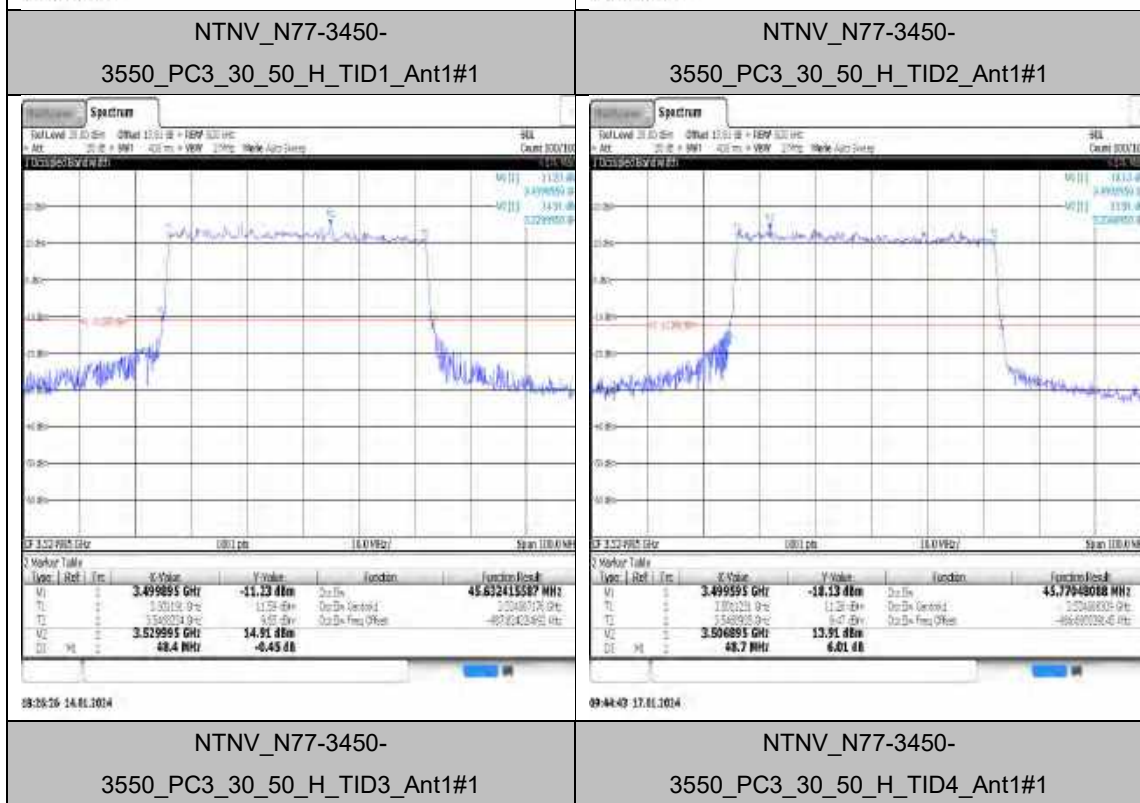
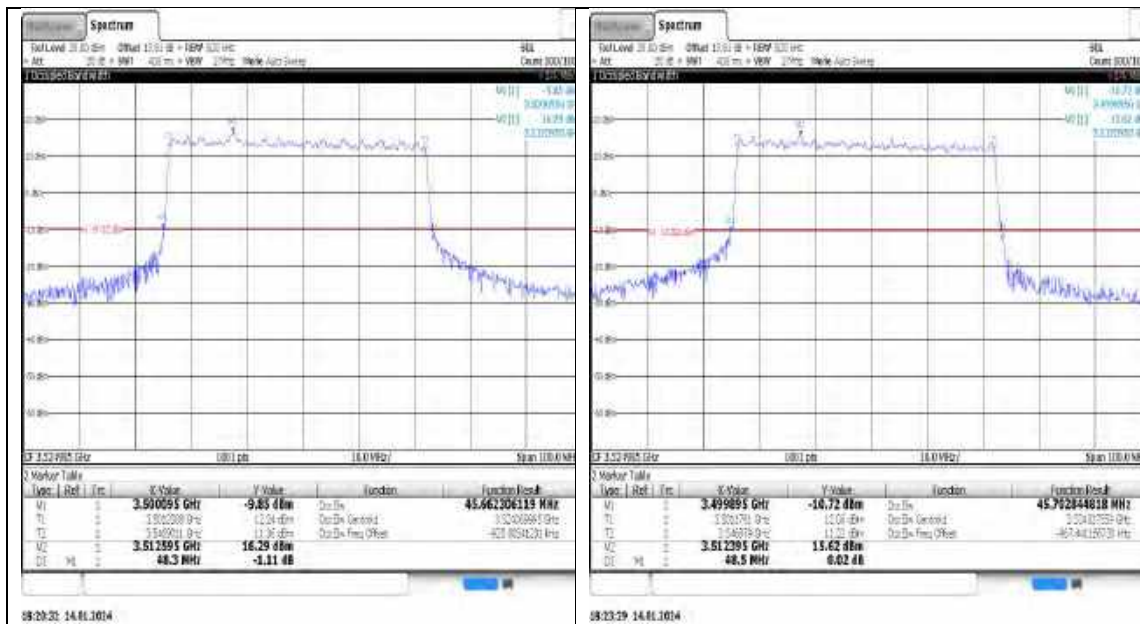
NTNV_N77-3450-
3550_PC3_30_50_M_TID1_Ant1#1

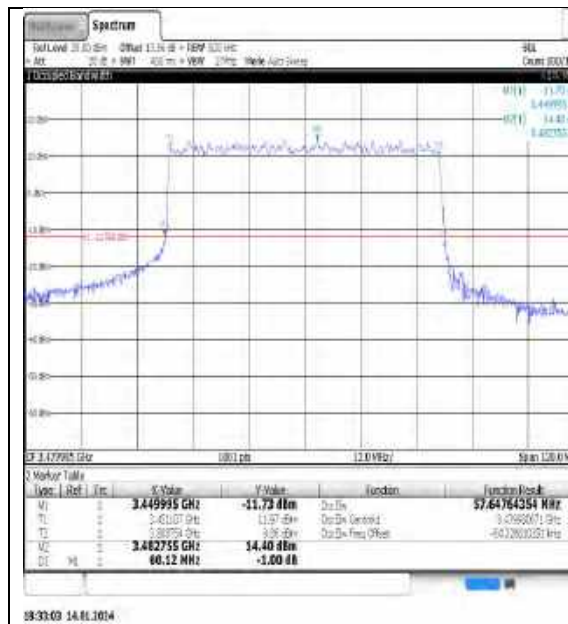
NTNV_N77-3450-
3550_PC3_30_50_M_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_50_M_TID3_Ant1#1

NTNV_N77-3450-
3550_PC3_30_50_M_TID4_Ant1#1

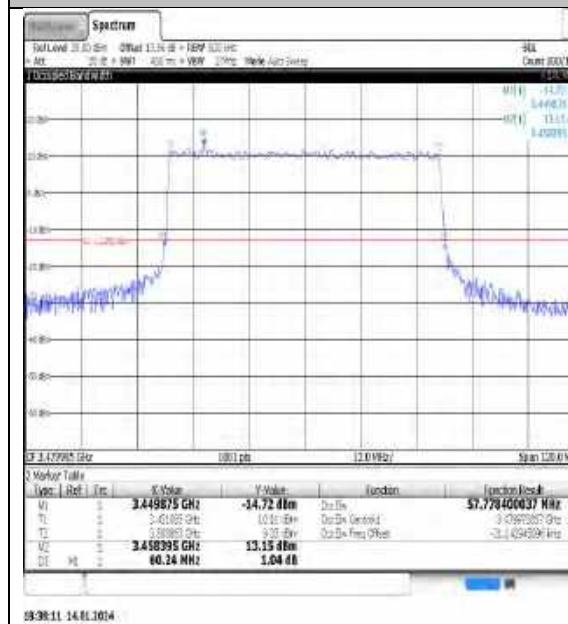




NTNV_N77-3450-
3550_PC3_30_60_L_TID1_Ant1#1



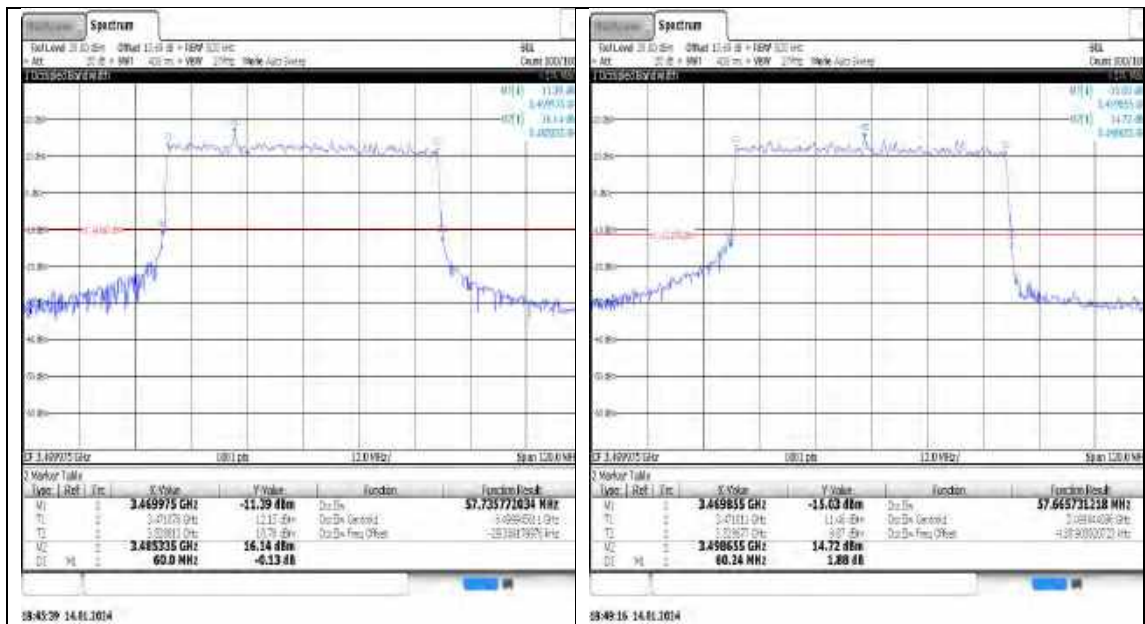
NTNV_N77-3450-
3550_PC3_30_60_L_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_60_L_TID3_Ant1#1

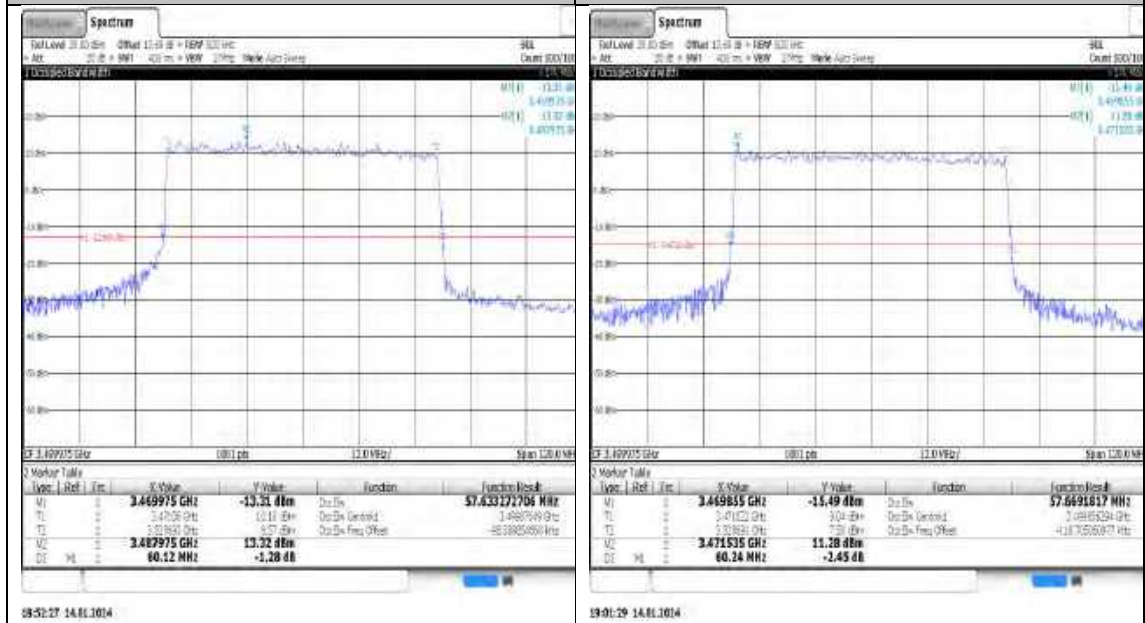


NTNV_N77-3450-
3550_PC3_30_60_L_TID4_Ant1#1



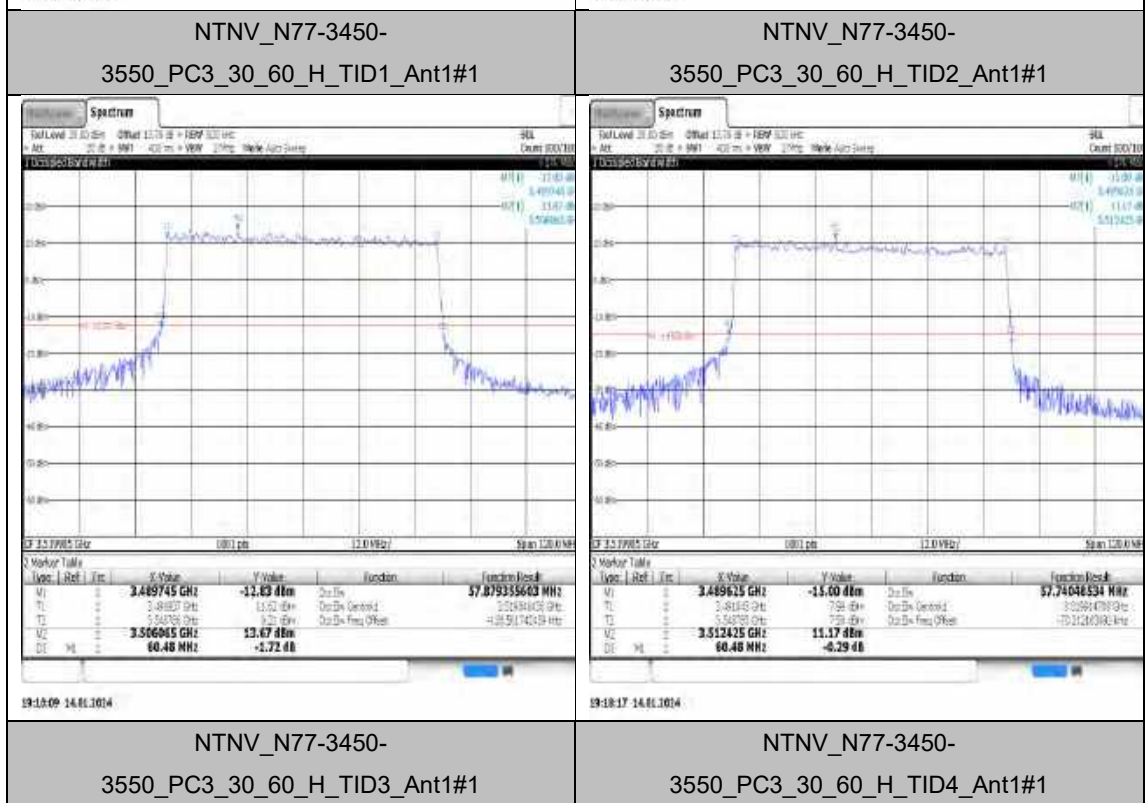
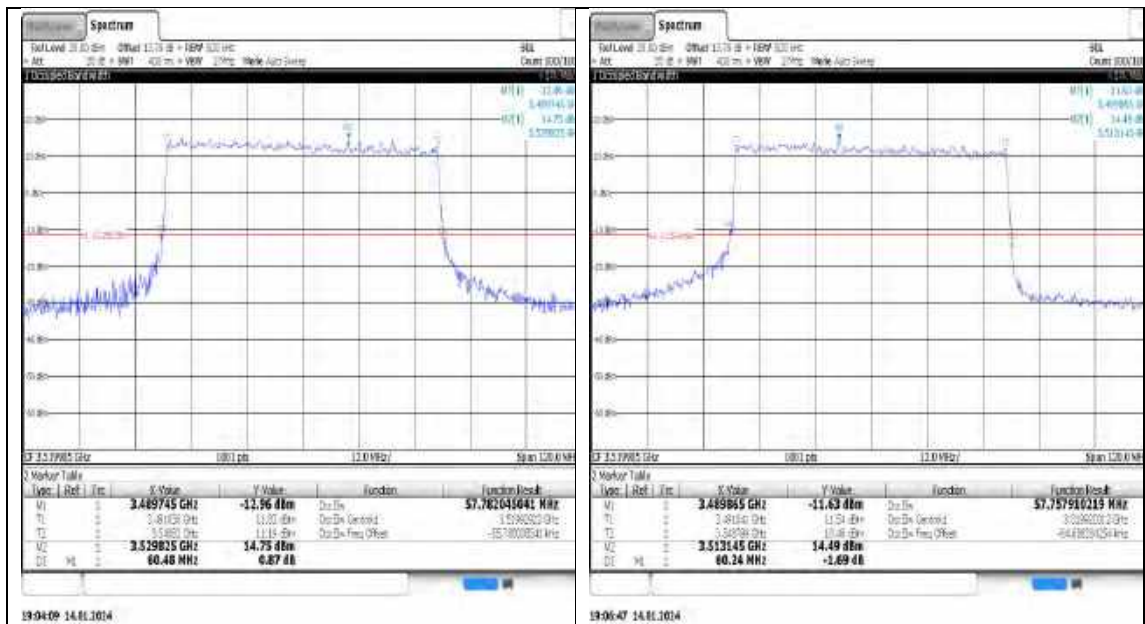
NTNV_N77-3450-
3550_PC3_30_60_M_TID1_Ant1#1

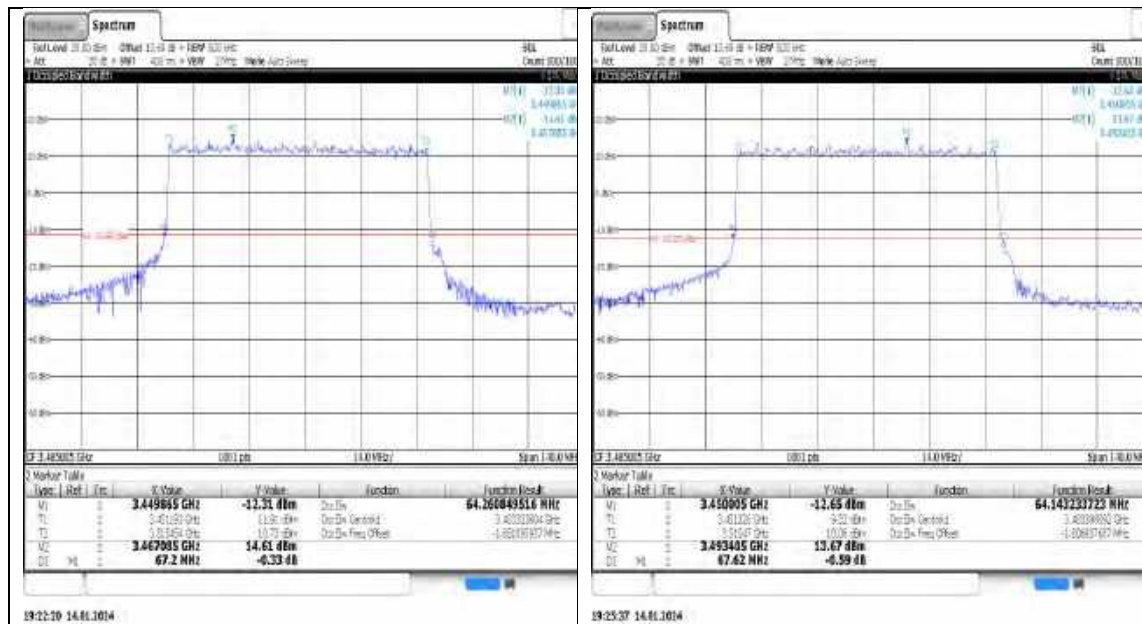
NTNV_N77-3450-
3550_PC3_30_60_M_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_60_M_TID3_Ant1#1

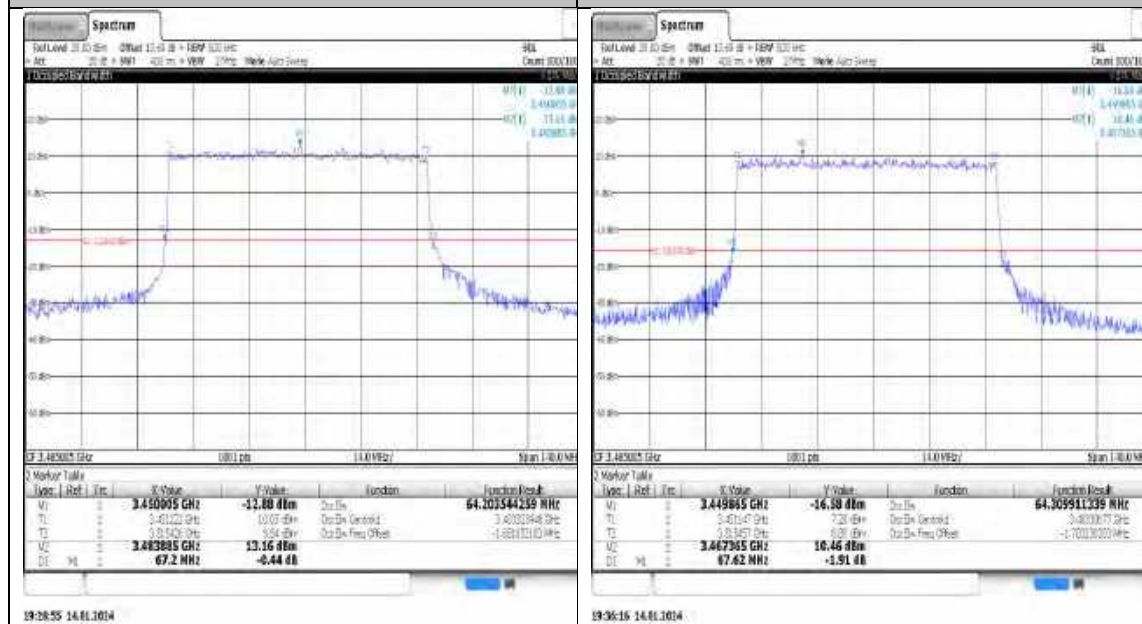
NTNV_N77-3450-
3550_PC3_30_60_M_TID4_Ant1#1





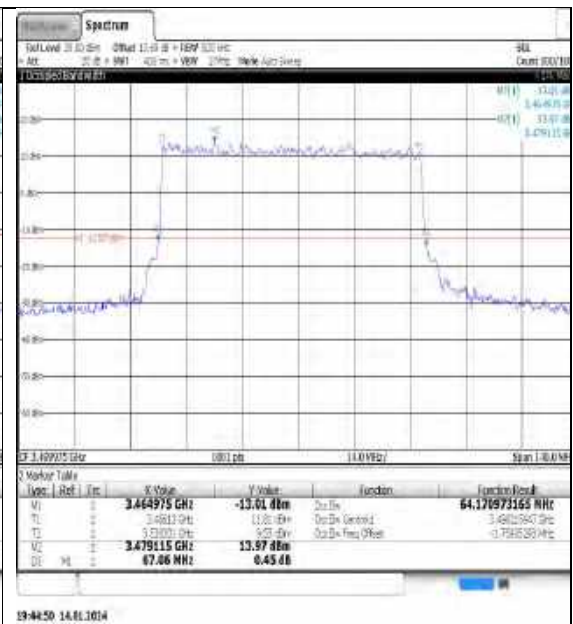
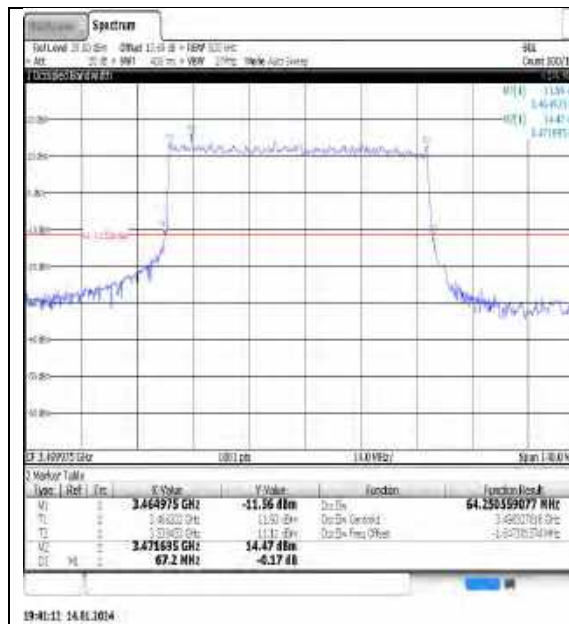
NTNV_N77-3450-
3550_PC3_30_70_L_TID1_Ant1#1

NTNV_N77-3450-
3550_PC3_30_70_L_TID2_Ant1#1



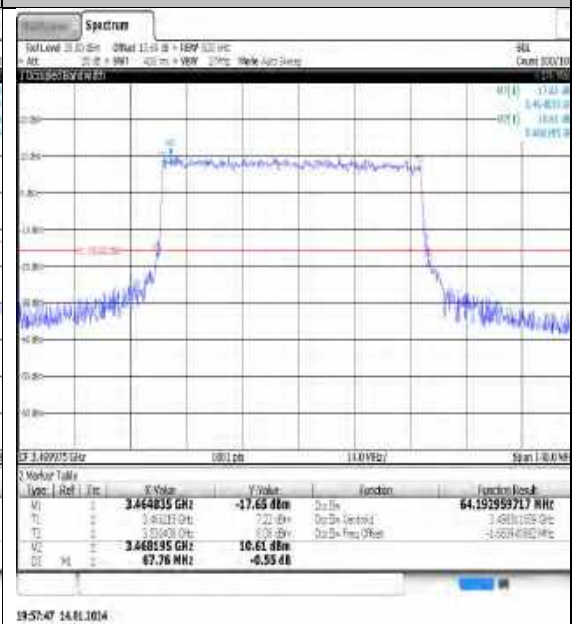
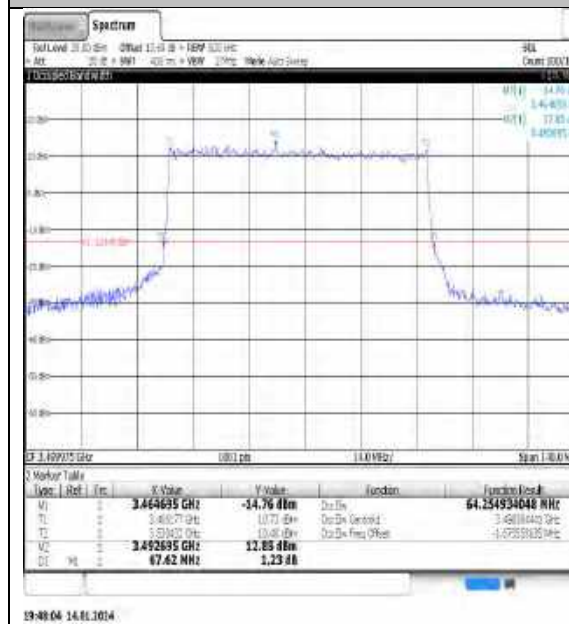
NTNV_N77-3450-
3550_PC3_30_70_L_TID3_Ant1#1

NTNV_N77-3450-
3550_PC3_30_70_L_TID4_Ant1#1



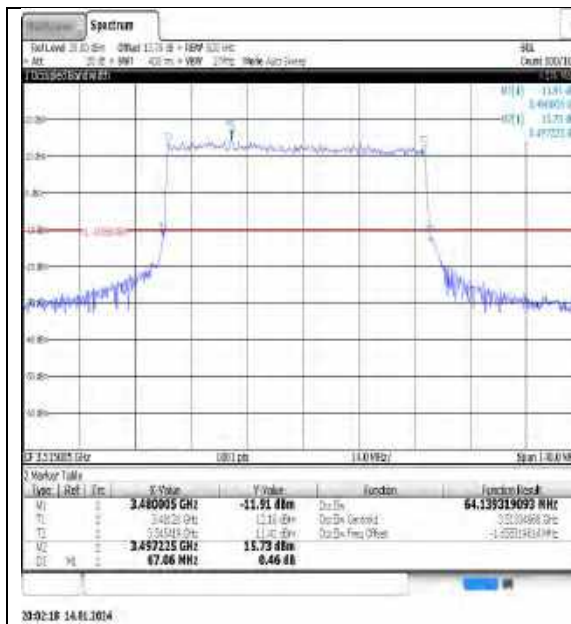
NTNV_N77-3450-
3550_PC3_30_70_M_TID1_Ant1#1

NTNV_N77-3450-
3550_PC3_30_70_M_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_70_M_TID3_Ant1#1

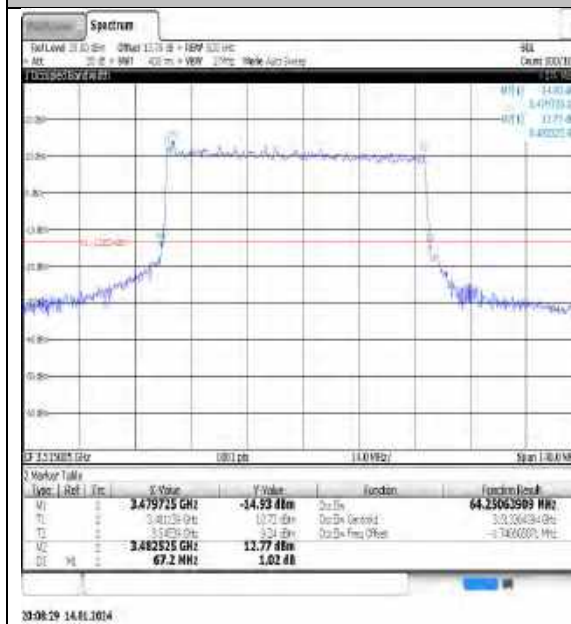
NTNV_N77-3450-
3550_PC3_30_70_M_TID4_Ant1#1



NTNV_N77-3450-
3550_PC3_30_70_H_TID1_Ant1#1



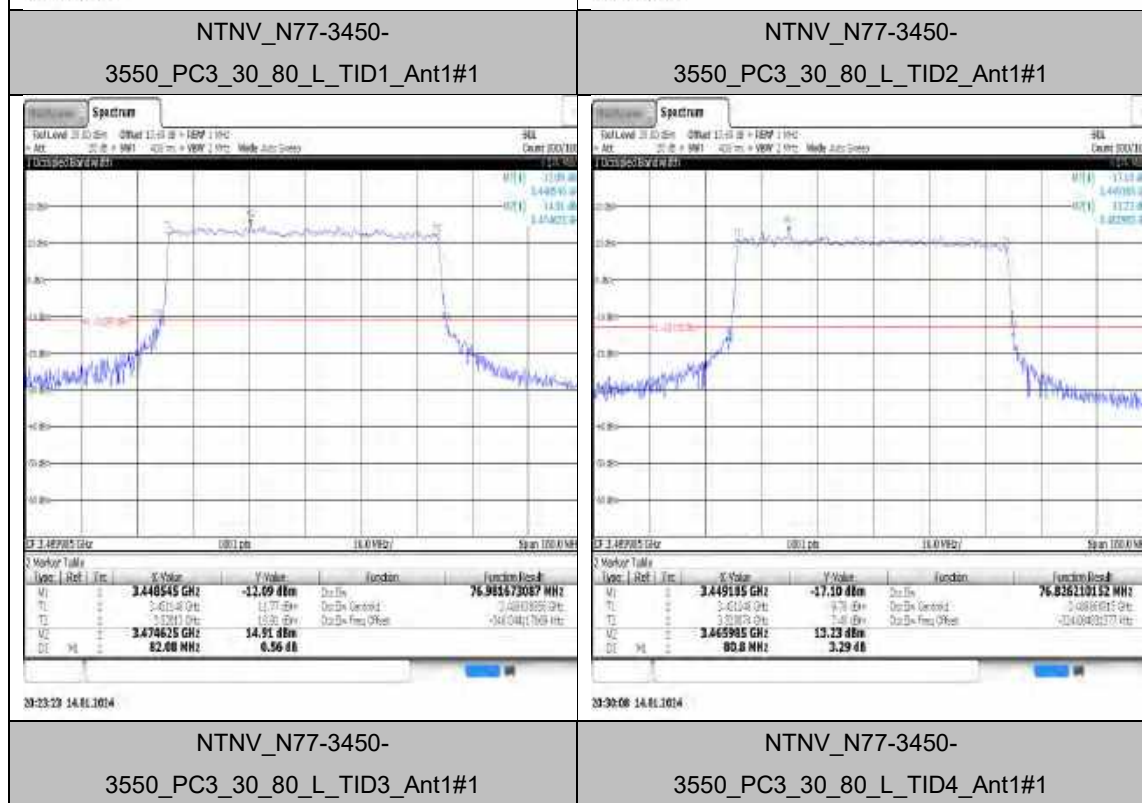
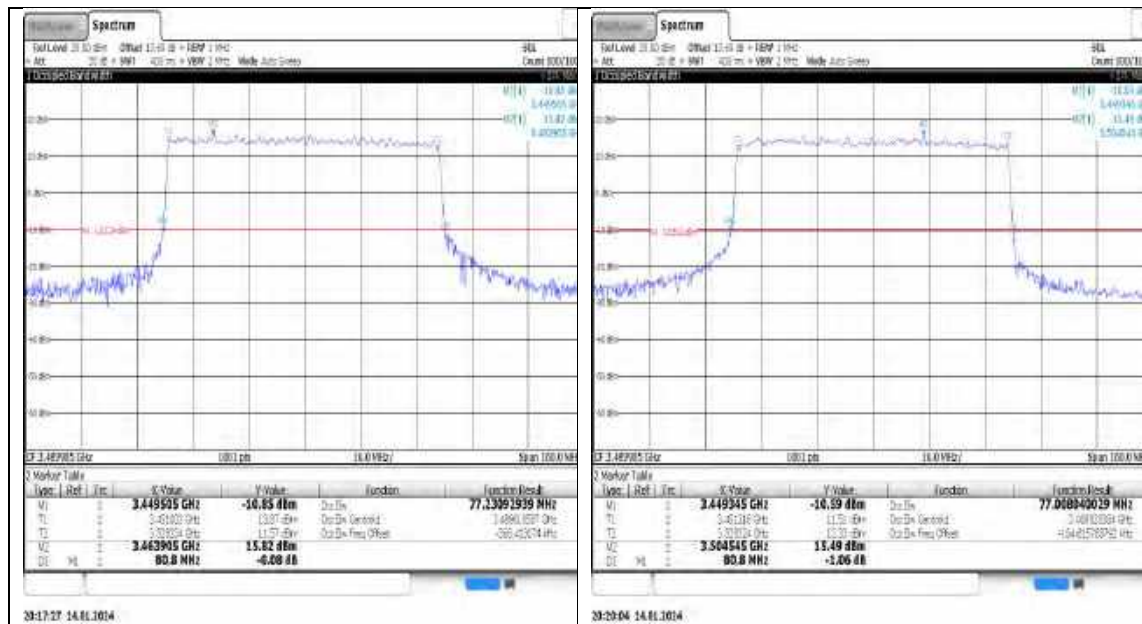
NTNV_N77-3450-
3550_PC3_30_70_H_TID2_Ant1#1

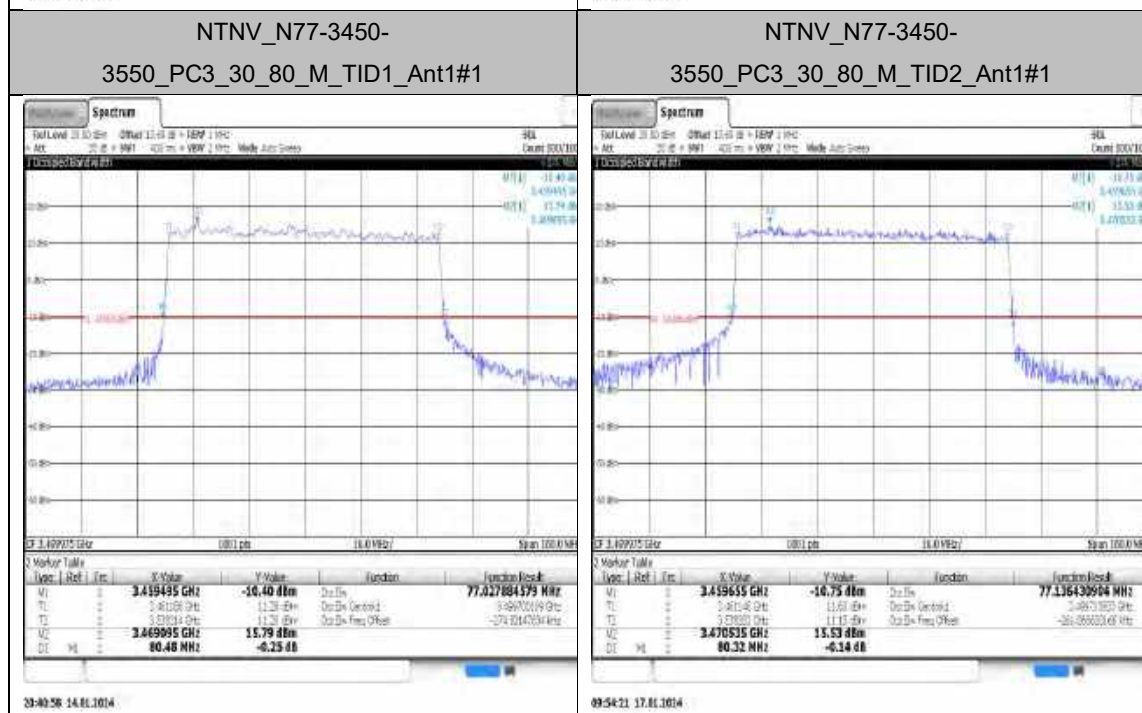
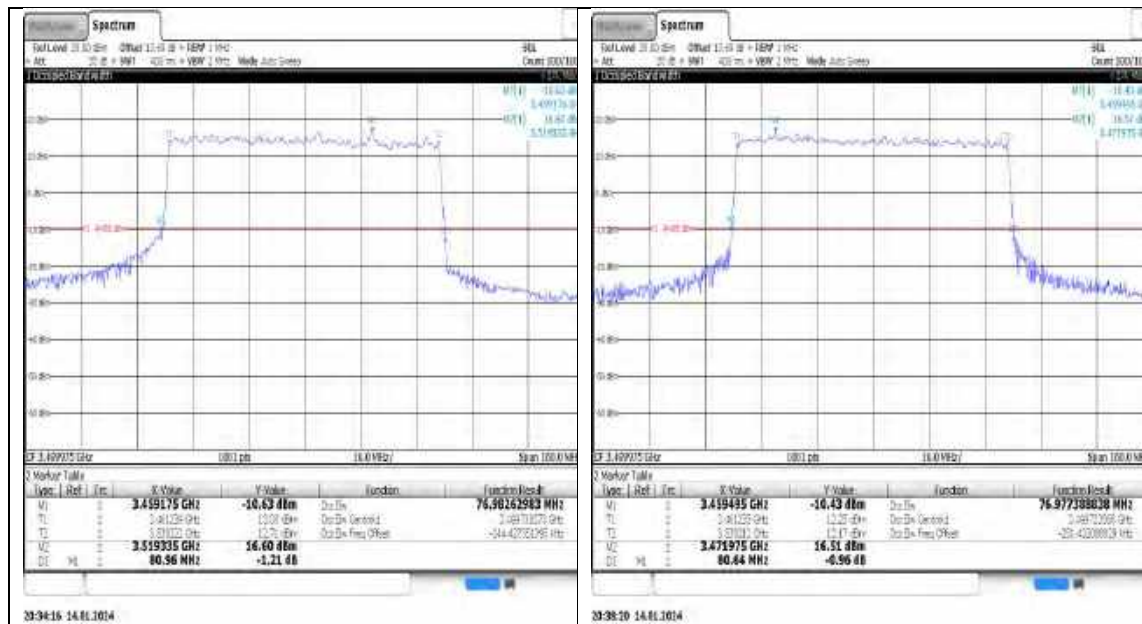


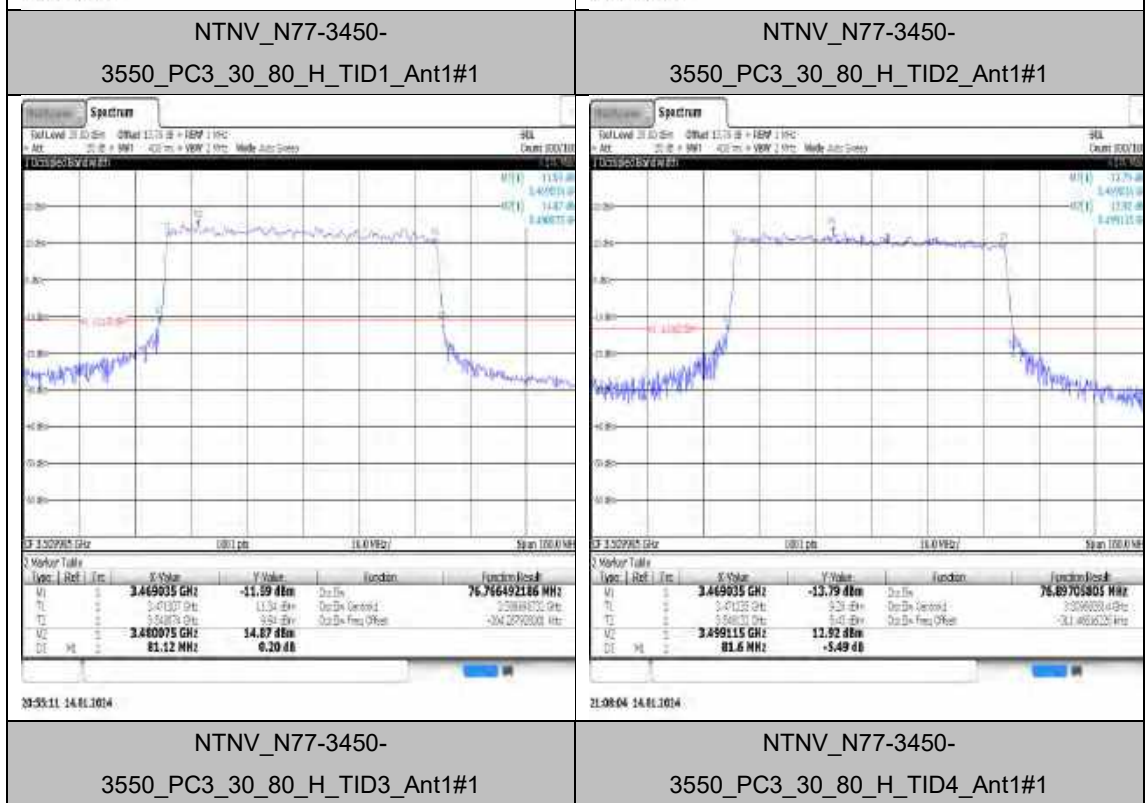
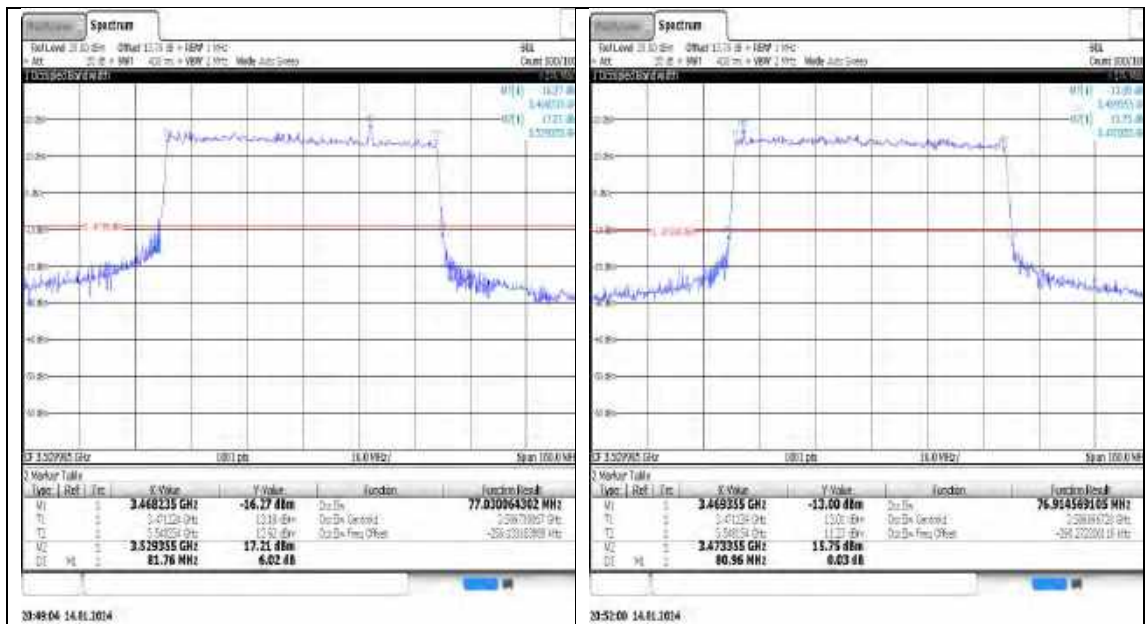
NTNV_N77-3450-
3550_PC3_30_70_H_TID3_Ant1#1

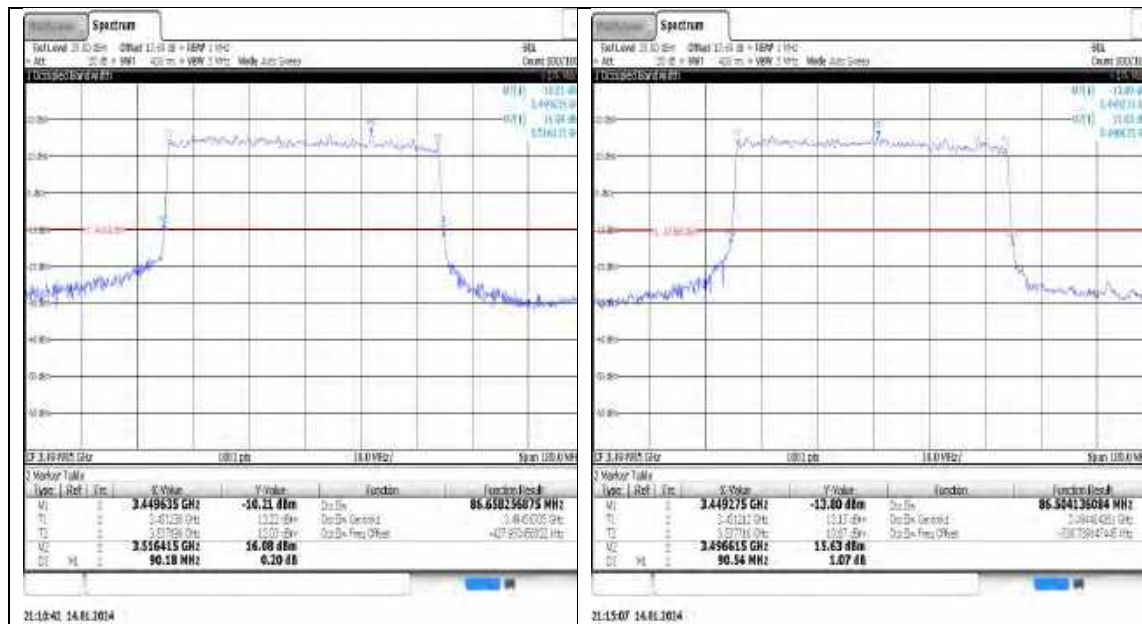


NTNV_N77-3450-
3550_PC3_30_70_H_TID4_Ant1#1



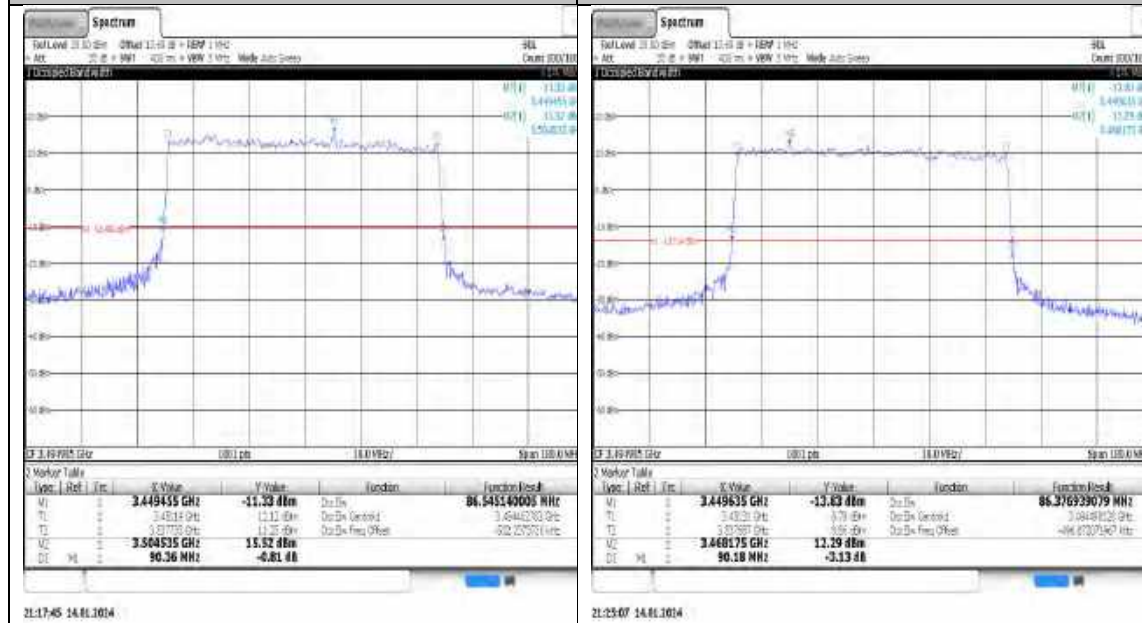






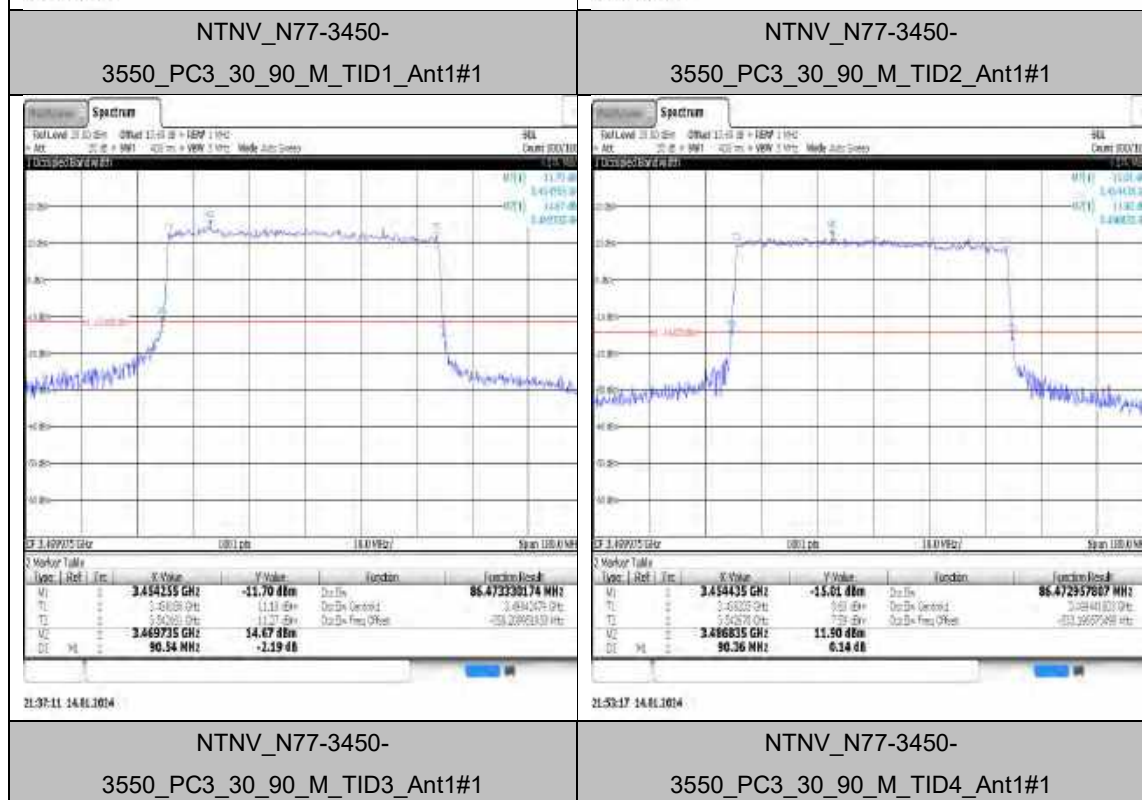
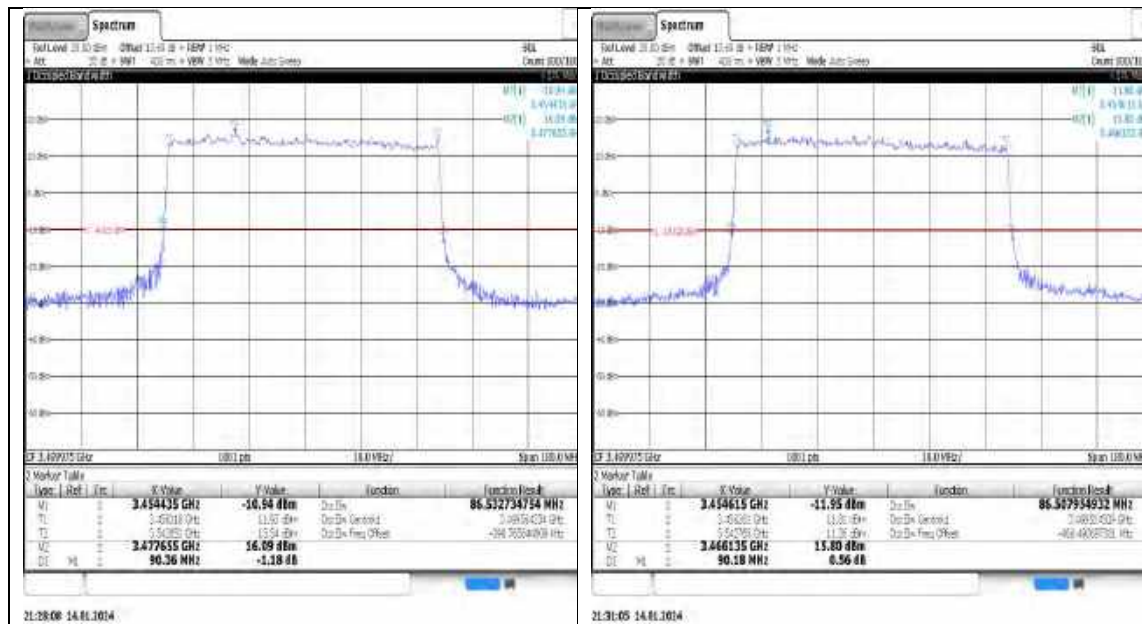
NTNV_N77-3450-
3550_PC3_30_90_L_TID1_Ant1#1

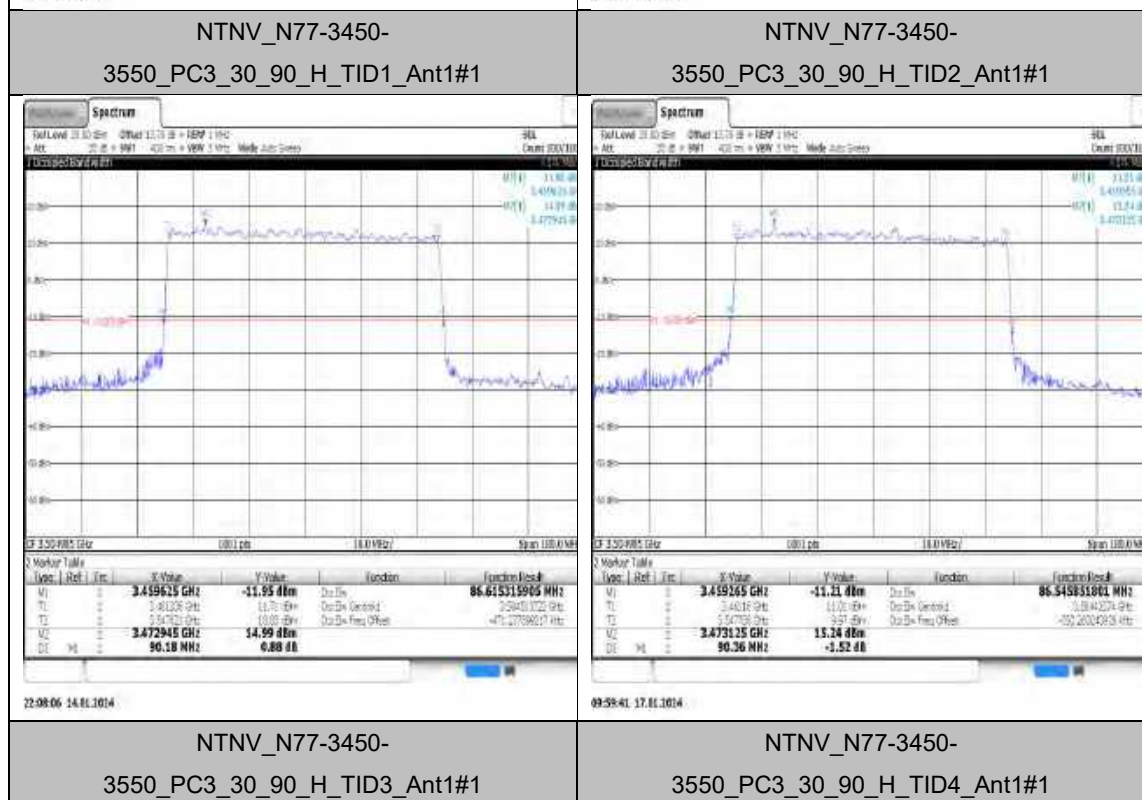
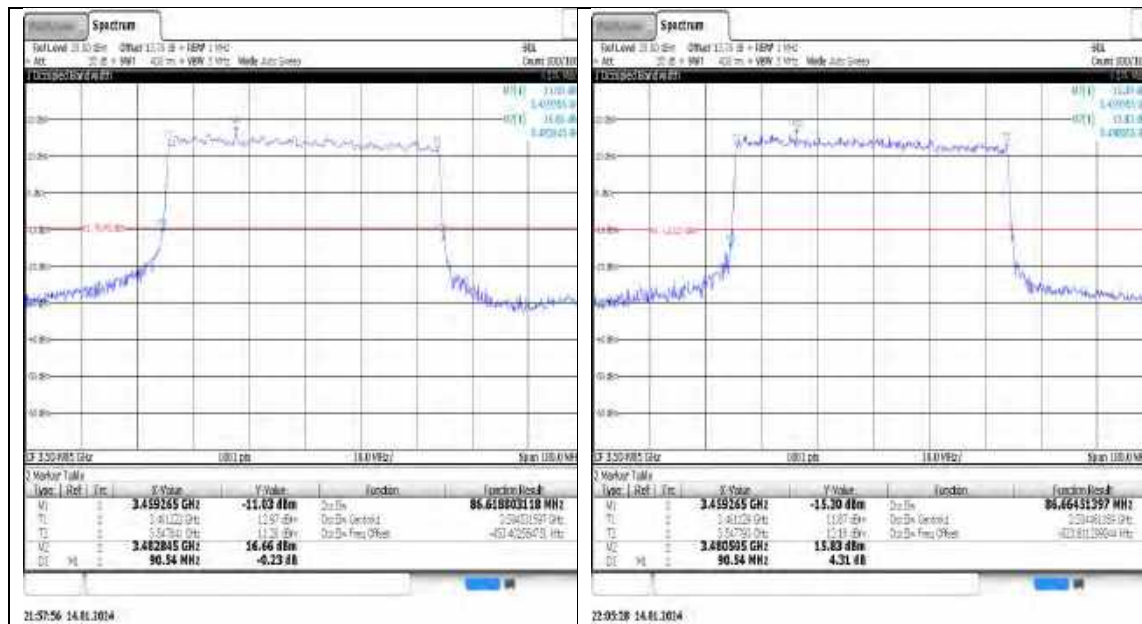
NTNV_N77-3450-
3550_PC3_30_90_L_TID2_Ant1#1

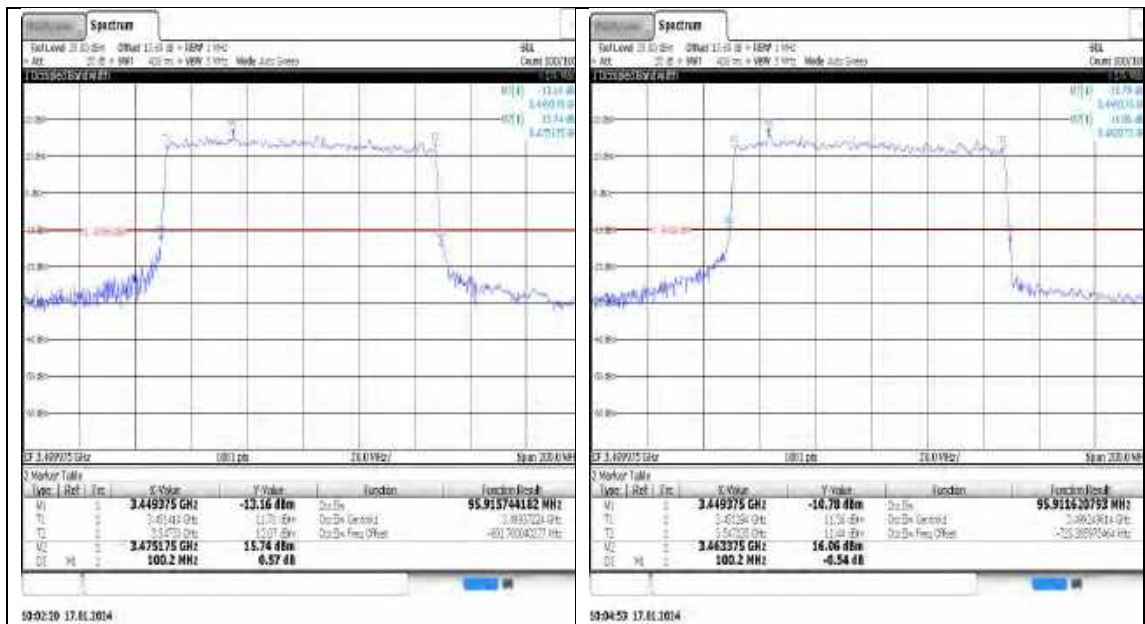


NTNV_N77-3450-
3550_PC3_30_90_L_TID3_Ant1#1

NTNV_N77-3450-
3550_PC3_30_90_L_TID4_Ant1#1

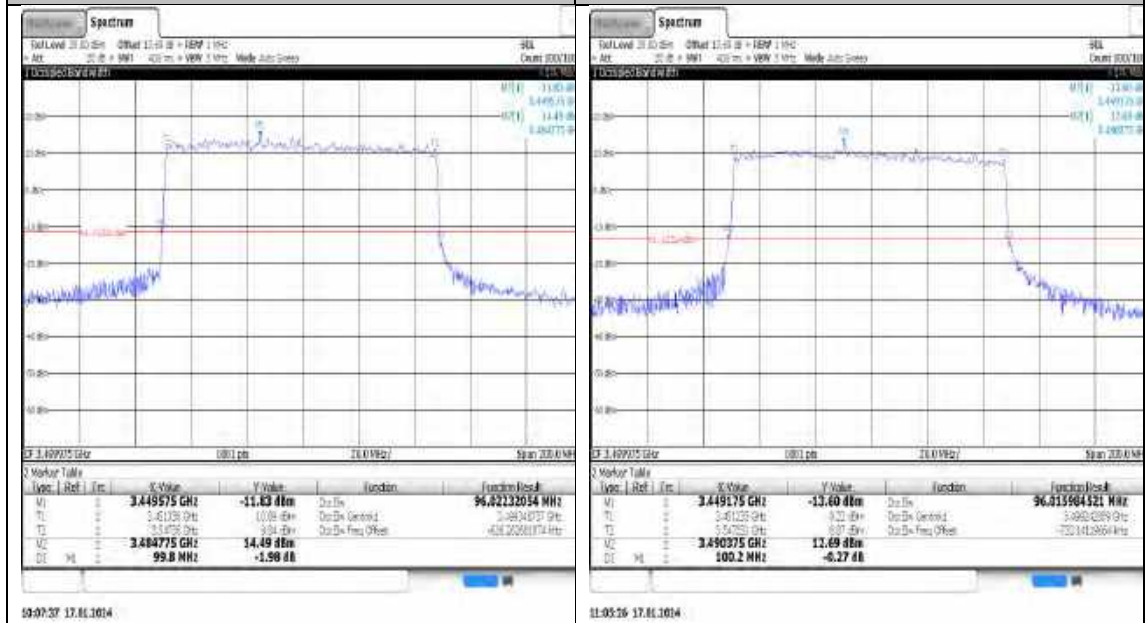






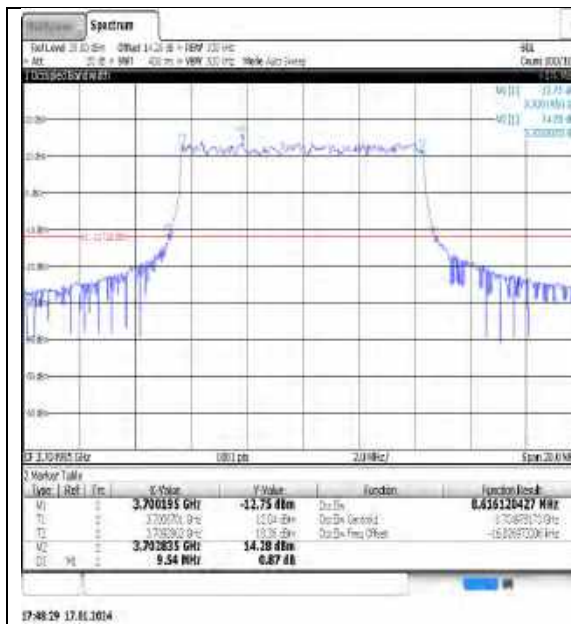
NTNV_N77-3450-
3550_PC3_30_100_M_TID1_Ant1#1

NTNV_N77-3450-
3550_PC3_30_100_M_TID2_Ant1#1



NTNV_N77-3450-
3550_PC3_30_100_M_TID3_Ant1#1

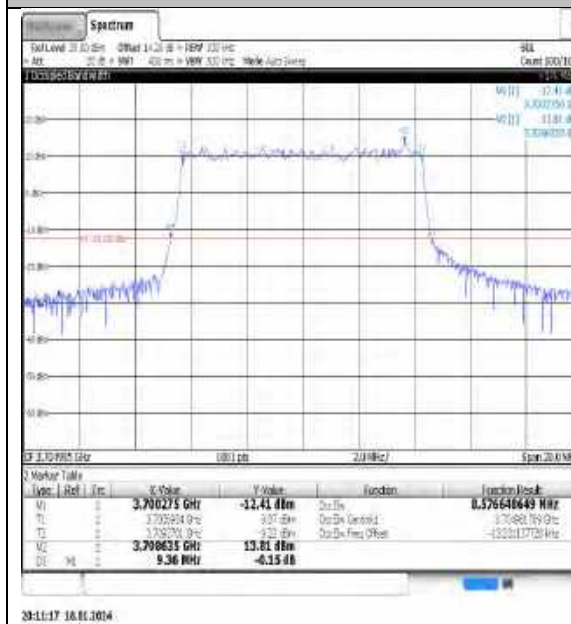
NTNV_N77-3450-
3550_PC3_30_100_M_TID4_Ant1#1



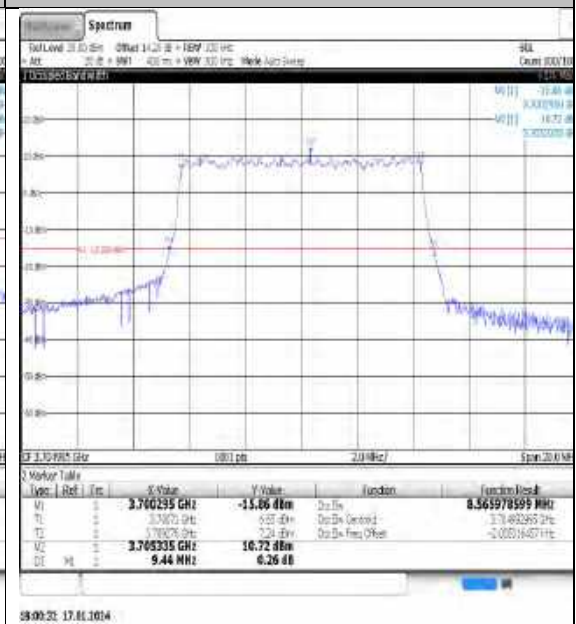
NTNV_N77-3700-
3980_PC3_30_10_L_TID1_Ant1#1



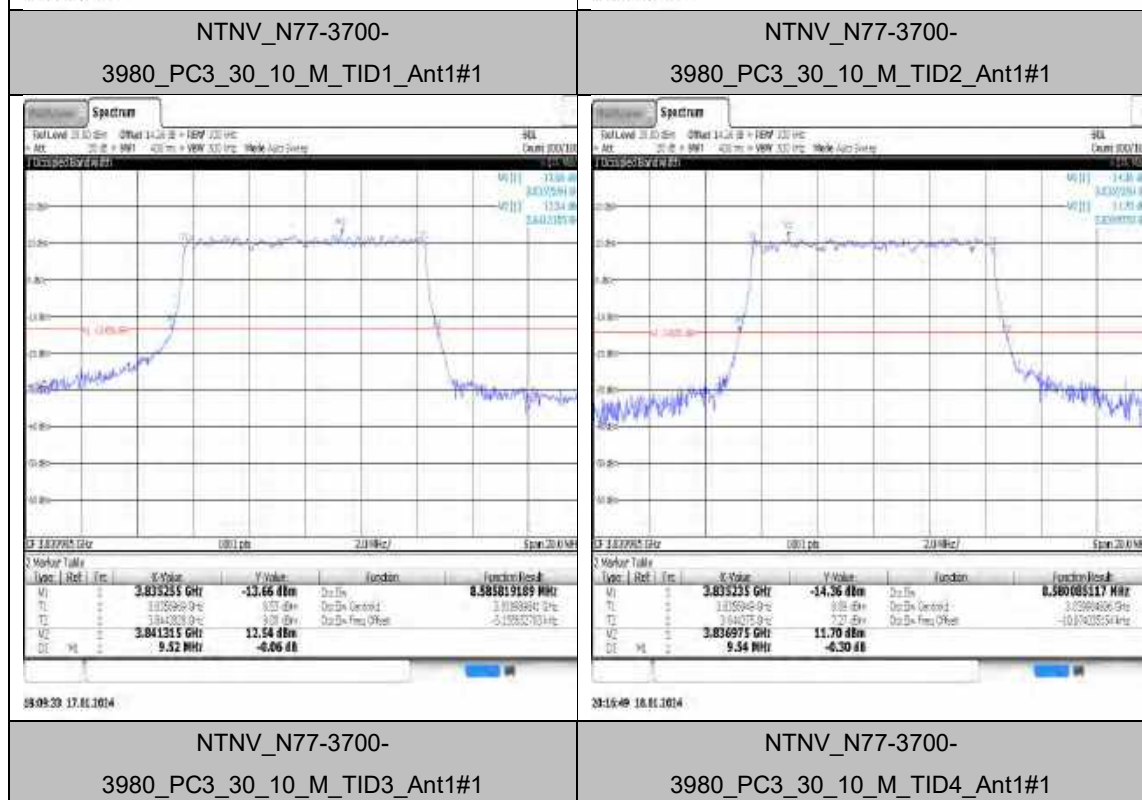
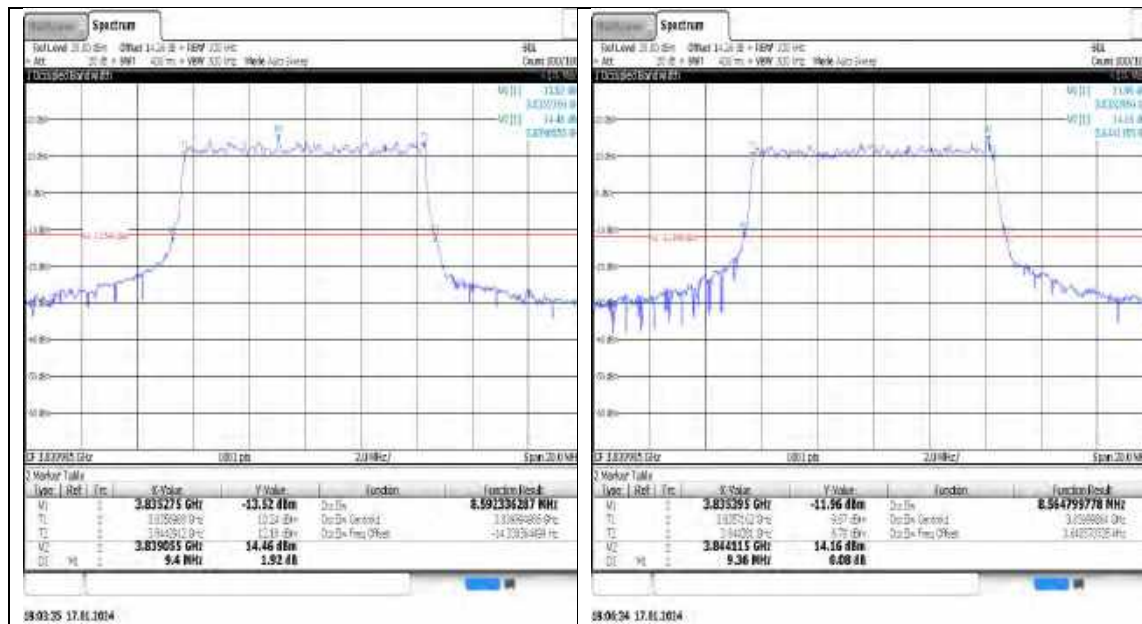
NTNV_N77-3700-
3980_PC3_30_10_L_TID2_Ant1#1

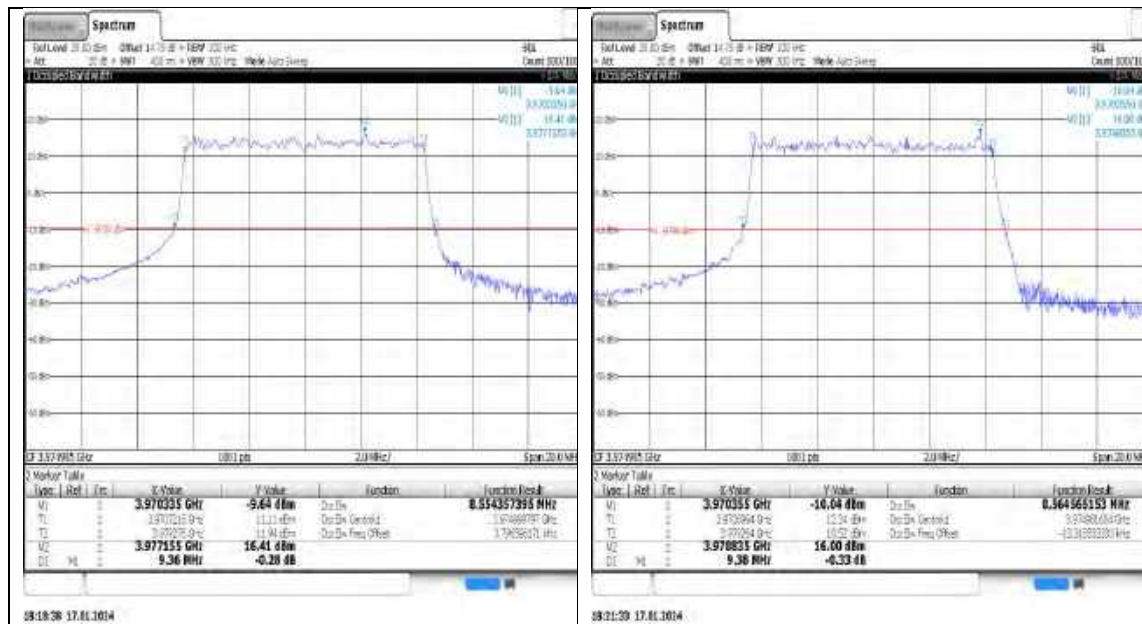


NTNV_N77-3700-
3980_PC3_30_10_L_TID3_Ant1#1



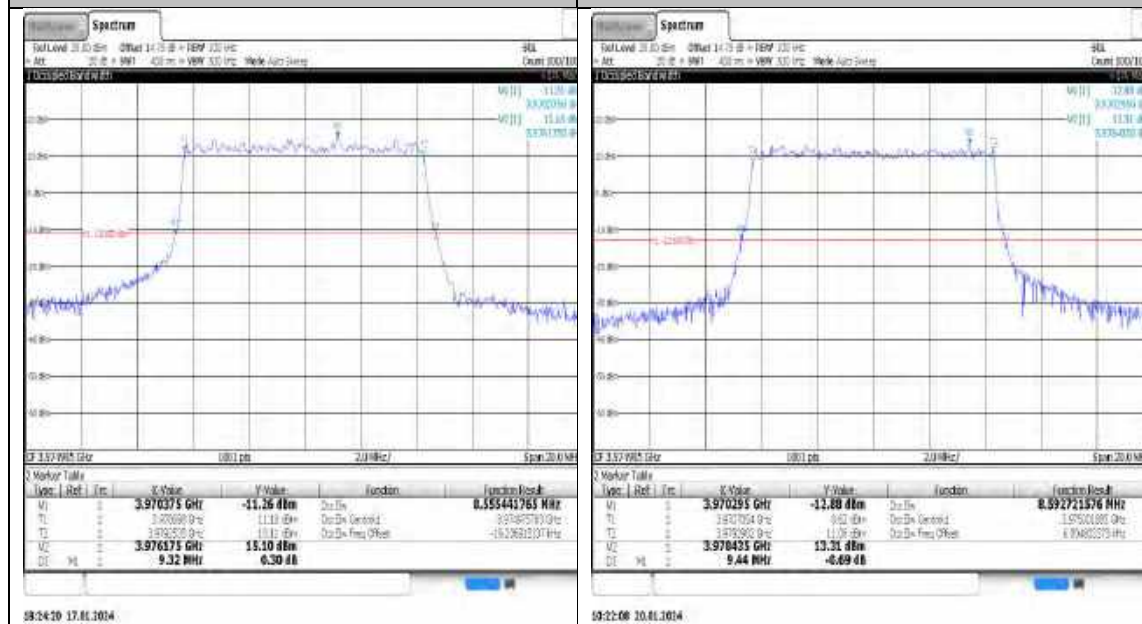
NTNV_N77-3700-
3980_PC3_30_10_L_TID4_Ant1#1





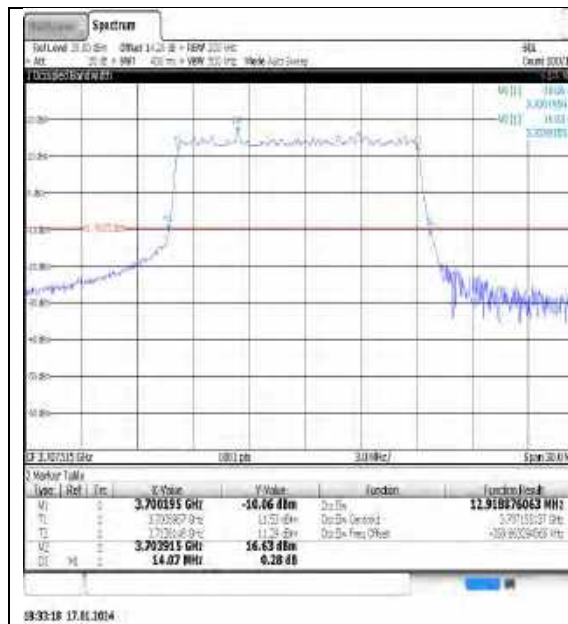
NTNV_N77-3700-
3980_PC3_30_10_H_TID1_Ant1#1

NTNV_N77-3700-
3980_PC3_30_10_H_TID2_Ant1#1



NTNV_N77-3700-
3980_PC3_30_10_H_TID3_Ant1#1

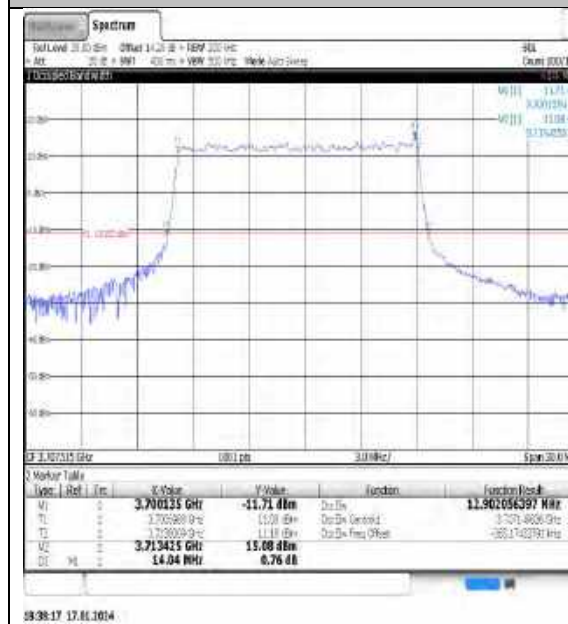
NTNV_N77-3700-
3980_PC3_30_10_H_TID4_Ant1#1



NTNV_N77-3700-
3980_PC3_30_15_L_TID1_Ant1#1



NTNV_N77-3700-
3980_PC3_30_15_L_TID2_Ant1#1



NTNV_N77-3700-
3980_PC3_30_15_L_TID3_Ant1#1



NTNV_N77-3700-
3980_PC3_30_15_L_TID4_Ant1#1



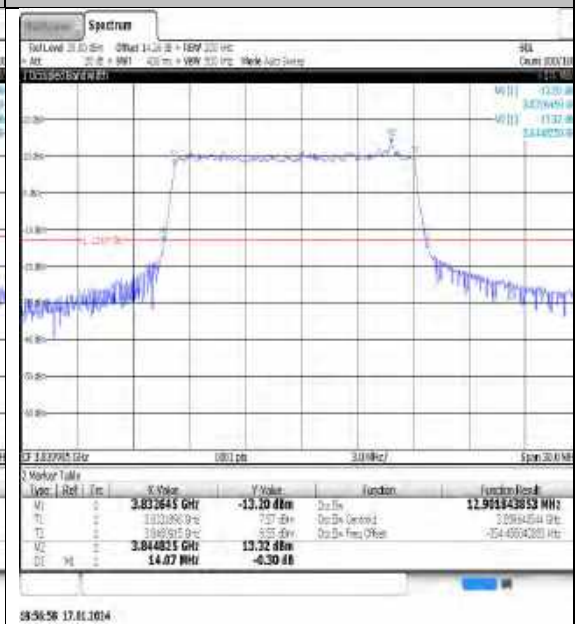
NTNV_N77-3700-
3980_PC3_30_15_M_TID1_Ant1#1



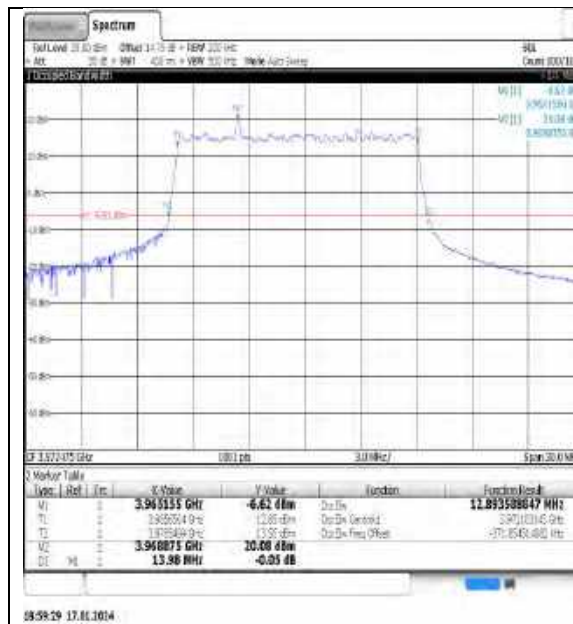
NTNV_N77-3700-
3980_PC3_30_15_M_TID2_Ant1#1



NTNV_N77-3700-
3980_PC3_30_15_M_TID3_Ant1#1



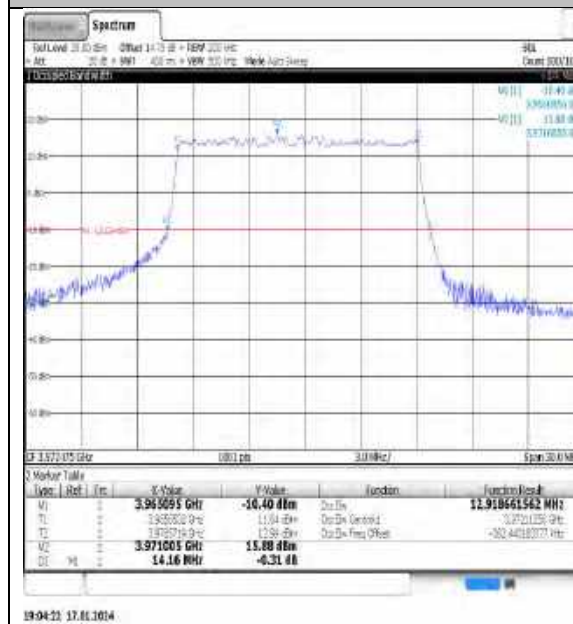
NTNV_N77-3700-
3980_PC3_30_15_M_TID4_Ant1#1



NTNV_N77-3700-
3980_PC3_30_15_H_TID1_Ant1#1



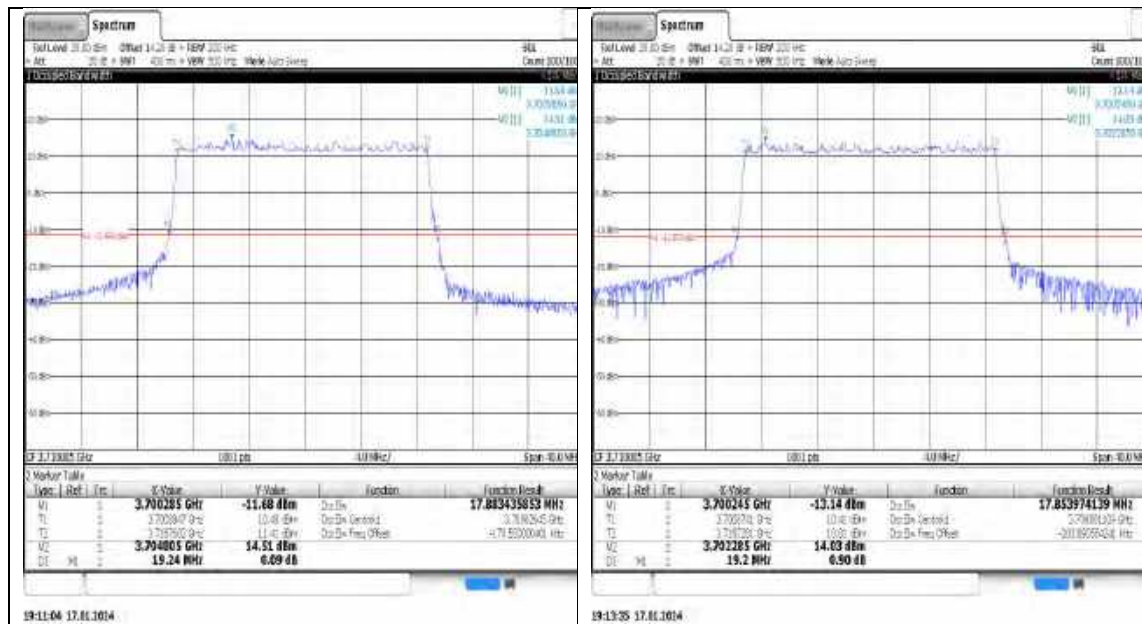
NTNV_N77-3700-
3980_PC3_30_15_H_TID2_Ant1#1



NTNV_N77-3700-
3980_PC3_30_15_H_TID3_Ant1#1

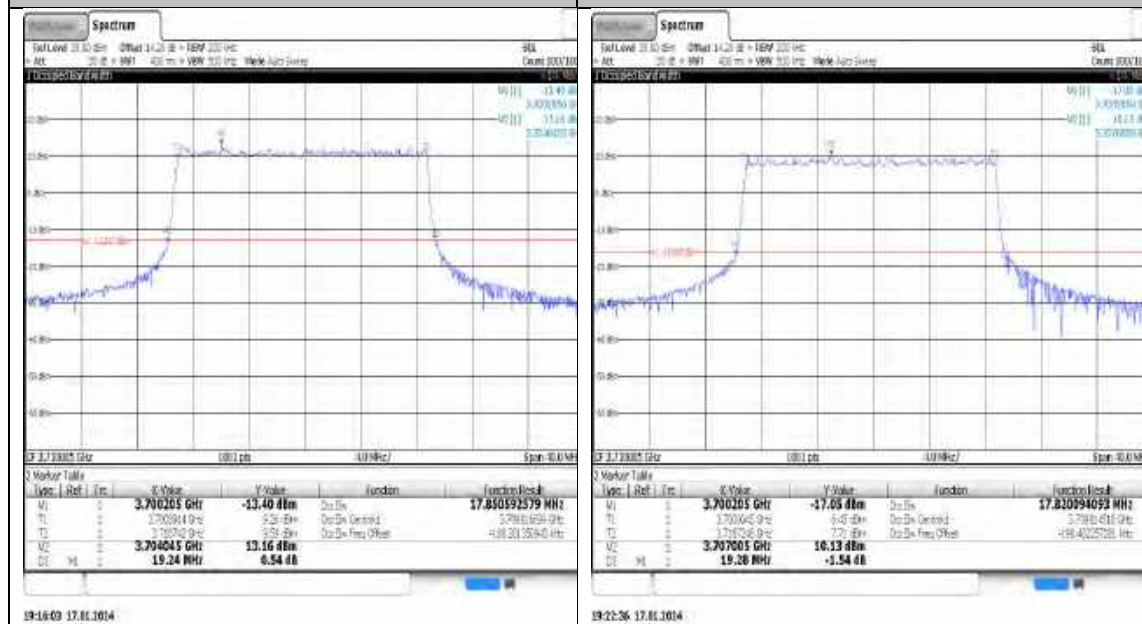


NTNV_N77-3700-
3980_PC3_30_15_H_TID4_Ant1#1



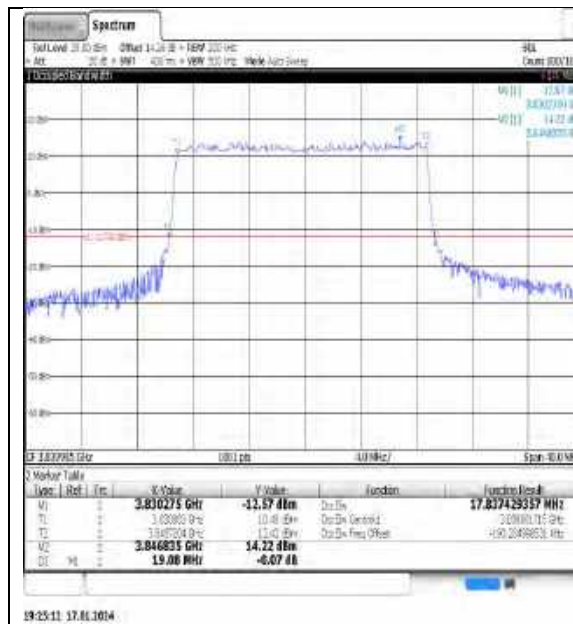
NTNV_N77-3700-
3980_PC3_30_20_L_TID1_Ant1#1

NTNV_N77-3700-
3980_PC3_30_20_L_TID2_Ant1#1



NTNV_N77-3700-
3980_PC3_30_20_L_TID3_Ant1#1

NTNV_N77-3700-
3980_PC3_30_20_L_TID4_Ant1#1



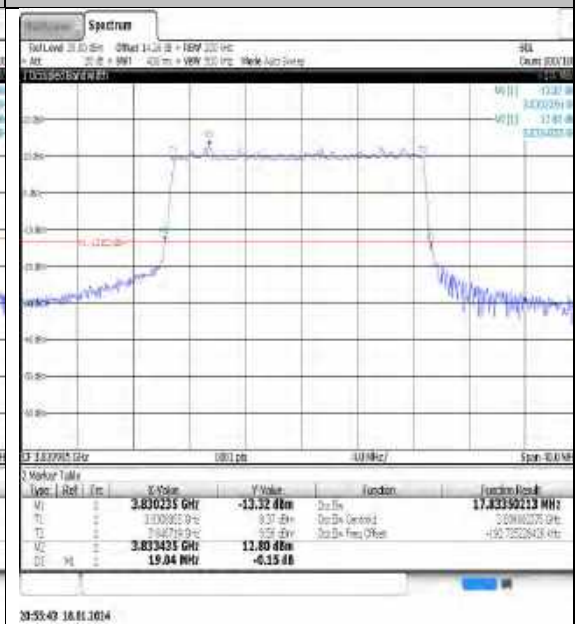
NTNV_N77-3700-
3980_PC3_30_20_M_TID1_Ant1#1



NTNV_N77-3700-
3980_PC3_30_20_M_TID2_Ant1#1



NTNV_N77-3700-
3980_PC3_30_20_M_TID3_Ant1#1



NTNV_N77-3700-
3980_PC3_30_20_M_TID4_Ant1#1