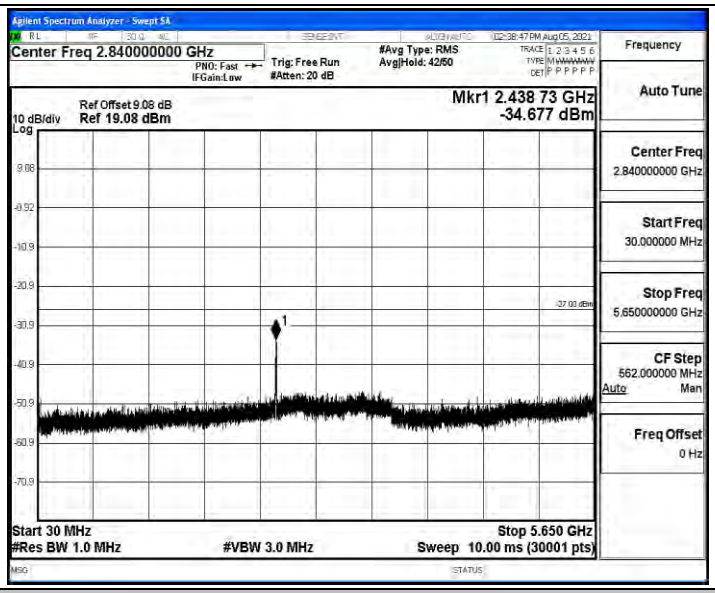
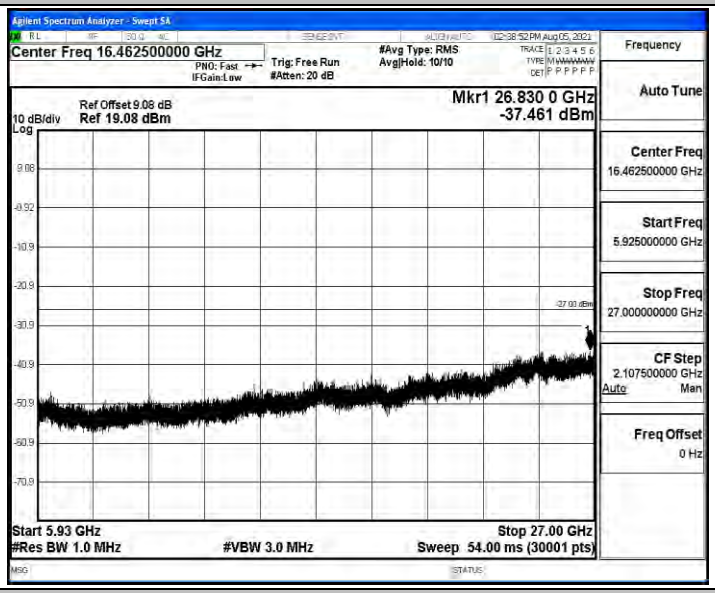
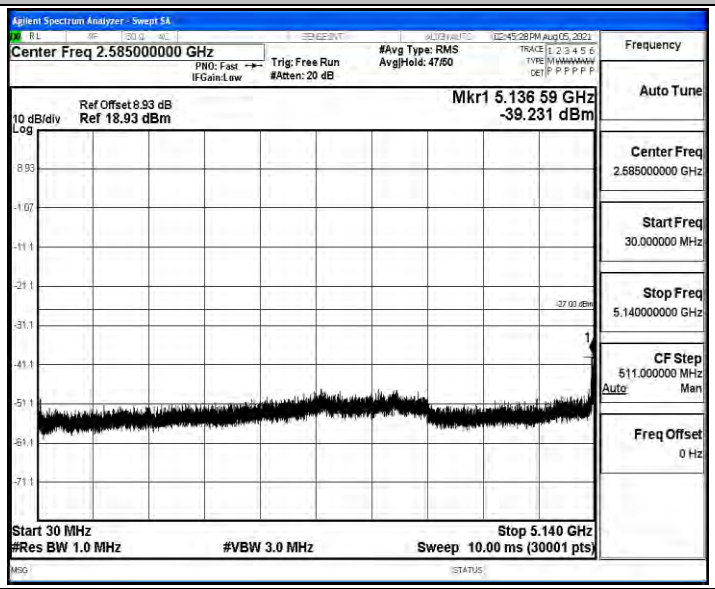


11N20SISO_ANT3_5825_5925~40000



11N40SISO_ANT3_5190_30~5140



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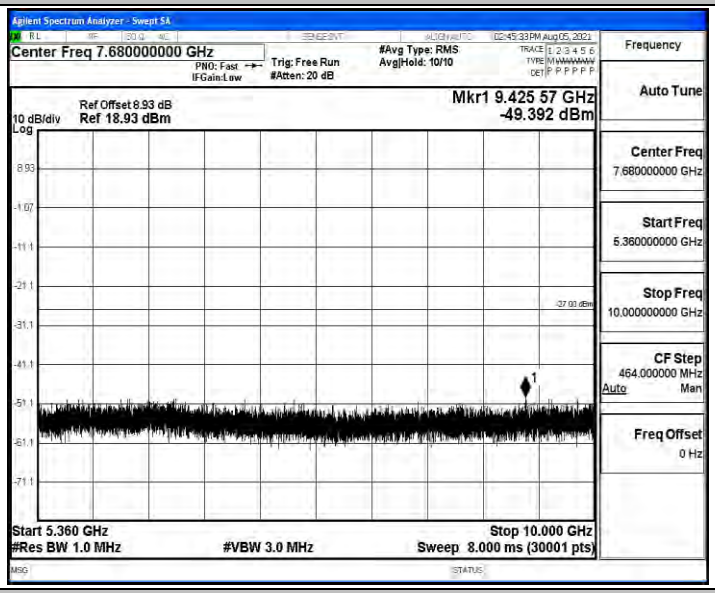

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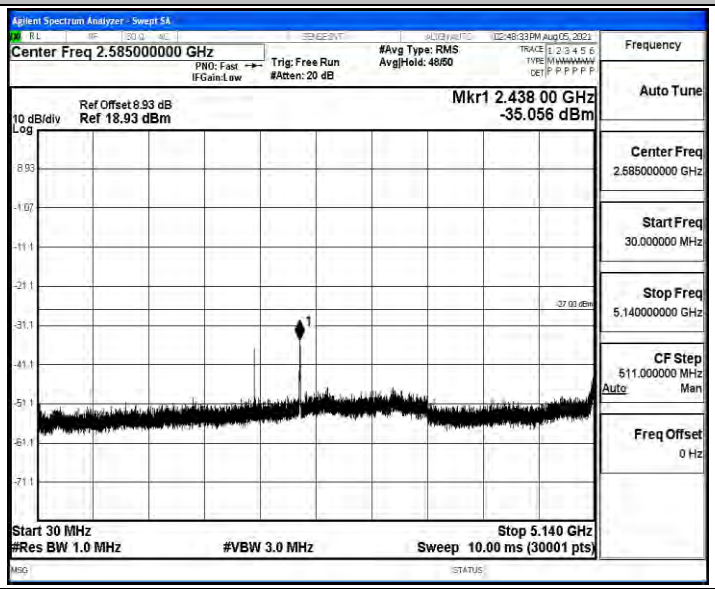

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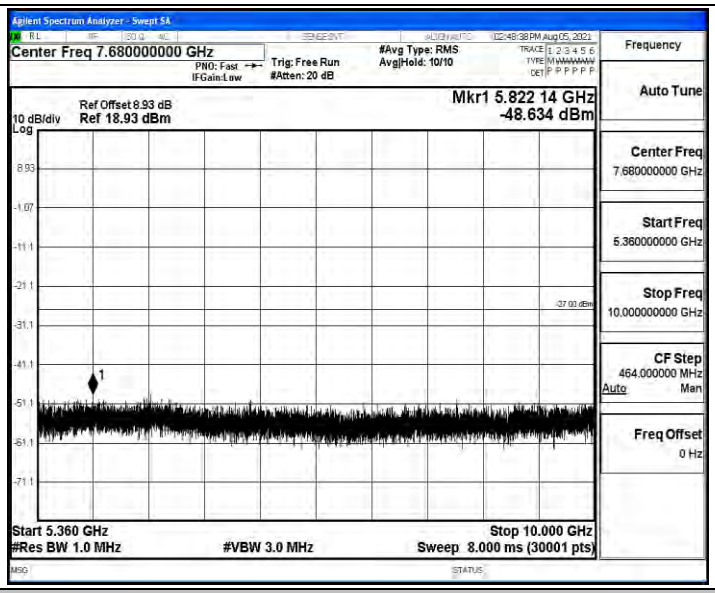
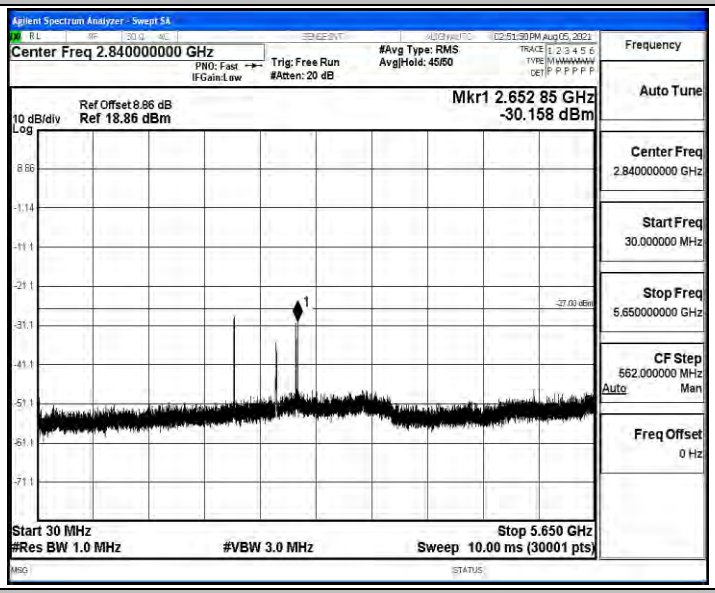
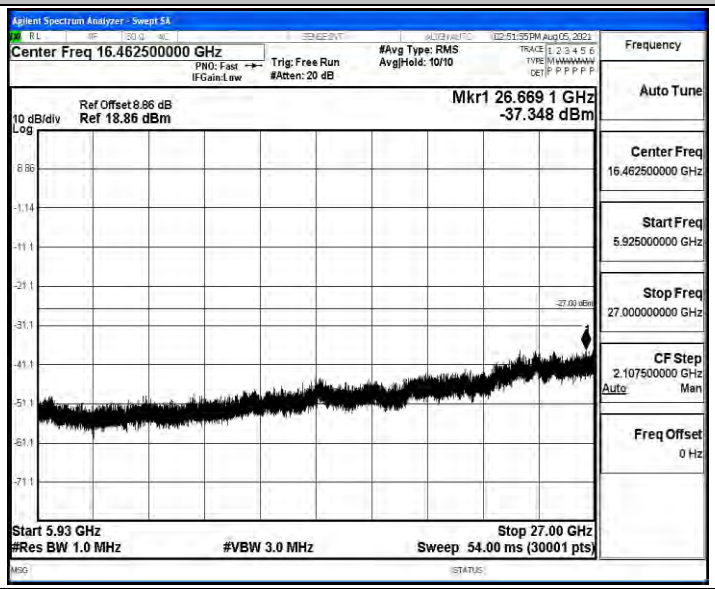


11N40SISO_ANT3_5755_30~5650



11N40SISO_ANT3_5755_5925~40000



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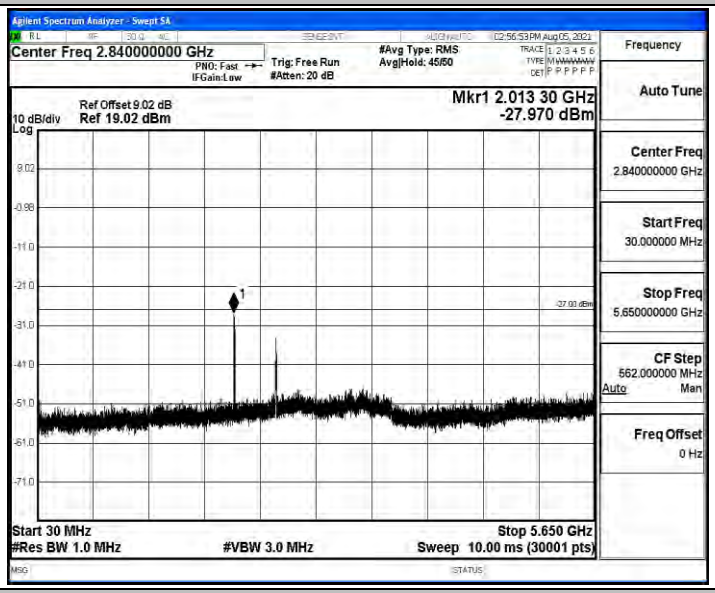
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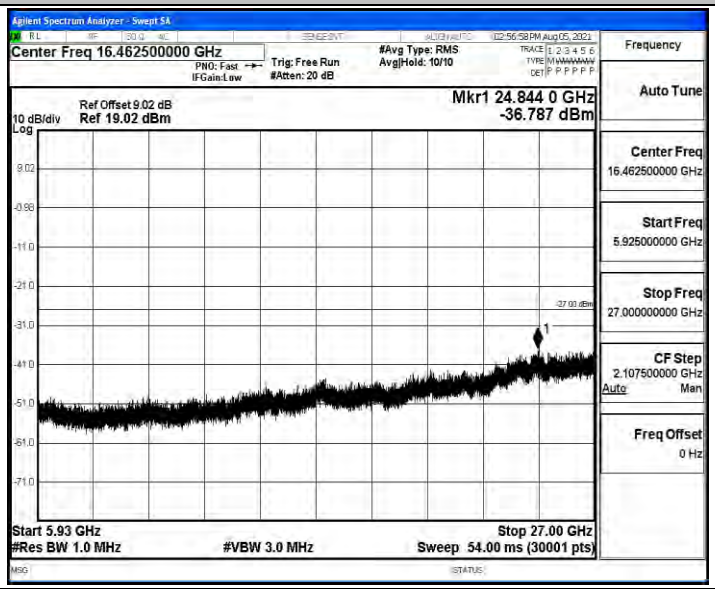
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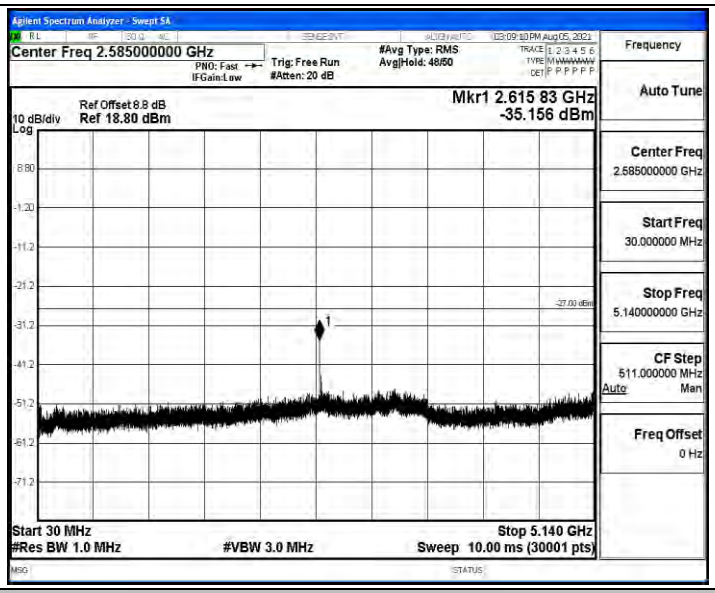

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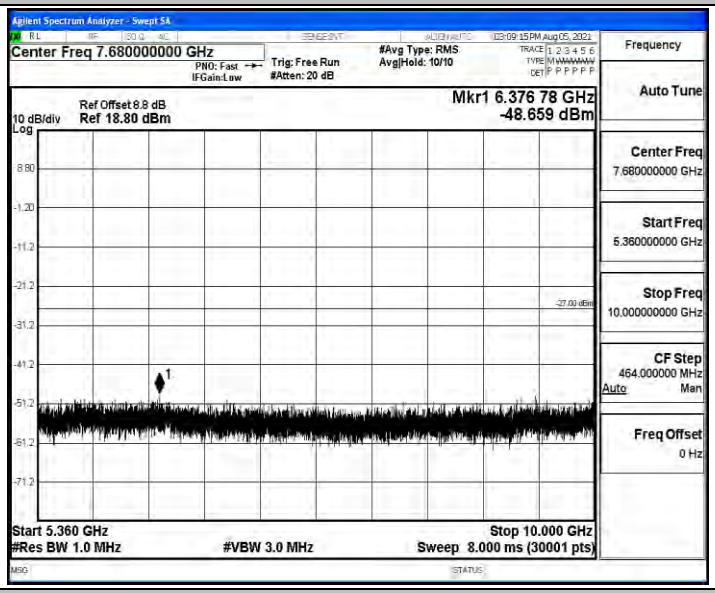

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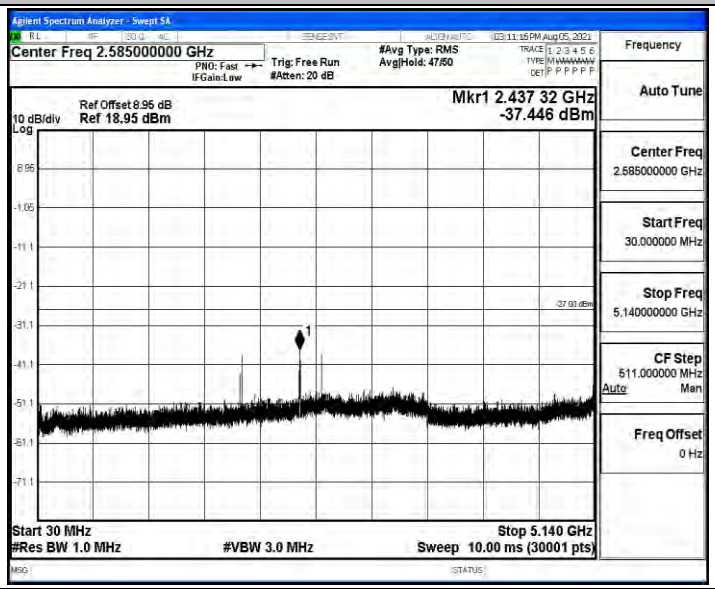

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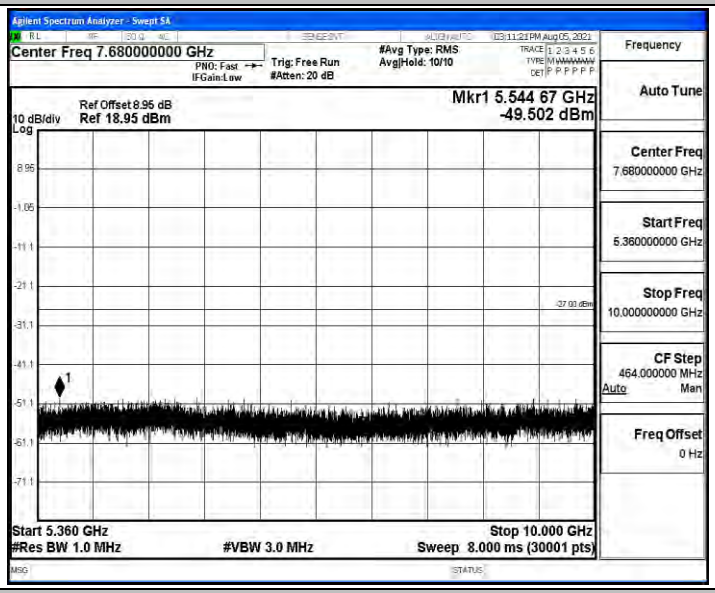
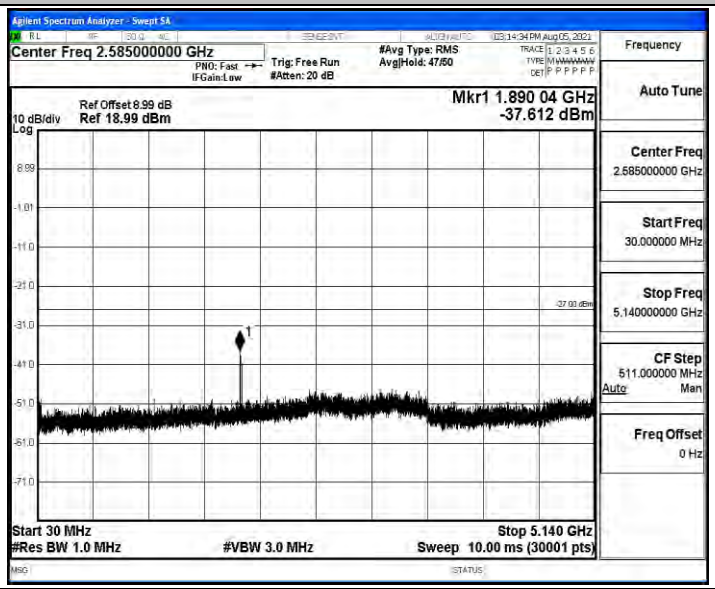

11AC20SISO_ANT3_5240_30~5140

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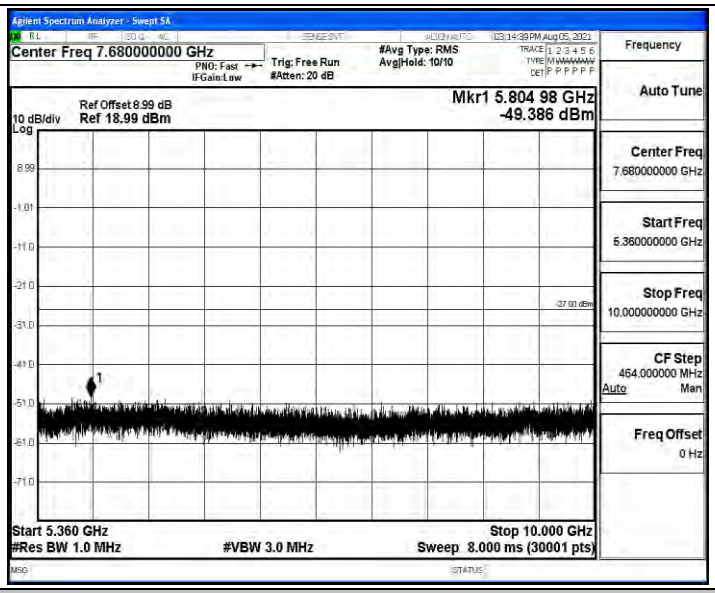
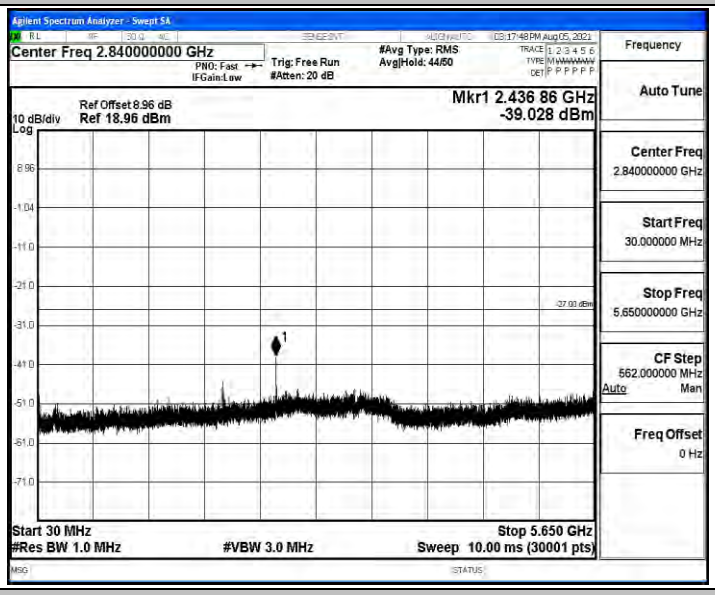
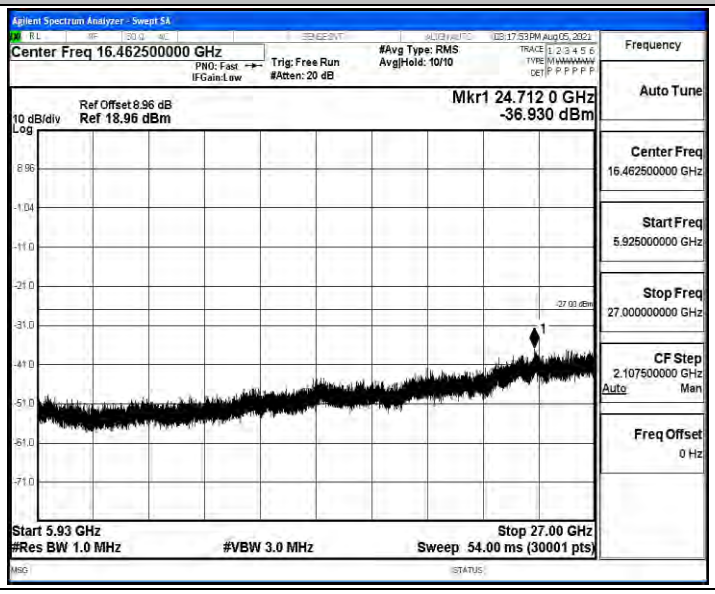
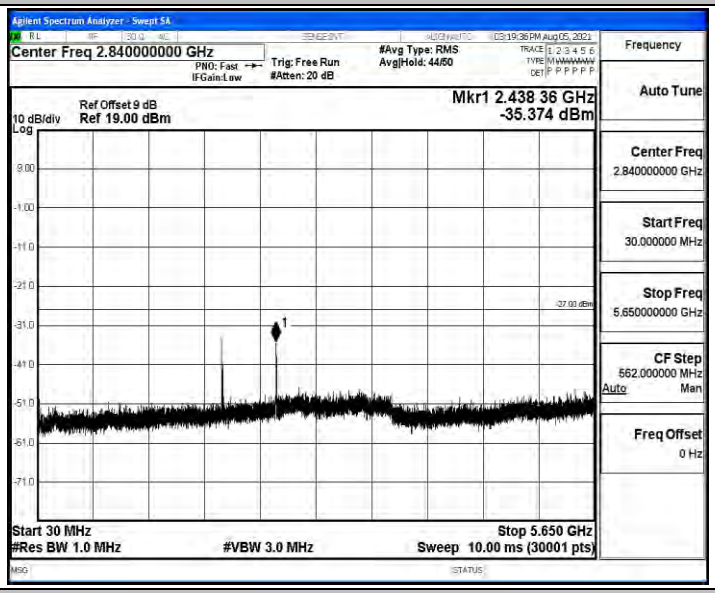


11AC20SISO_ANT3_5745_30~5650



11AC20SISO_ANT3_5745_5925~40000



11AC20SISO_ANT3_5785_30~5650

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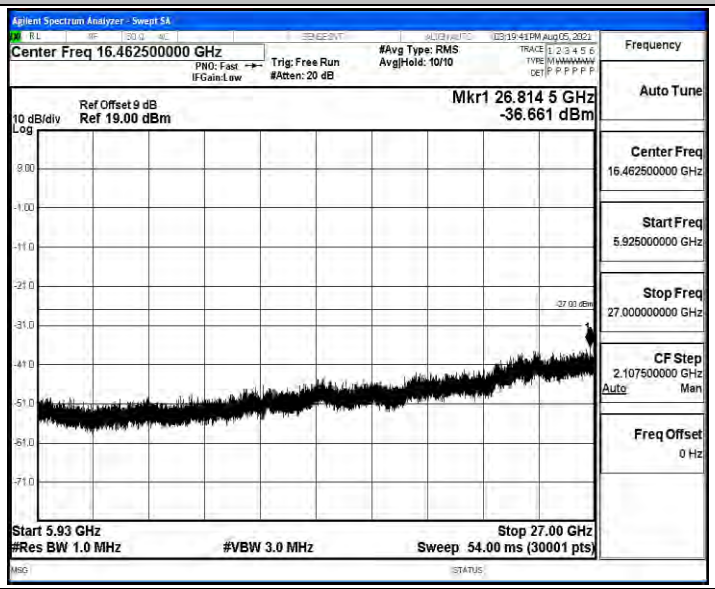
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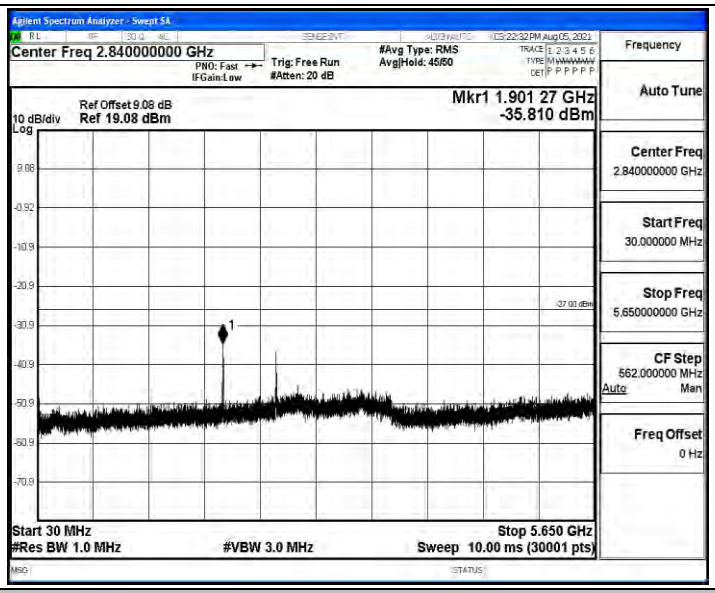

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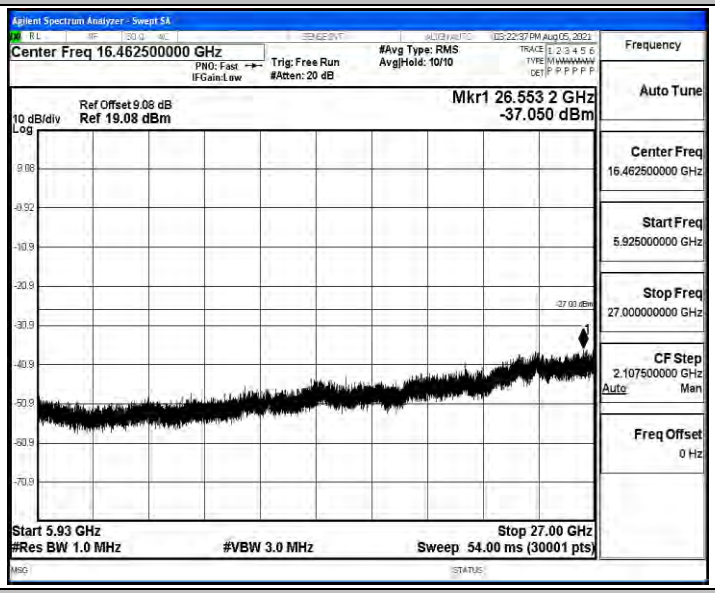
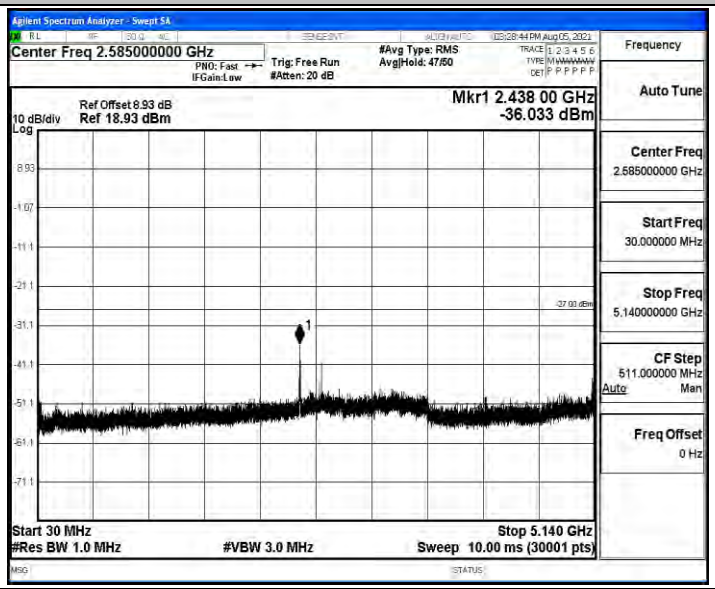


11AC40SISO_ANT3_5190_30~5140



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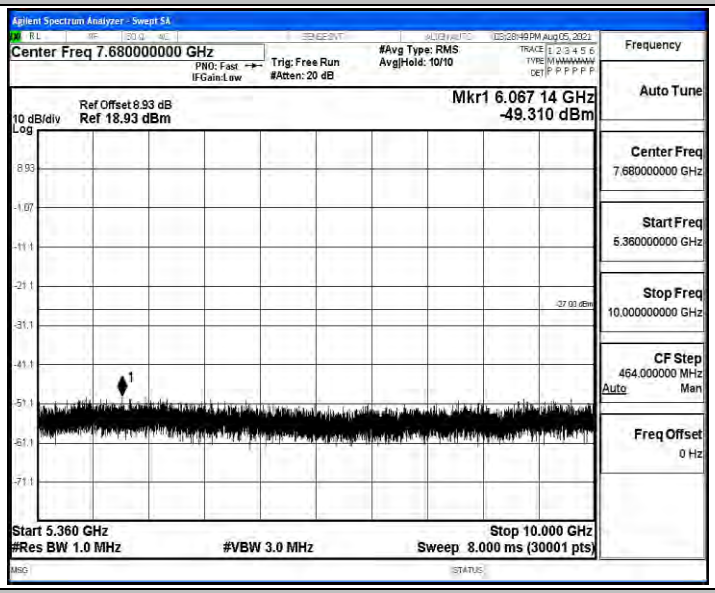
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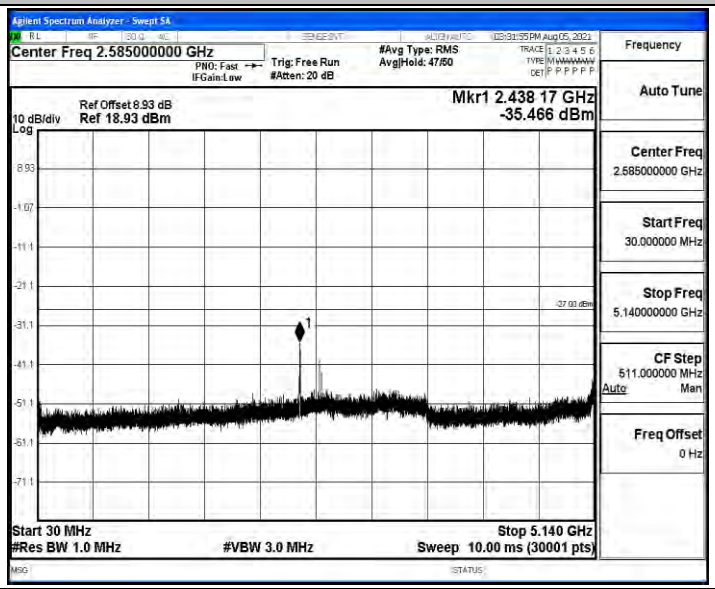
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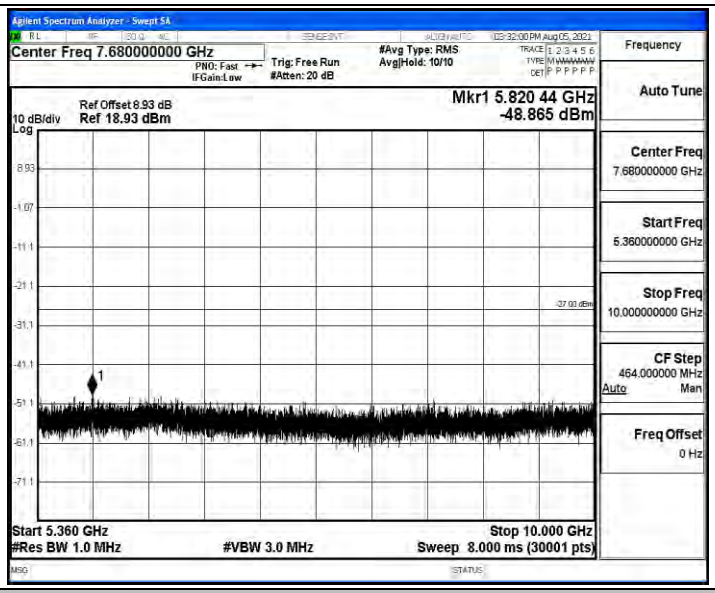
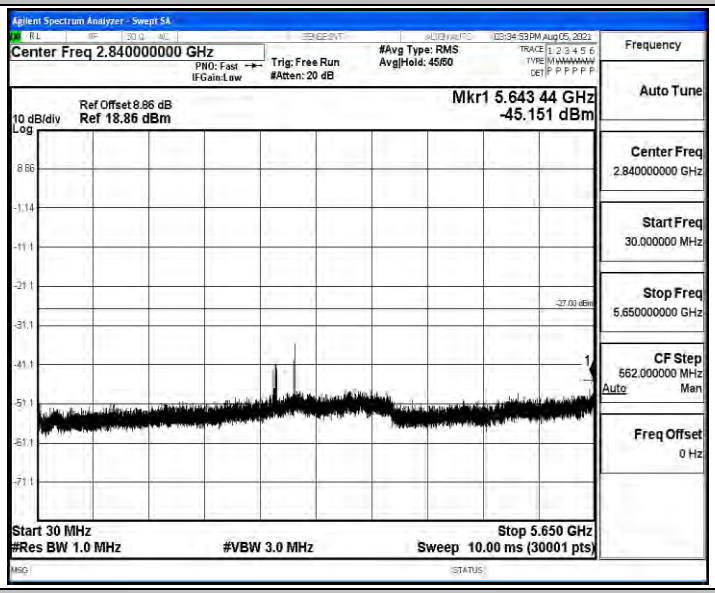


11AC40SISO_ANT3_5755_30~5650



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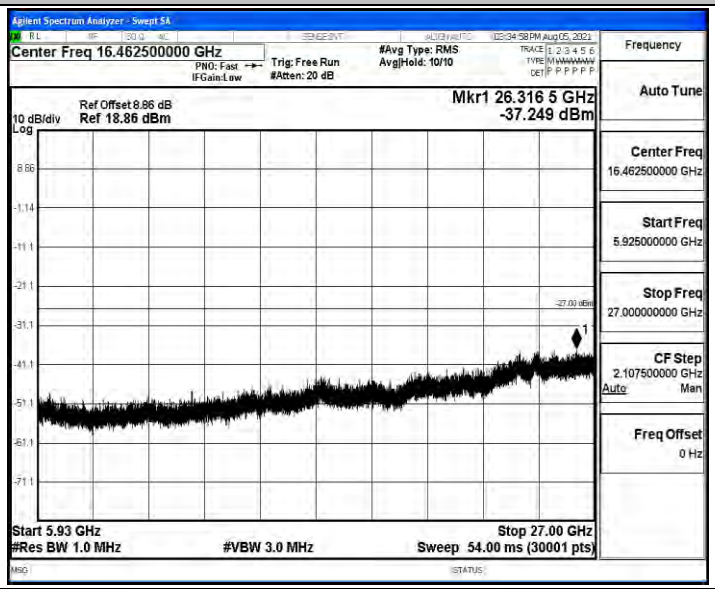

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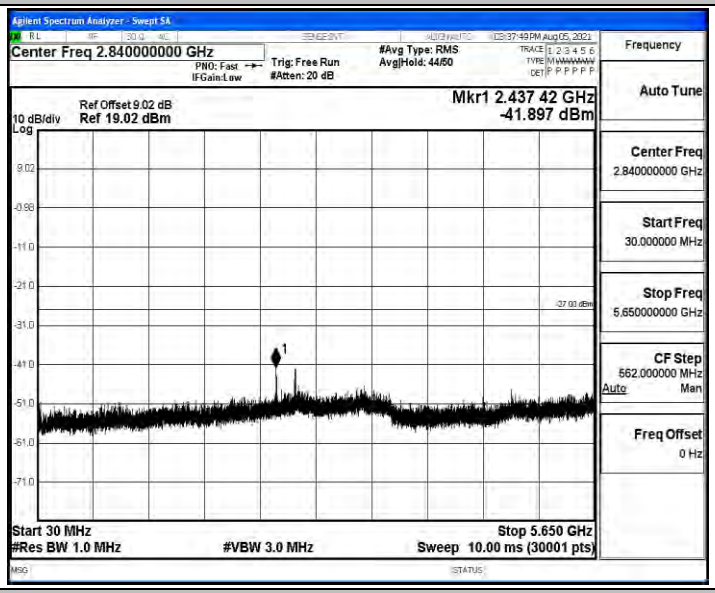
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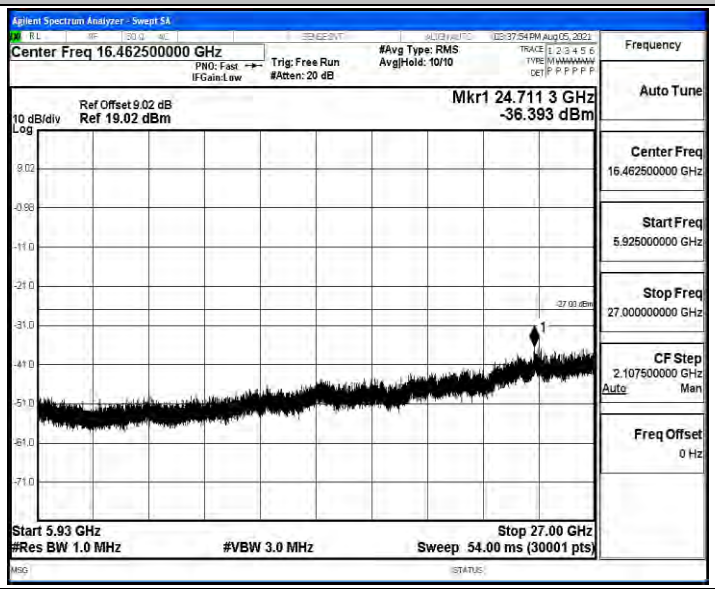
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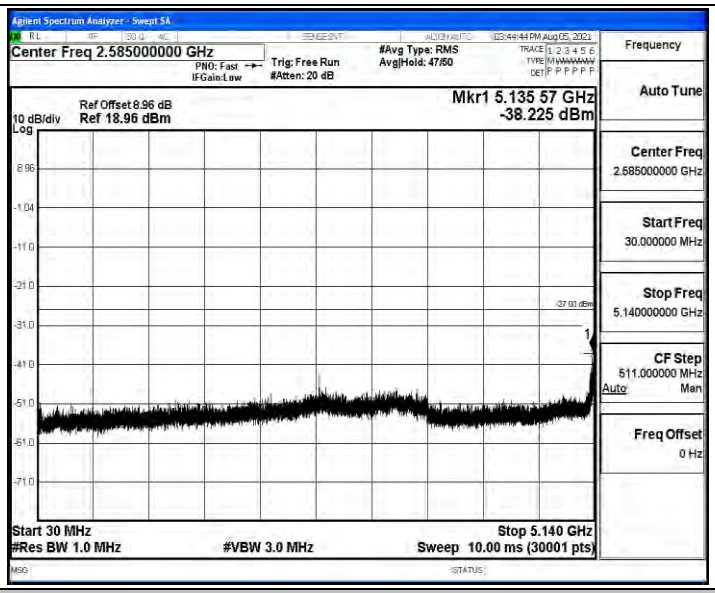

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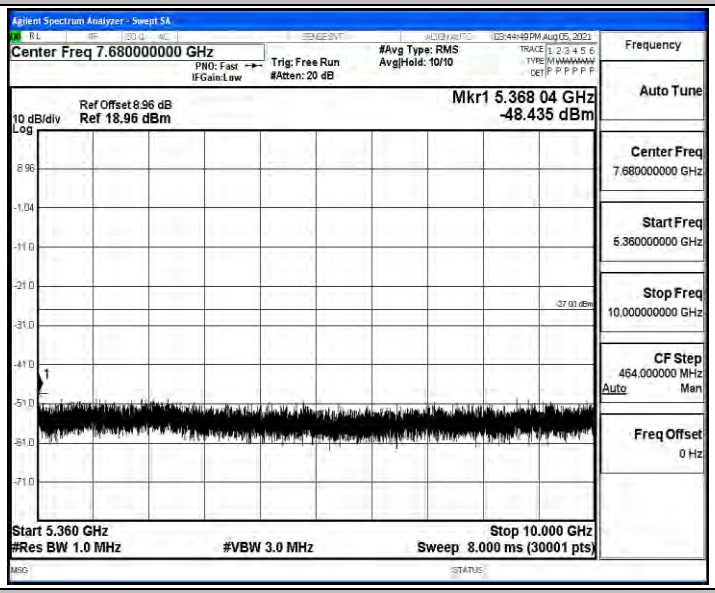

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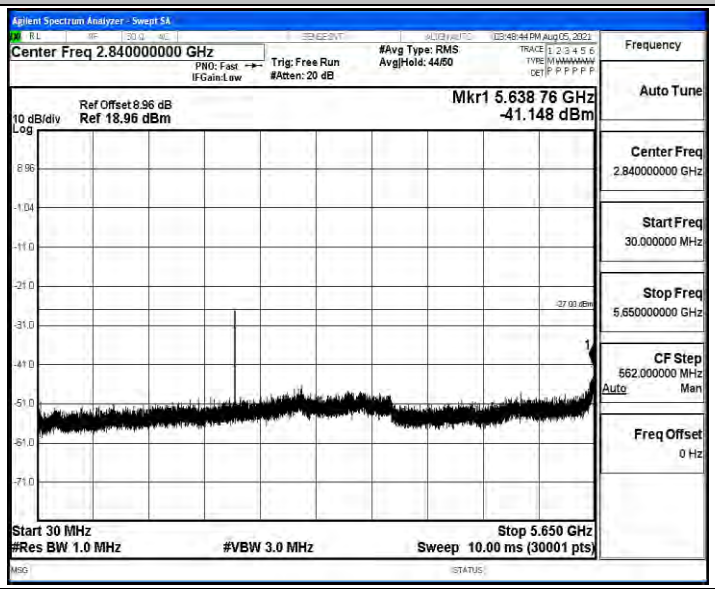

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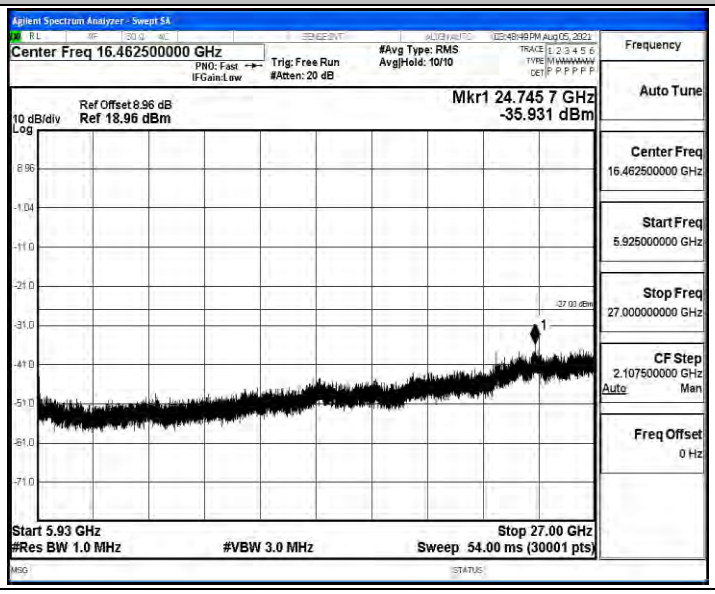

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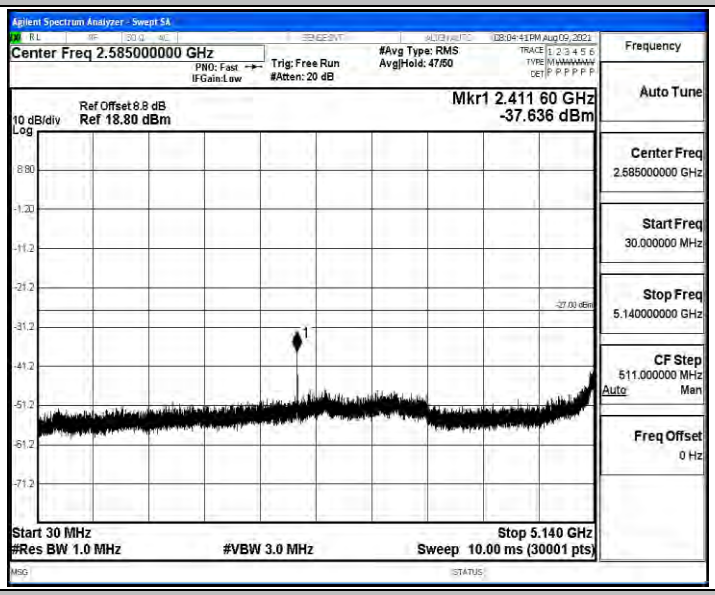

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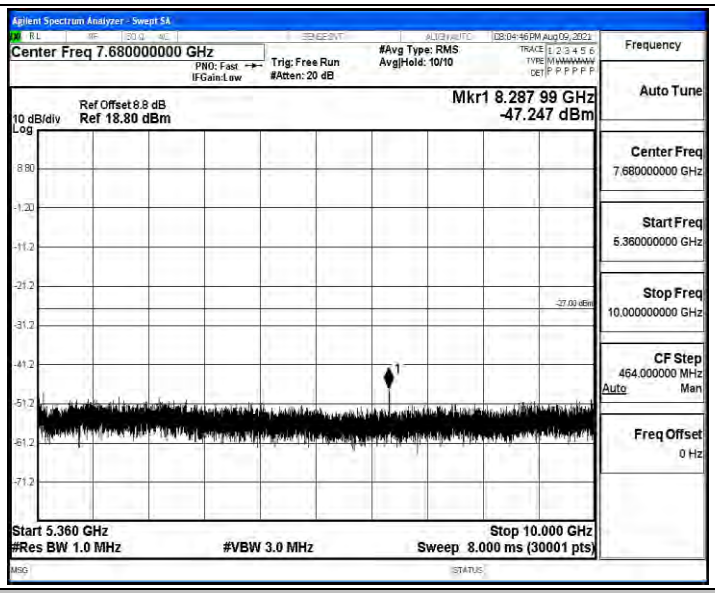

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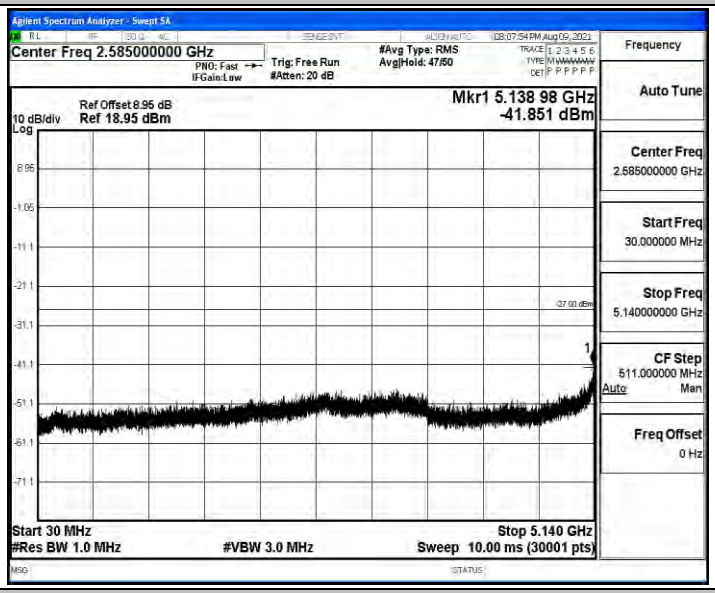

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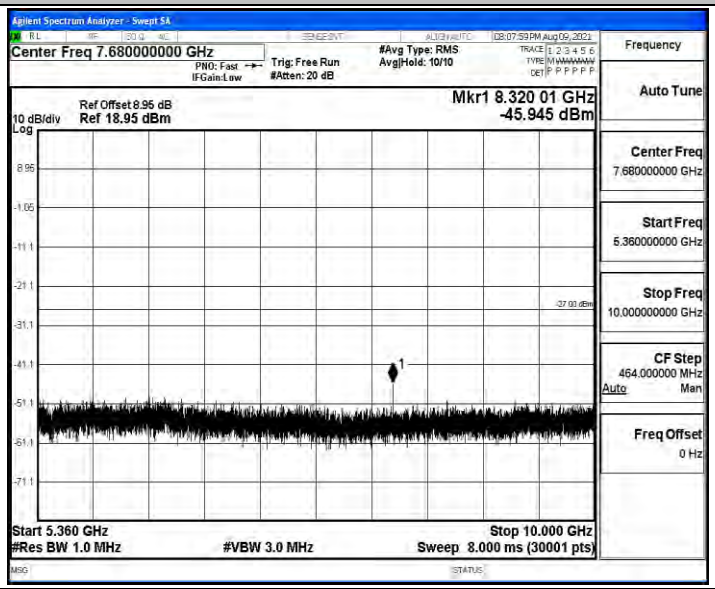

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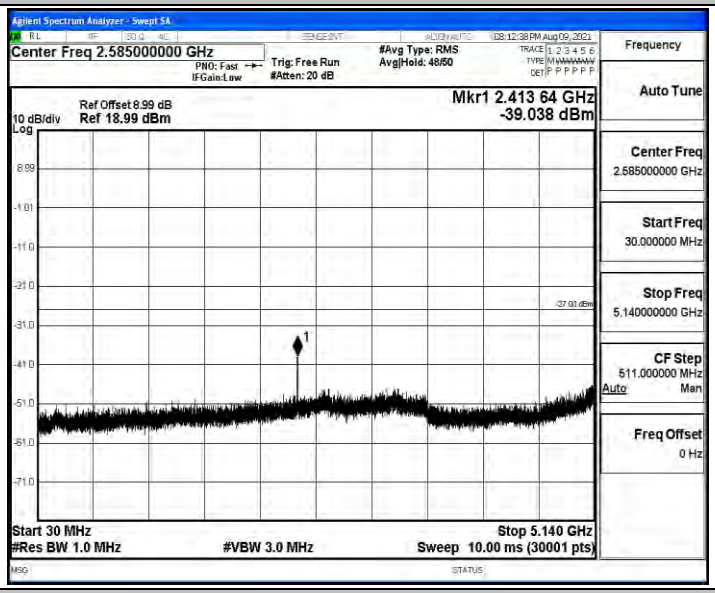
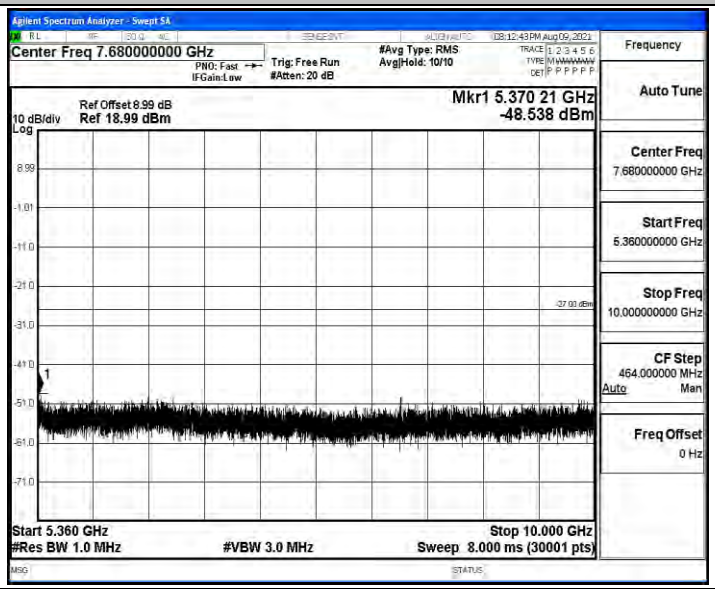


11A_ANT4_5240_5360~10000



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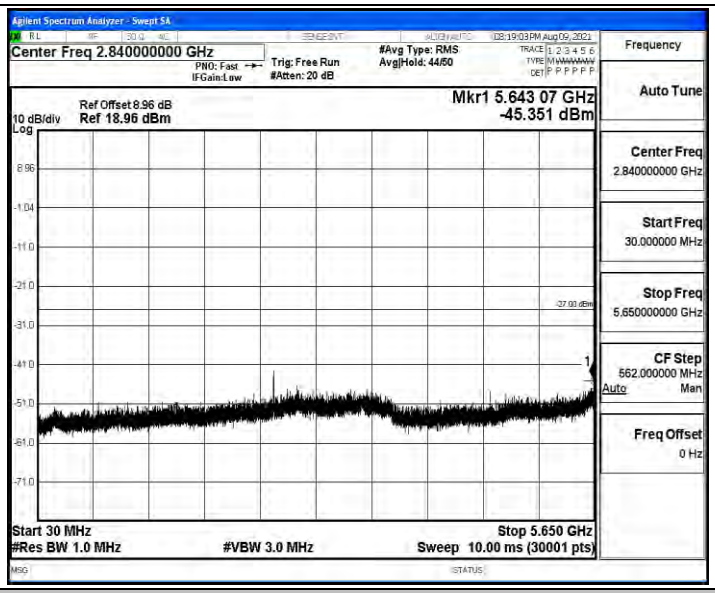

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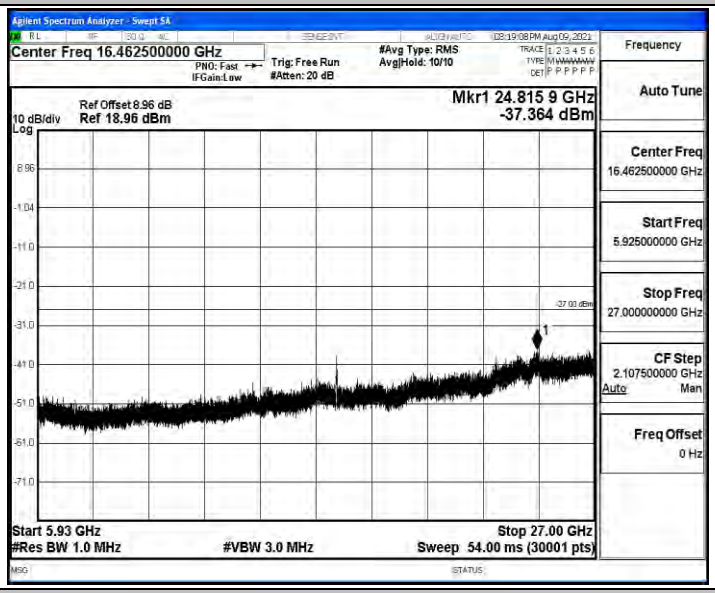

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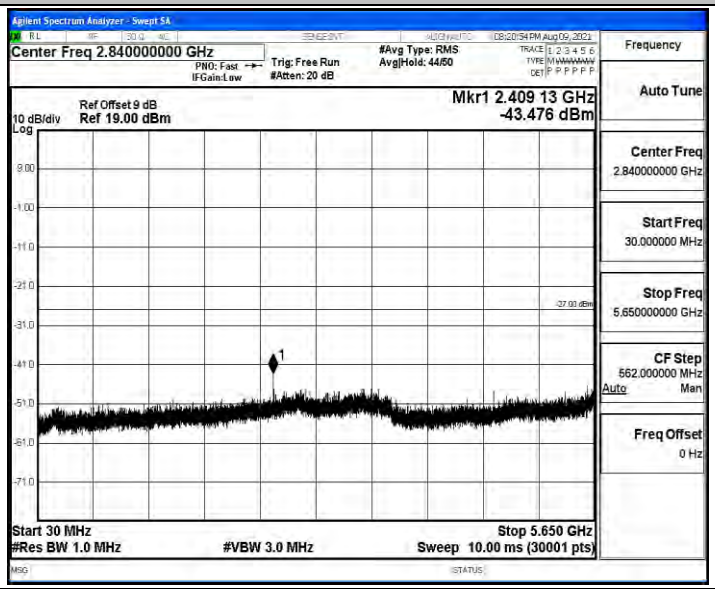

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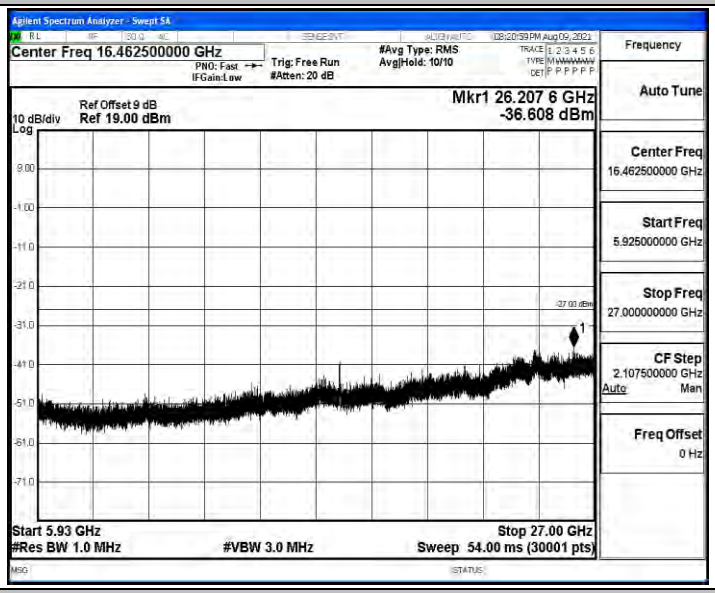

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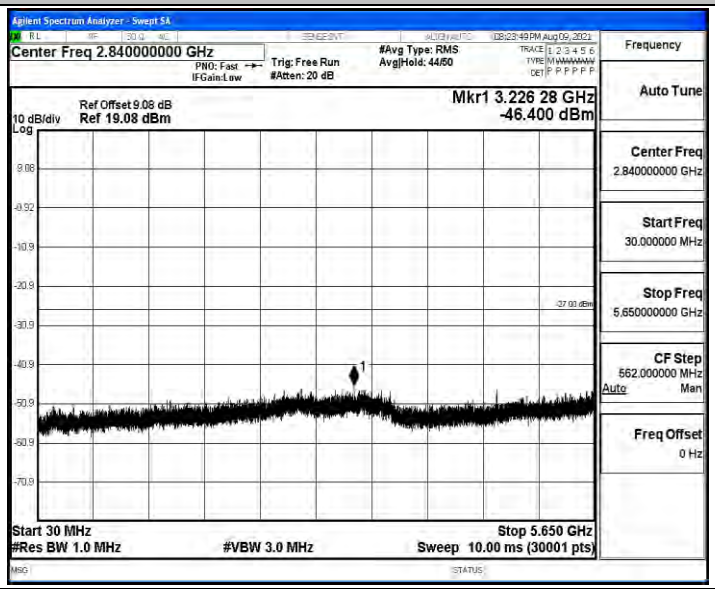
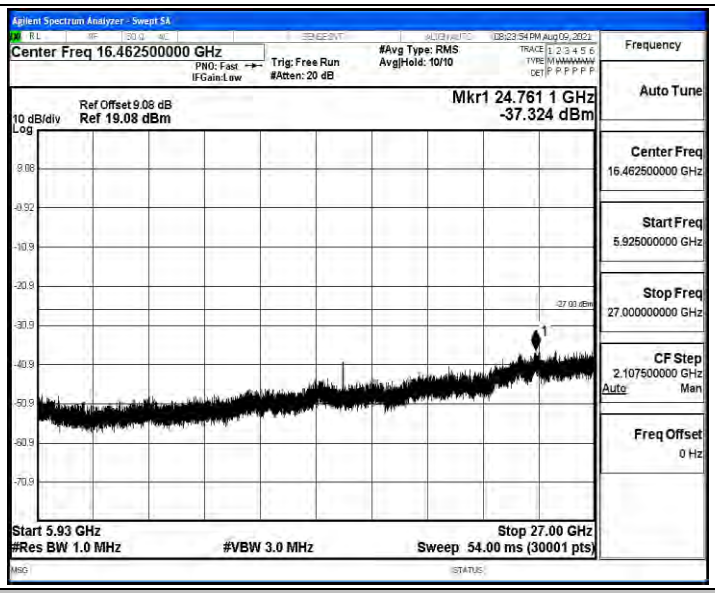
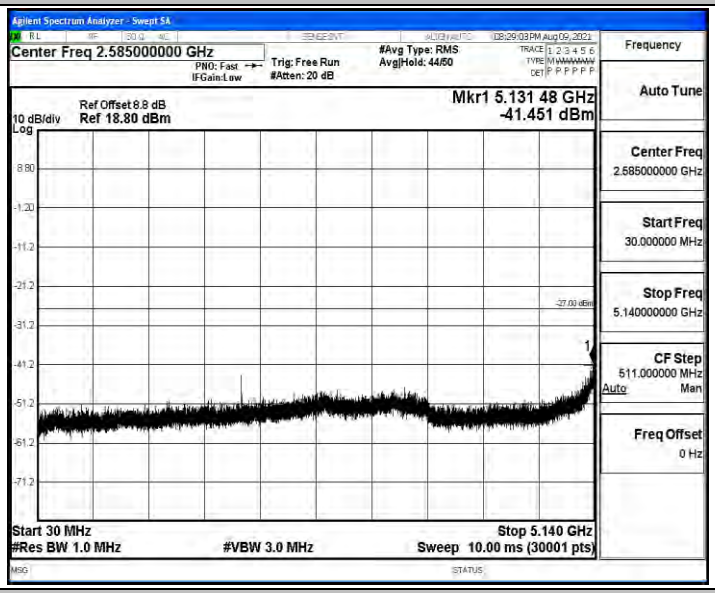


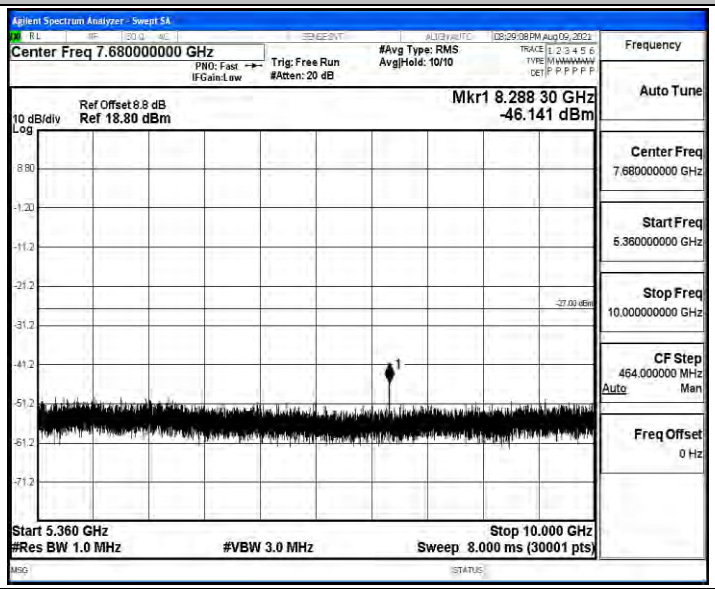
11A_ANT4_5825_5925~40000

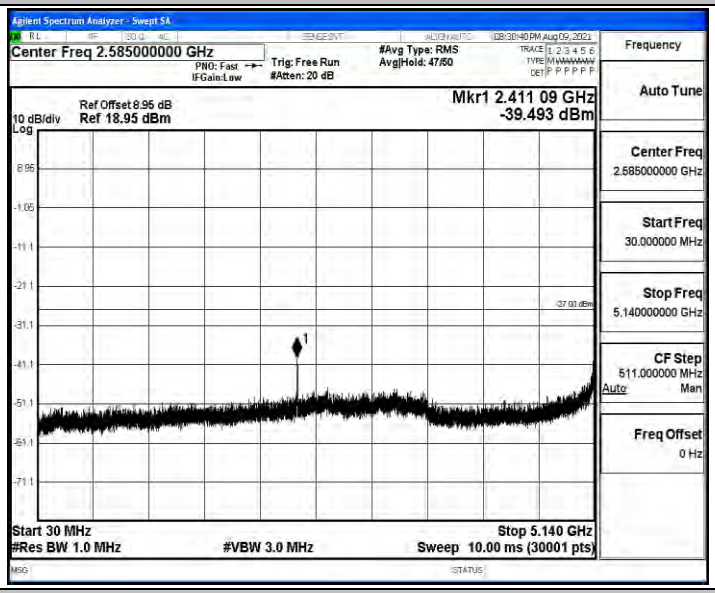
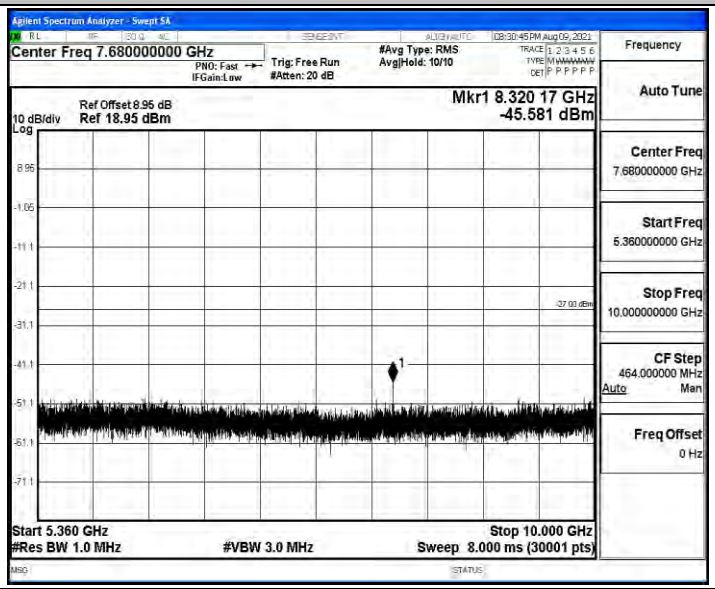


11N20SISO_ANT4_5180_30~5140



11N20SISO_ANT4_5180_5360~10000



11N20SISO_ANT4_5200_30~5140

11N20SISO_ANT4_5200_5360~10000

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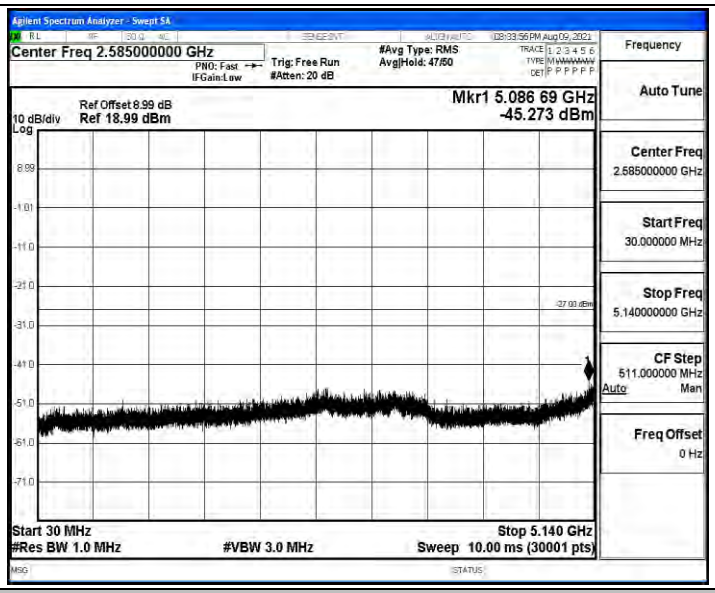
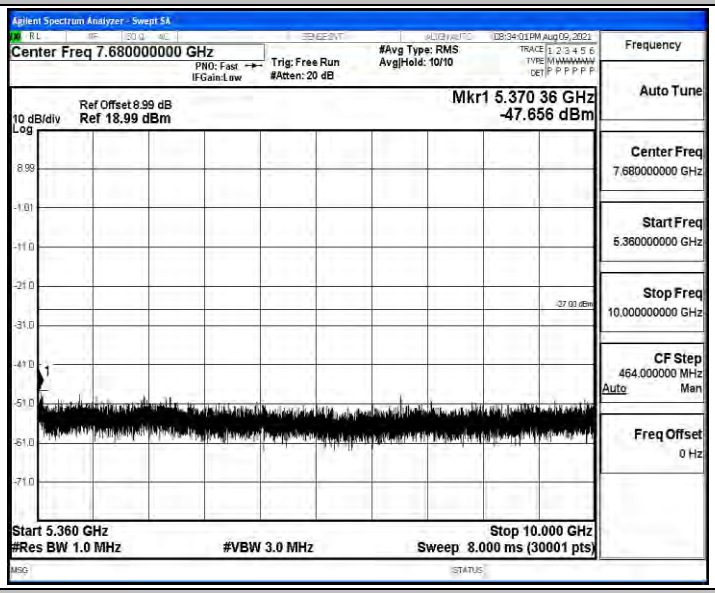
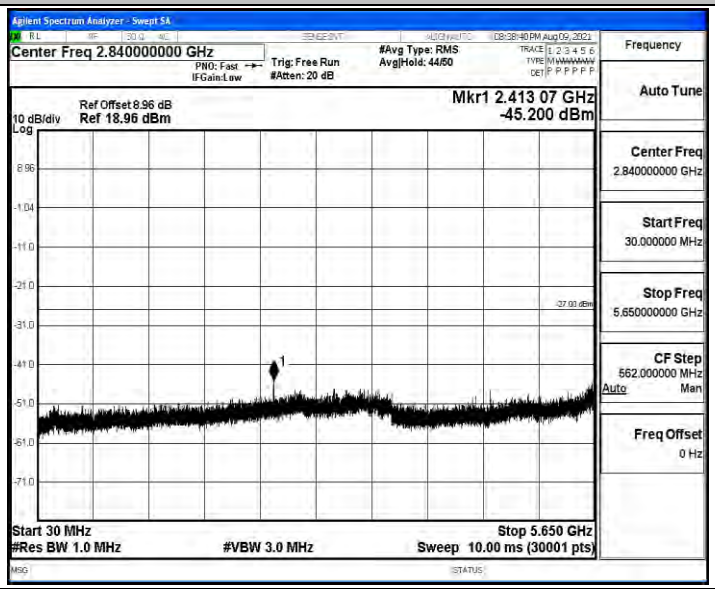


11N20SISO_ANT4_5240_5360~10000



11N20SISO_ANT4_5745_30~5650



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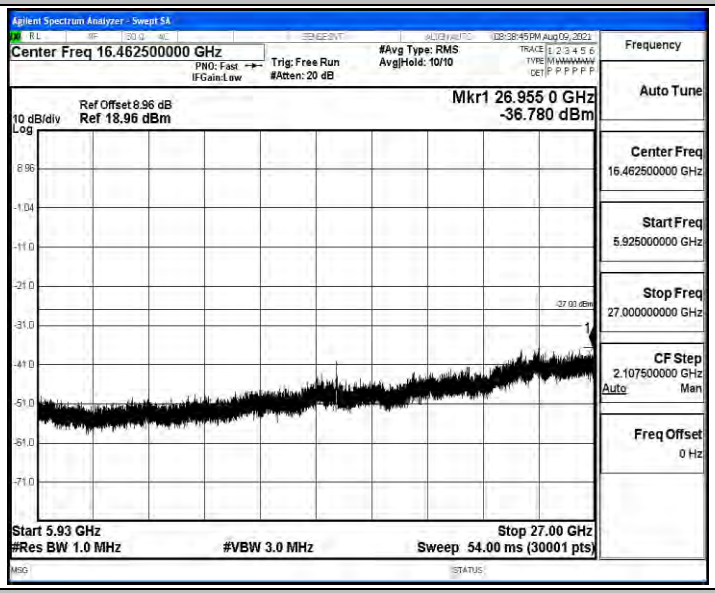
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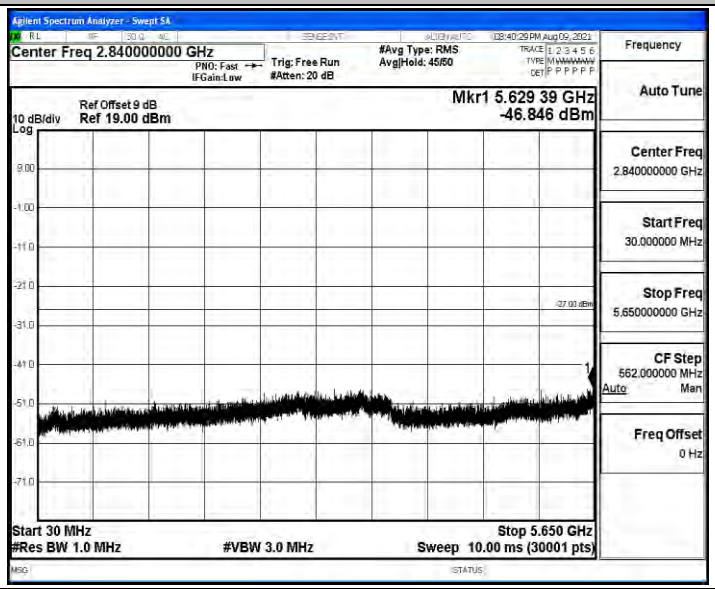
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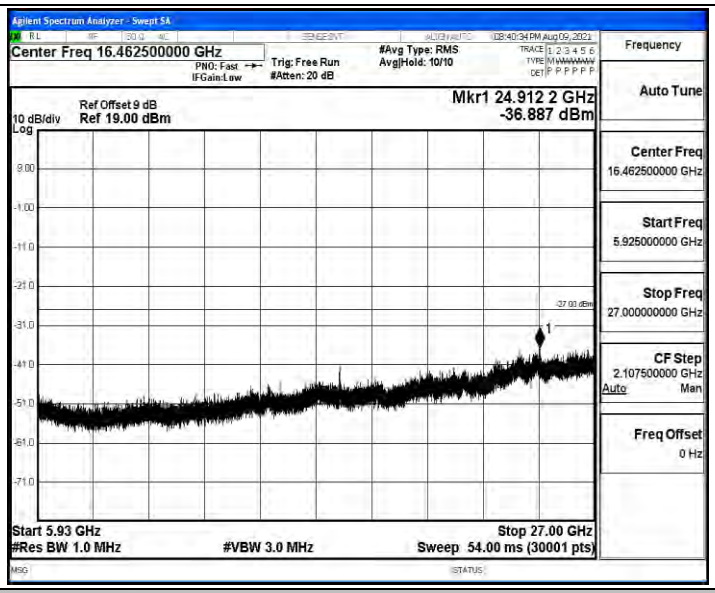
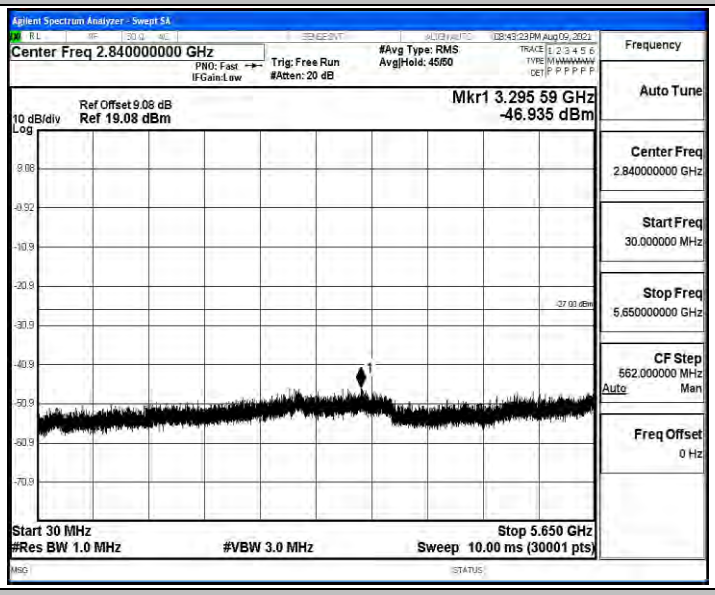
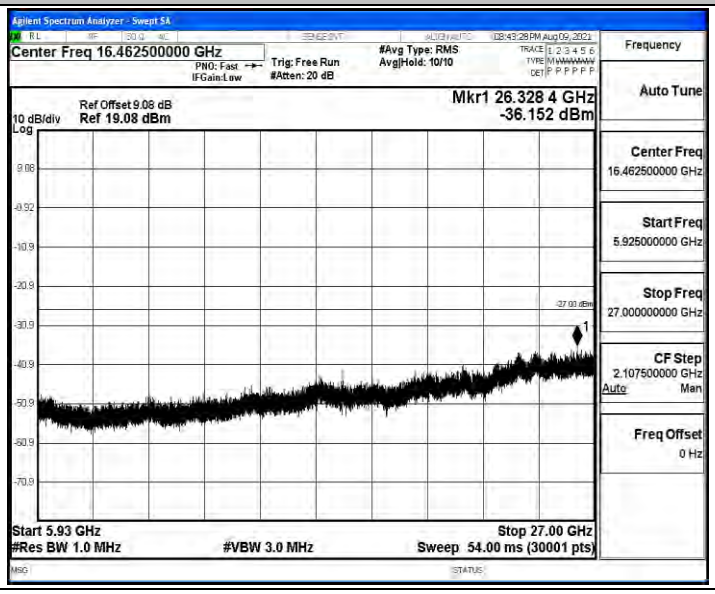
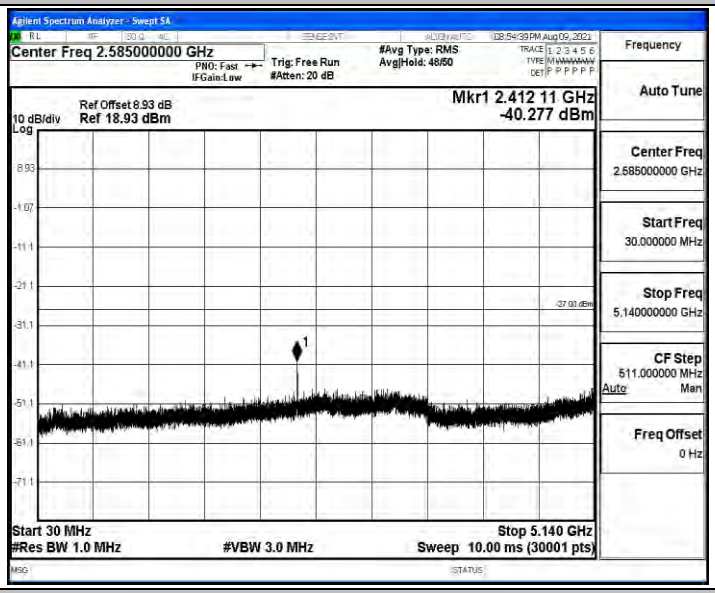
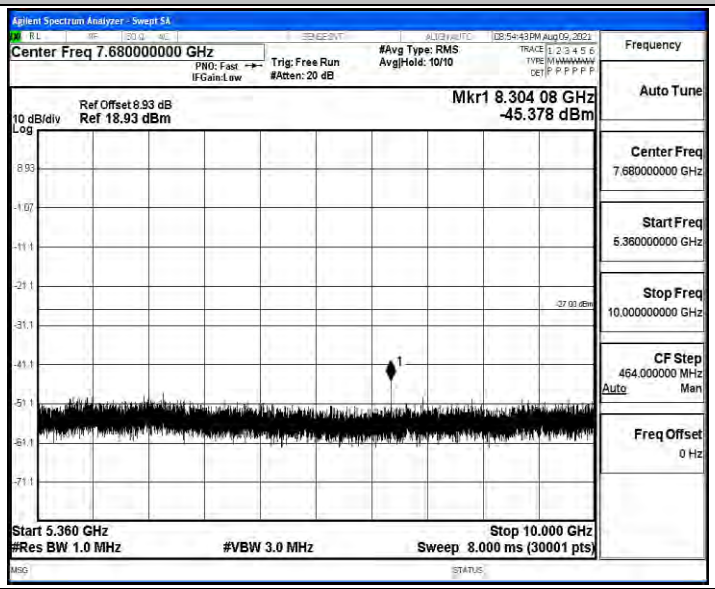


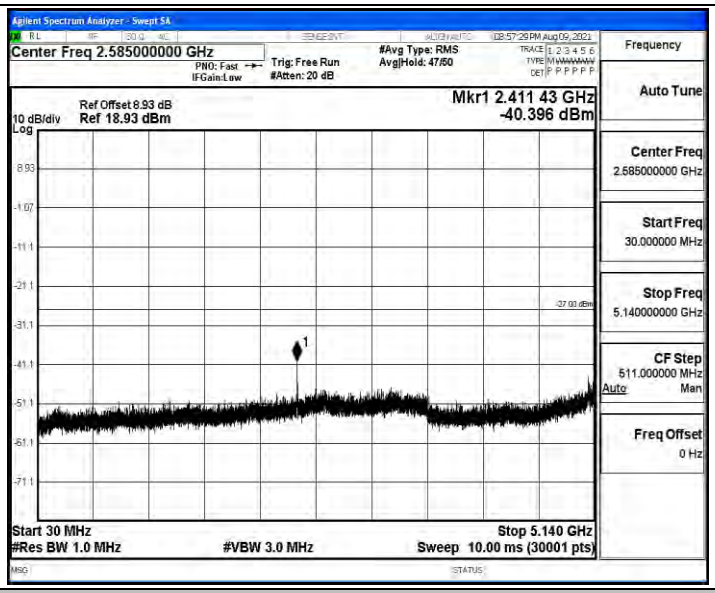
11N20SISO_ANT4_5825_30~5650



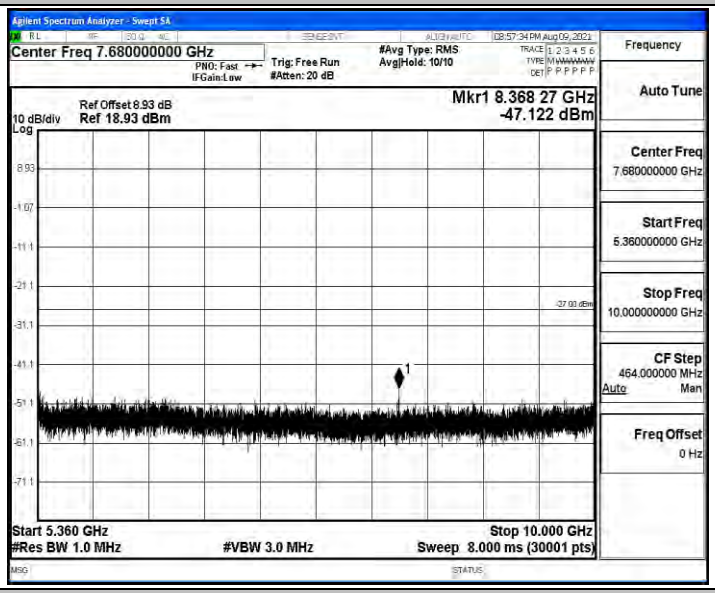
11N20SISO_ANT4_5825_5925~40000



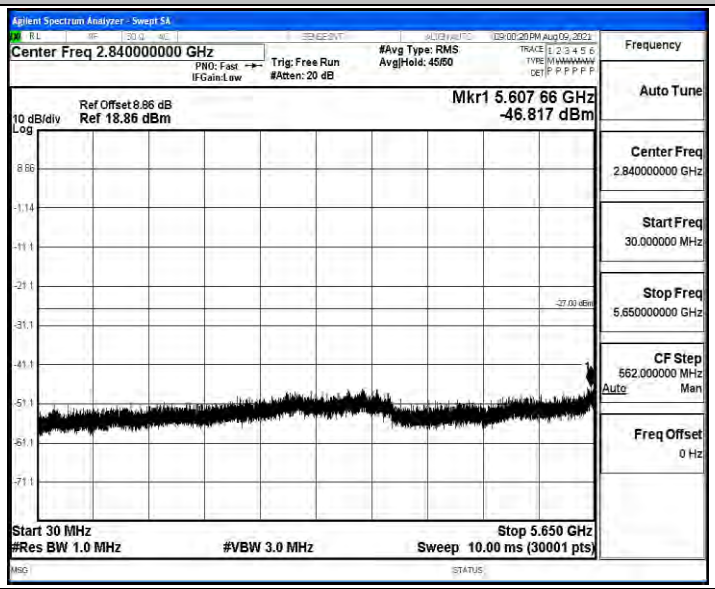
11N40SISO_ANT4_5190_30~5140

11N40SISO_ANT4_5190_5360~10000

11N40SISO_ANT4_5230_30~5140

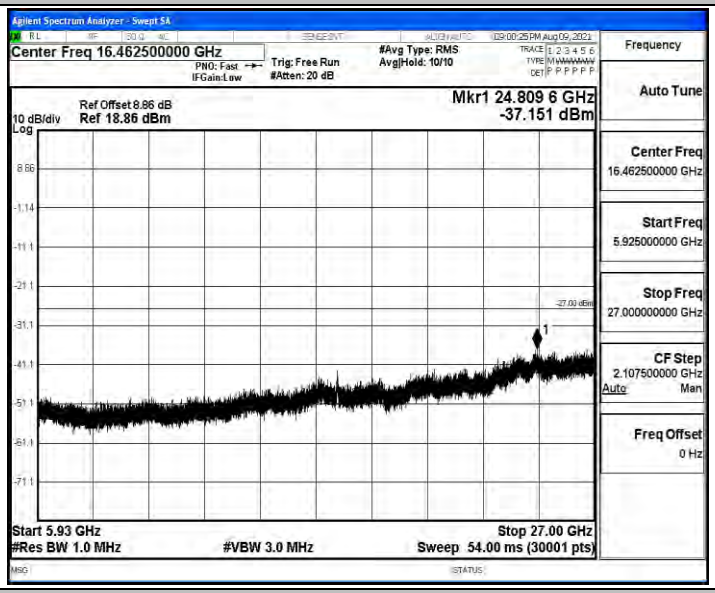
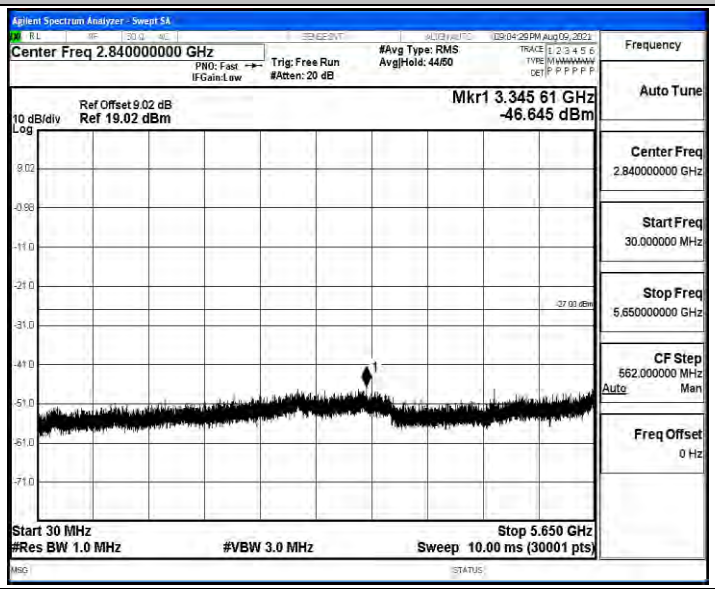


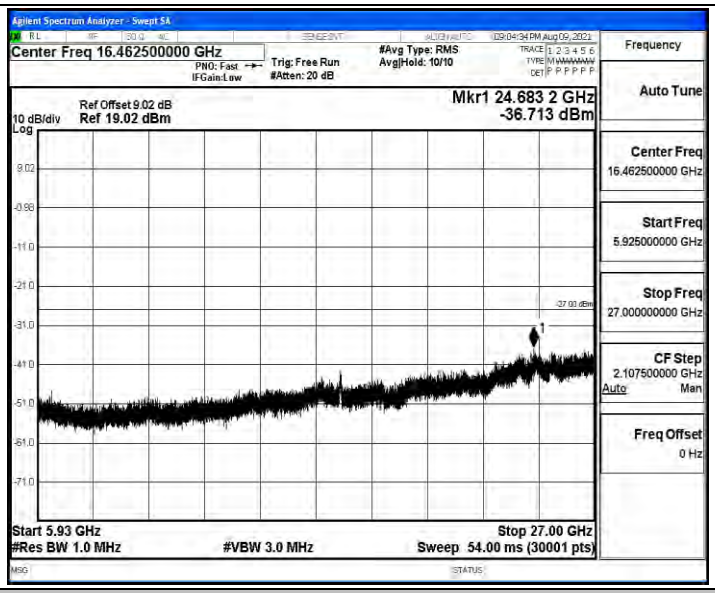
11N40SISO_ANT4_5230_5360~10000



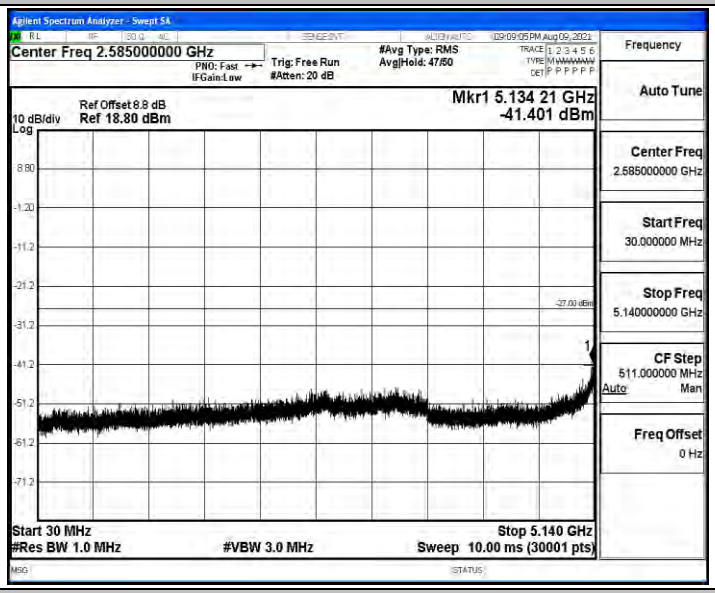
11N40SISO_ANT4_5755_30~5650



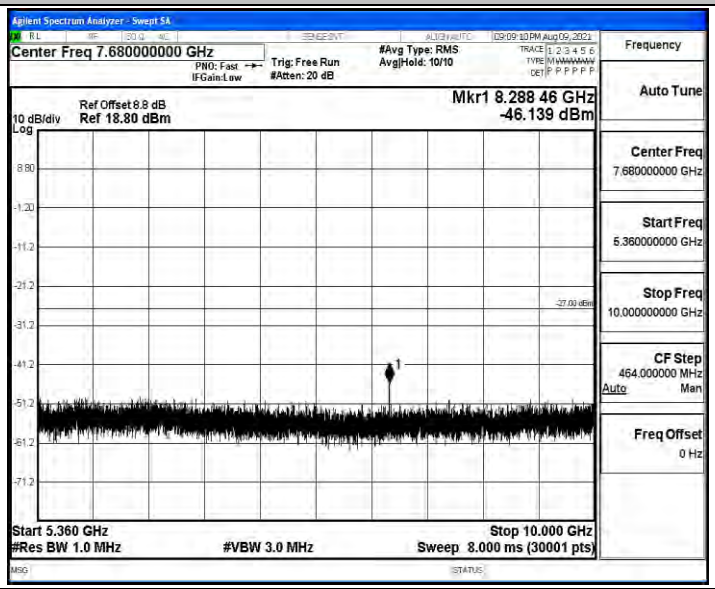
11N40SISO_ANT4_5755_5925~40000

11N40SISO_ANT4_5795_30~5650

11N40SISO_ANT4_5795_5925~40000

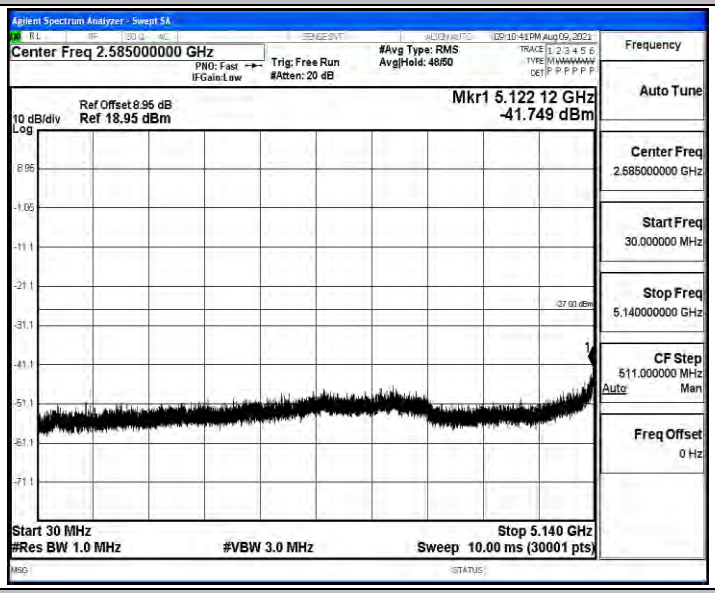
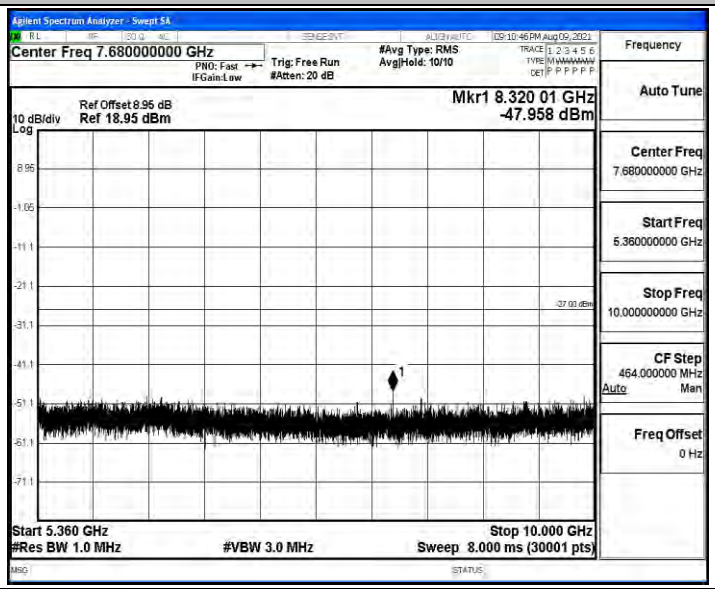


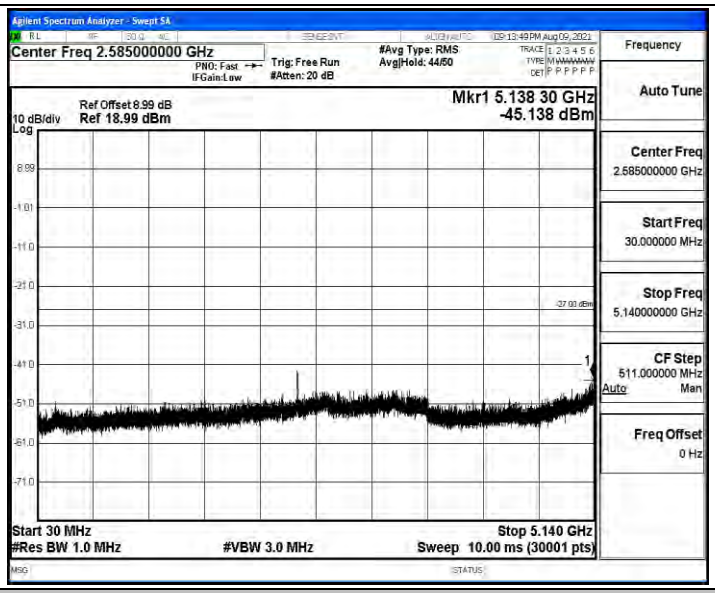
11AC20SISO_ANT4_5180_30~5140



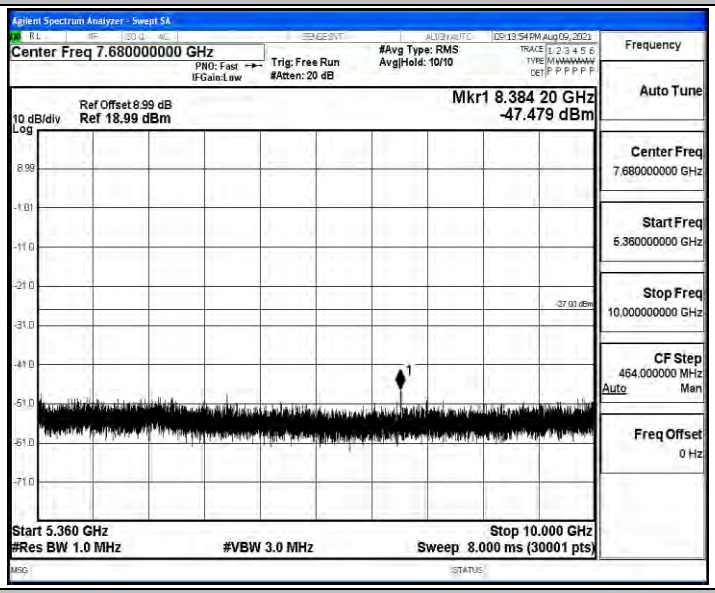
11AC20SISO_ANT4_5180_5360~10000



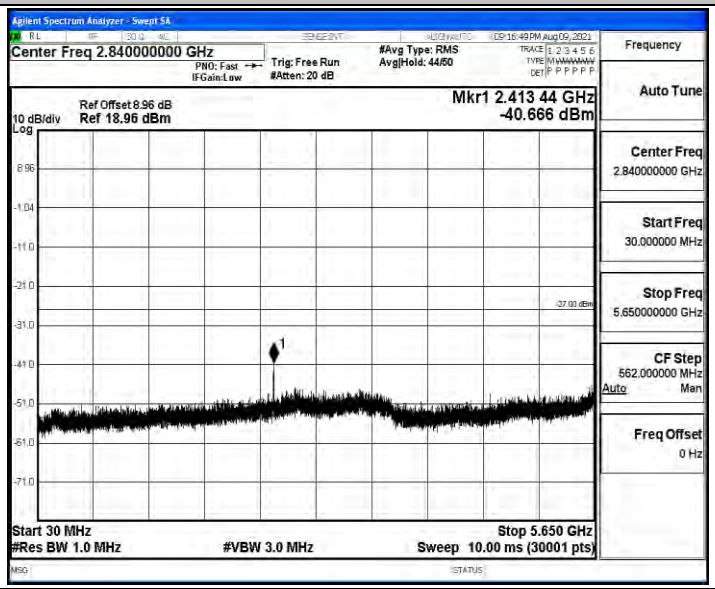
11AC20SISO_ANT4_5200_30~5140

11AC20SISO_ANT4_5200_5360~10000

11AC20SISO_ANT4_5240_30~5140

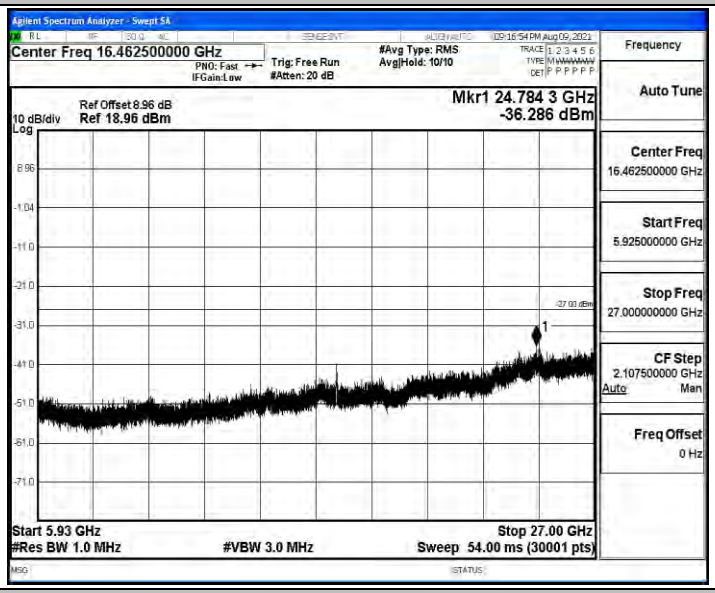


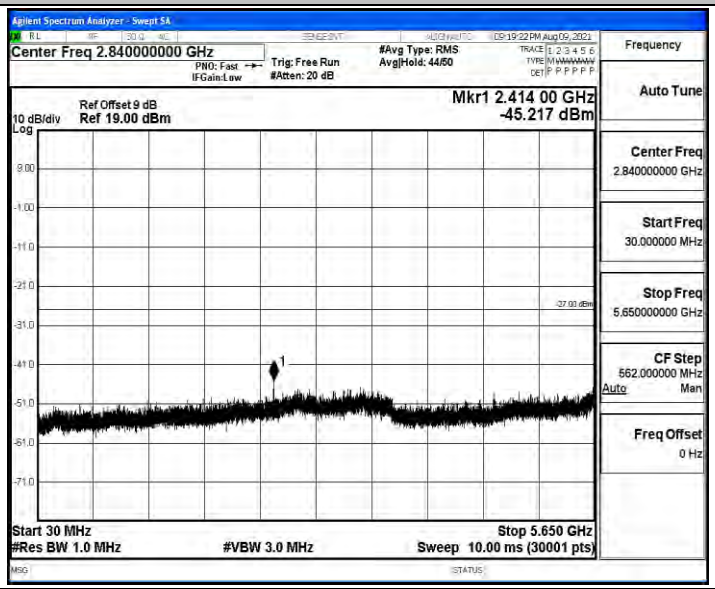
11AC20SISO_ANT4_5240_5360~10000



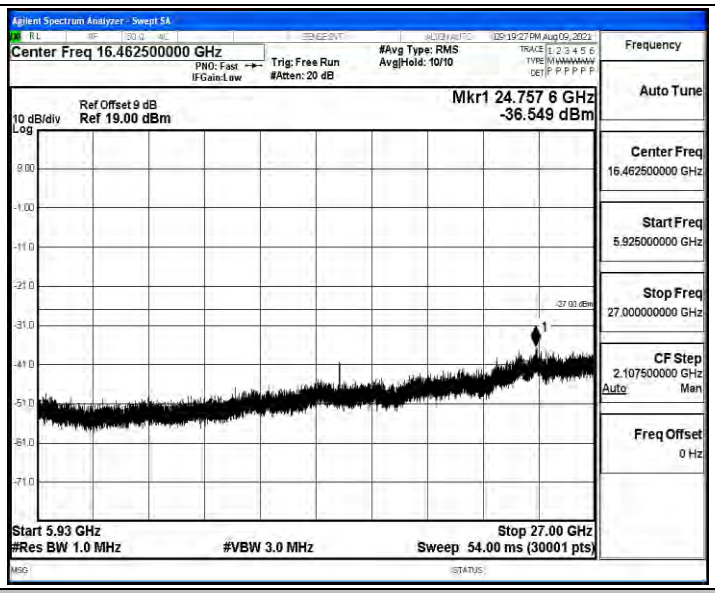
11AC20SISO_ANT4_5745_30~5650



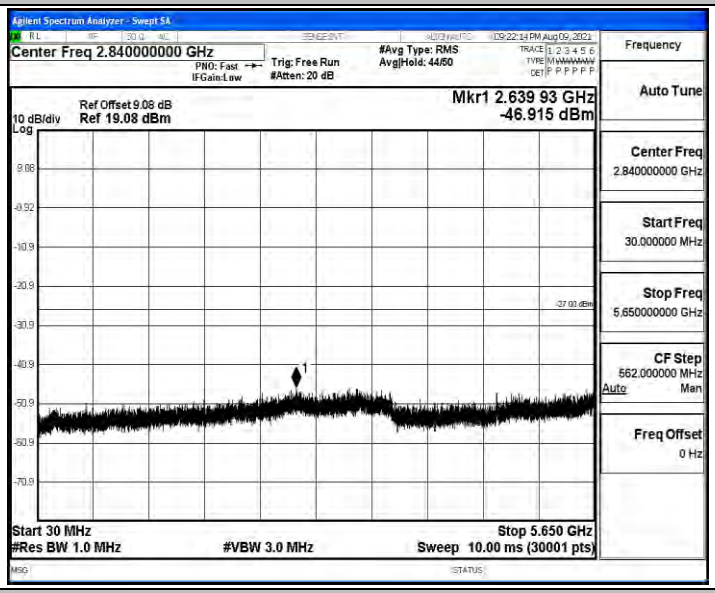
11AC20SISO_ANT4_5745_5925~40000


11AC20SISO_ANT4_5785_30~5650


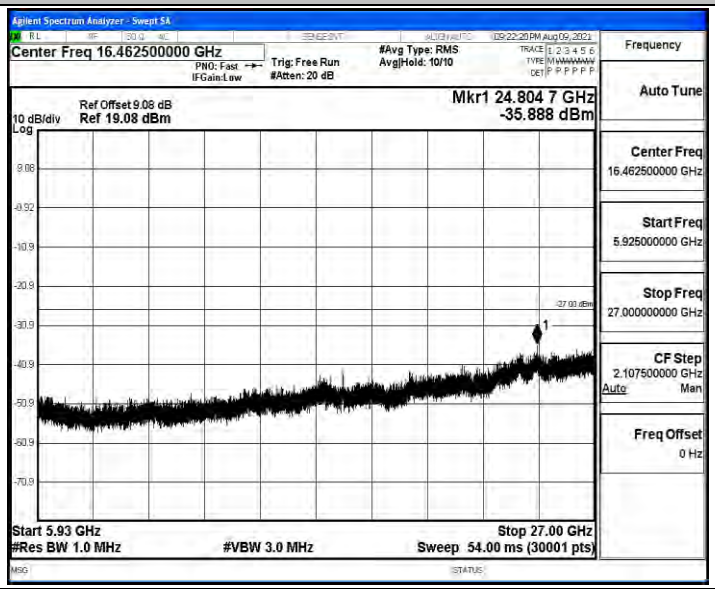
11AC20SISO_ANT4_5785_5925~40000

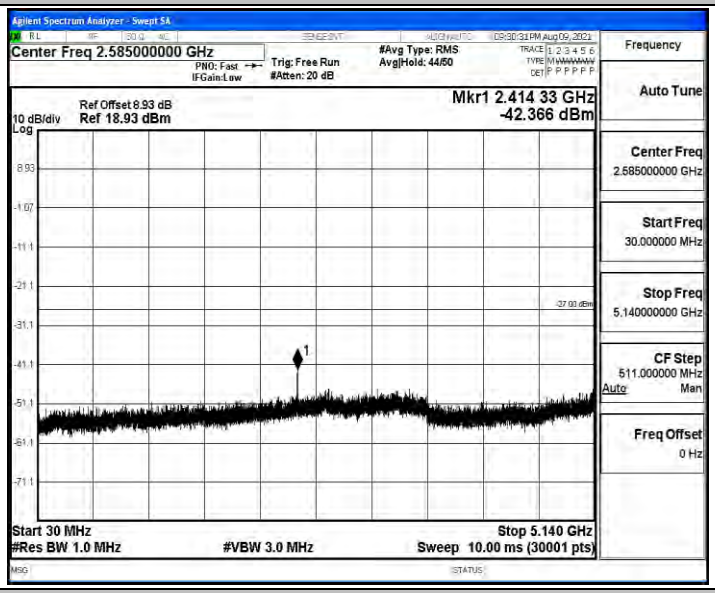
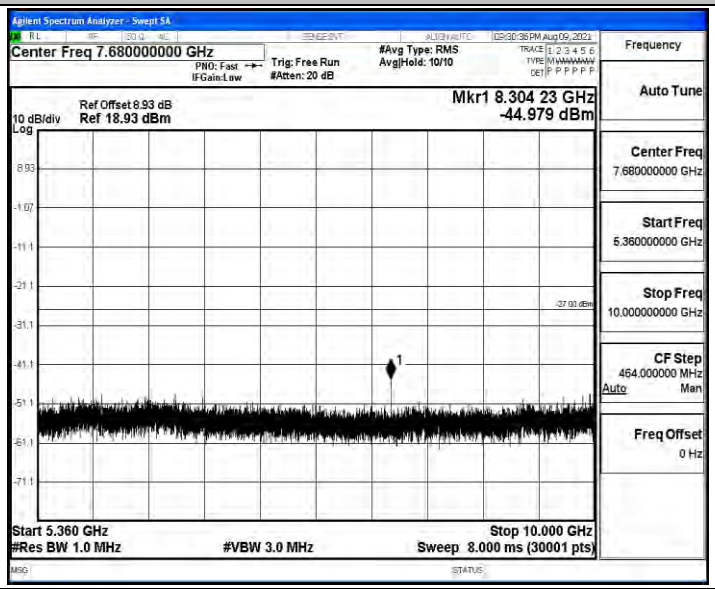


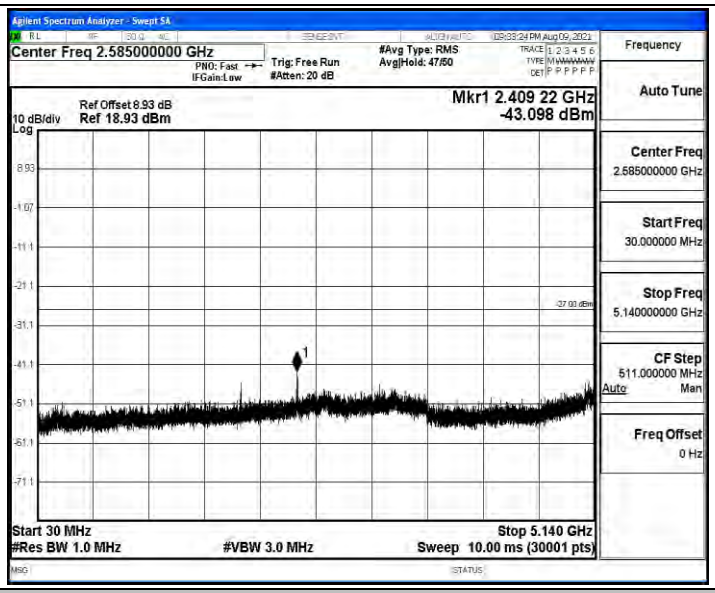
11AC20SISO_ANT4_5825_30~5650



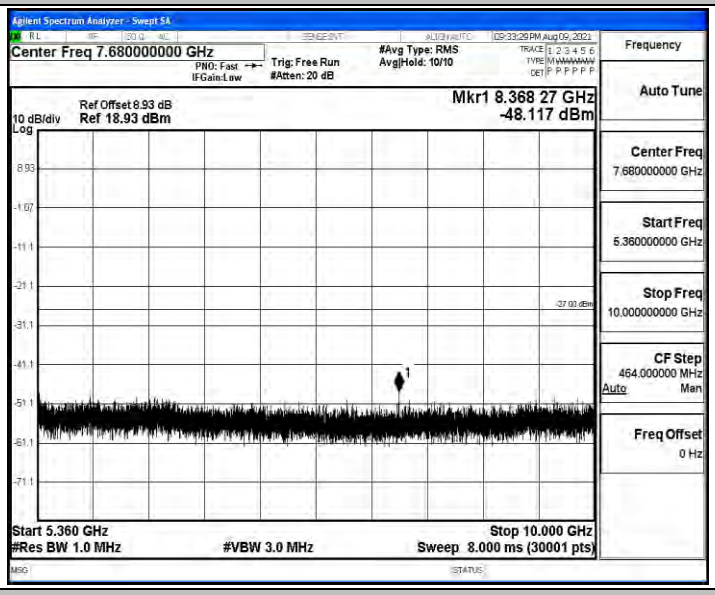
11AC20SISO_ANT4_5825_5925~40000



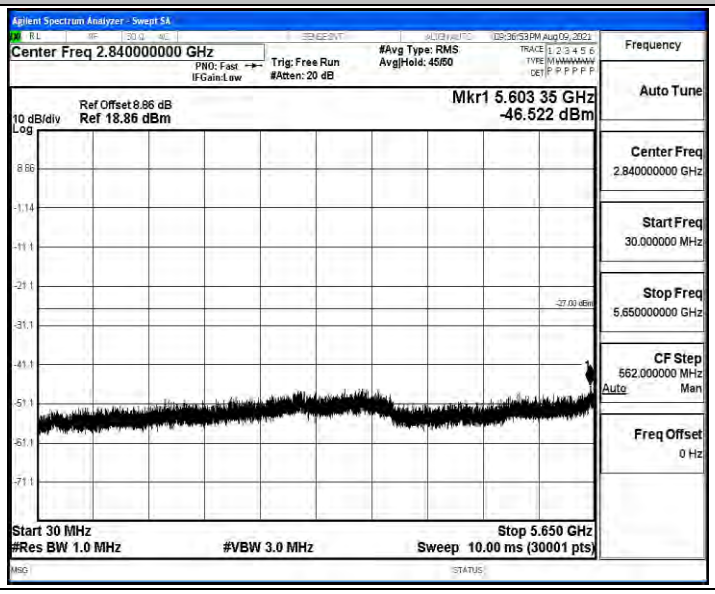
11AC40SISO_ANT4_5190_30~5140

11AC40SISO_ANT4_5190_5360~10000

11AC40SISO_ANT4_5230_30~5140

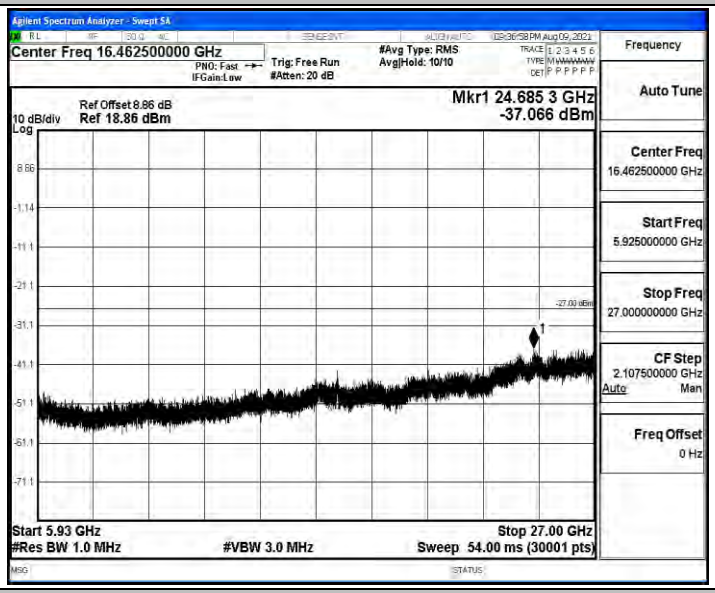


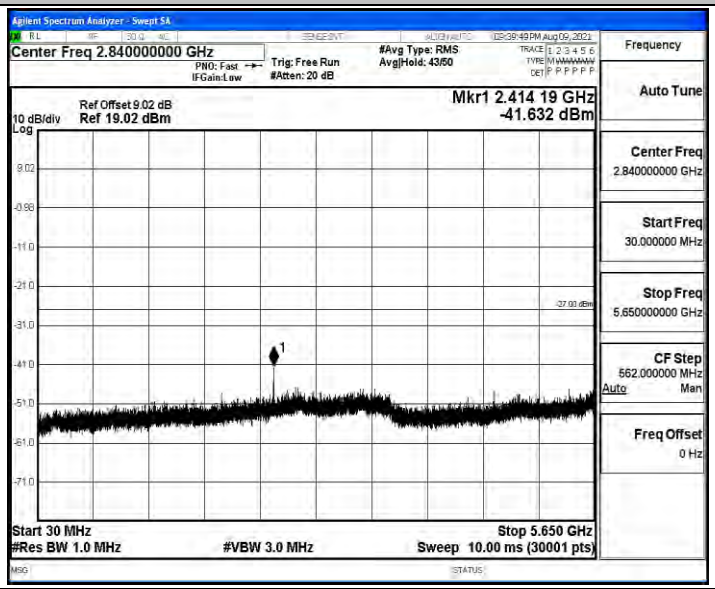
11AC40SISO_ANT4_5230_5360~10000



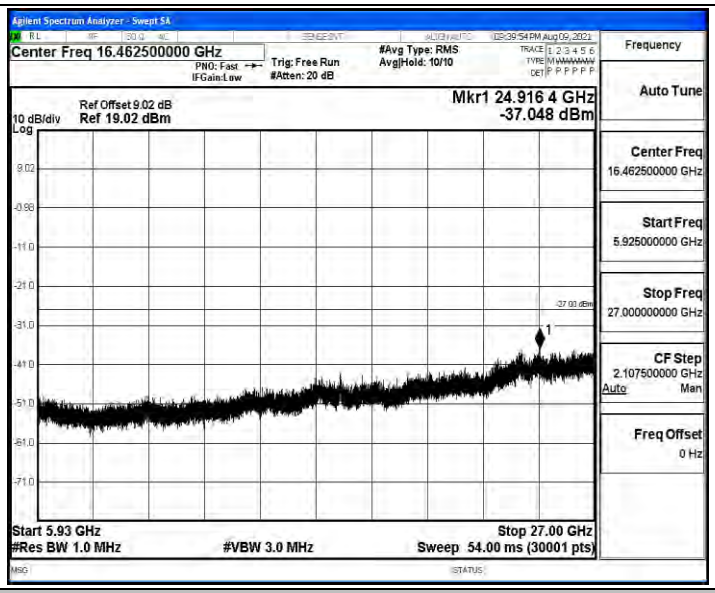
11AC40SISO_ANT4_5755_30~5650



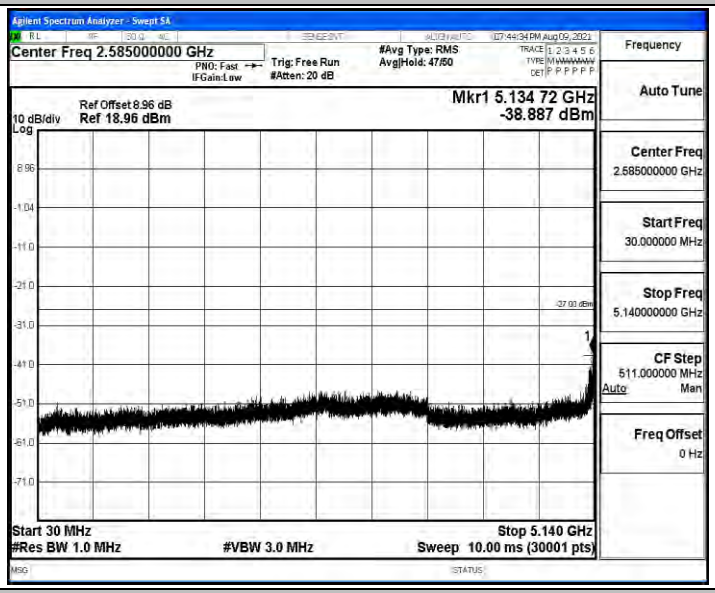
11AC40SISO_ANT4_5755_5925~40000


11AC40SISO_ANT4_5795_30~5650


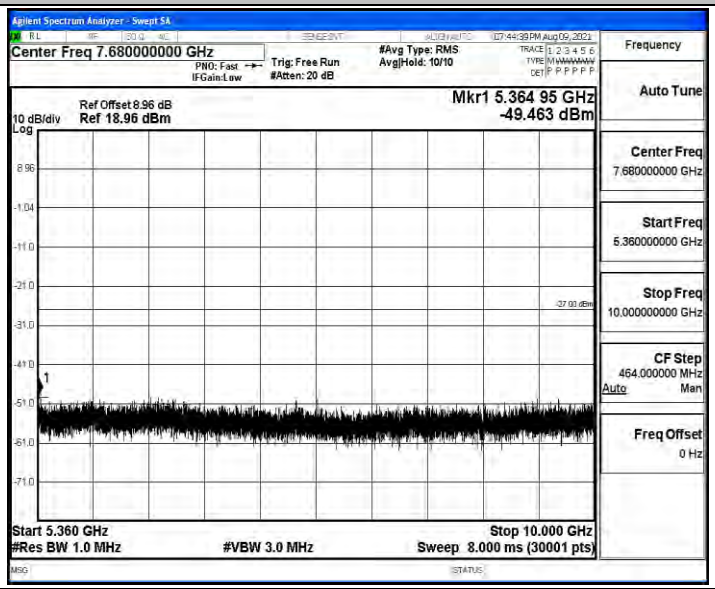
11AC40SISO_ANT4_5795_5925~40000

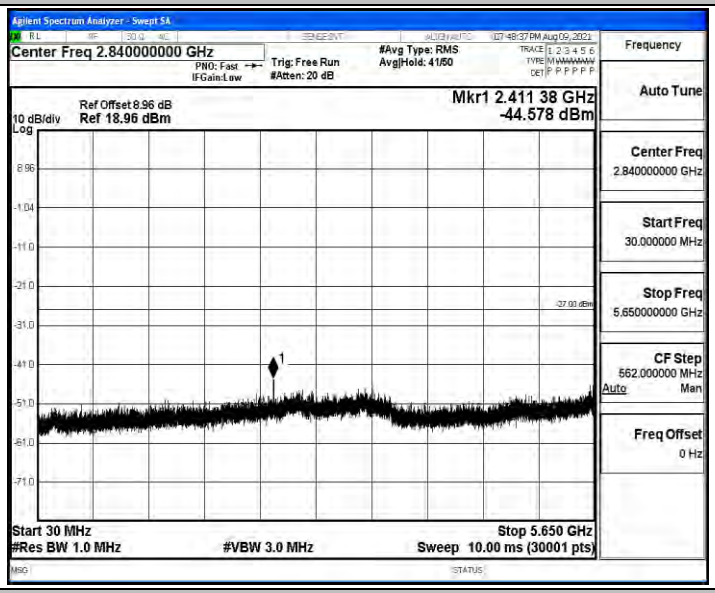


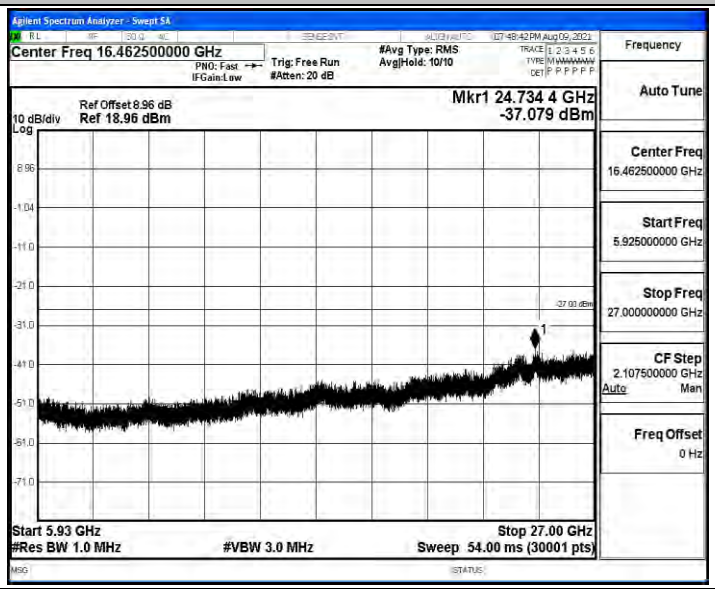
11AC80SISO_ANT4_5210_30~5140



11AC80SISO_ANT4_5210_5360~10000



11AC80SISO_ANT4_5775_30~5650


11AC80SISO_ANT4_5775_5925~40000


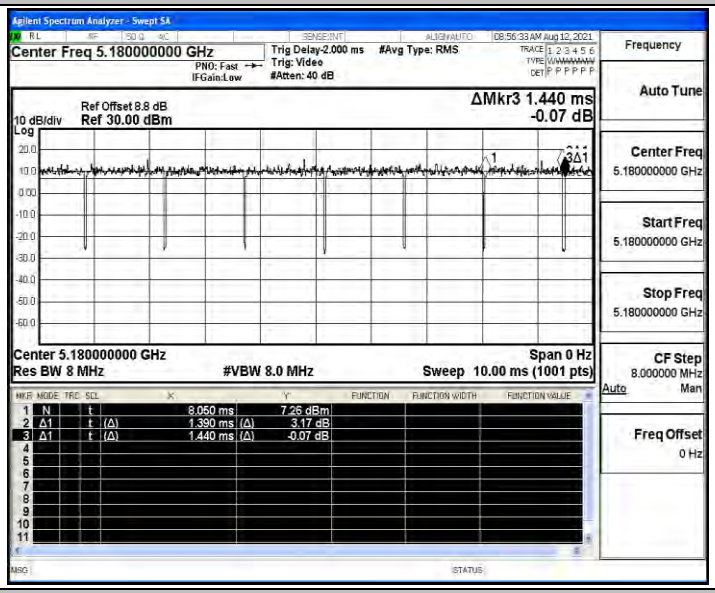
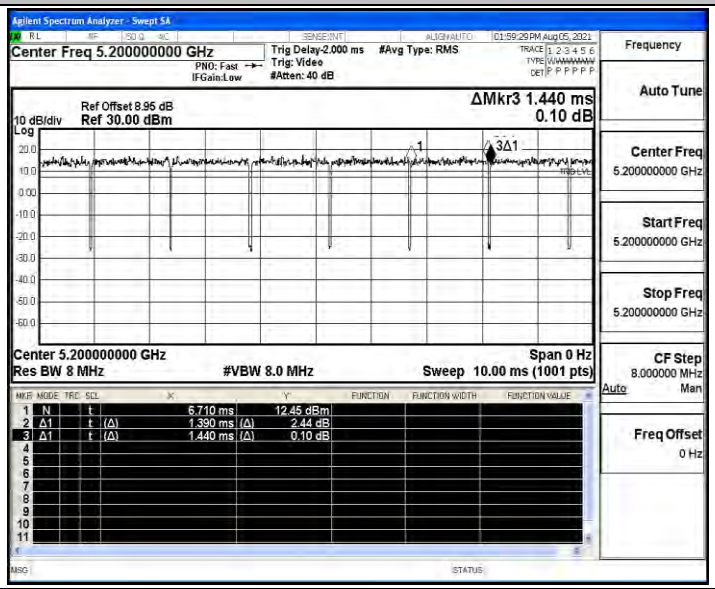
B.1: Duty Cycle

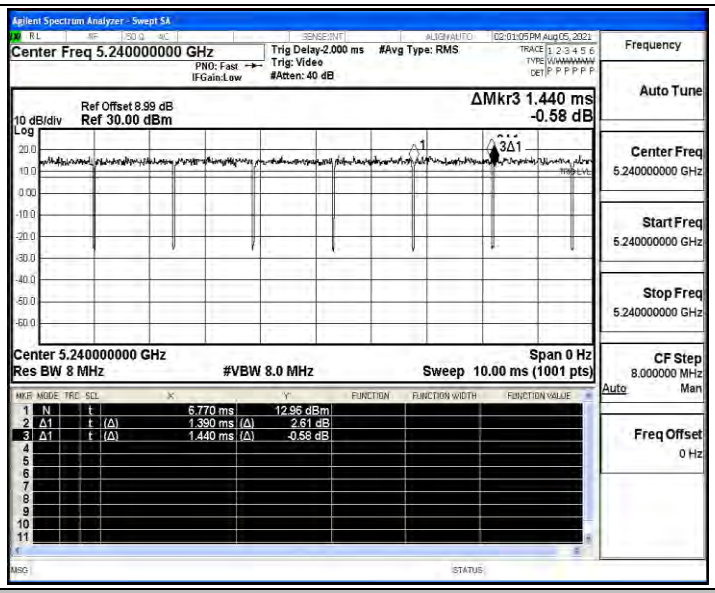
Test Result

TestMode	Antenna	Channel	Transmission Duration [ms]	Transmission Period [ms]	Duty Cycle [%]	Limit	Verdict
11A	ANT3	5180	1.39	1.44	96.53	---	PASS
		5200	1.39	1.44	96.53	---	PASS
		5240	1.39	1.44	96.53	---	PASS
		5745	1.39	1.44	96.53	---	PASS
		5785	1.39	1.44	96.53	---	PASS
		5825	1.39	1.44	96.53	---	PASS
11N20SISO	ANT3	5180	1.30	1.35	96.30	---	PASS
		5200	1.30	1.35	96.30	---	PASS
		5240	1.30	1.35	96.30	---	PASS
		5745	1.30	1.35	96.30	---	PASS
		5785	1.30	1.35	96.30	---	PASS
		5825	1.30	1.35	96.30	---	PASS
11N40SISO	ANT3	5190	0.64	0.69	92.75	---	PASS
		5230	0.64	0.69	92.75	---	PASS
		5755	0.64	0.69	92.75	---	PASS
		5795	0.64	0.69	92.75	---	PASS
11AC20SISO	ANT3	5180	1.31	1.36	96.32	---	PASS
		5200	1.31	1.36	96.32	---	PASS
		5240	1.31	1.36	96.32	---	PASS
		5745	1.31	1.36	96.32	---	PASS
		5785	1.31	1.36	96.32	---	PASS
		5825	1.31	1.36	96.32	---	PASS
11AC40SISO	ANT3	5190	0.65	0.70	92.86	---	PASS
		5230	0.65	0.70	92.86	---	PASS
		5755	0.65	0.70	92.86	---	PASS
		5795	0.65	0.70	92.86	---	PASS
11AC80SISO	ANT3	5210	0.32	0.37	86.49	---	PASS
		5775	0.32	0.37	86.49	---	PASS

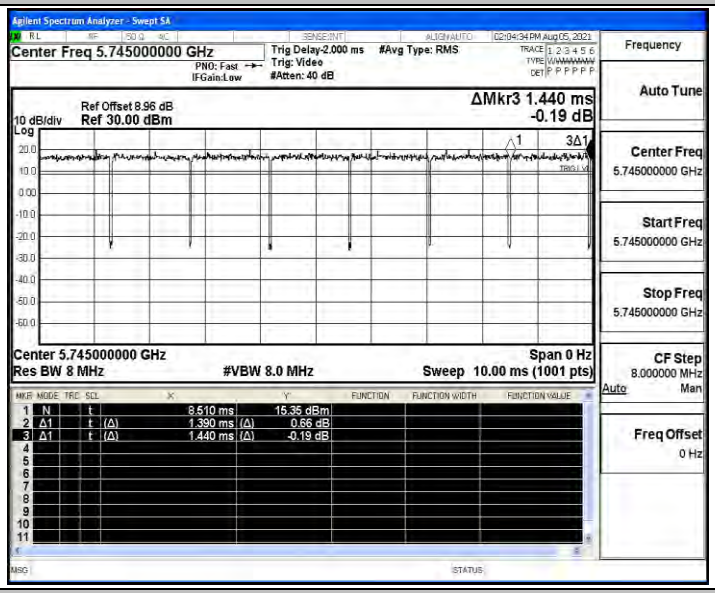
TestMode	Antenna	Channel	Transmission Duration [ms]	Transmission Period [ms]	Duty Cycle [%]	Limit	Verdict
11A	ANT4	5180	1.39	1.43	97.20	---	PASS
		5200	1.39	1.43	97.20	---	PASS
		5240	1.39	1.43	97.20	---	PASS
		5745	1.39	1.43	97.20	---	PASS
		5785	1.39	1.43	97.20	---	PASS
		5825	1.39	1.43	97.20	---	PASS
11N20SISO	ANT4	5180	1.30	1.34	97.01	---	PASS
		5200	1.30	1.34	97.01	---	PASS
		5240	1.30	1.34	97.01	---	PASS
		5745	1.30	1.34	97.01	---	PASS
		5785	1.30	1.34	97.01	---	PASS
		5825	1.30	1.34	97.01	---	PASS
11N40SISO	ANT4	5190	0.64	0.68	94.12	---	PASS
		5230	0.64	0.68	94.12	---	PASS
		5755	0.64	0.68	94.12	---	PASS
		5795	0.64	0.68	94.12	---	PASS
11AC20SISO	ANT4	5180	1.30	1.34	97.01	---	PASS
		5200	1.30	1.34	97.01	---	PASS
		5240	1.30	1.34	97.01	---	PASS
		5745	1.30	1.34	97.01	---	PASS
		5785	1.30	1.34	97.01	---	PASS
		5825	1.30	1.34	97.01	---	PASS
11AC40SISO	ANT4	5190	0.64	0.68	94.12	---	PASS
		5230	0.64	0.68	94.12	---	PASS
		5755	0.64	0.68	94.12	---	PASS
		5795	0.64	0.68	94.12	---	PASS
11AC80SISO	ANT4	5210	0.32	0.37	86.49	---	PASS
		5775	0.32	0.37	86.49	---	PASS

Test Graphs

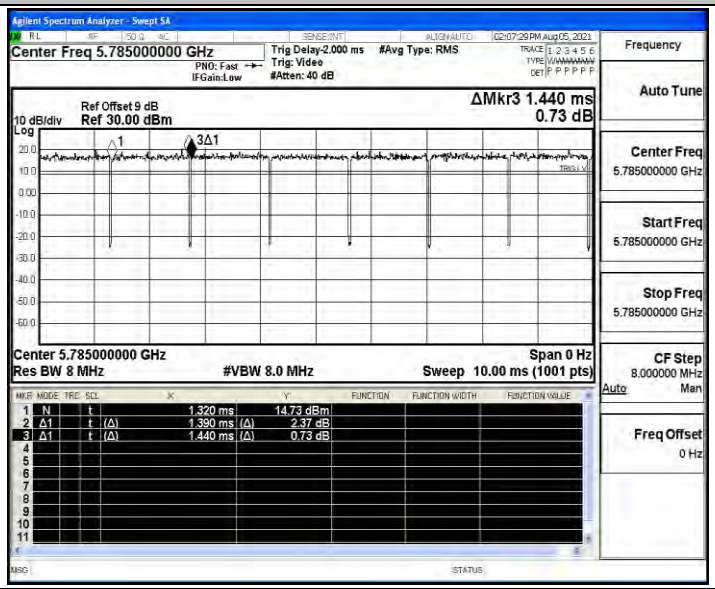
11A_ANT3_5180

11A_ANT3_5200

11A_ANT3_5240

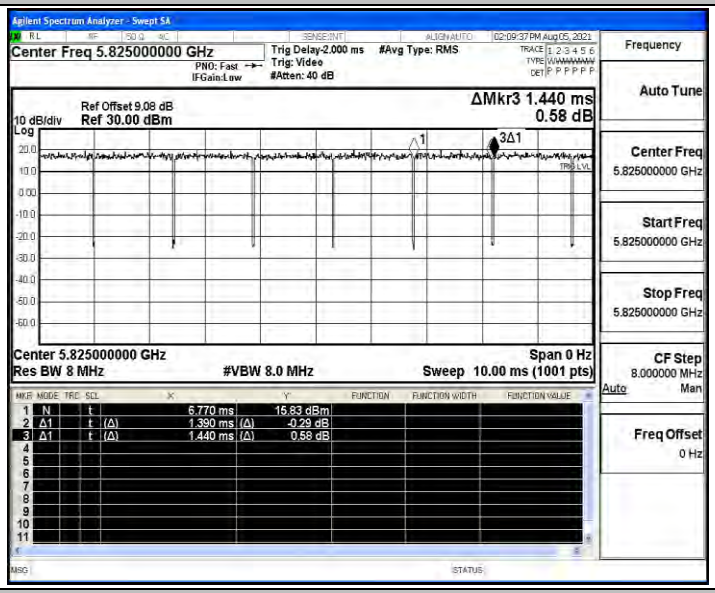


11A_ANT3_5745



11A_ANT3_5785



11A_ANT3_5825

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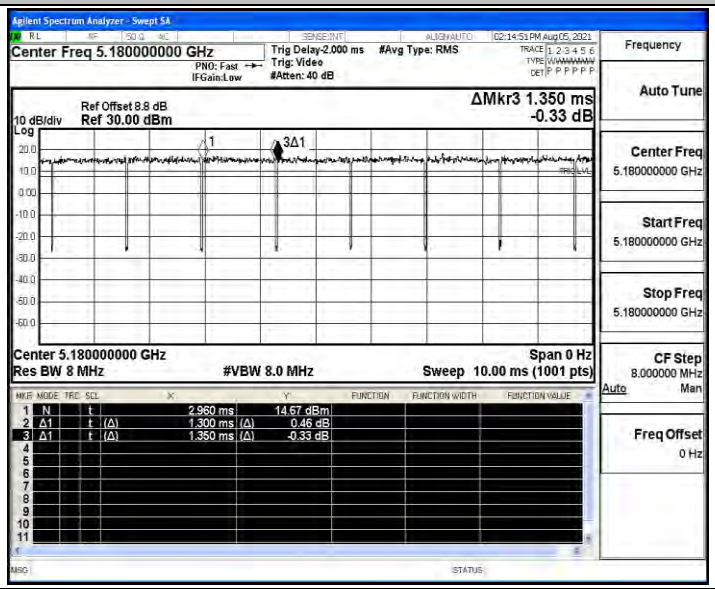
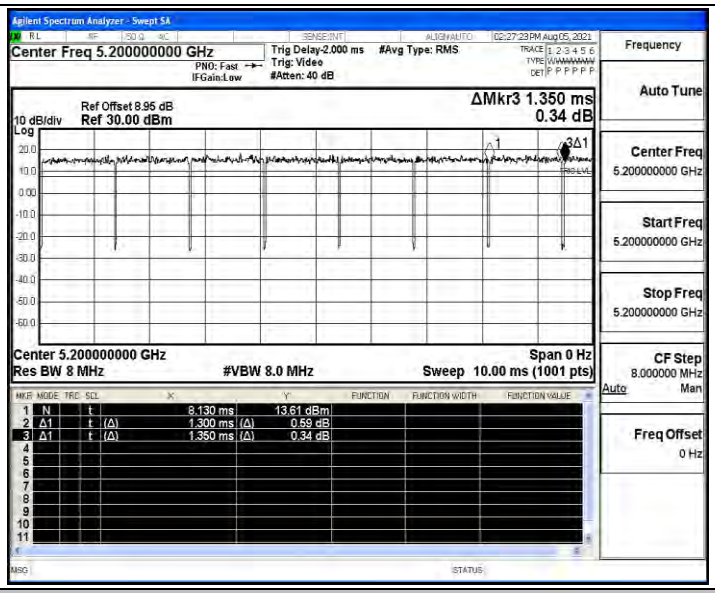
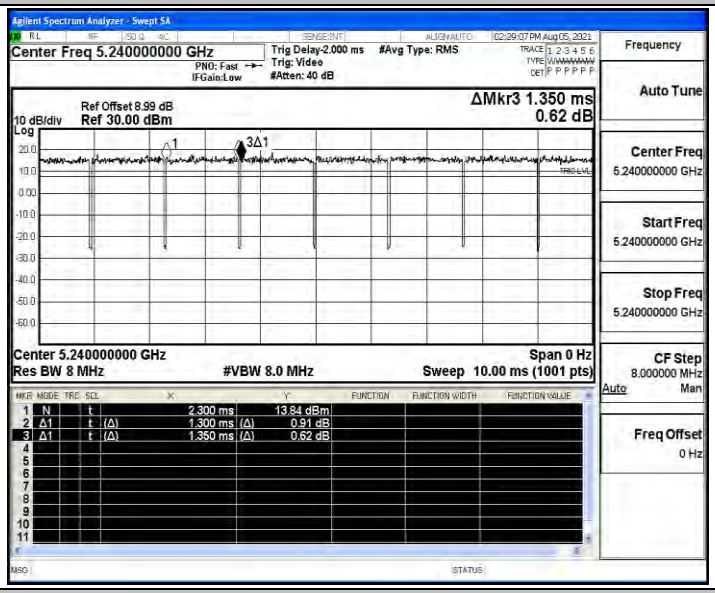
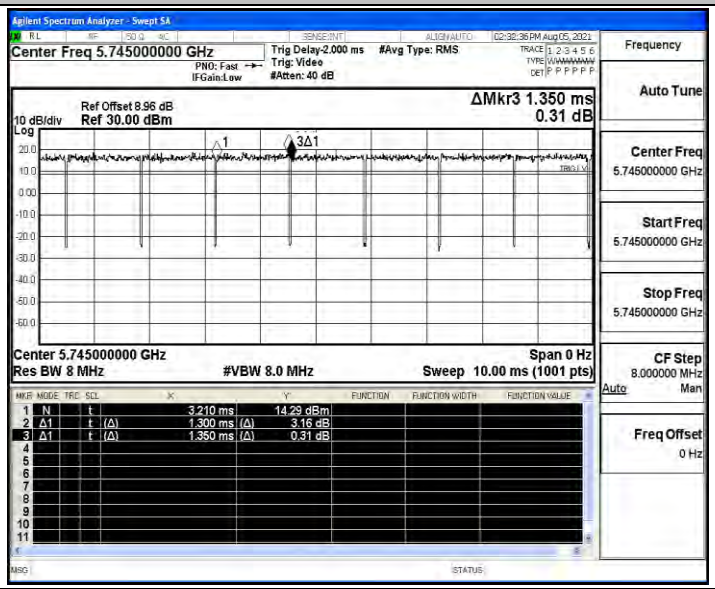

11N20SISO_ANT3_5200



11N20SISO_ANT3_5240



11N20SISO_ANT3_5745



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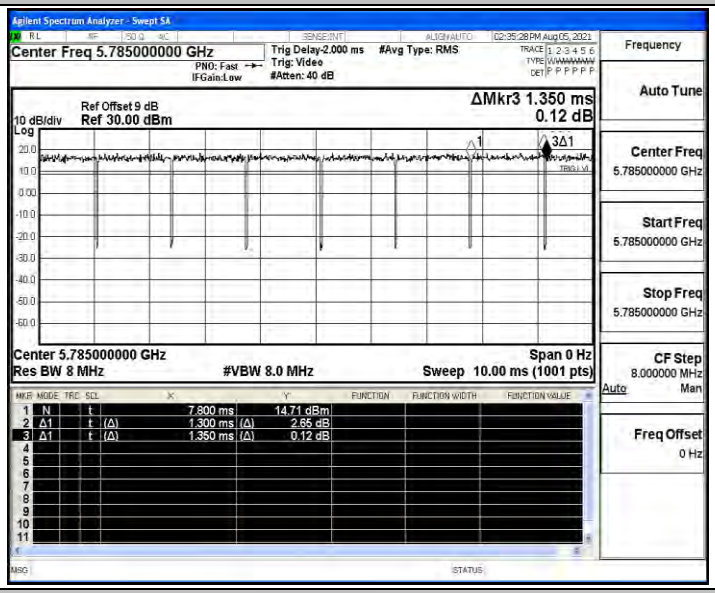
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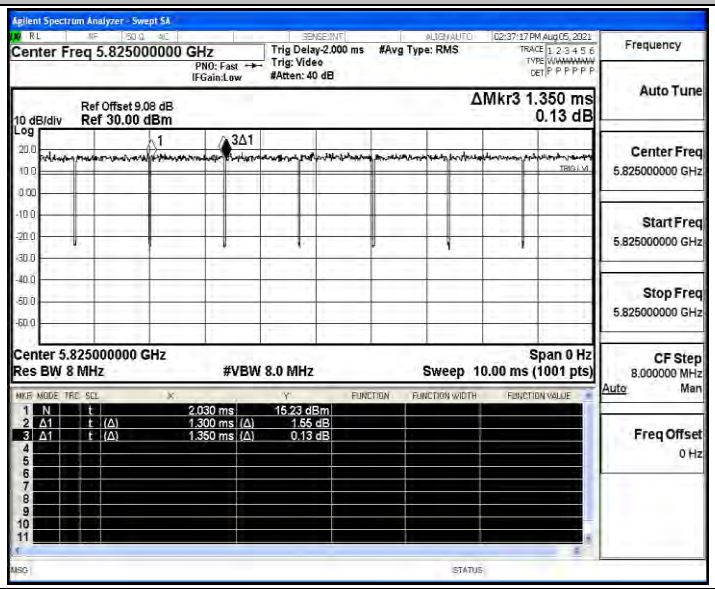
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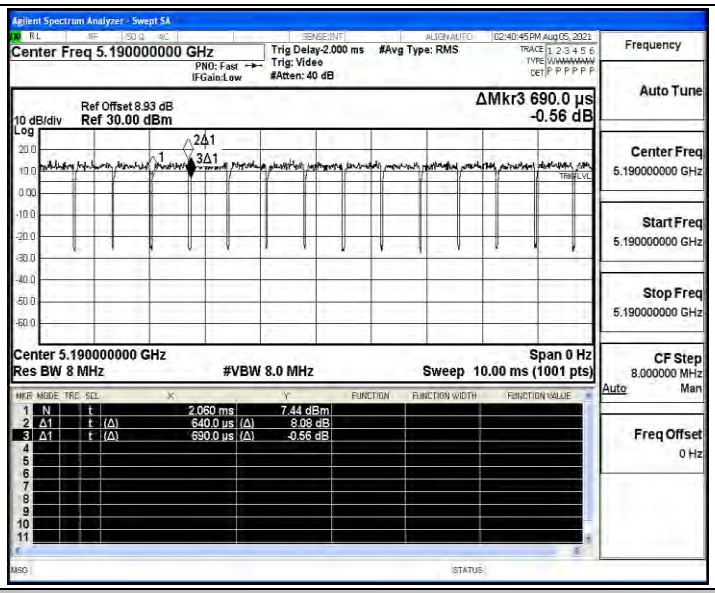

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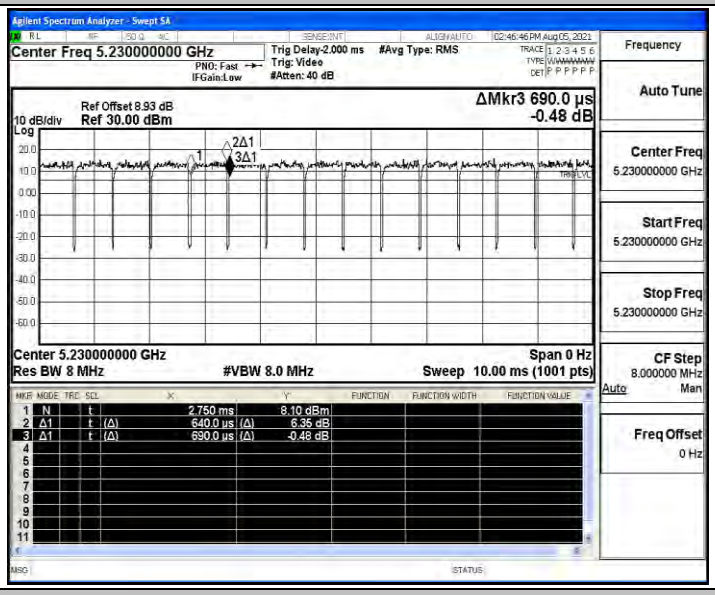

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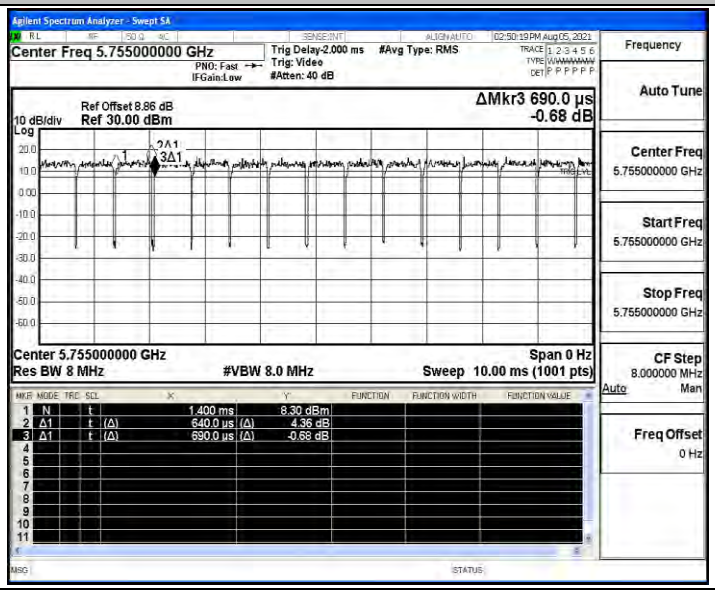

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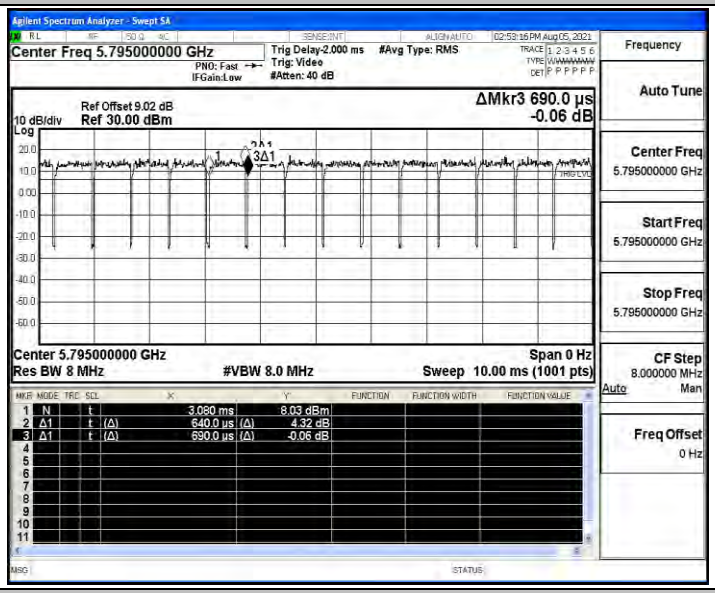
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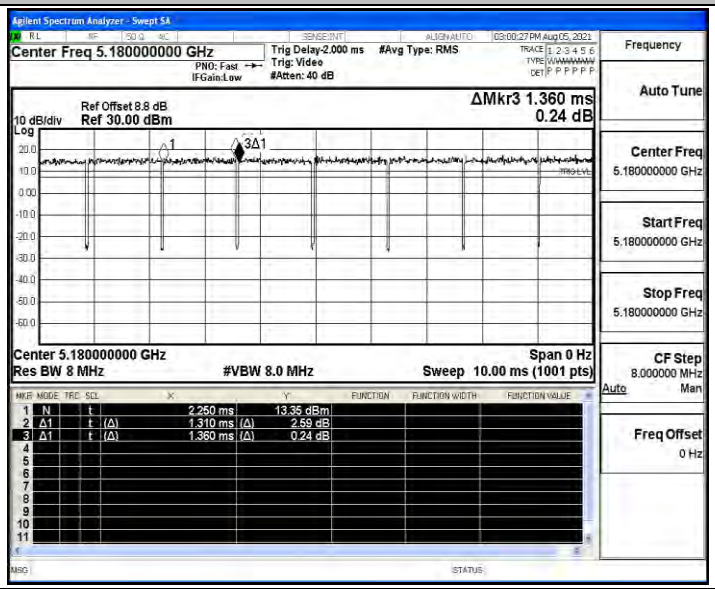
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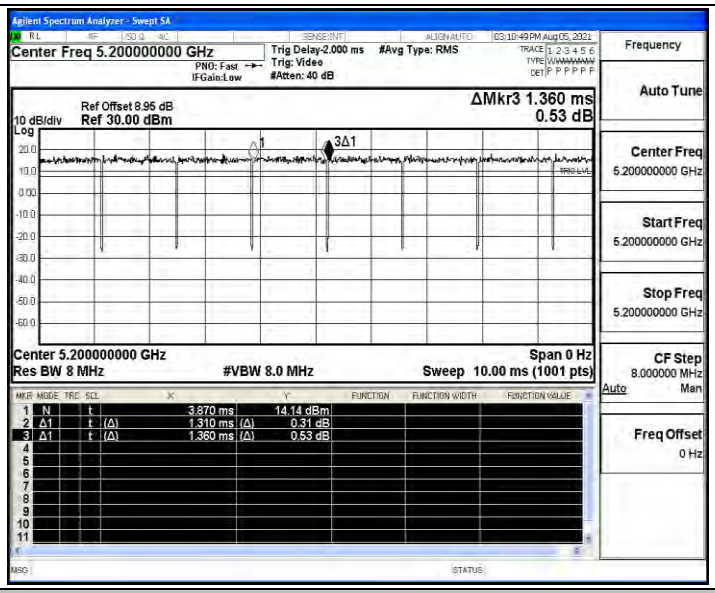

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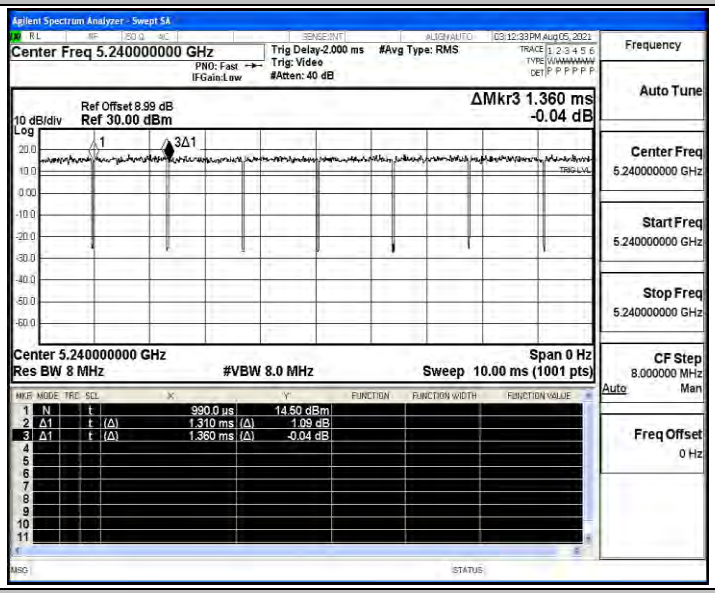

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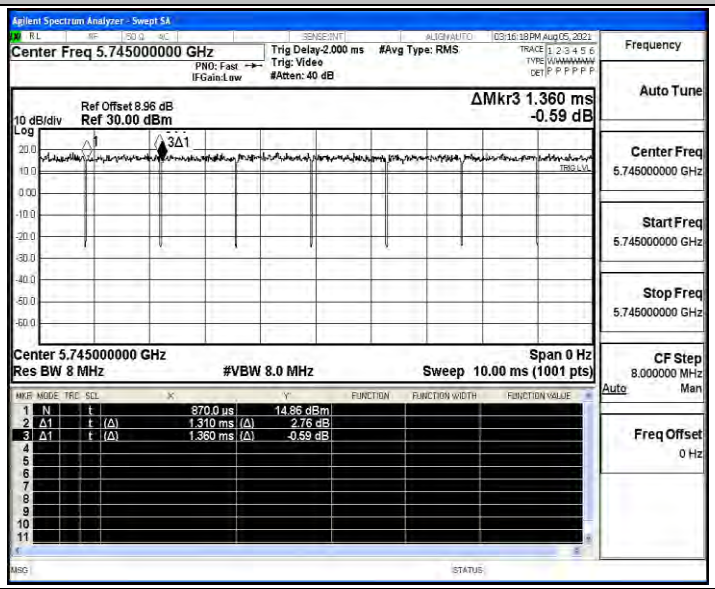

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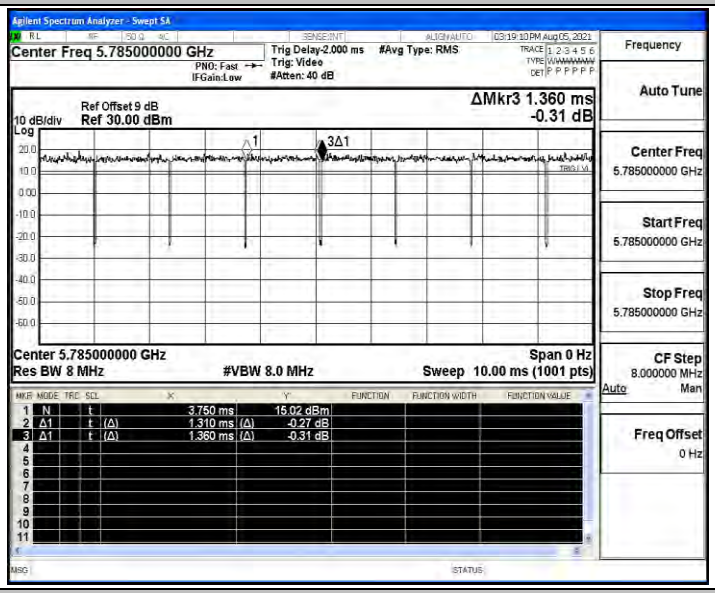
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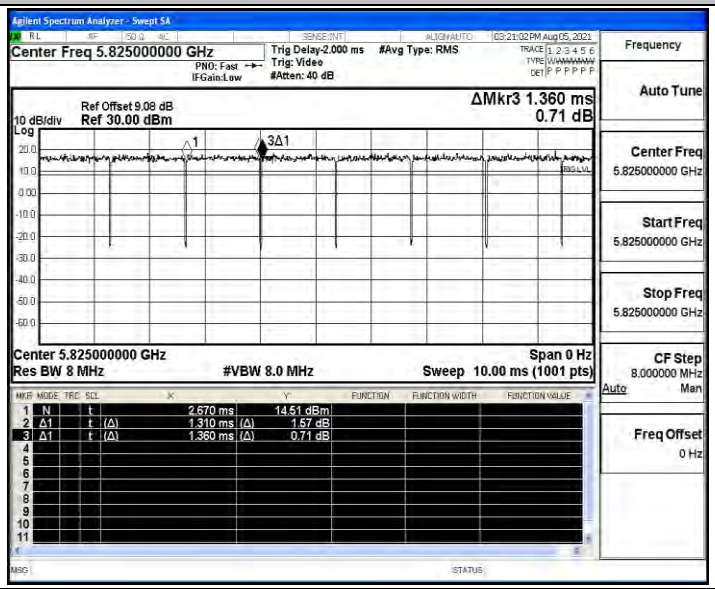
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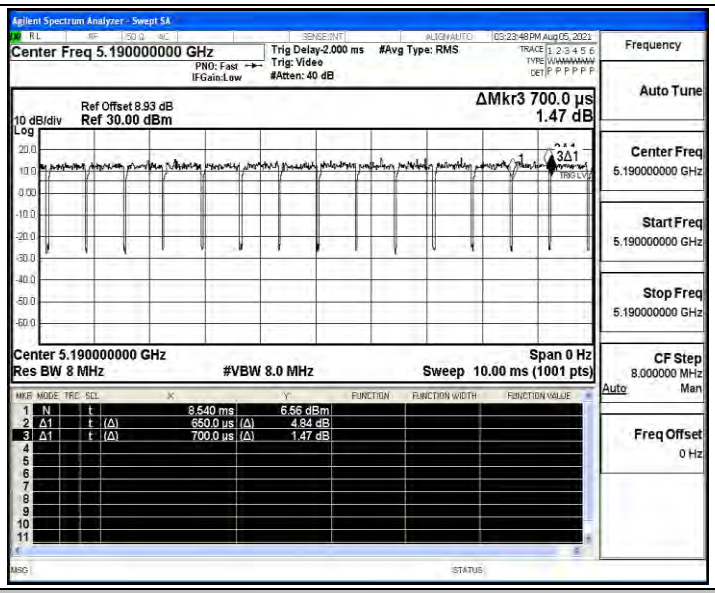

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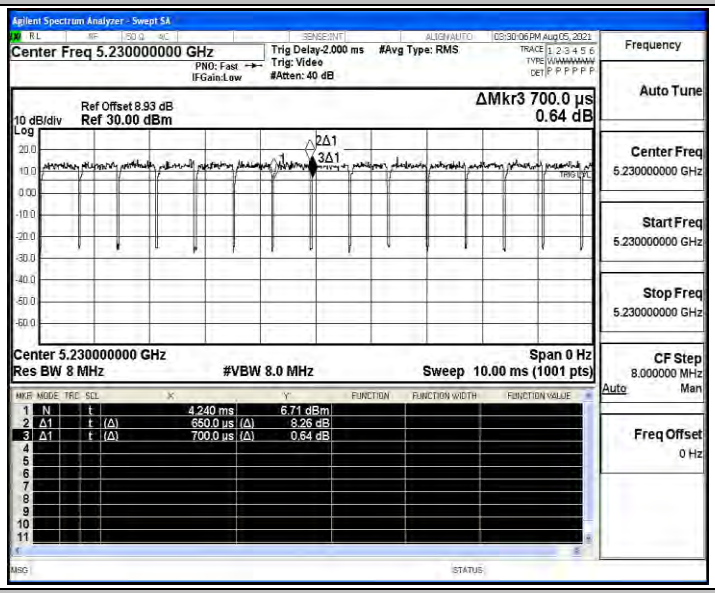

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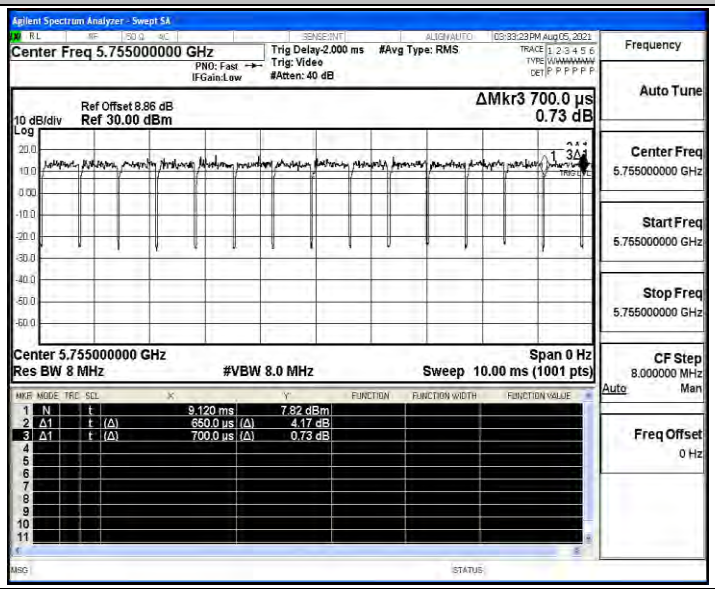

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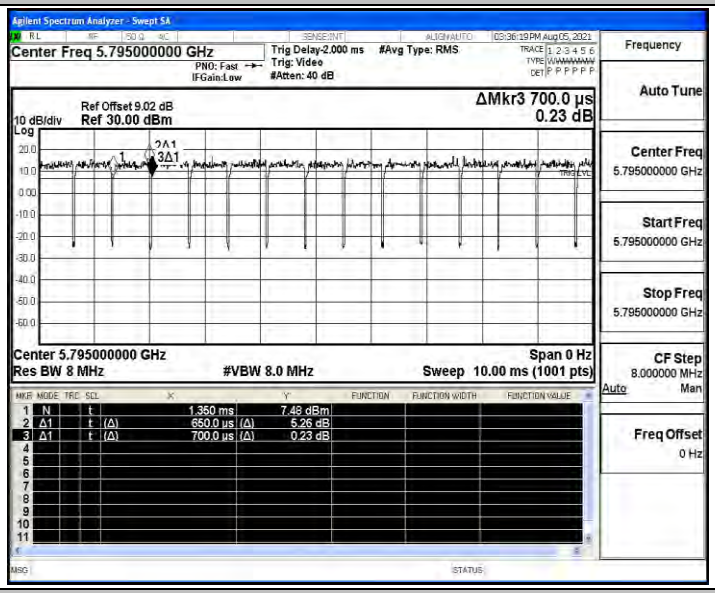
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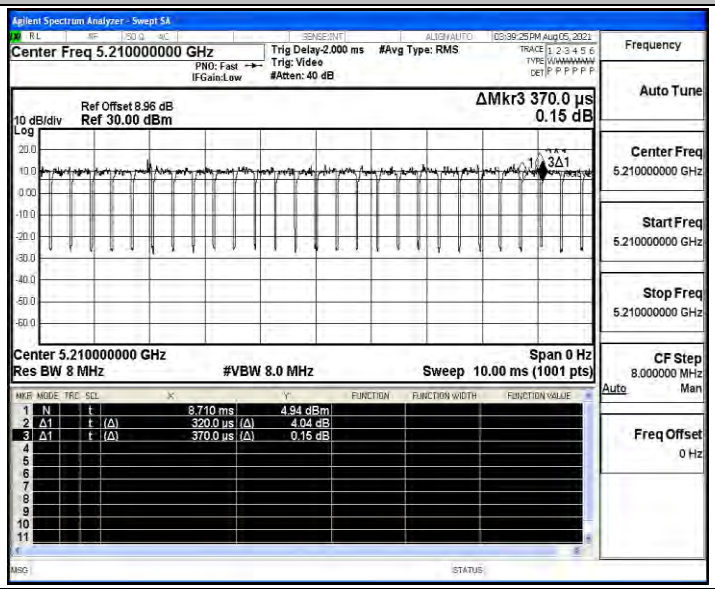
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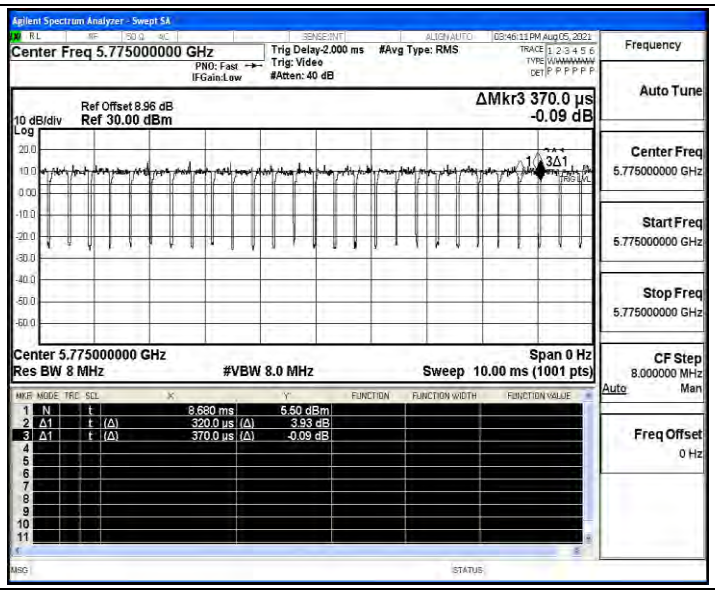

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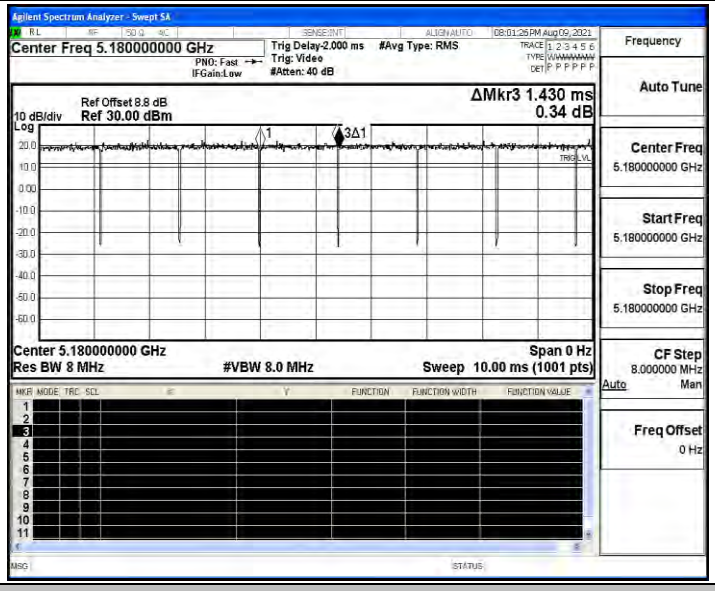

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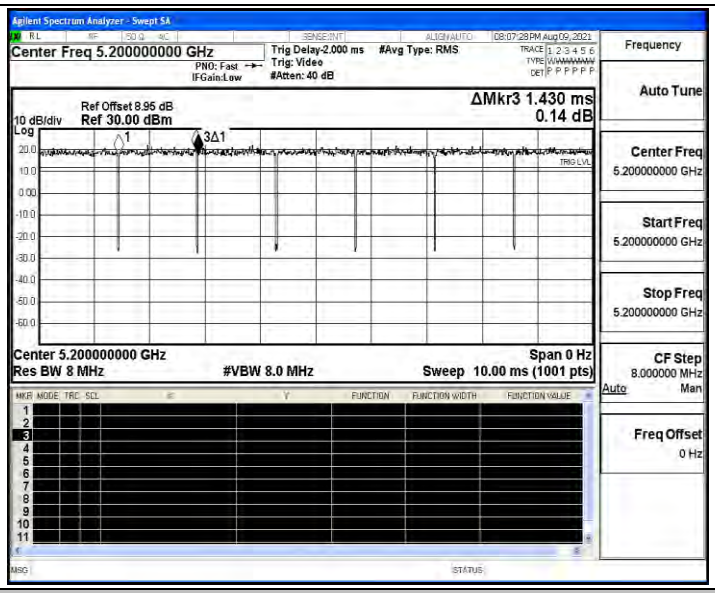

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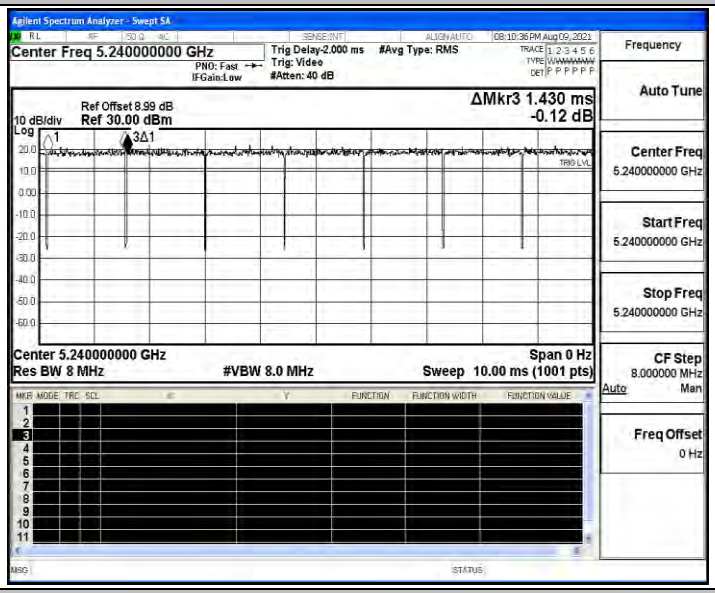

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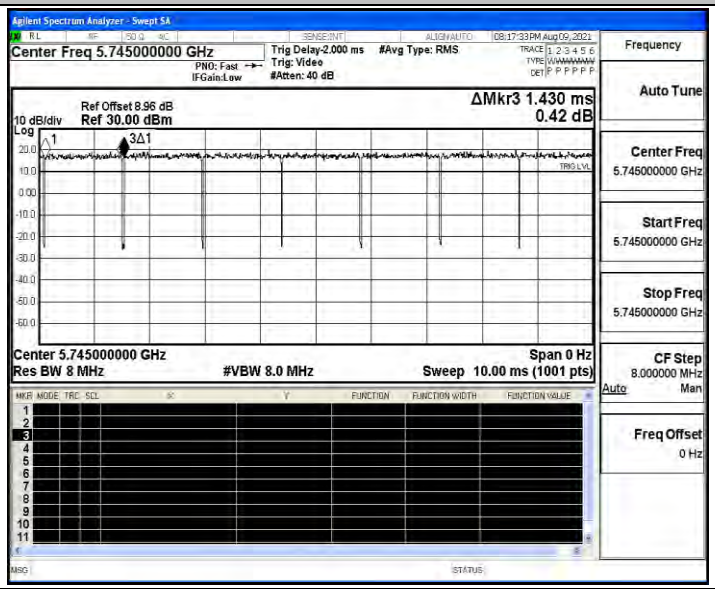

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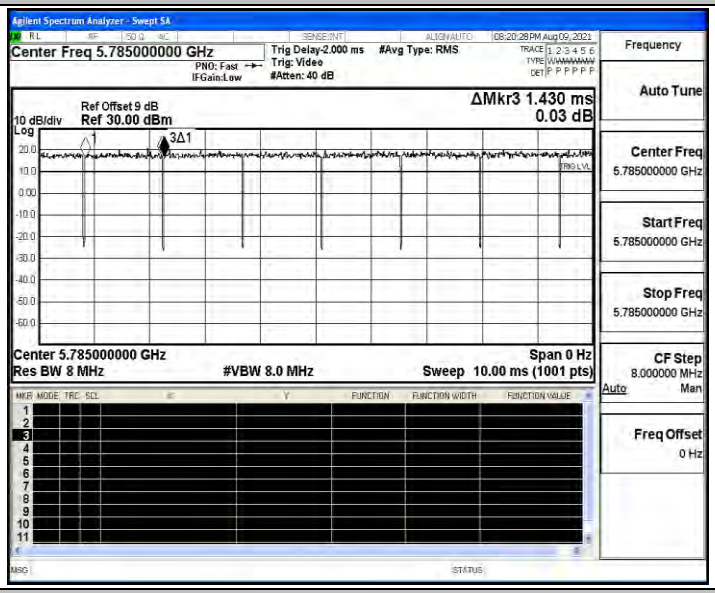
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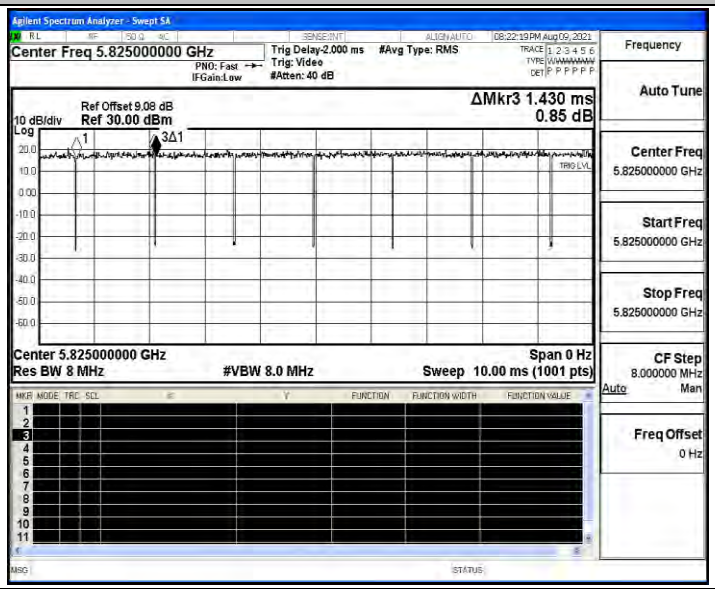
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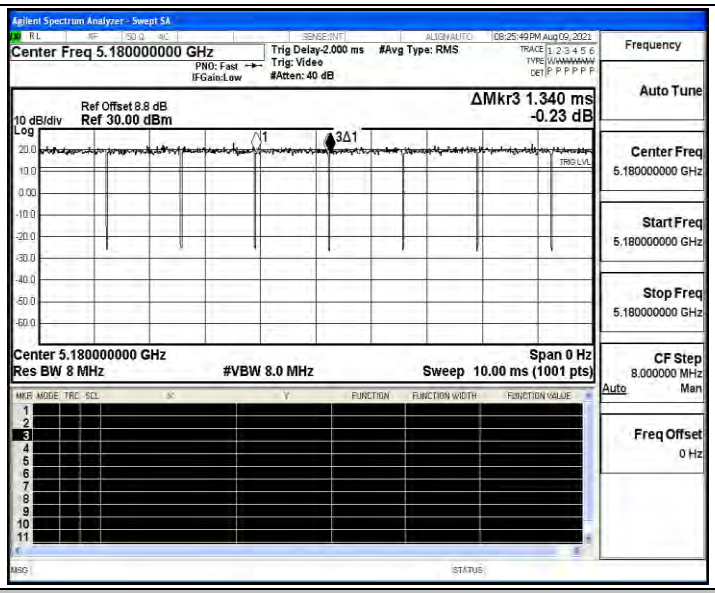

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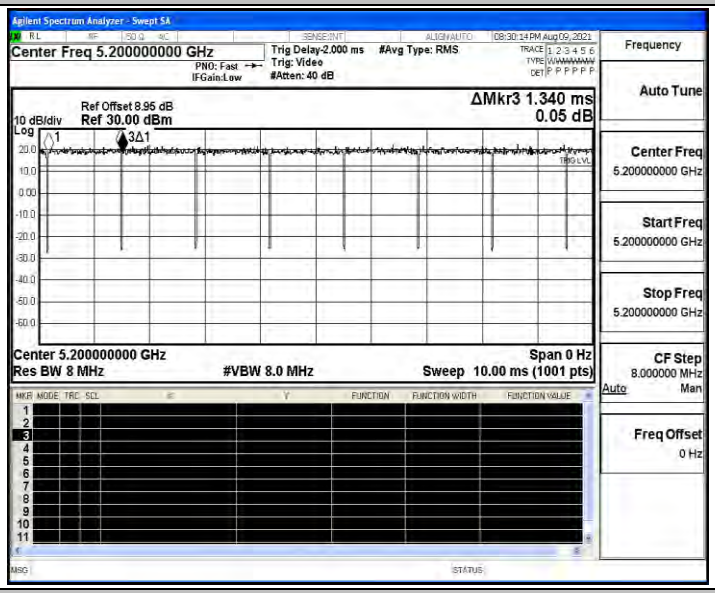

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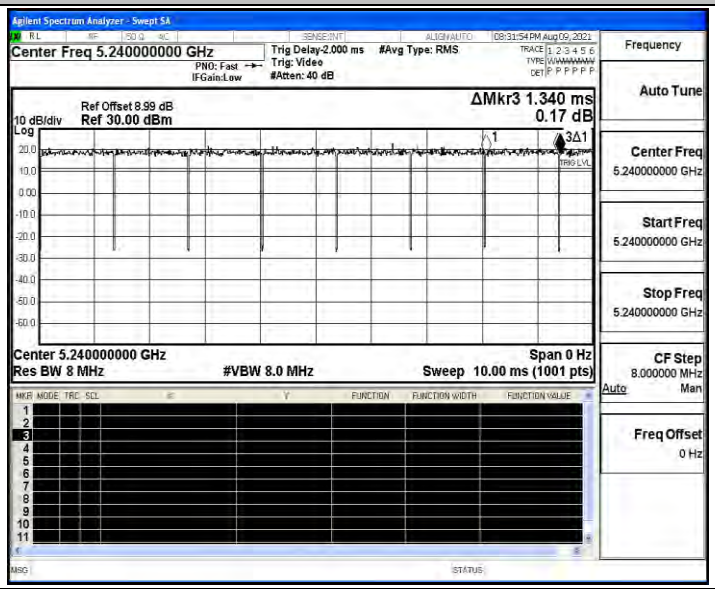

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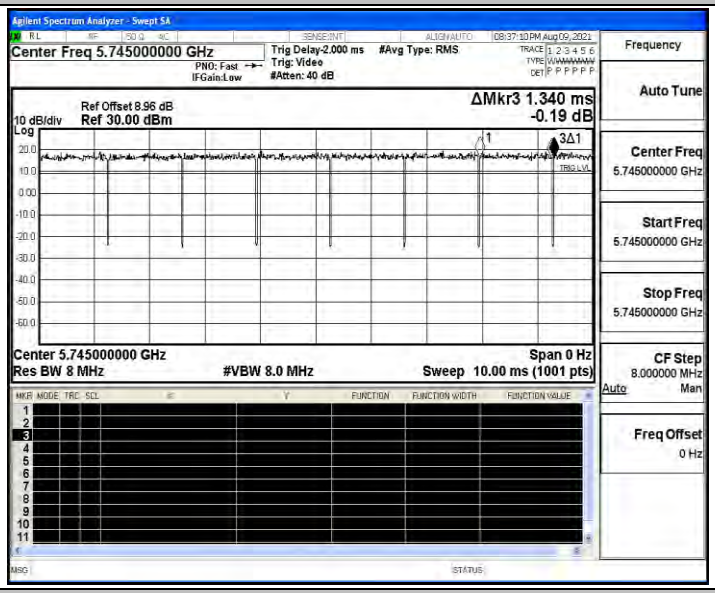
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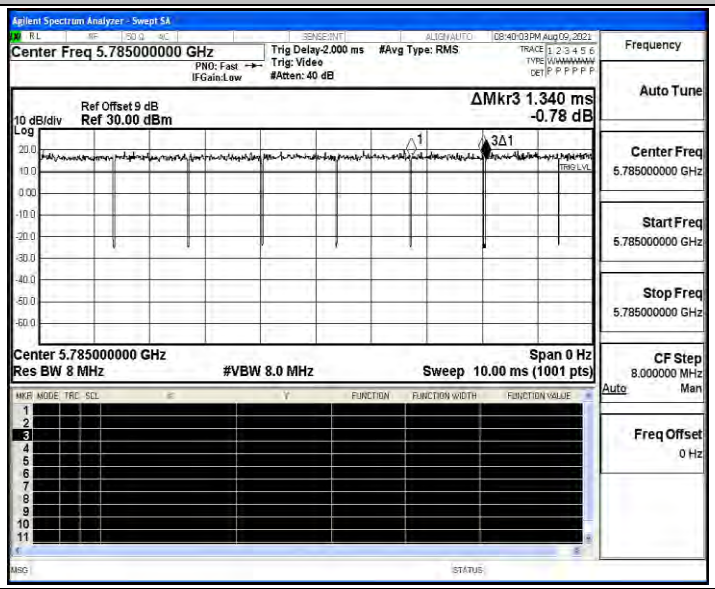
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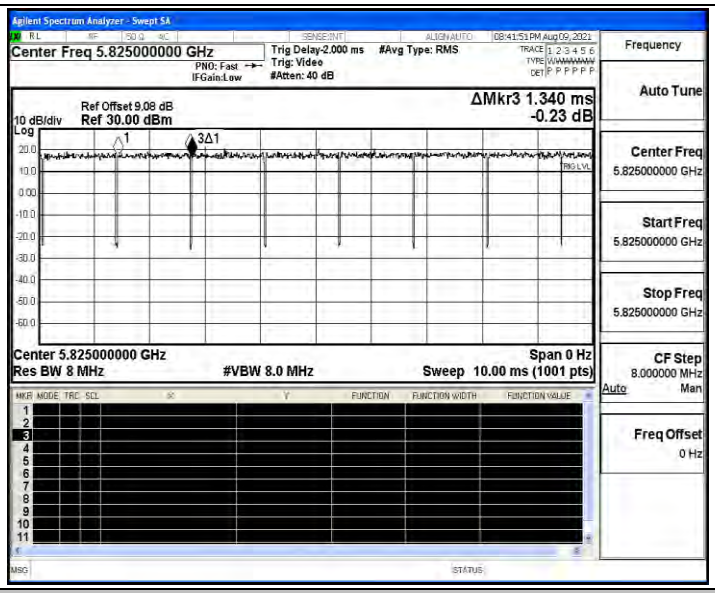

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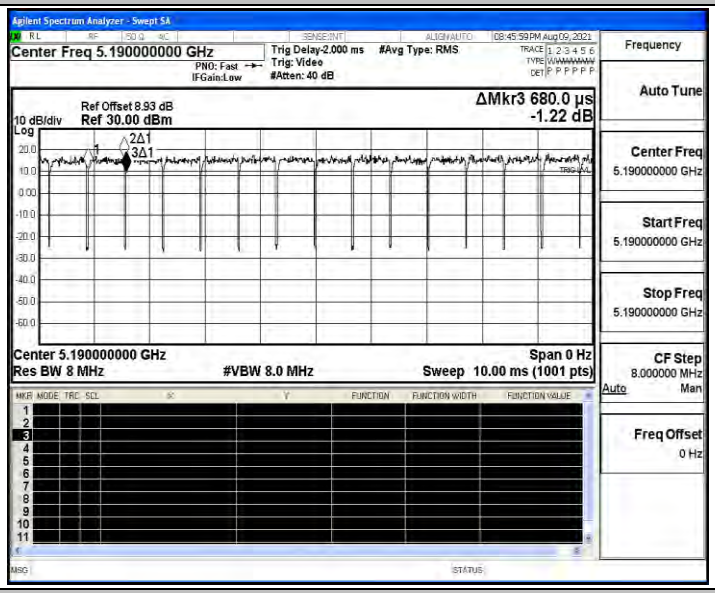

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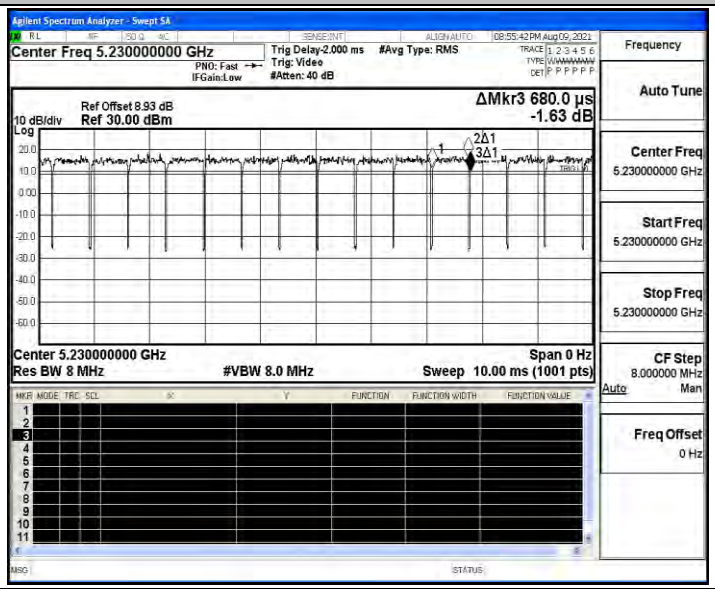

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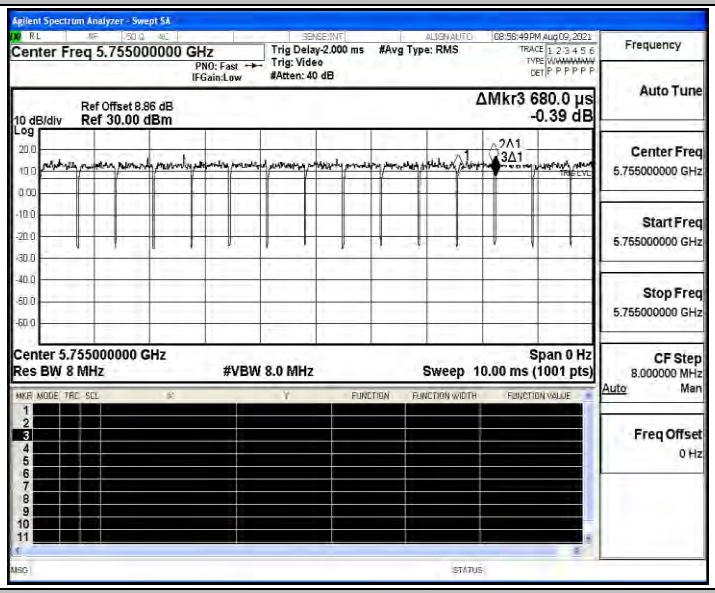
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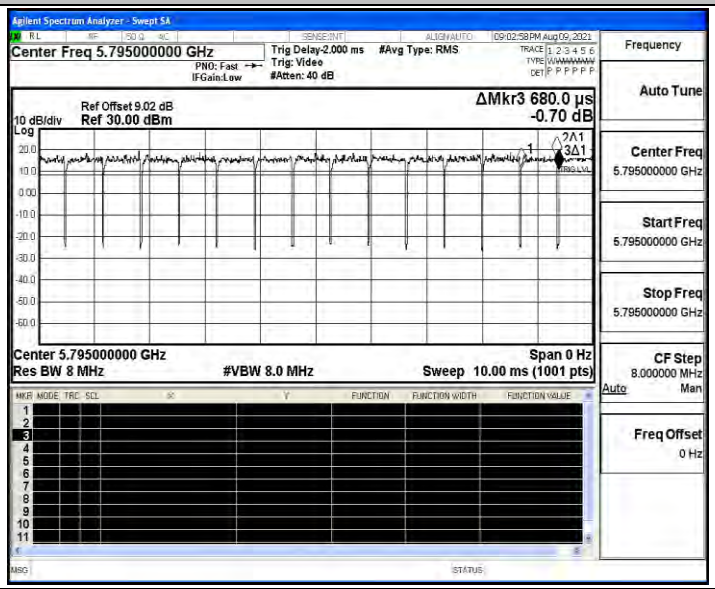
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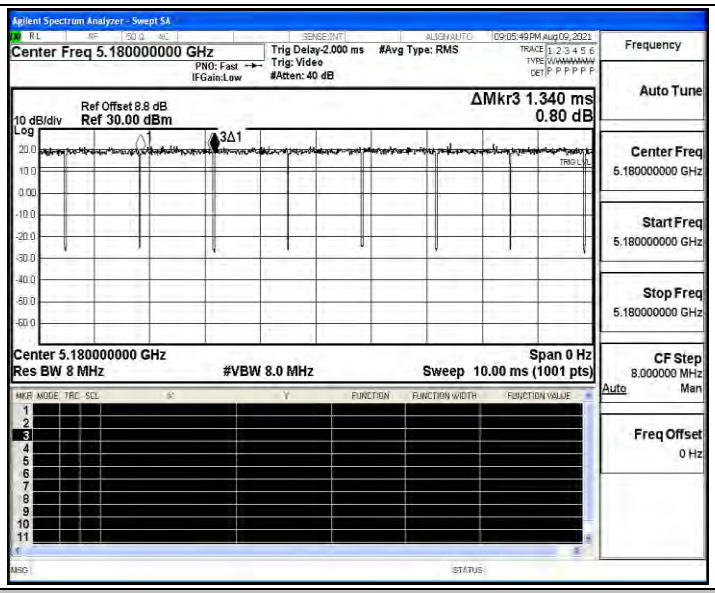

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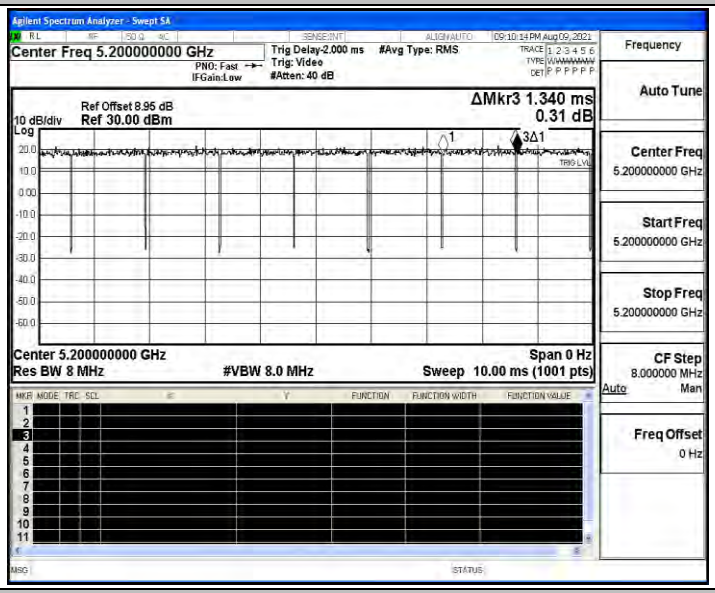

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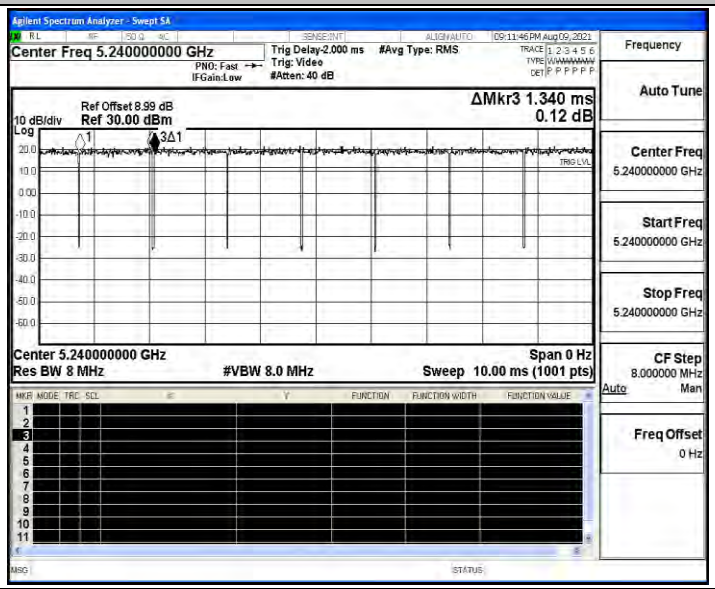

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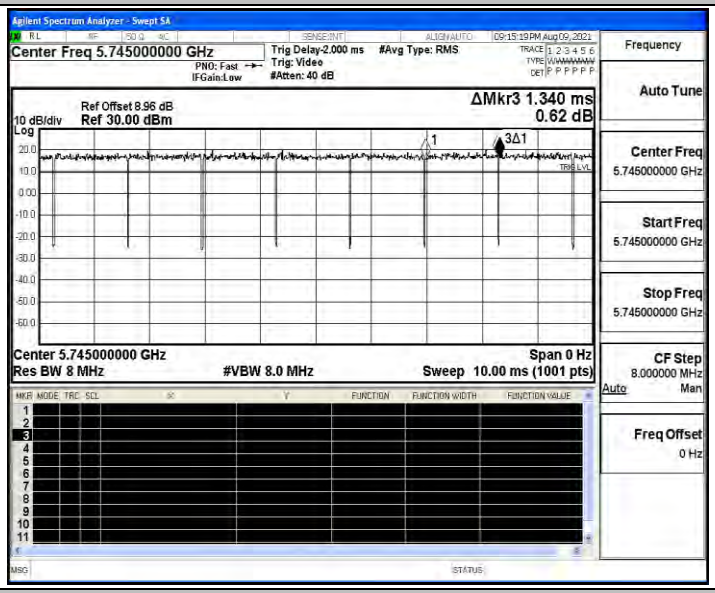
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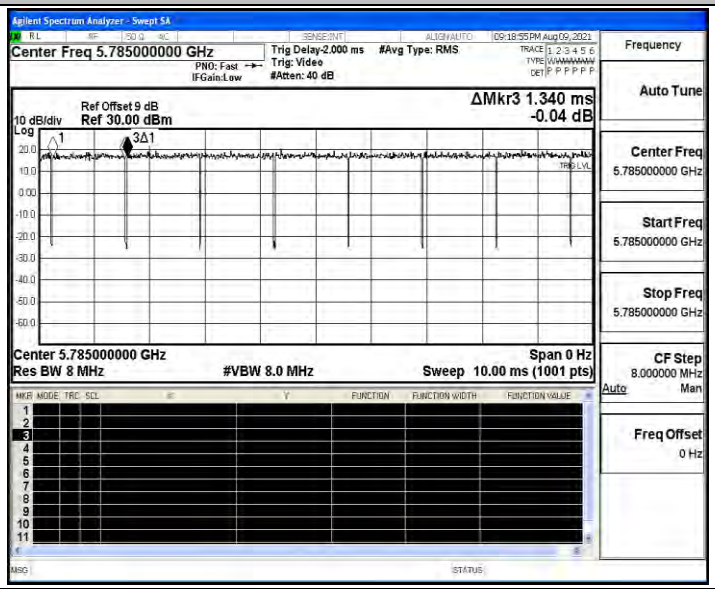
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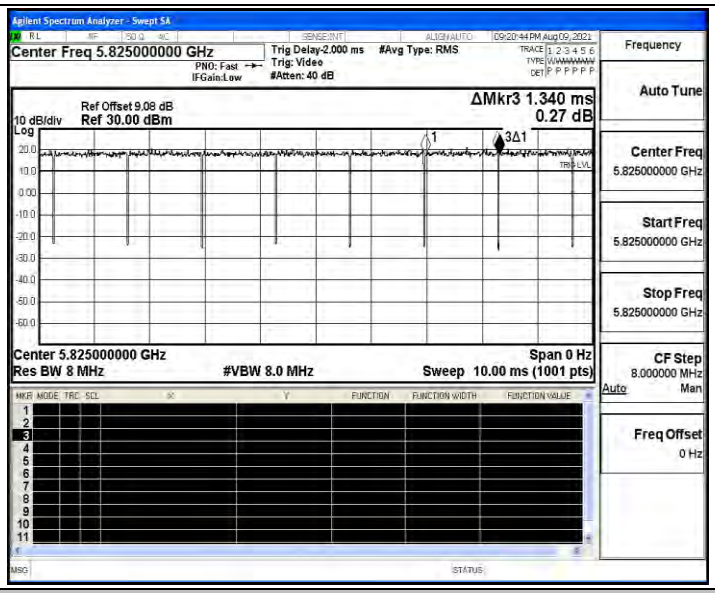

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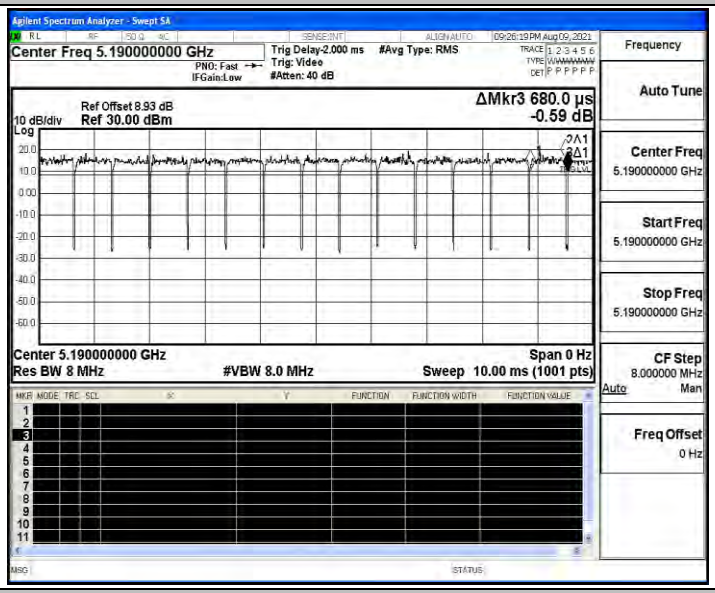

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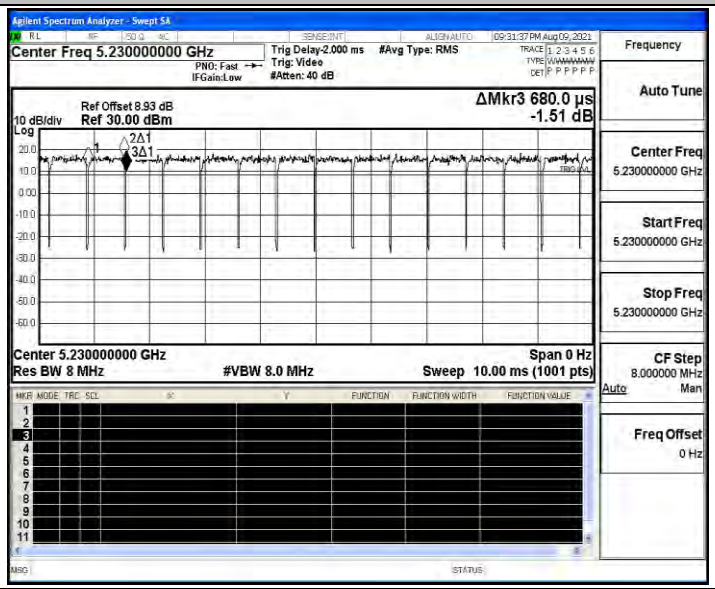

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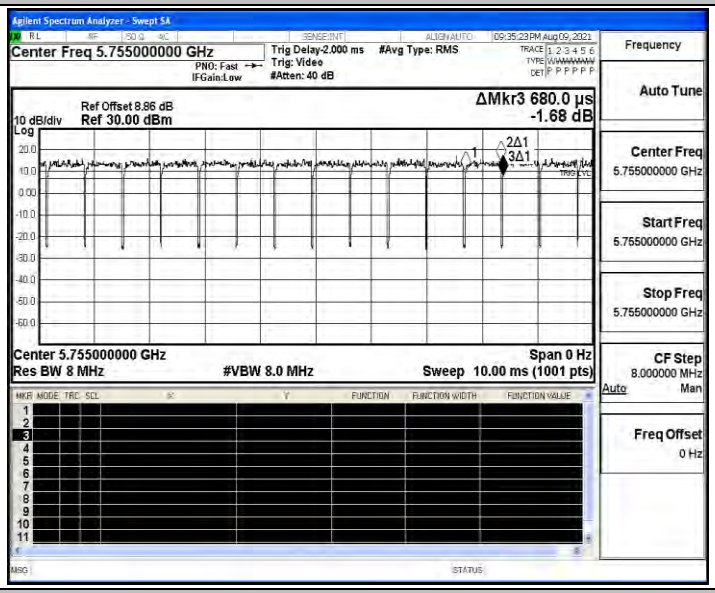
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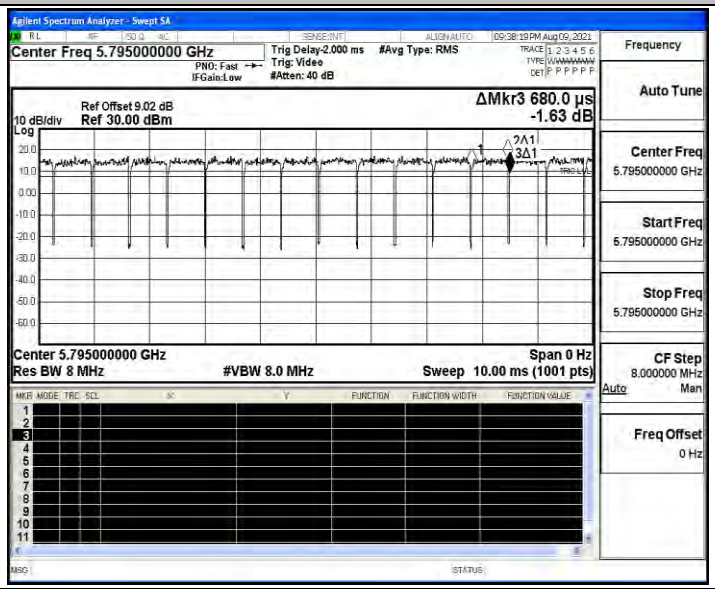
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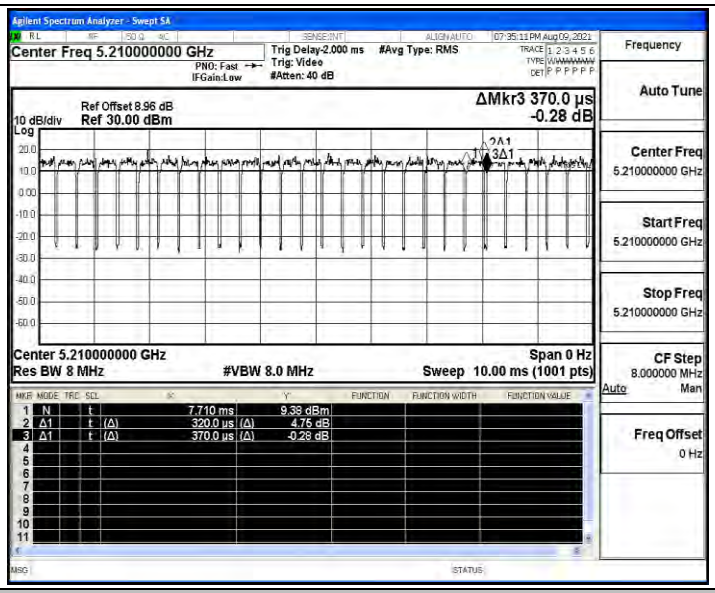

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