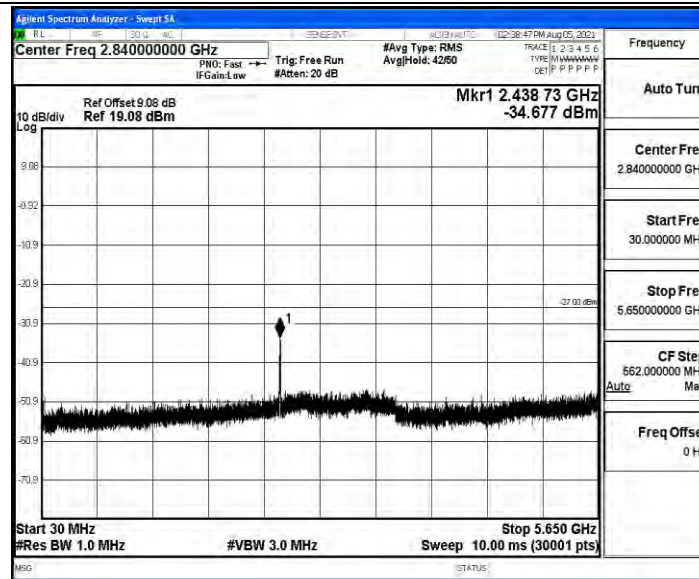
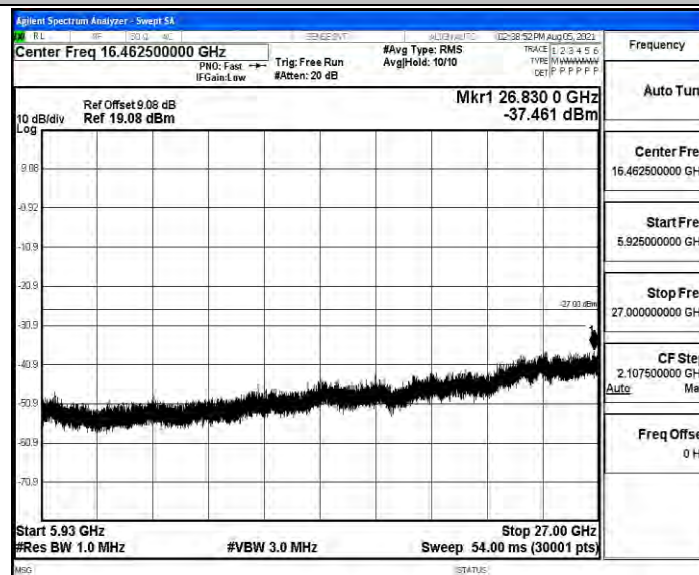
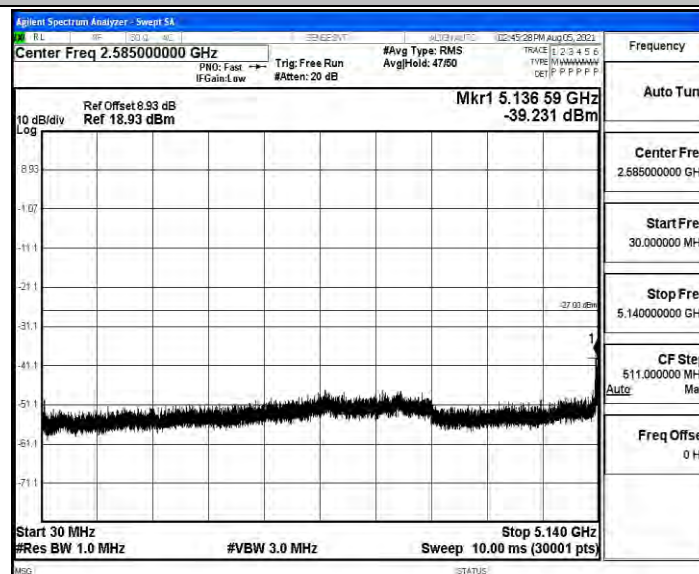




11N20SISO_ANT3_5825_5925~40000

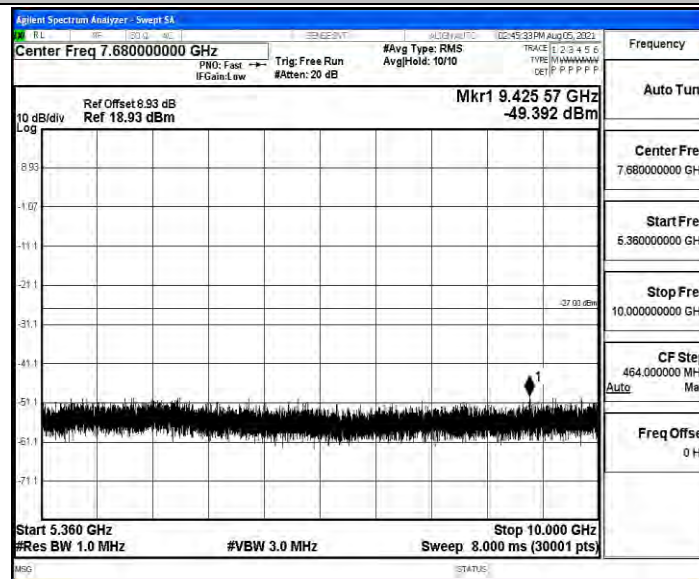


11N40SISO_ANT3_5190_30~5140

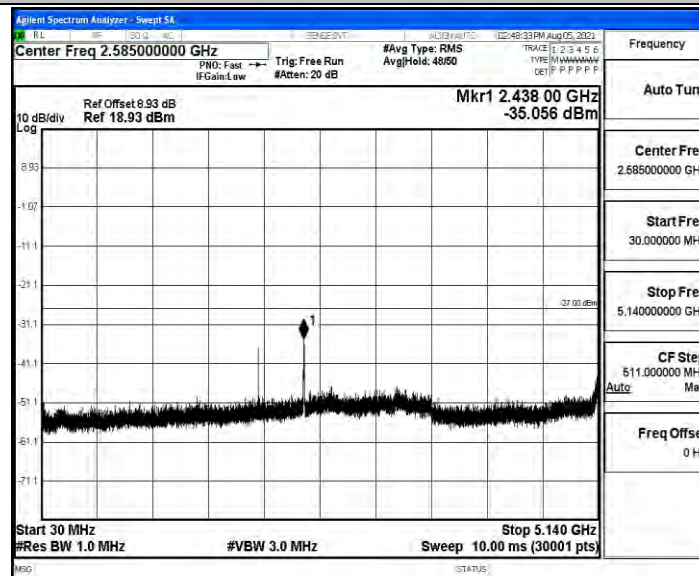




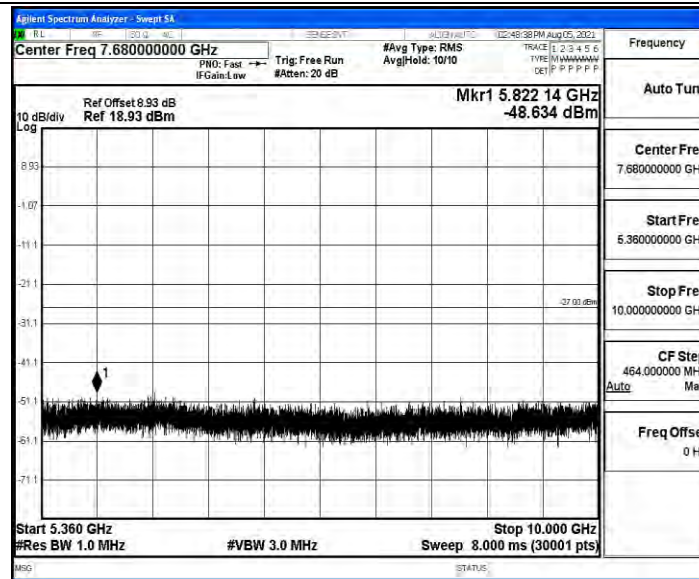
11N40SISO_ANT3_5190_5360~10000



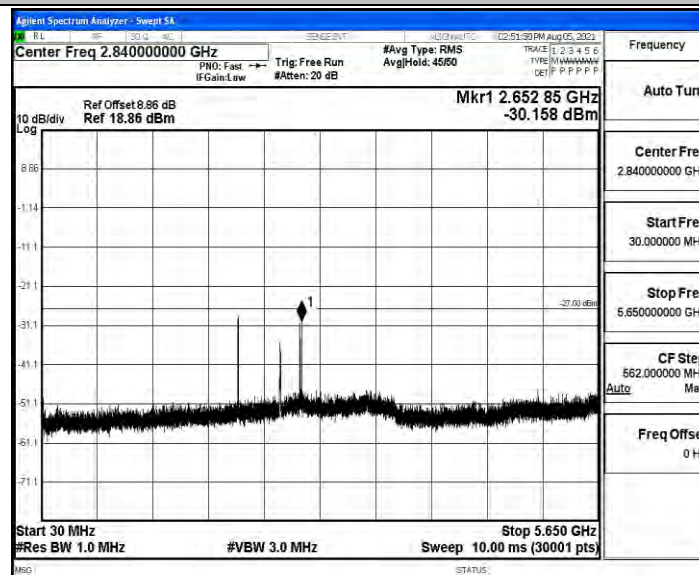
11N40SISO_ANT3_5230_30~5140



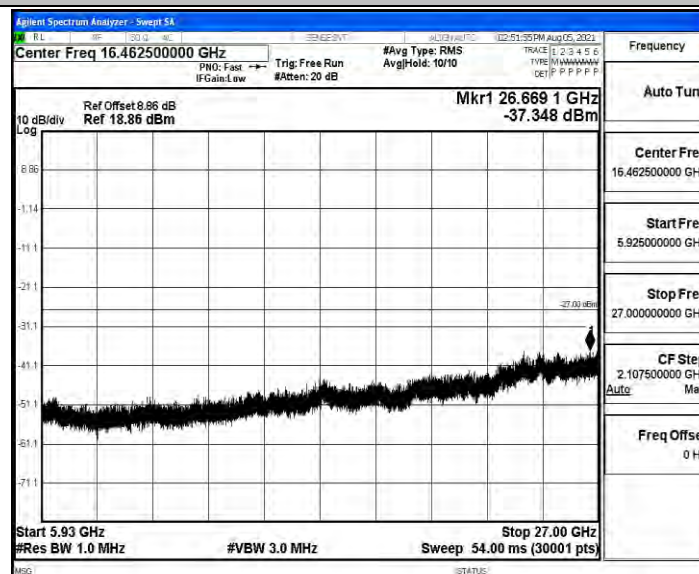
11N40SISO_ANT3_5230_5360~10000

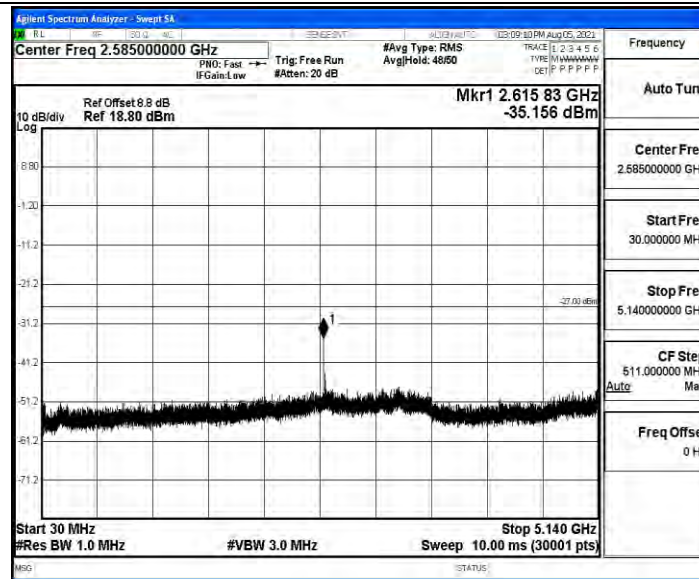


11N40SISO_ANT3_5755_30~5650

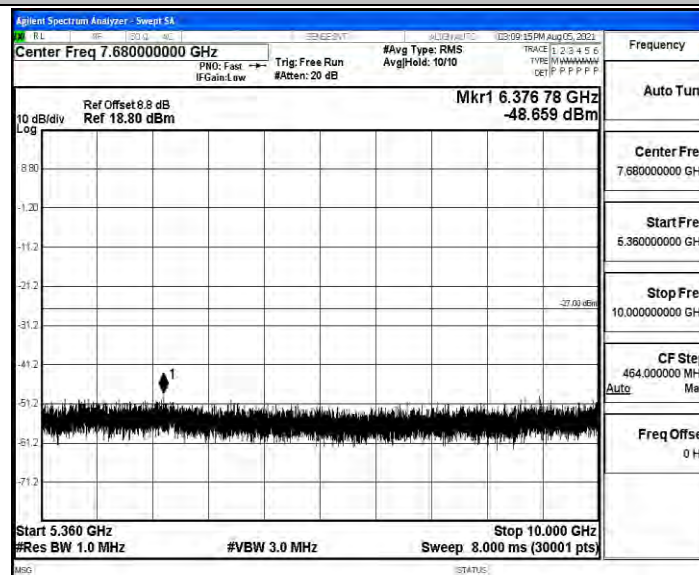


11N40SISO_ANT3_5755_5925~40000

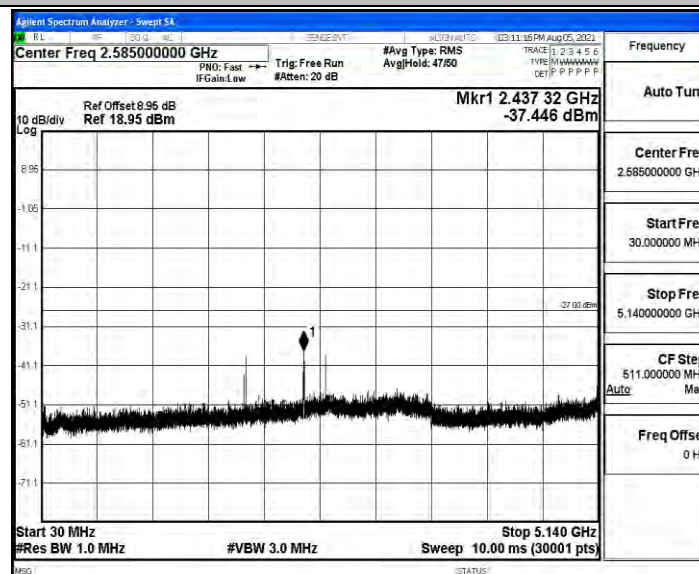




11AC20SISO_ANT3_5180_5360~10000

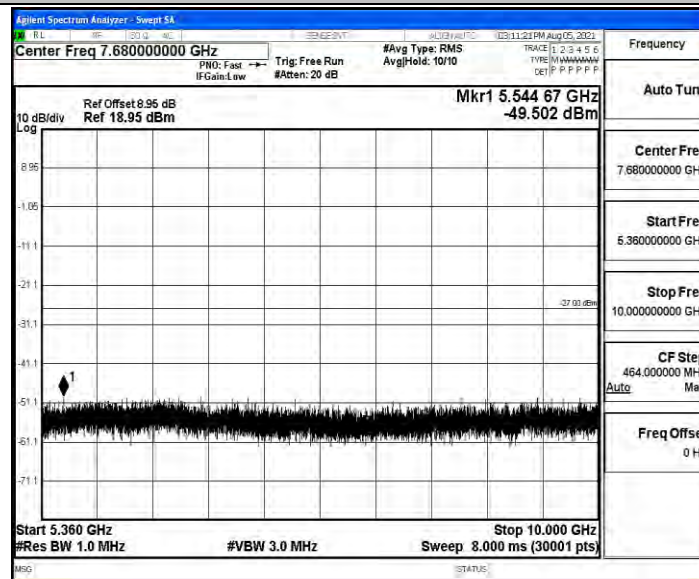


11AC20SISO_ANT3_5200_30~5140

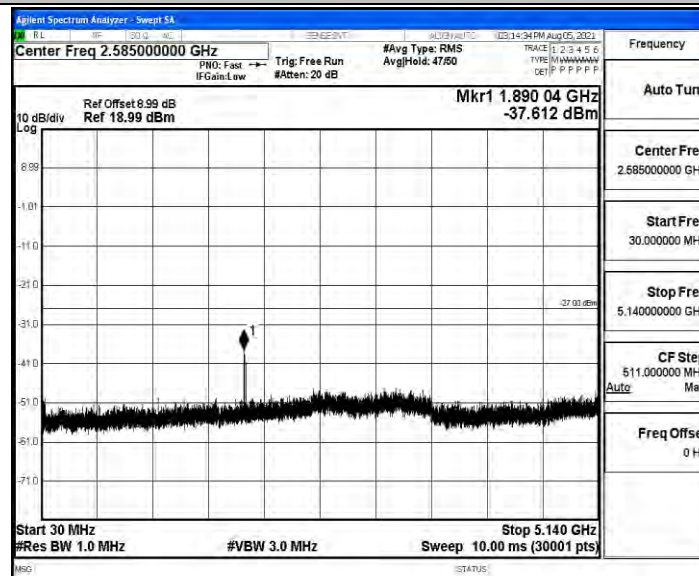




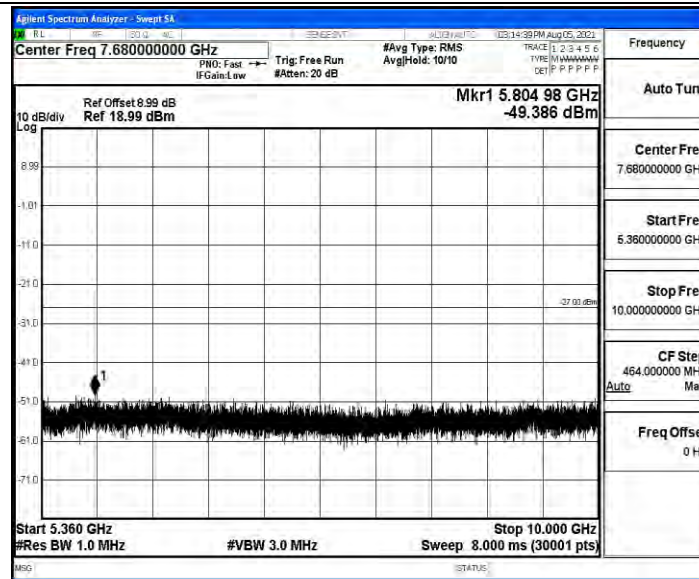
11AC20SISO_ANT3_5200_5360~10000



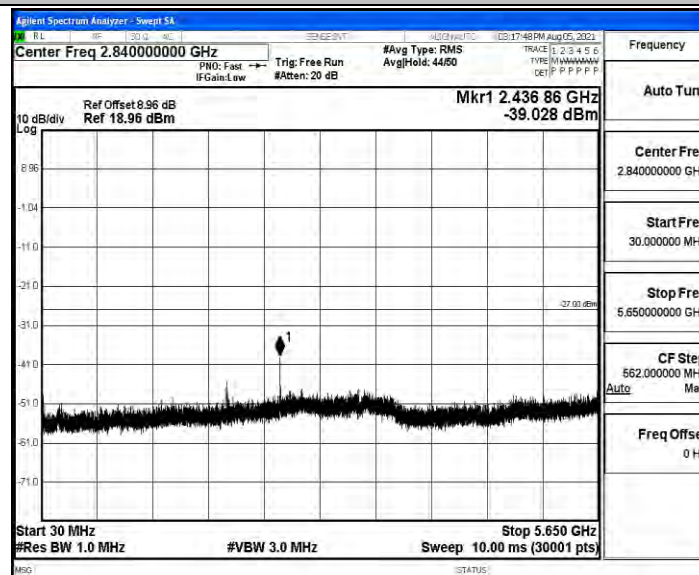
11AC20SISO_ANT3_5240_30~5140



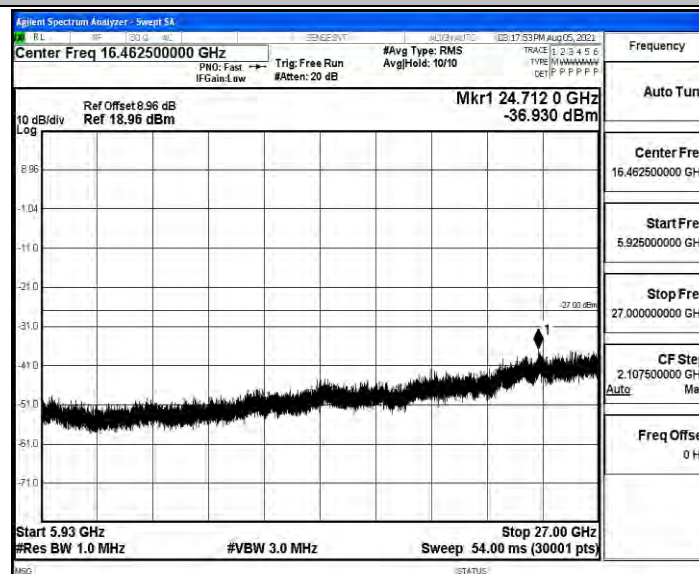
11AC20SISO_ANT3_5240_5360~10000



11AC20SISO_ANT3_5745_30~5650

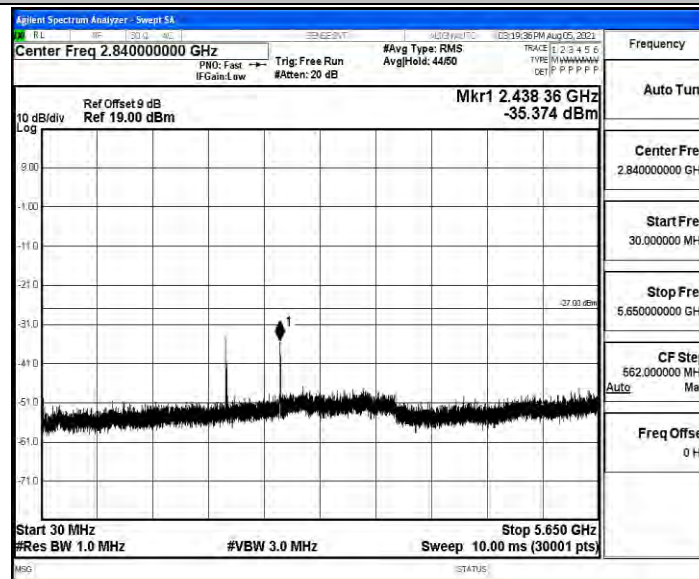


11AC20SISO_ANT3_5745_5925~40000

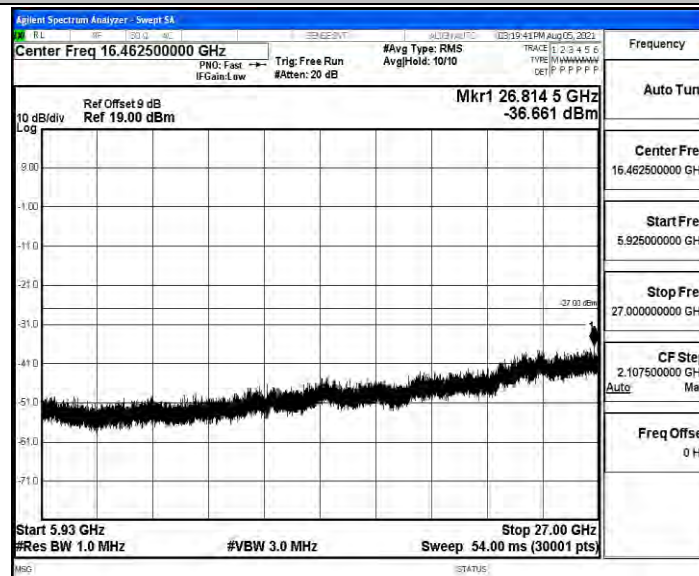




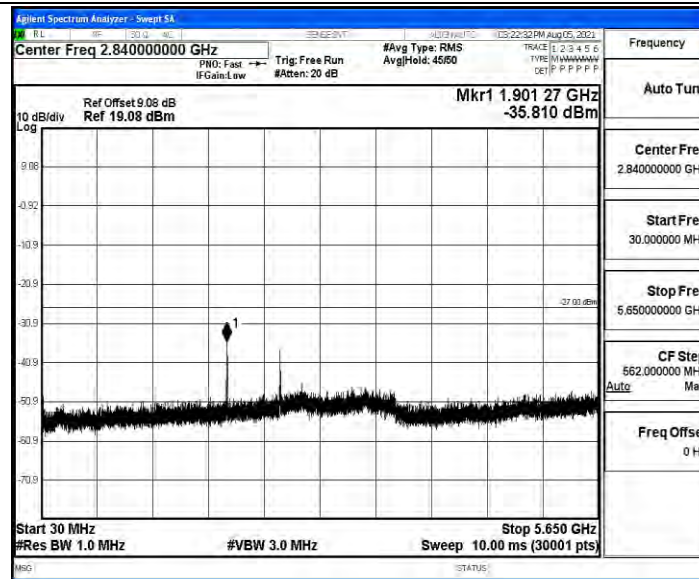
11AC20SISO_ANT3_5785_30~5650



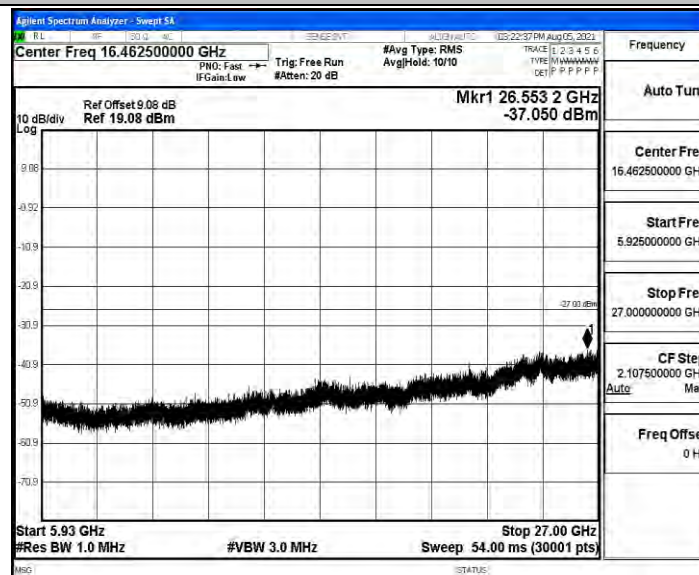
11AC20SISO_ANT3_5785_5925~40000



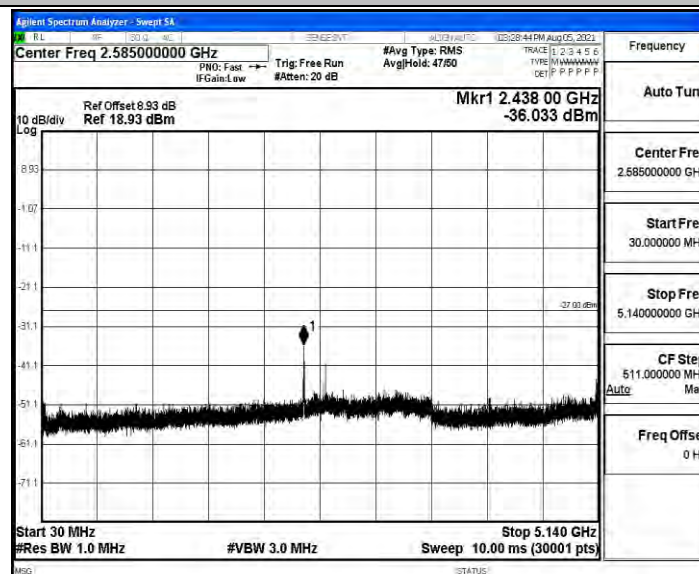
11AC20SISO_ANT3_5825_30~5650



11AC20SISO_ANT3_5825_5925~40000

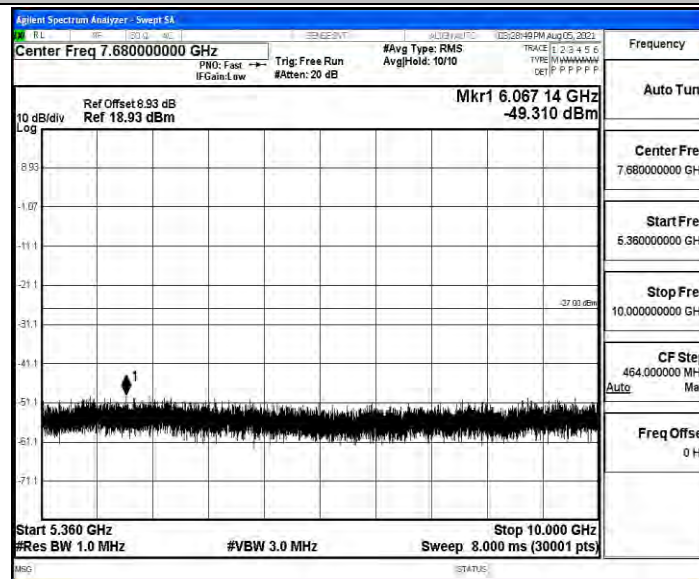


11AC40SISO_ANT3_5190_30~5140

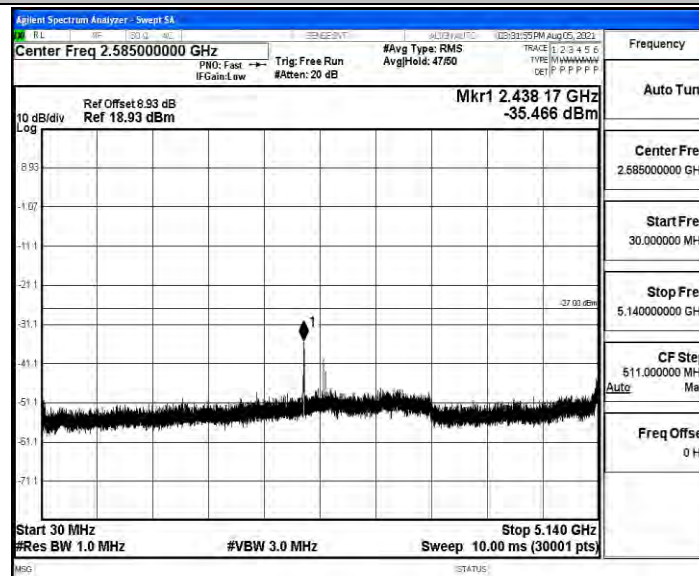




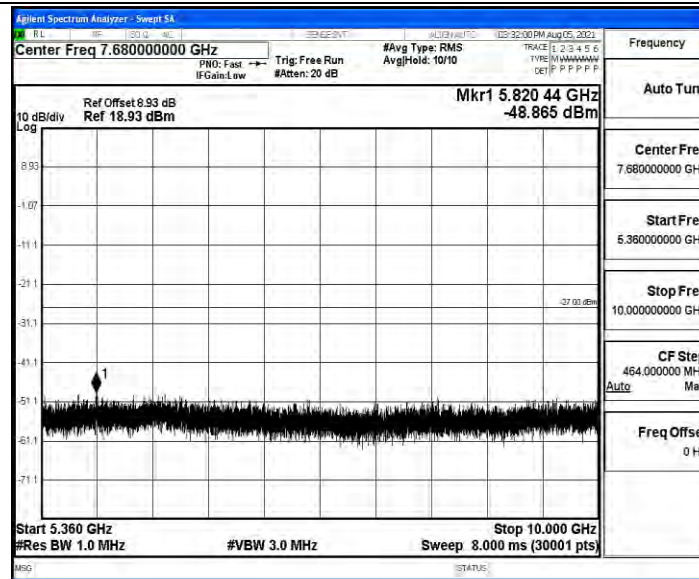
11AC40SISO_ANT3_5190_5360~10000



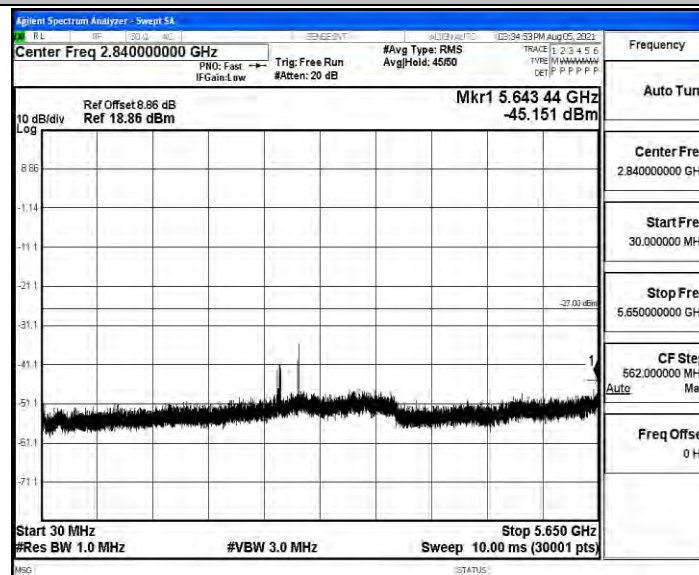
11AC40SISO_ANT3_5230_30~5140



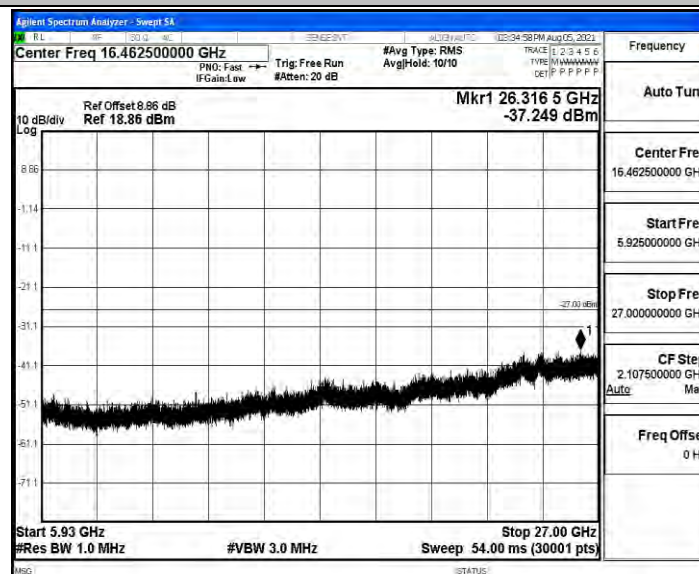
11AC40SISO_ANT3_5230_5360~10000



11AC40SISO_ANT3_5755_30~5650

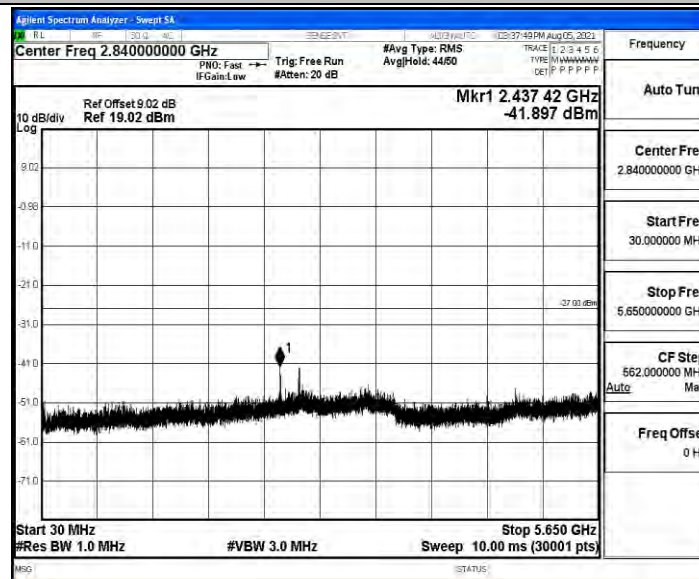


11AC40SISO_ANT3_5755_5925~40000

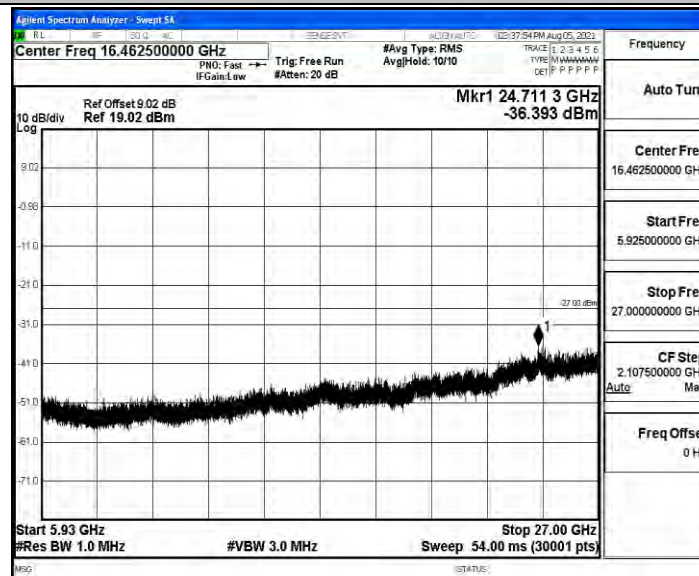




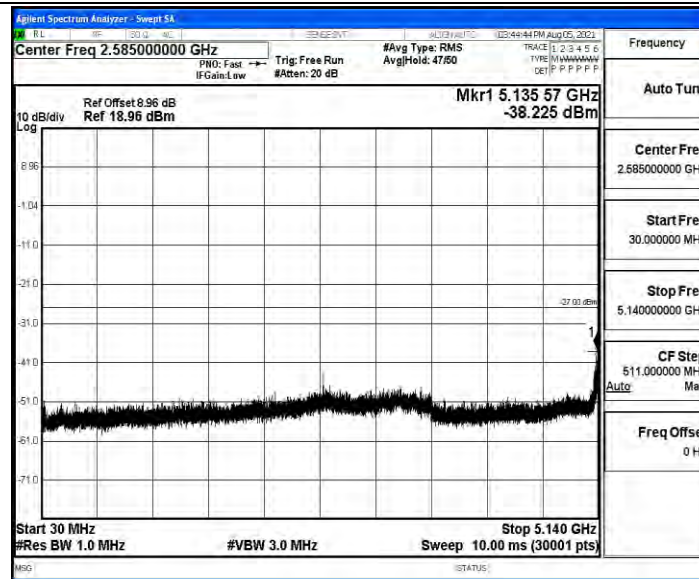
11AC40SISO_ANT3_5795_30~5650



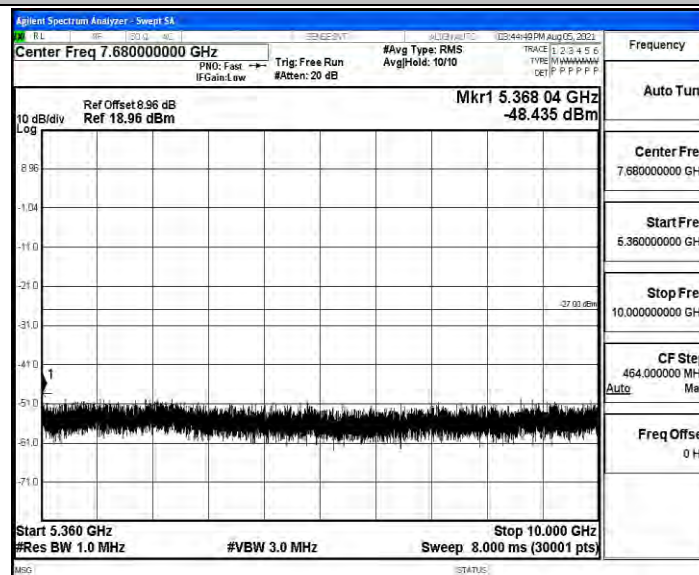
11AC40SISO_ANT3_5795_5925~40000



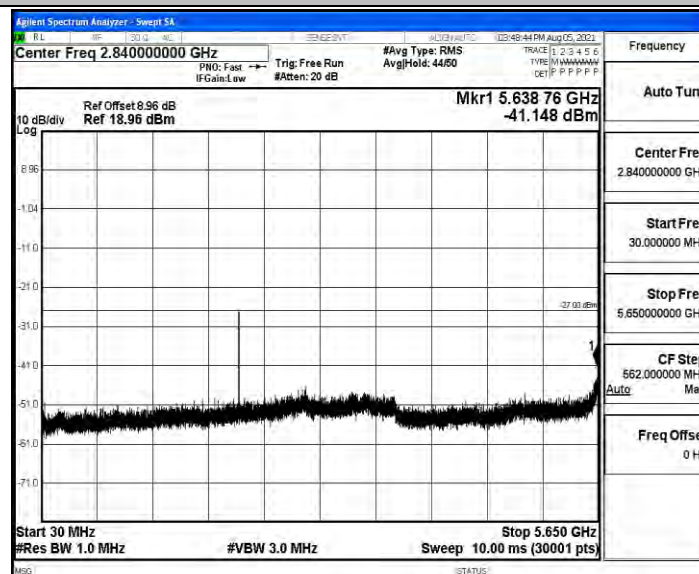
11AC80SISO_ANT3_5210_30~5140



11AC80SISO_ANT3_5210_5360~10000

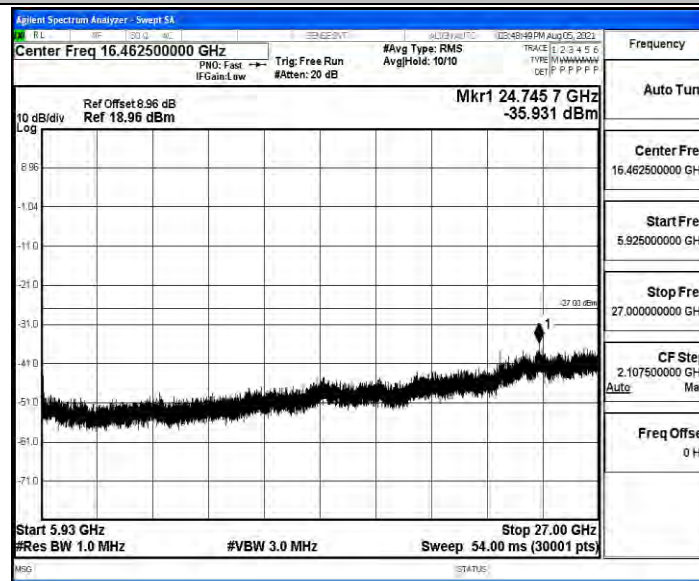


11AC80SISO_ANT3_5775_30~5650

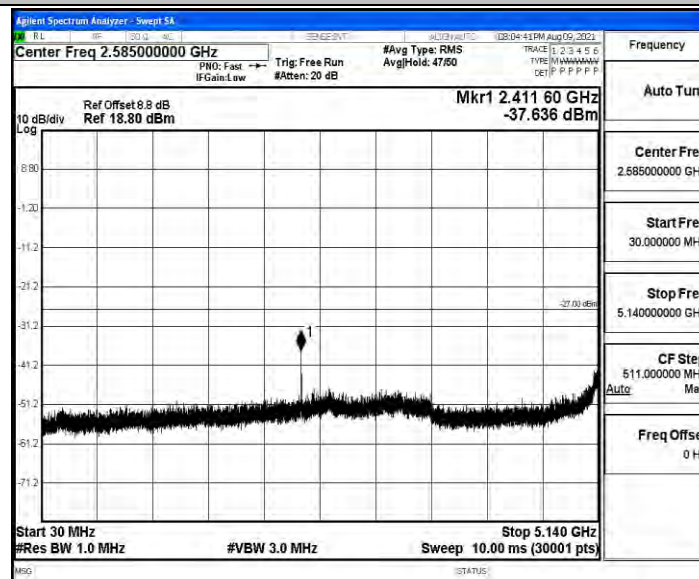




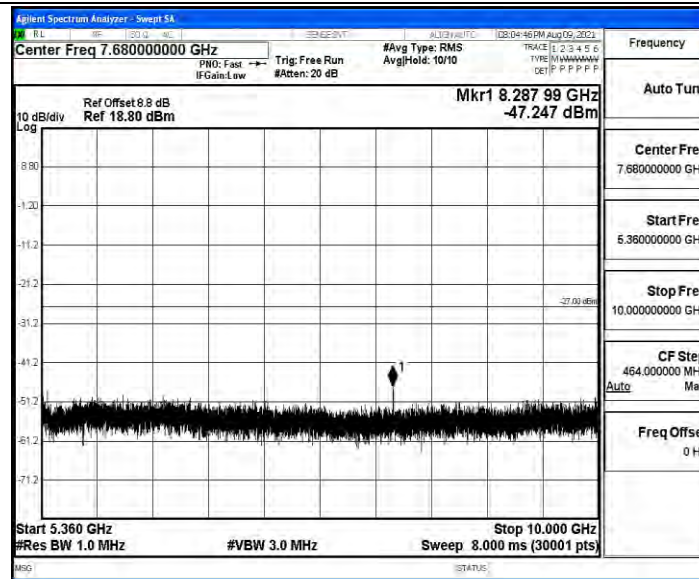
11AC80SISO_ANT3_5775_5925~40000



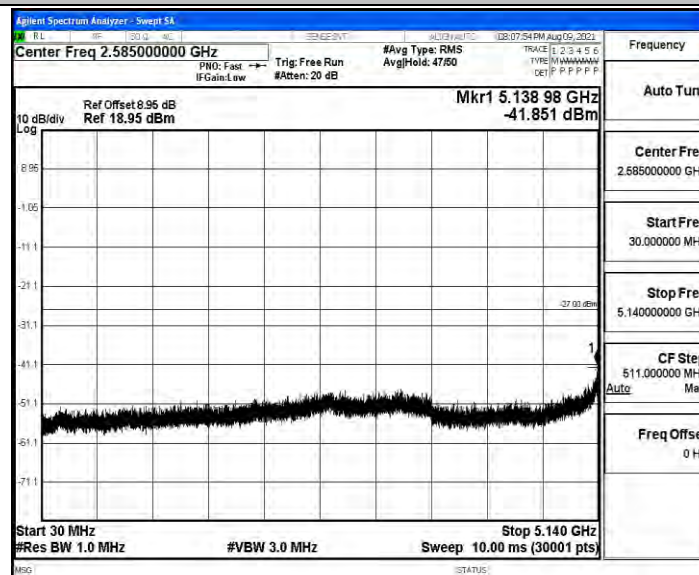
11A_ANT4_5180_30~5140



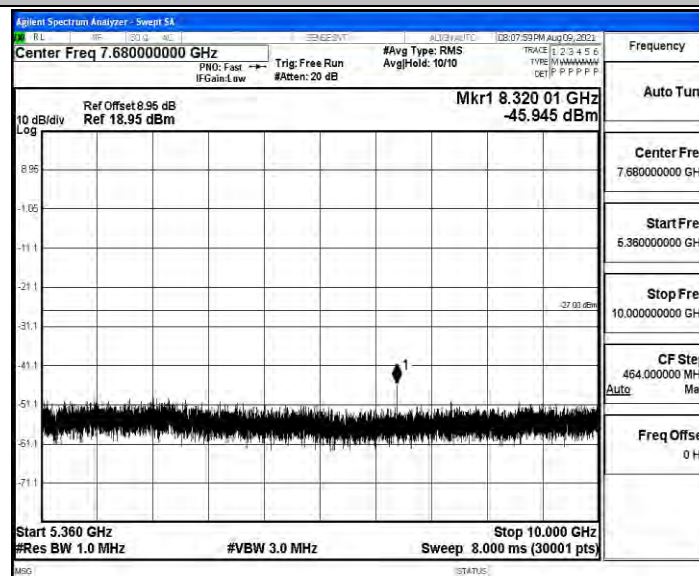
11A_ANT4_5180_5360~10000



11A_ANT4_5200_30~5140

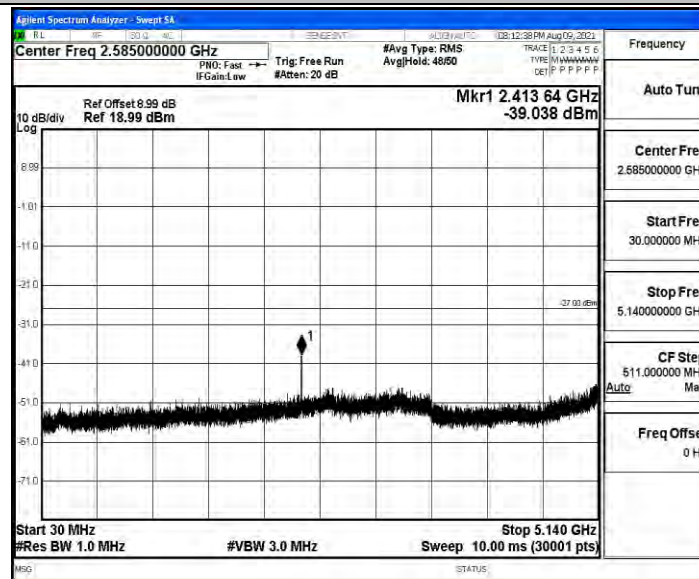


11A_ANT4_5200_5360~10000

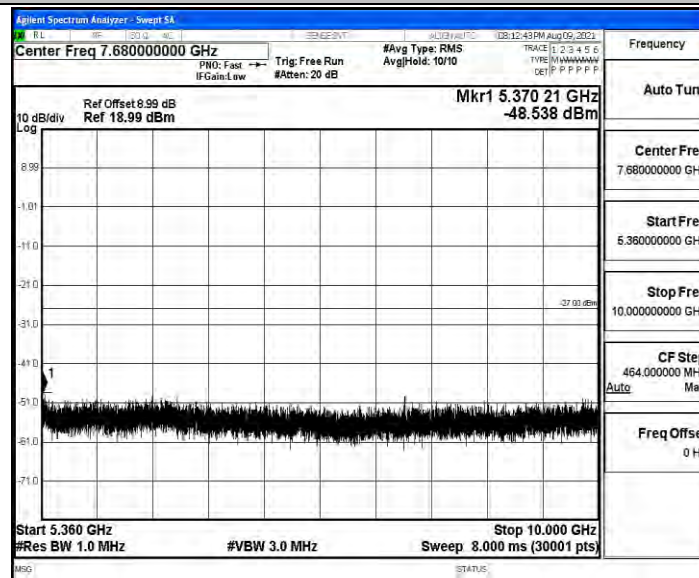




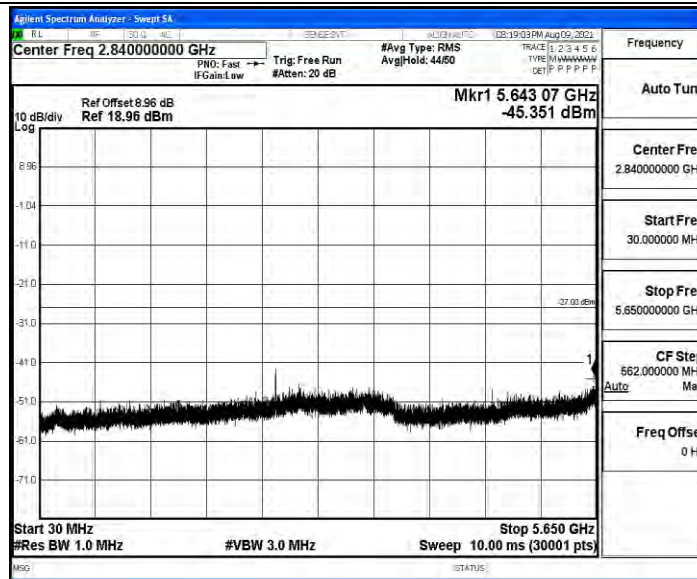
11A_ANT4_5240_30~5140



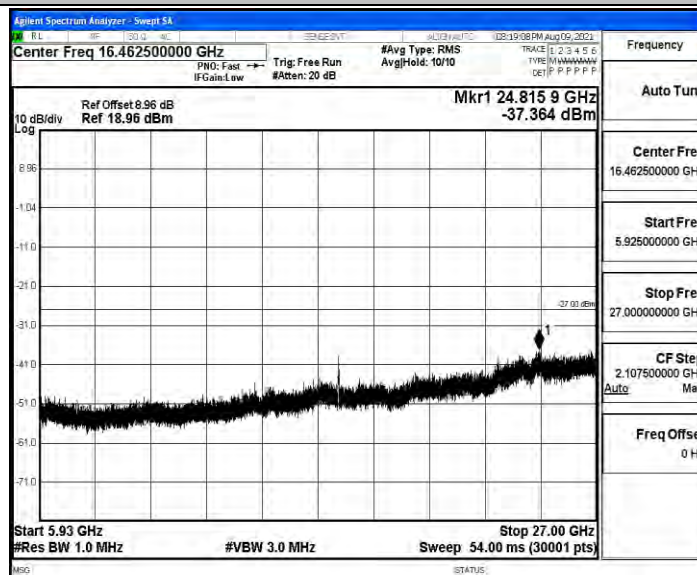
11A_ANT4_5240_5360~10000



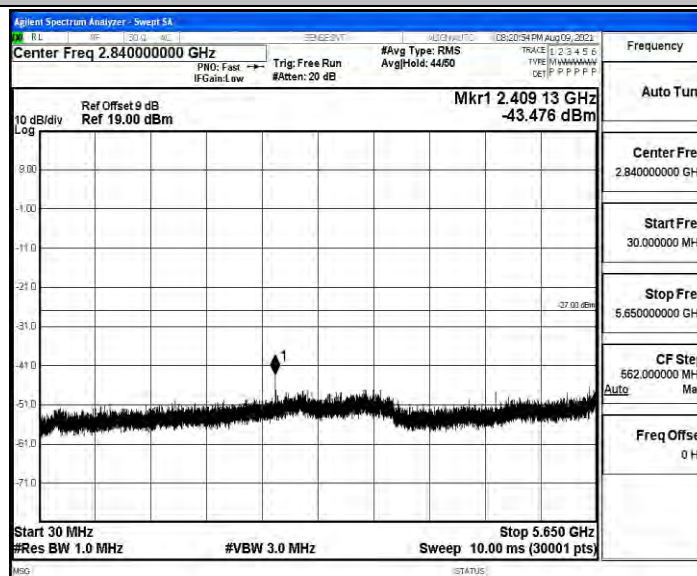
11A_ANT4_5745_30~5650

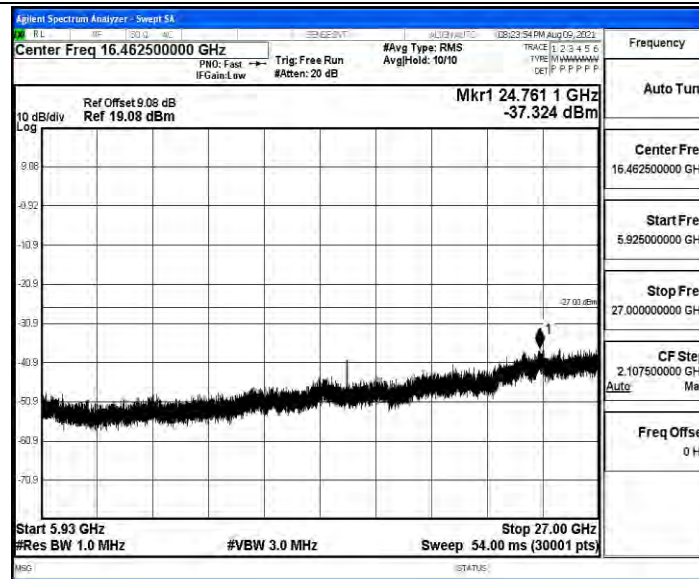


11A_ANT4_5745_5925~40000

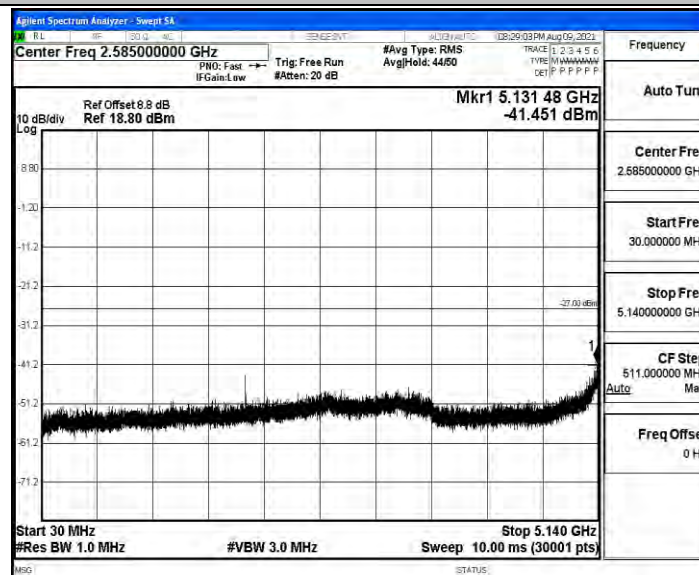


11A_ANT4_5785_30~5650

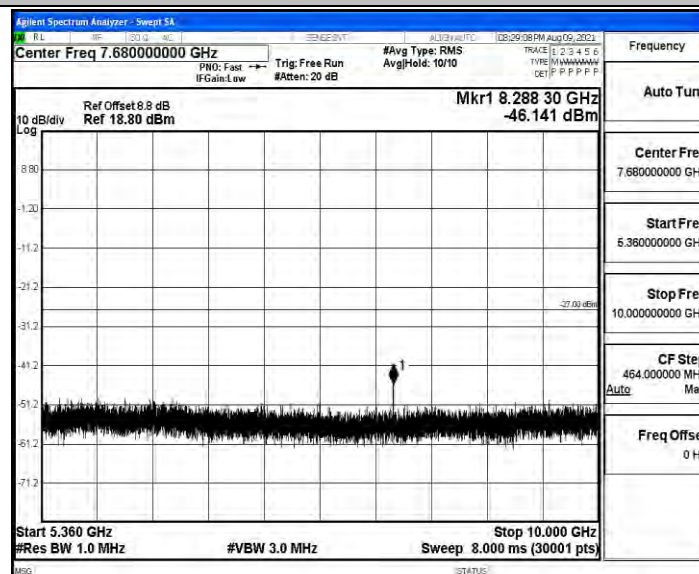




11N20SISO_ANT4_5180_30~5140

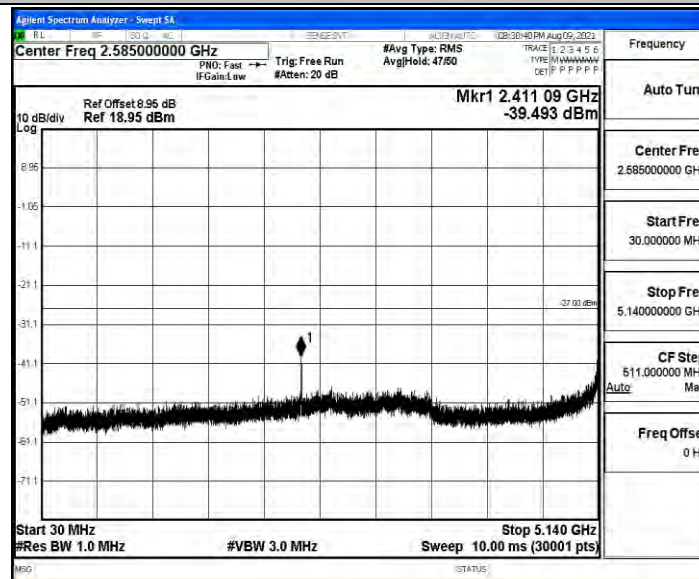


11N20SISO_ANT4_5180_5360~10000

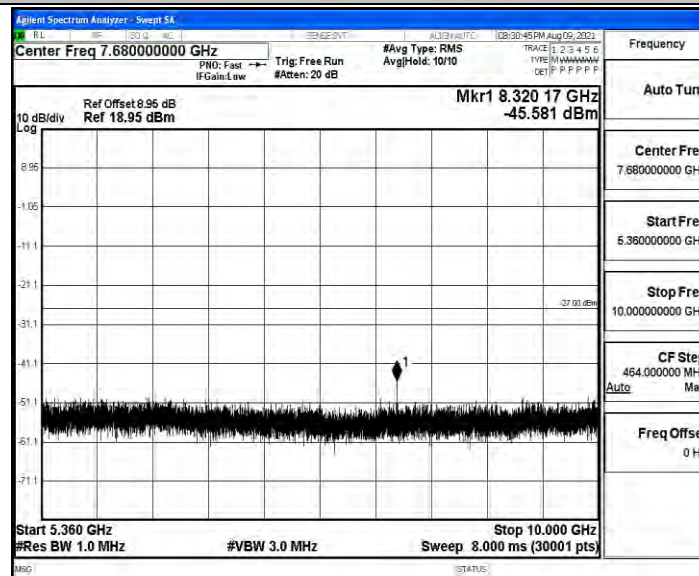




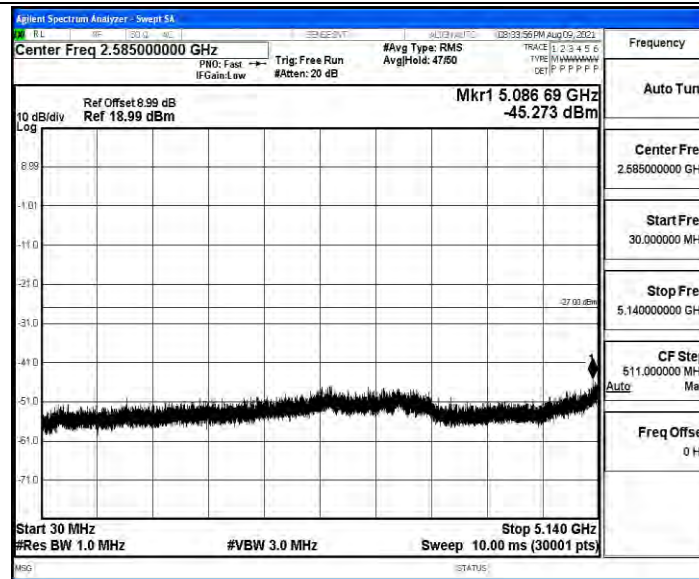
11N20SISO_ANT4_5200_30~5140



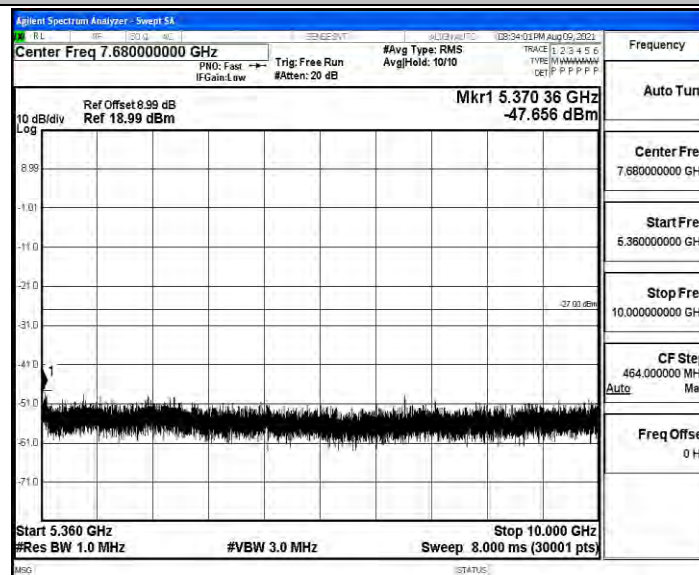
11N20SISO_ANT4_5200_5360~10000



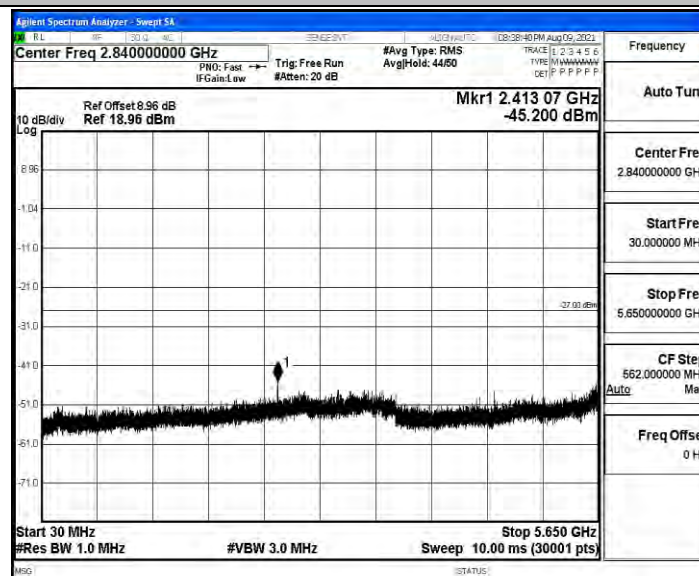
11N20SISO_ANT4_5240_30~5140



11N20SISO_ANT4_5240_5360~10000

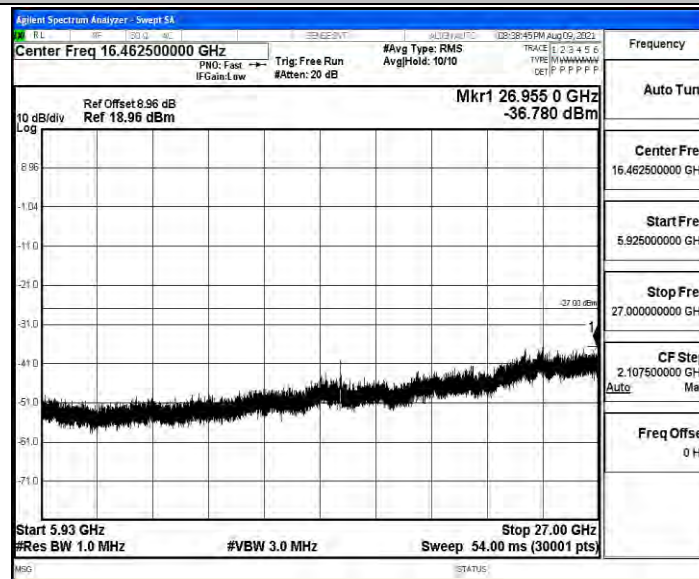


11N20SISO_ANT4_5745_30~5650

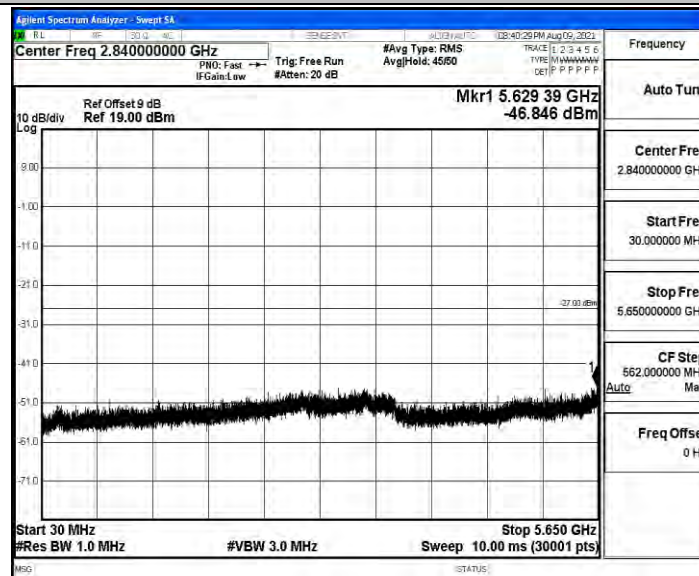




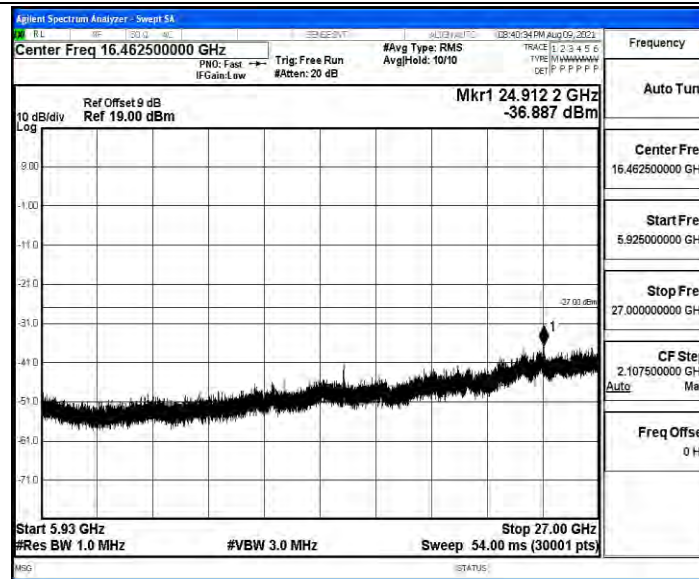
11N20SISO_ANT4_5745_5925~40000



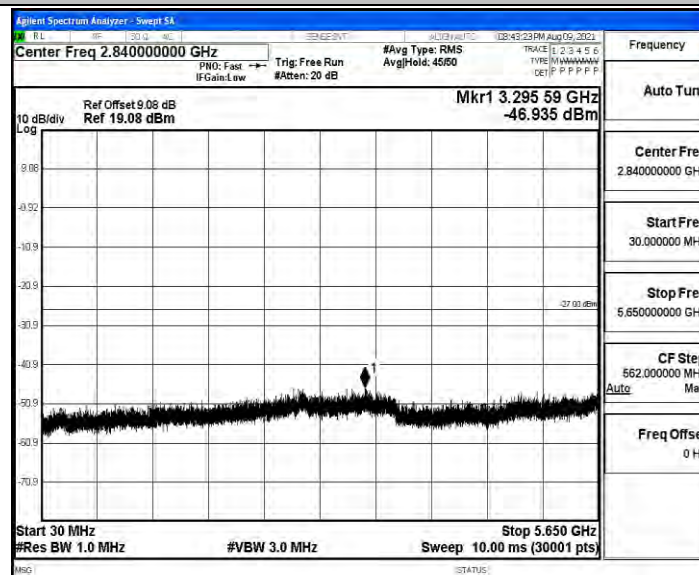
11N20SISO_ANT4_5785_30~5650



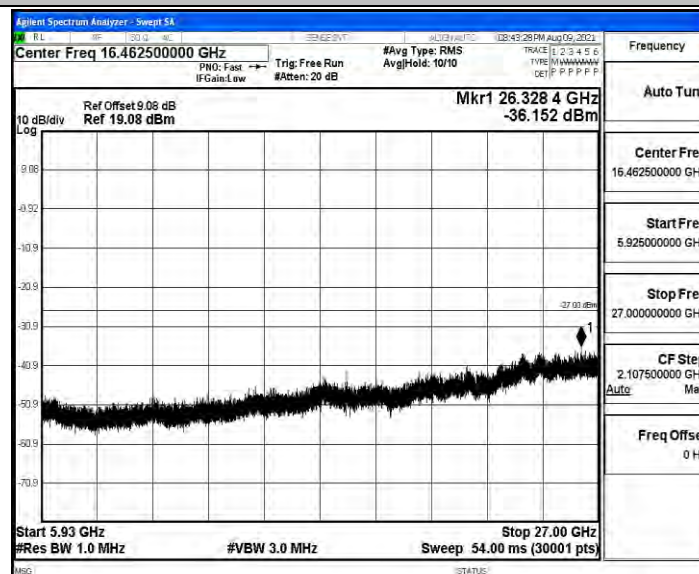
11N20SISO_ANT4_5785_5925~40000



11N20SISO_ANT4_5825_30~5650

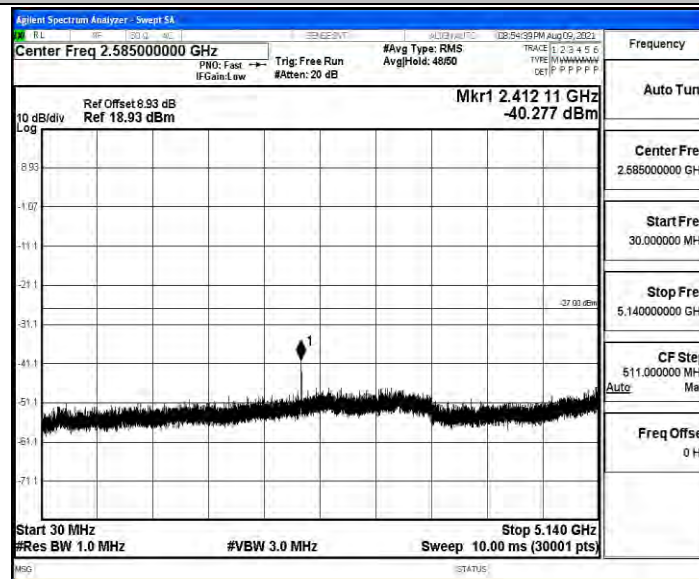


11N20SISO_ANT4_5825_5925~40000

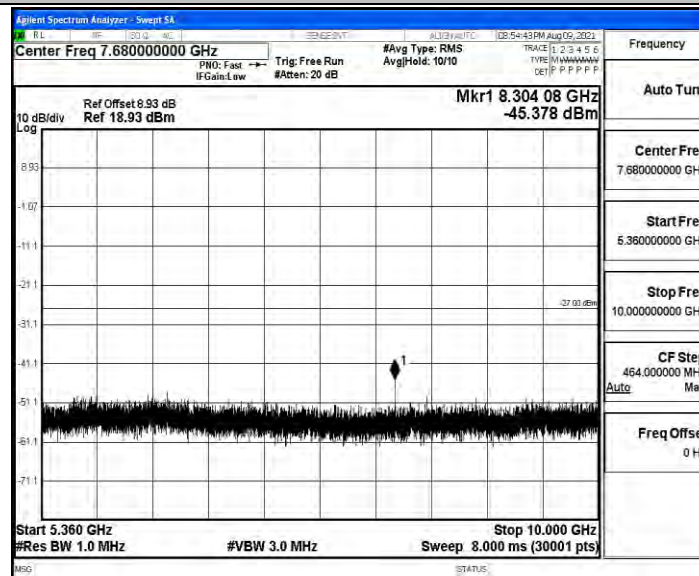




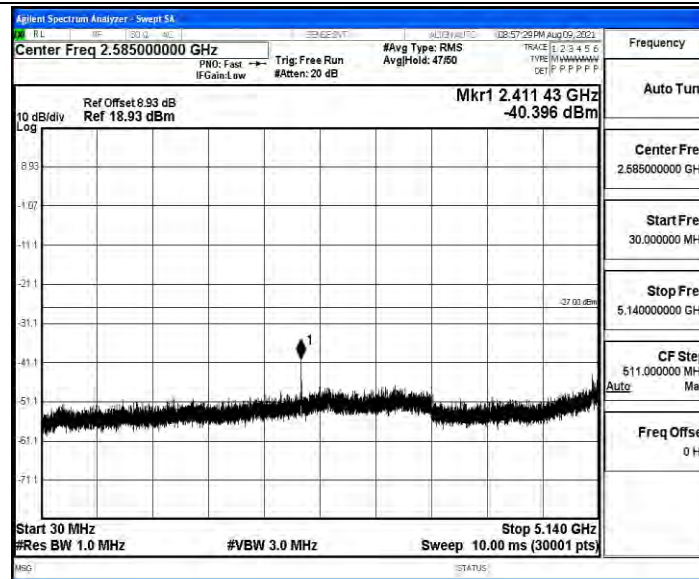
11N40SISO_ANT4_5190_30~5140



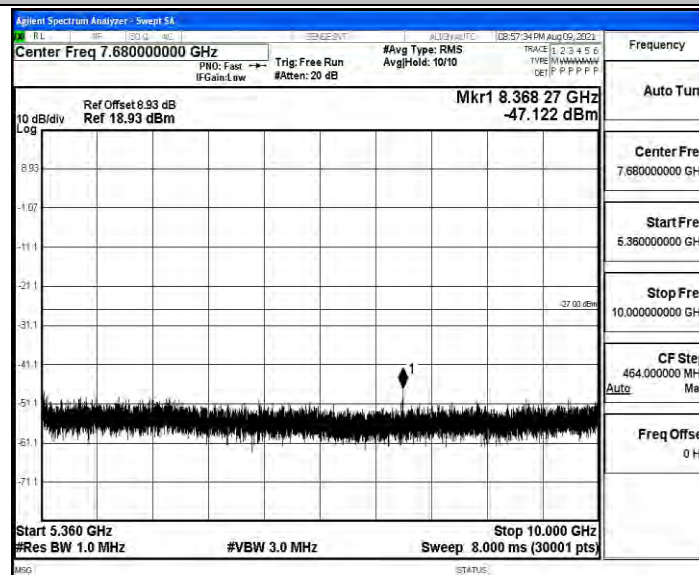
11N40SISO_ANT4_5190_5360~10000



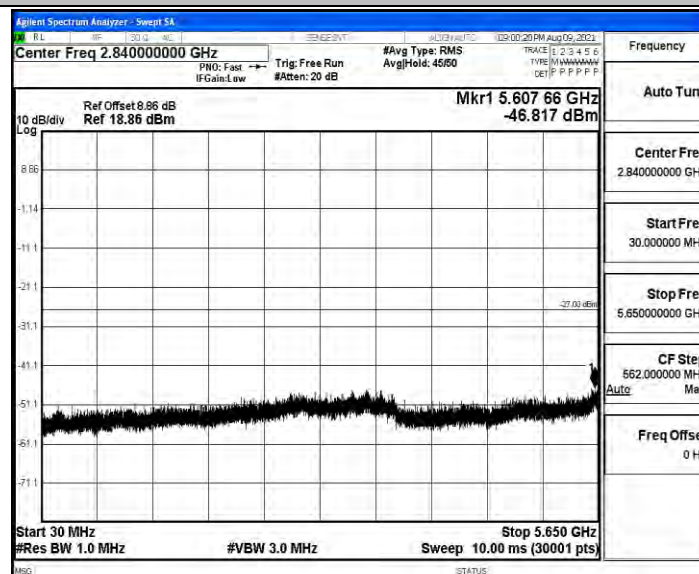
11N40SISO_ANT4_5230_30~5140



11N40SISO_ANT4_5230_5360~10000

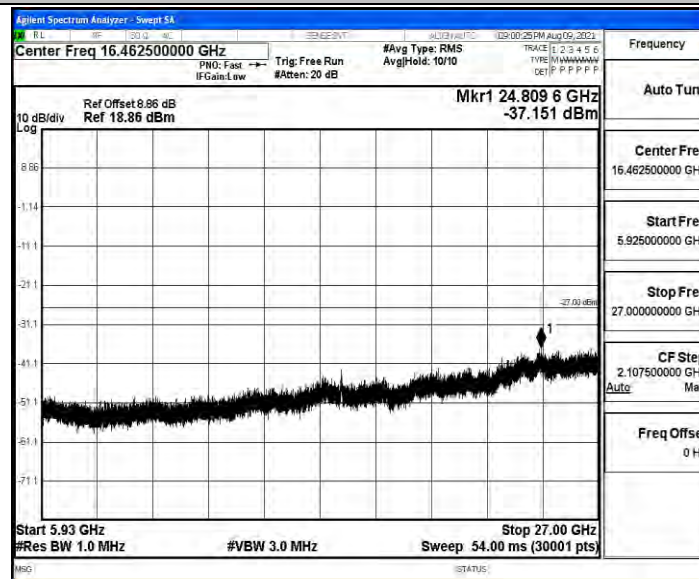


11N40SISO_ANT4_5755_30~5650

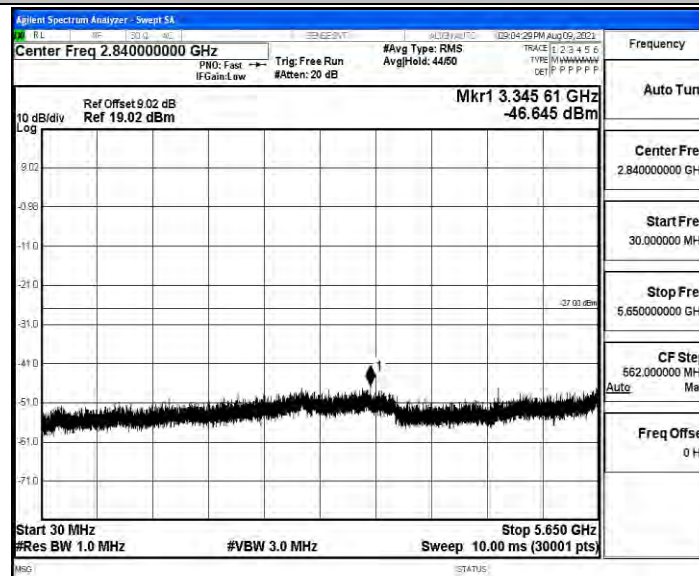




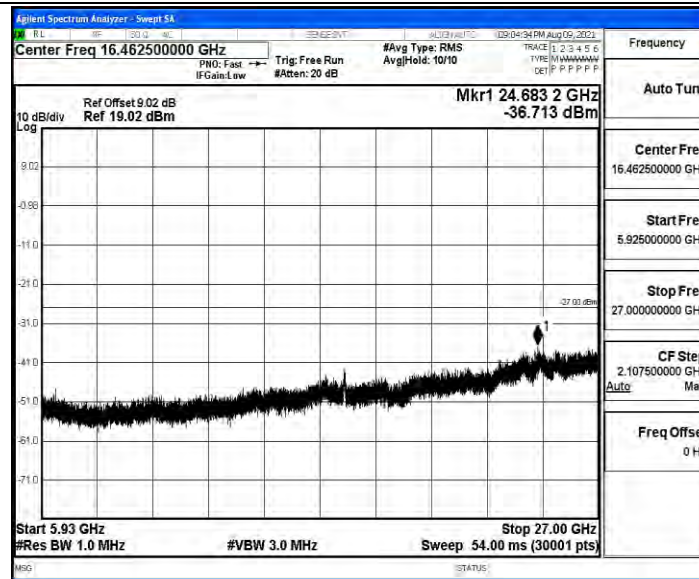
11N40SISO_ANT4_5755_5925~40000



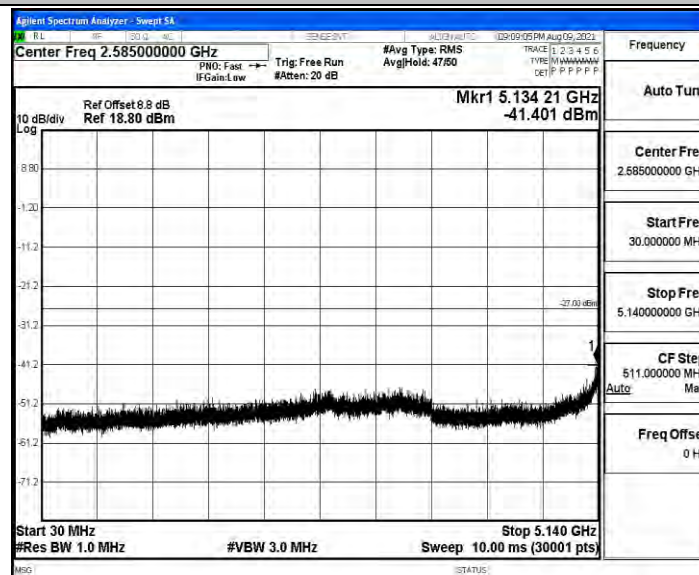
11N40SISO_ANT4_5795_30~5650



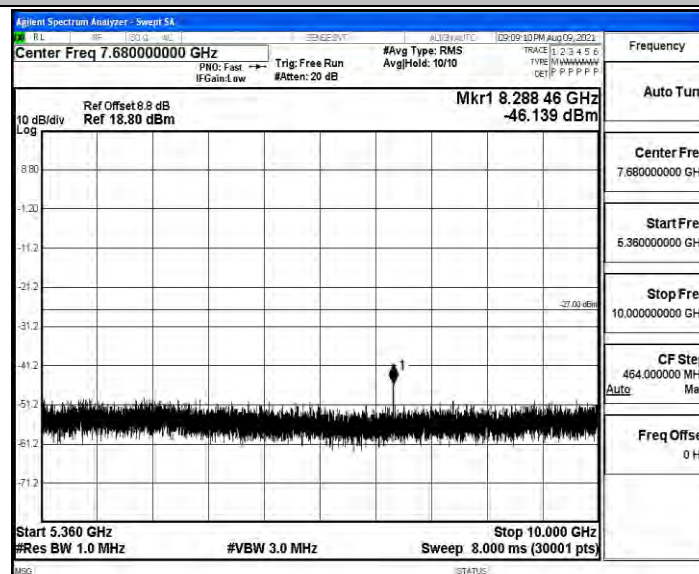
11N40SISO_ANT4_5795_5925~40000



11AC20SISO_ANT4_5180_30~5140

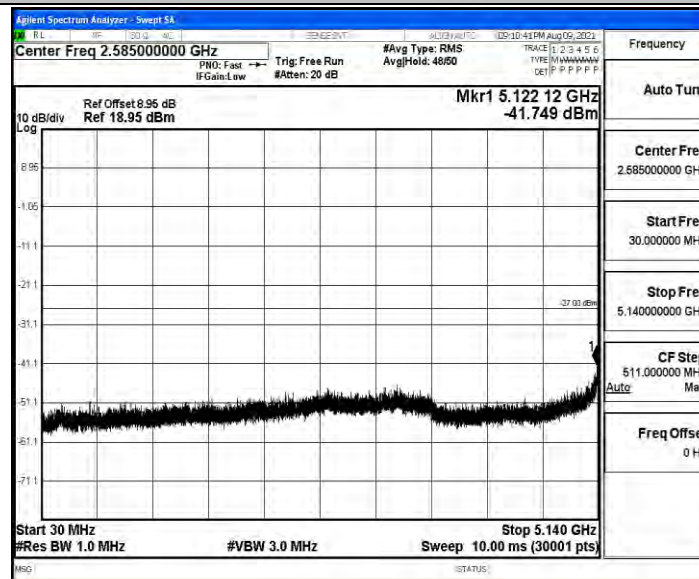


11AC20SISO_ANT4_5180_5360~10000

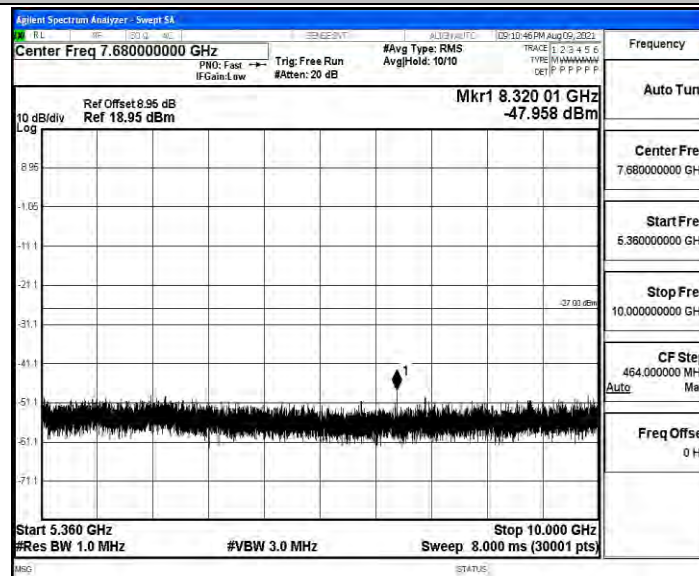




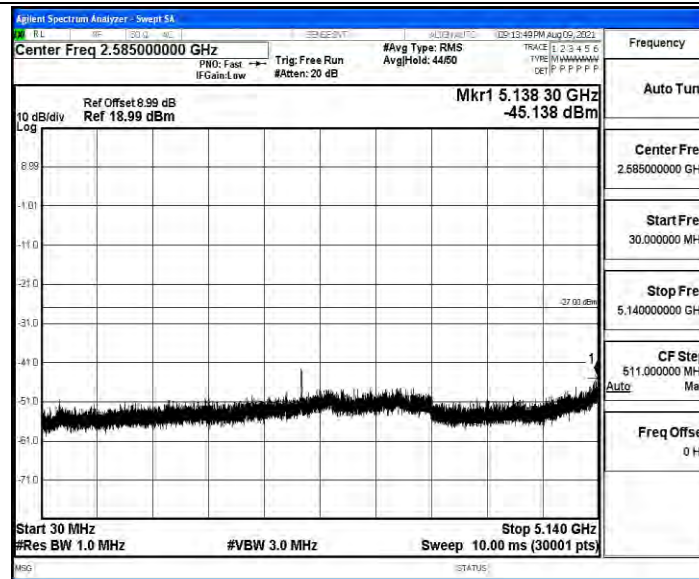
11AC20SISO_ANT4_5200_30~5140



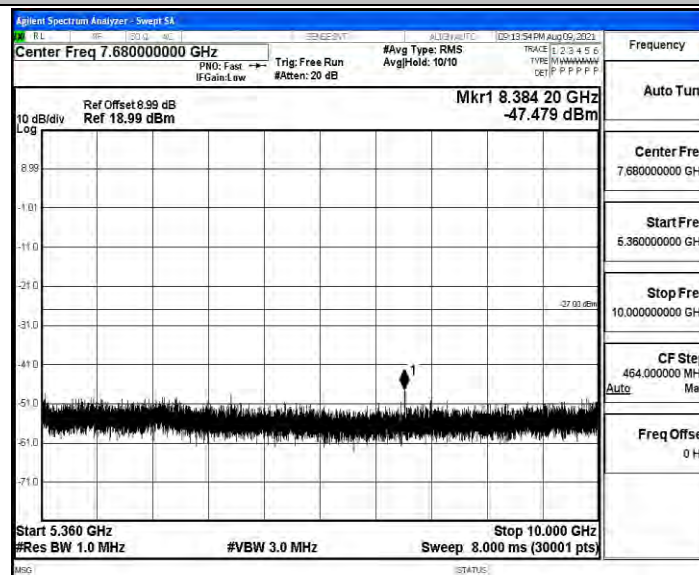
11AC20SISO_ANT4_5200_5360~10000



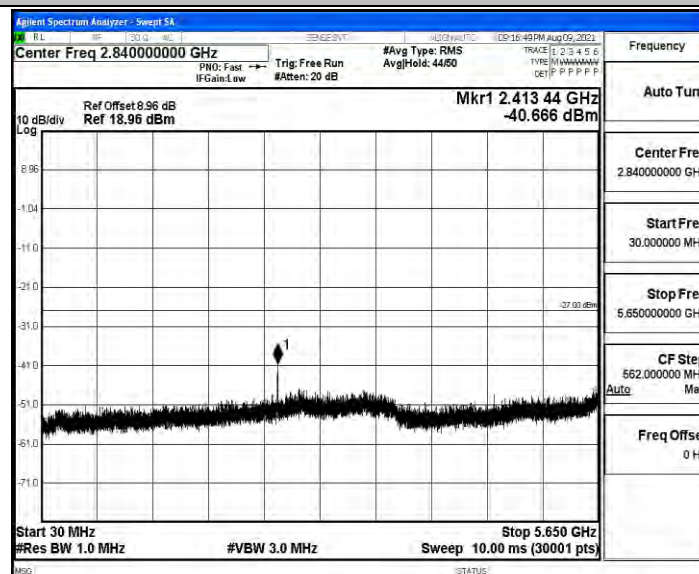
11AC20SISO_ANT4_5240_30~5140

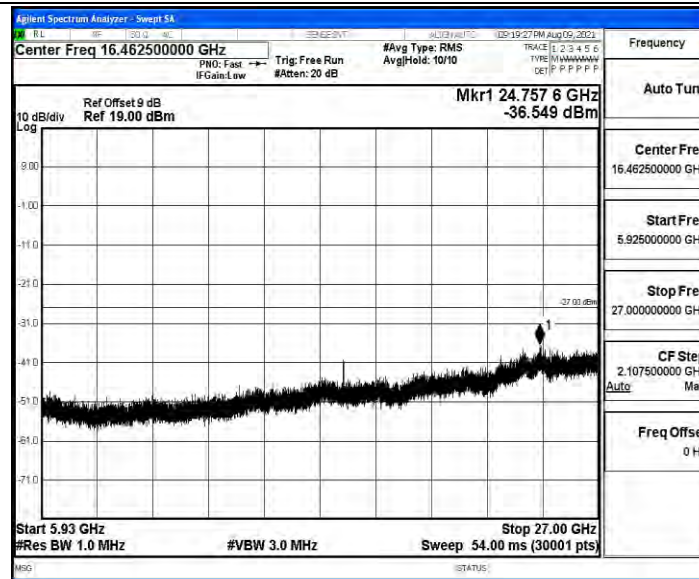


11AC20SISO_ANT4_5240_5360~10000

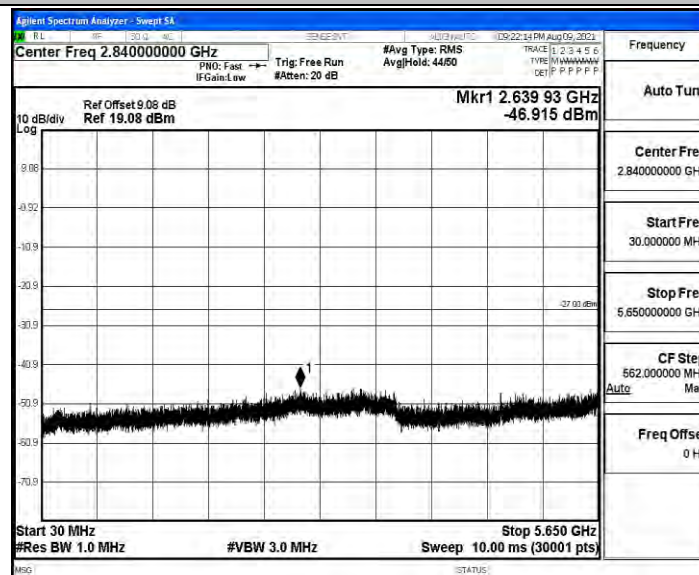


11AC20SISO_ANT4_5745_30~5650

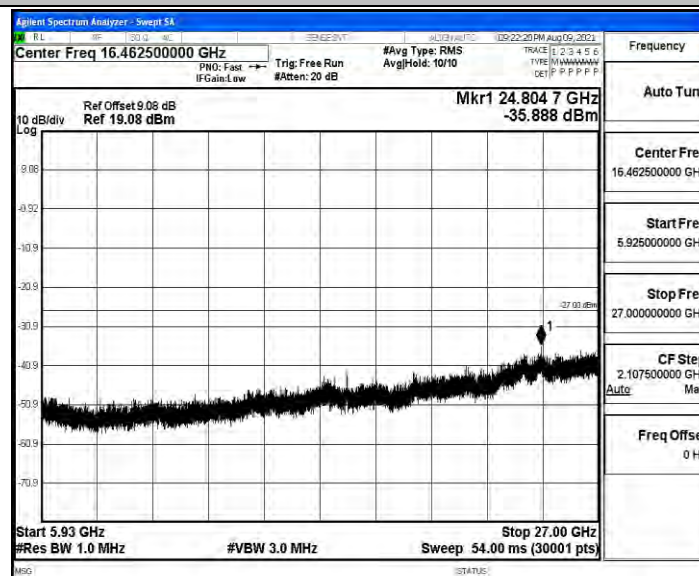




11AC20SISO_ANT4_5825_30~5650

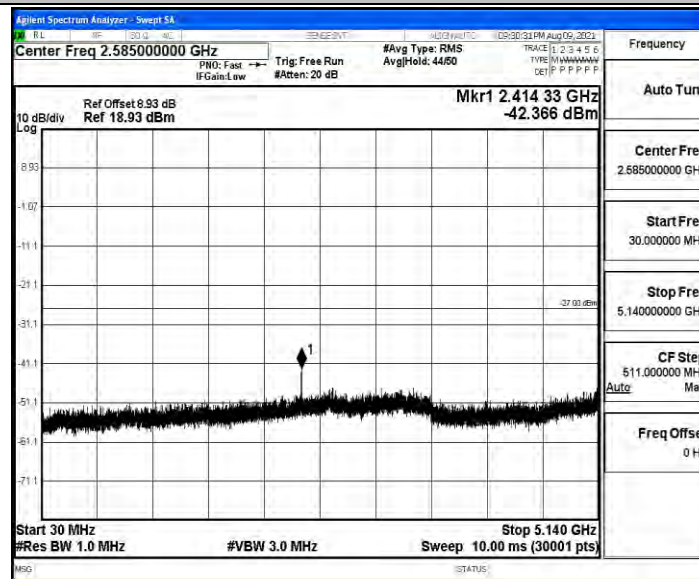


11AC20SISO_ANT4_5825_5925~40000

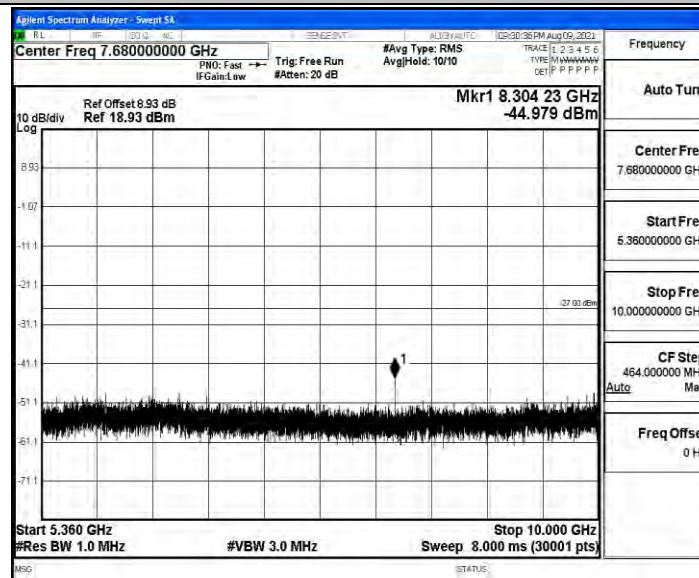




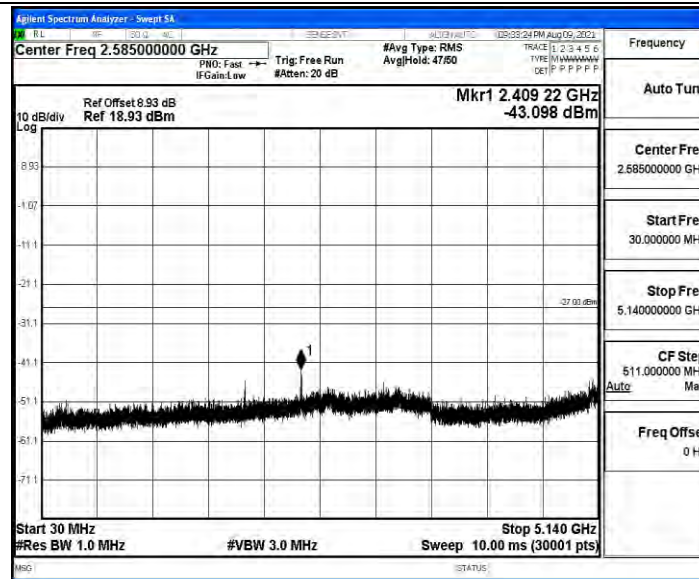
11AC40SISO_ANT4_5190_30~5140



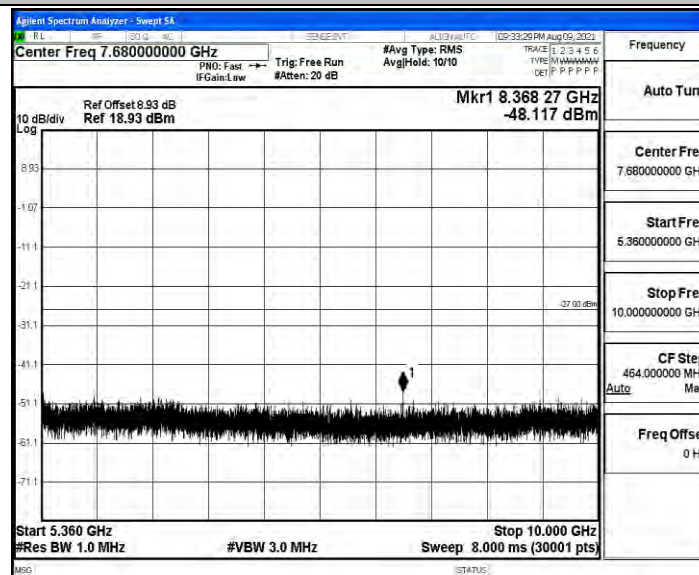
11AC40SISO_ANT4_5190_5360~10000



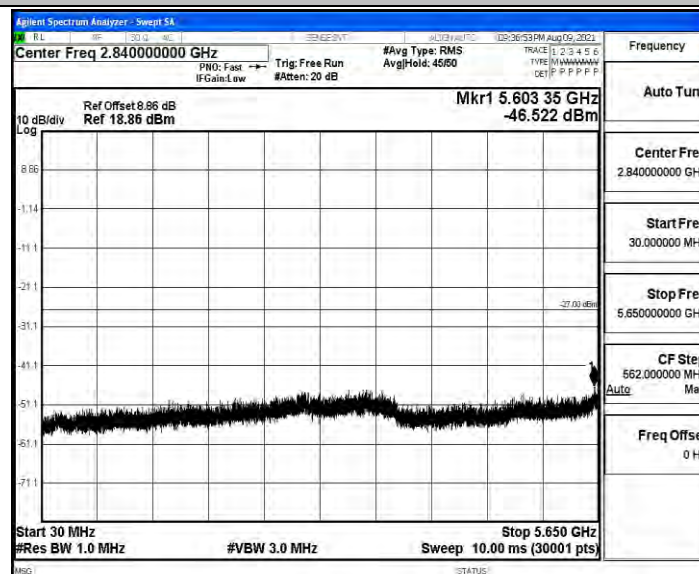
11AC40SISO_ANT4_5230_30~5140

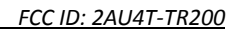


11AC40SISO_ANT4_5230_5360~10000

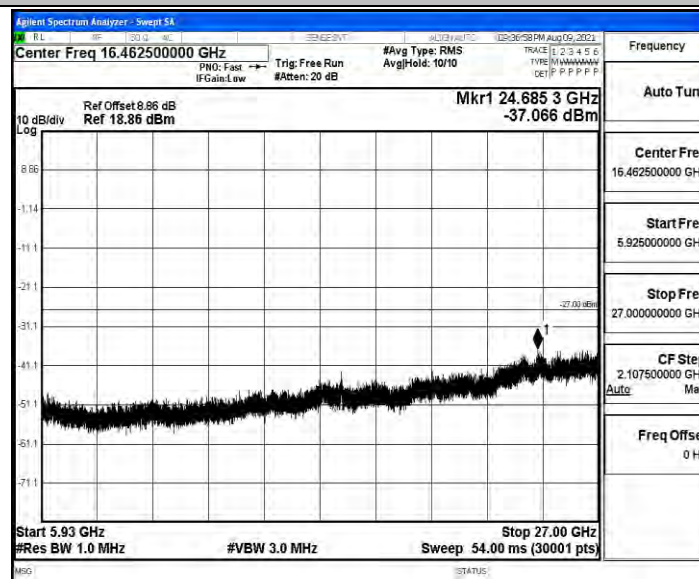


11AC40SISO_ANT4_5755_30~5650

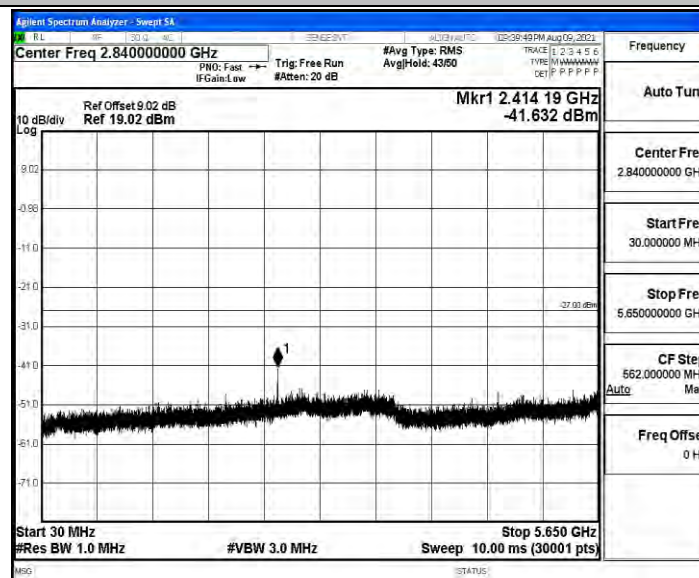




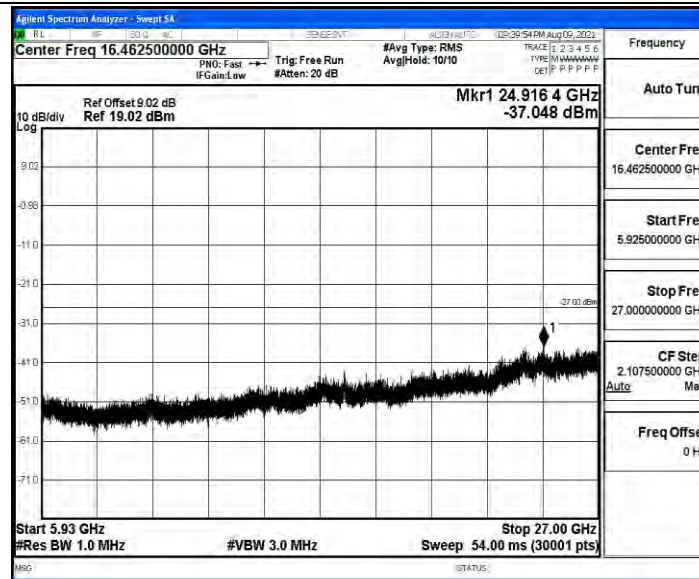
11AC40SISO_ANT4_5755_5925~40000



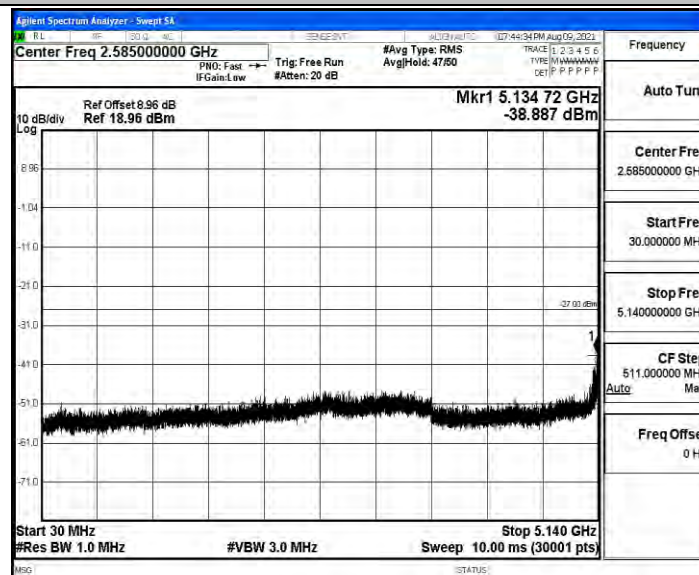
11AC40SISO_ANT4_5795_30~5650



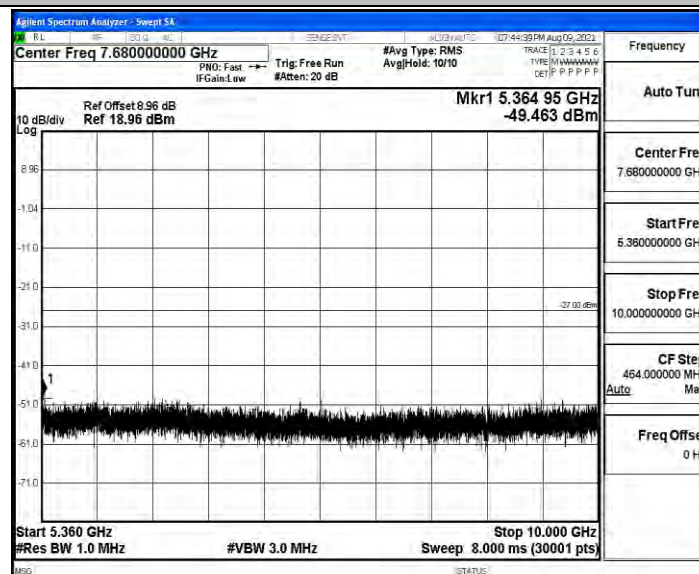
11AC40SISO_ANT4_5795_5925~40000



11AC80SISO_ANT4_5210_30~5140

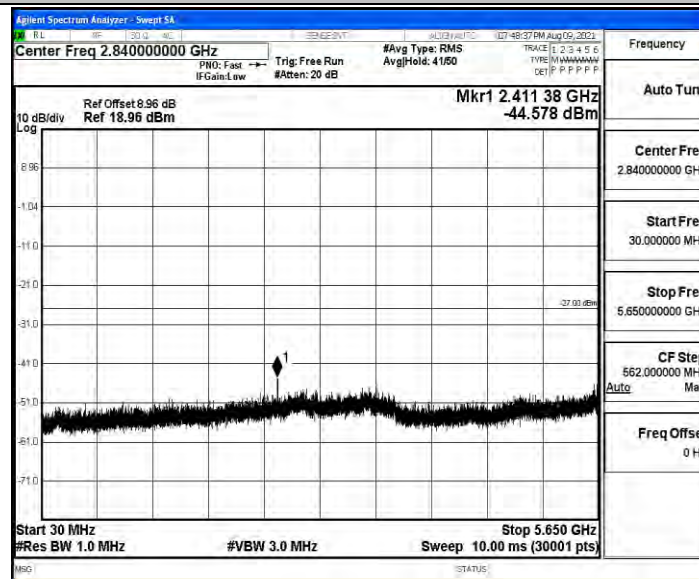


11AC80SISO_ANT4_5210_5360~10000

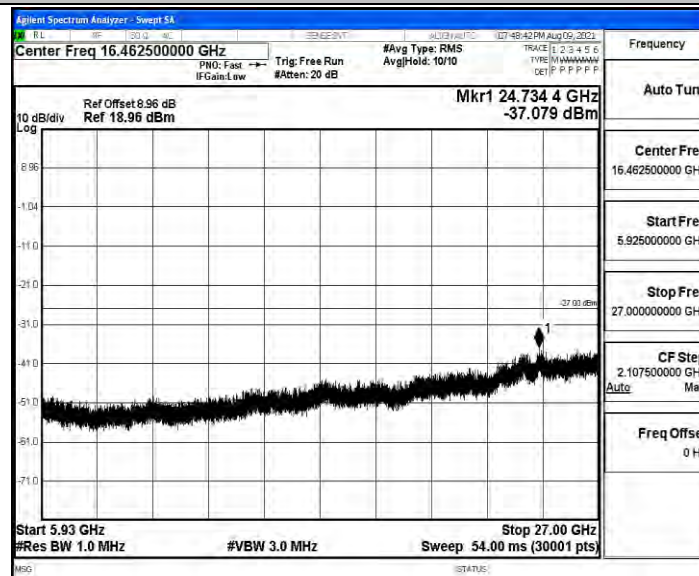




11AC80SISO_ANT4_5775_30~5650



11AC80SISO_ANT4_5775_5925~40000





B.1: Duty Cycle

Test Result

TestMode	Antenna	Channel	Transmission Duration [ms]	Transmission Period [ms]	Duty Cycle [%]	Limit	Verdict
11A	ANT3	5180	1.39	1.44	96.53	---	PASS
		5200	1.39	1.44	96.53	---	PASS
		5240	1.39	1.44	96.53	---	PASS
		5745	1.39	1.44	96.53	---	PASS
		5785	1.39	1.44	96.53	---	PASS
		5825	1.39	1.44	96.53	---	PASS
11N20SISO	ANT3	5180	1.30	1.35	96.30	---	PASS
		5200	1.30	1.35	96.30	---	PASS
		5240	1.30	1.35	96.30	---	PASS
		5745	1.30	1.35	96.30	---	PASS
		5785	1.30	1.35	96.30	---	PASS
		5825	1.30	1.35	96.30	---	PASS
11N40SISO	ANT3	5190	0.64	0.69	92.75	---	PASS
		5230	0.64	0.69	92.75	---	PASS
		5755	0.64	0.69	92.75	---	PASS
		5795	0.64	0.69	92.75	---	PASS
11AC20SISO	ANT3	5180	1.31	1.36	96.32	---	PASS
		5200	1.31	1.36	96.32	---	PASS
		5240	1.31	1.36	96.32	---	PASS
		5745	1.31	1.36	96.32	---	PASS
		5785	1.31	1.36	96.32	---	PASS
		5825	1.31	1.36	96.32	---	PASS
11AC40SISO	ANT3	5190	0.65	0.70	92.86	---	PASS
		5230	0.65	0.70	92.86	---	PASS
		5755	0.65	0.70	92.86	---	PASS
		5795	0.65	0.70	92.86	---	PASS
11AC80SISO	ANT3	5210	0.32	0.37	86.49	---	PASS
		5775	0.32	0.37	86.49	---	PASS

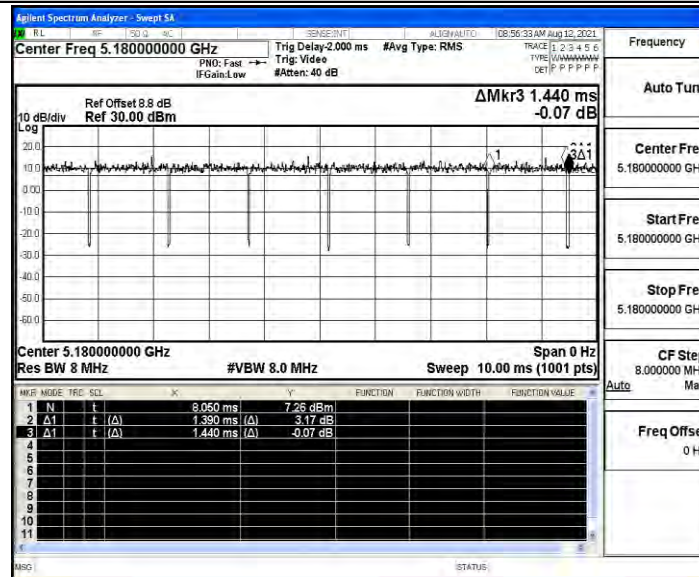


TestMode	Antenna	Channel	Transmission Duration [ms]	Transmission Period [ms]	Duty Cycle [%]	Limit	Verdict
11A	ANT4	5180	1.39	1.43	97.20	---	PASS
		5200	1.39	1.43	97.20	---	PASS
		5240	1.39	1.43	97.20	---	PASS
		5745	1.39	1.43	97.20	---	PASS
		5785	1.39	1.43	97.20	---	PASS
		5825	1.39	1.43	97.20	---	PASS
11N20SISO	ANT4	5180	1.30	1.34	97.01	---	PASS
		5200	1.30	1.34	97.01	---	PASS
		5240	1.30	1.34	97.01	---	PASS
		5745	1.30	1.34	97.01	---	PASS
		5785	1.30	1.34	97.01	---	PASS
		5825	1.30	1.34	97.01	---	PASS
11N40SISO	ANT4	5190	0.64	0.68	94.12	---	PASS
		5230	0.64	0.68	94.12	---	PASS
		5755	0.64	0.68	94.12	---	PASS
		5795	0.64	0.68	94.12	---	PASS
11AC20SISO	ANT4	5180	1.30	1.34	97.01	---	PASS
		5200	1.30	1.34	97.01	---	PASS
		5240	1.30	1.34	97.01	---	PASS
		5745	1.30	1.34	97.01	---	PASS
		5785	1.30	1.34	97.01	---	PASS
		5825	1.30	1.34	97.01	---	PASS
11AC40SISO	ANT4	5190	0.64	0.68	94.12	---	PASS
		5230	0.64	0.68	94.12	---	PASS
		5755	0.64	0.68	94.12	---	PASS
		5795	0.64	0.68	94.12	---	PASS
11AC80SISO	ANT4	5210	0.32	0.37	86.49	---	PASS
		5775	0.32	0.37	86.49	---	PASS

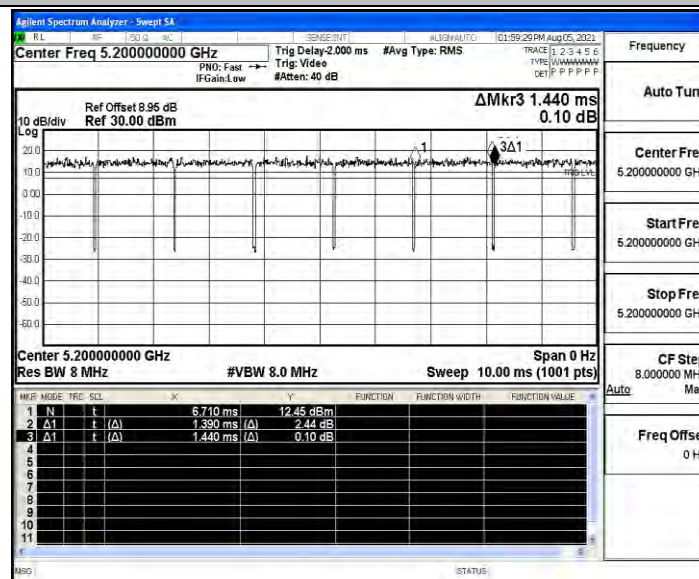


Test Graphs

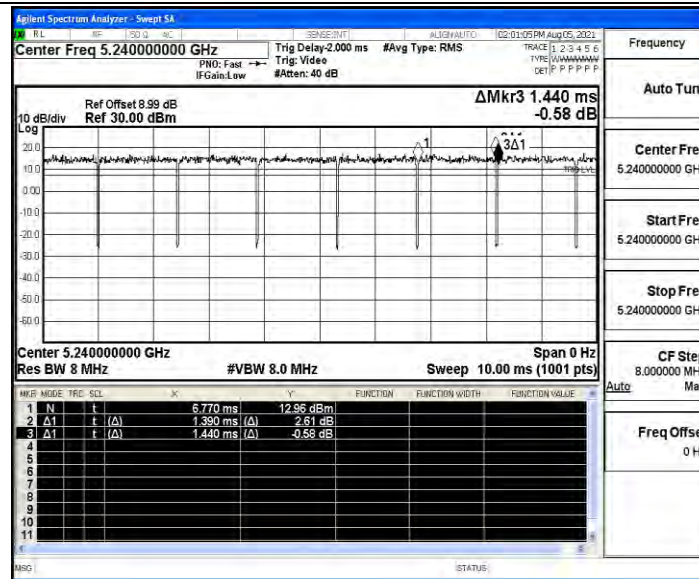
11A_ANT3_5180



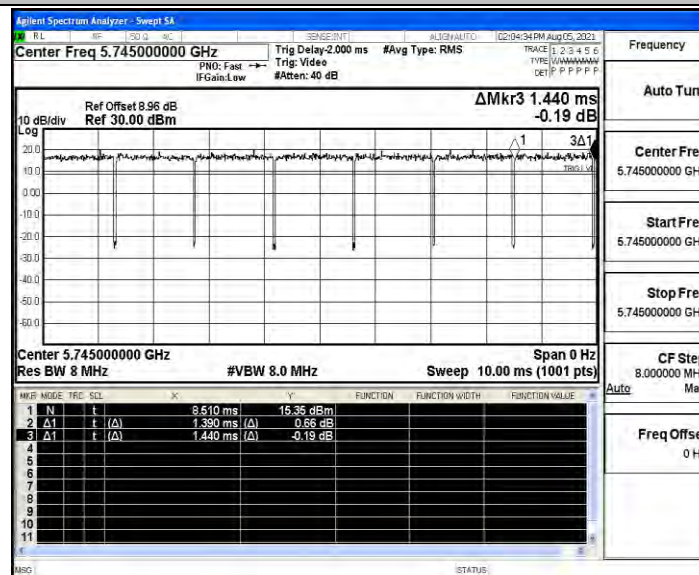
11A_ANT3_5200



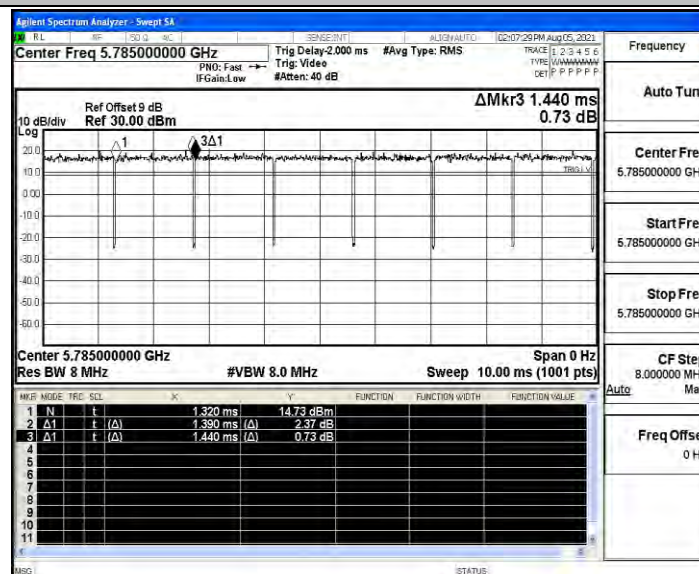
11A_ANT3_5240



11A_ANT3_5745

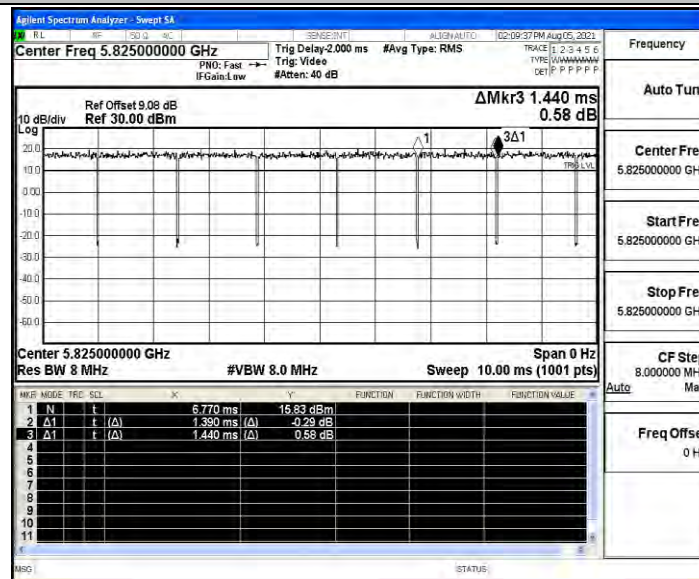


11A_ANT3_5785

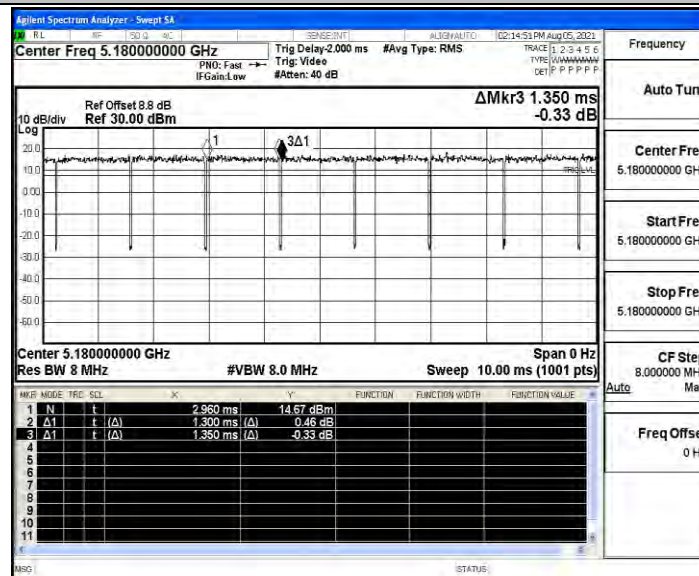




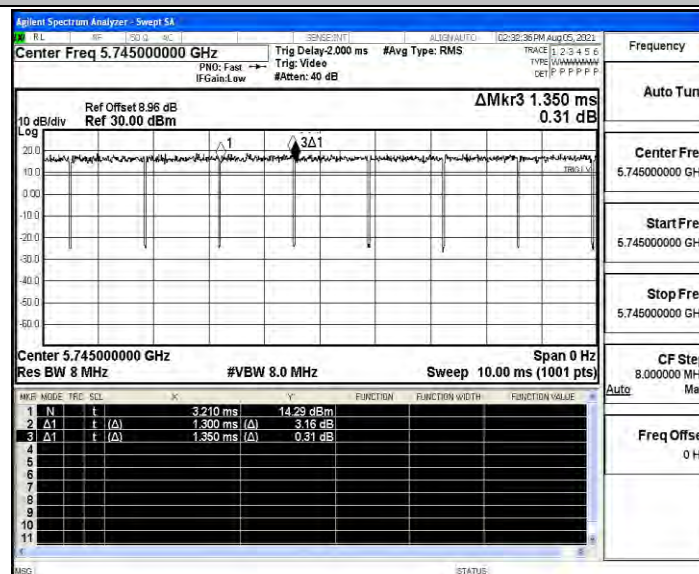
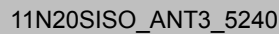
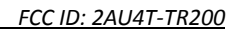
11A_ANT3_5825



11N20SISO_ANT3_5180

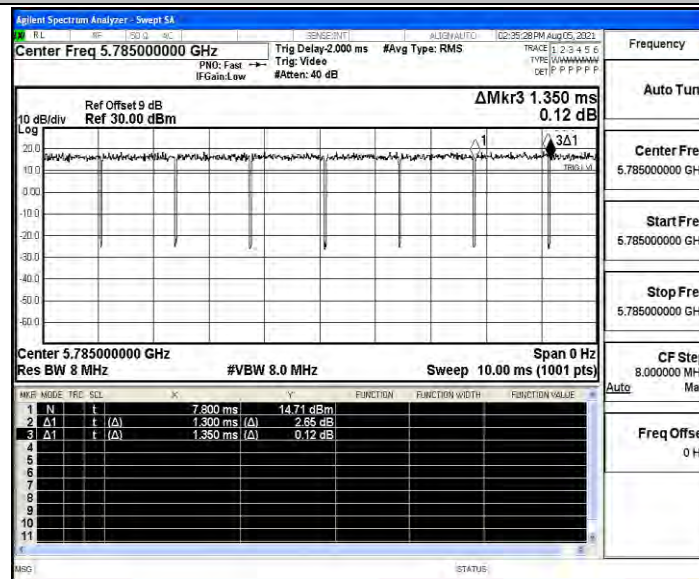


11N20SISO_ANT3_5200

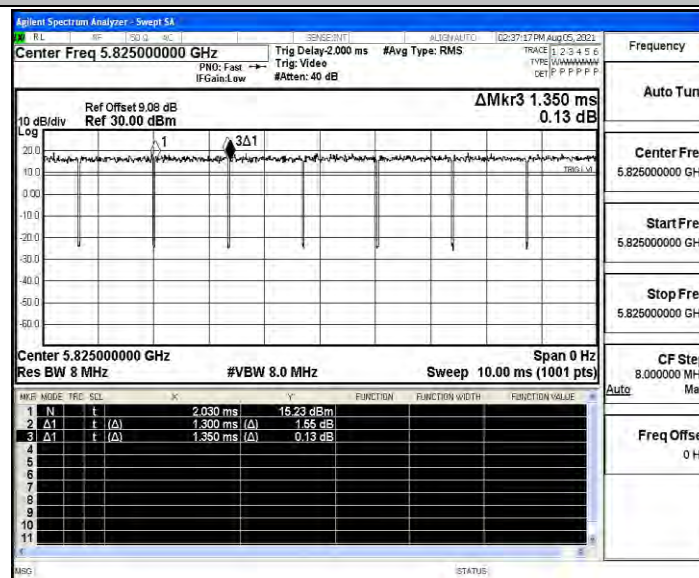




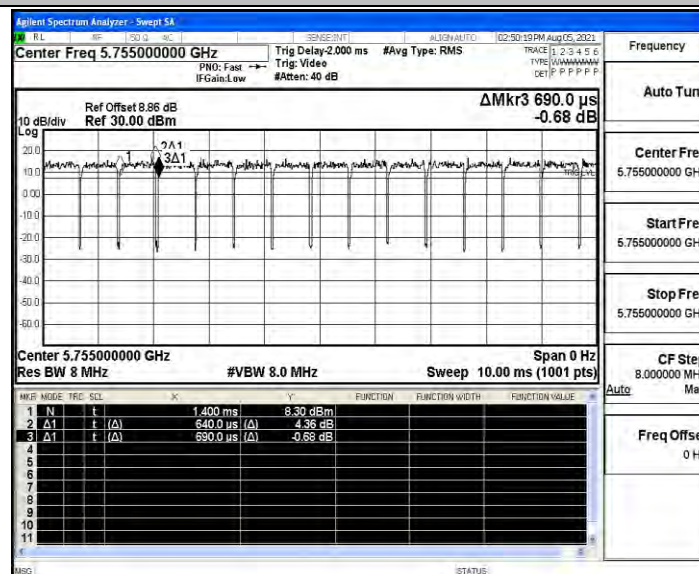
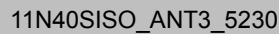
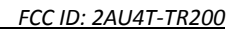
11N20SISO_ANT3_5785



11N20SISO_ANT3_5825

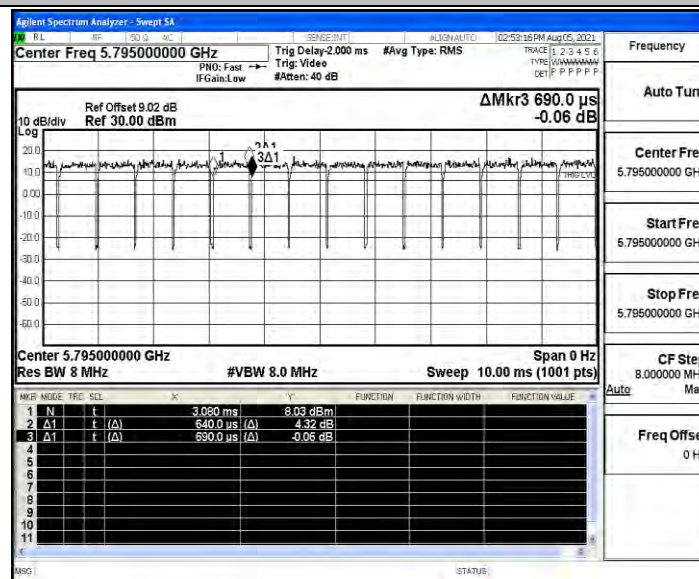


11N40SISO_ANT3_5190

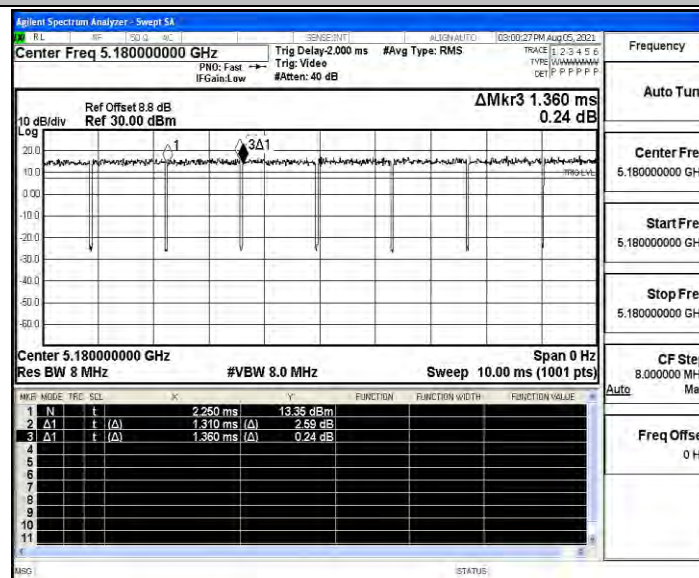




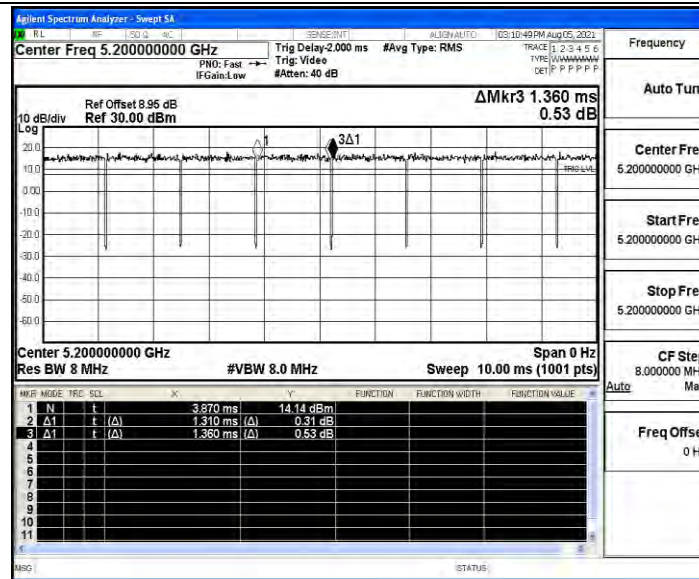
11N40SISO_ANT3_5795



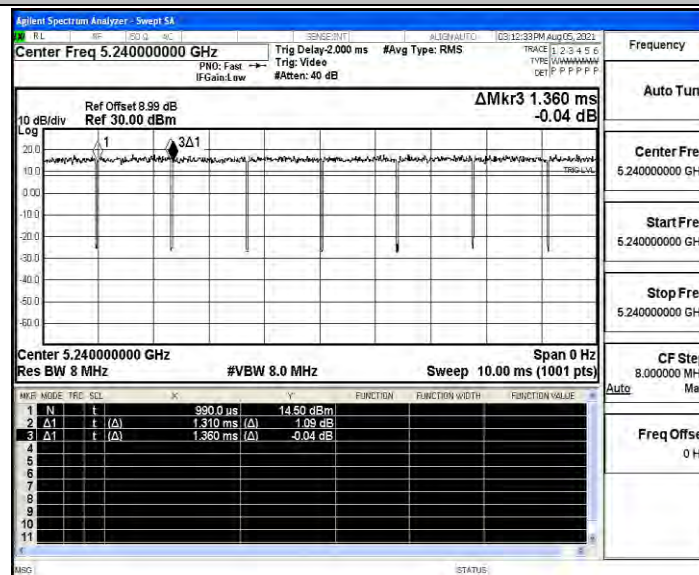
11AC20SISO_ANT3_5180



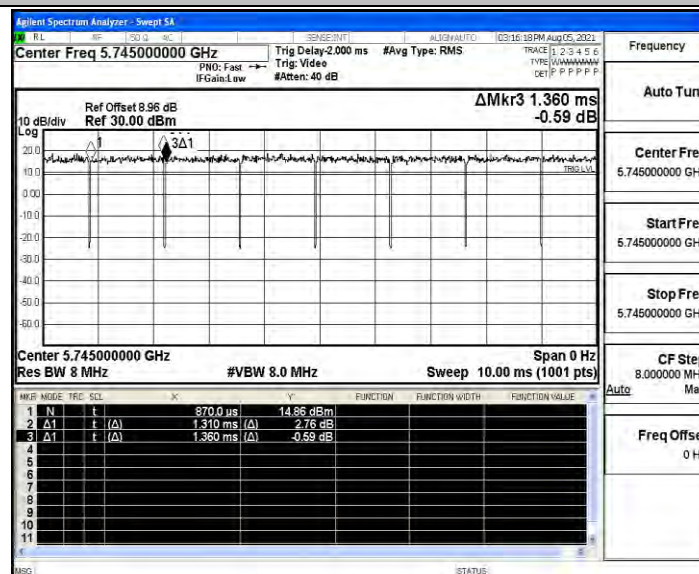
11AC20SISO_ANT3_5200



11AC20SISO_ANT3_5240

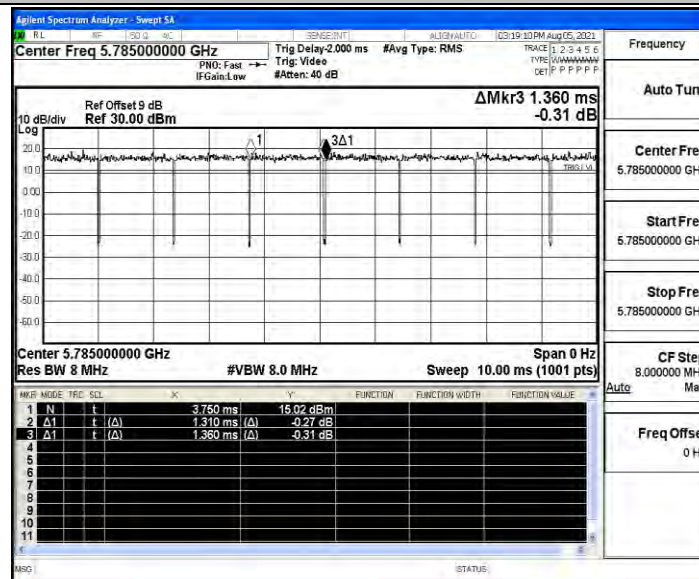


11AC20SISO_ANT3_5745

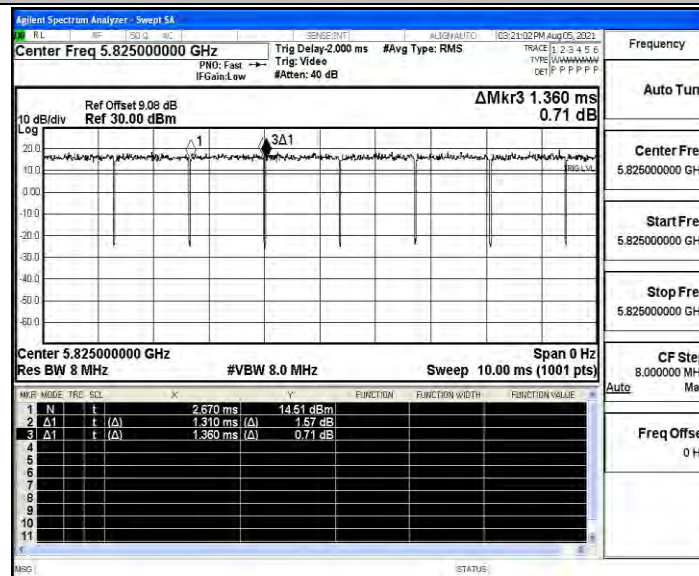




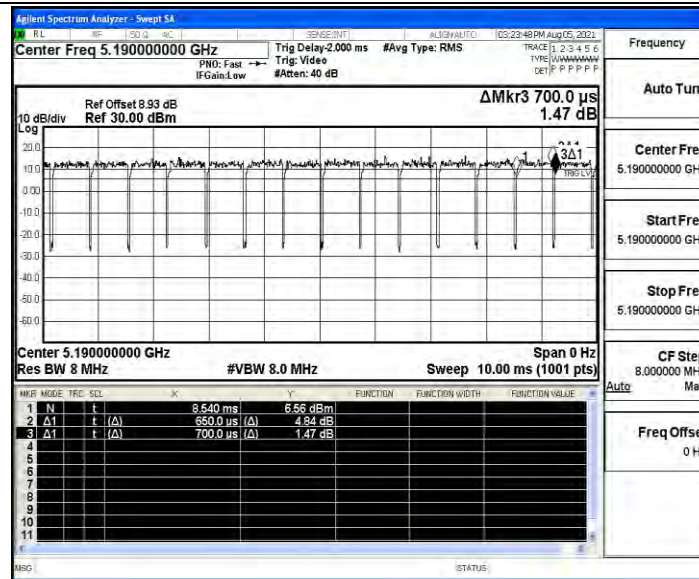
11AC20SISO_ANT3_5785



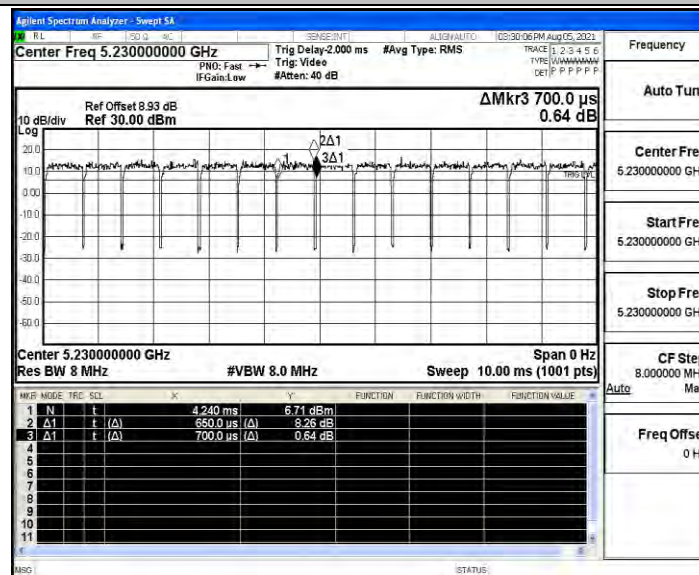
11AC20SISO_ANT3_5825



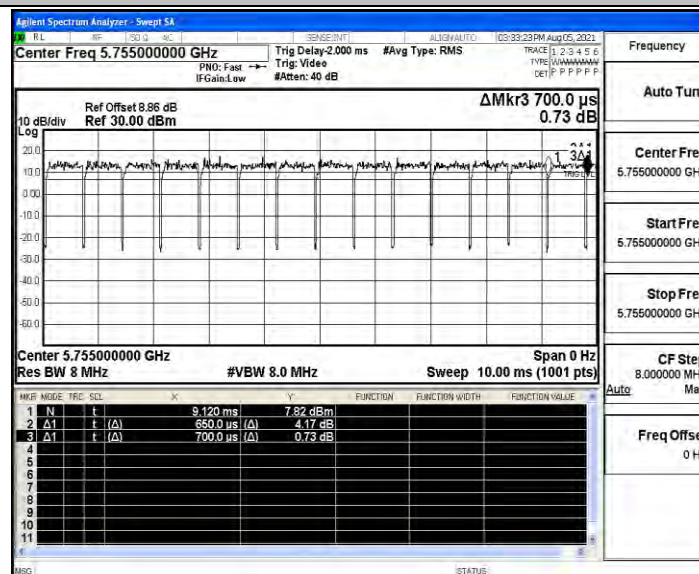
11AC40SISO_ANT3_5190



11AC40SISO_ANT3_5230

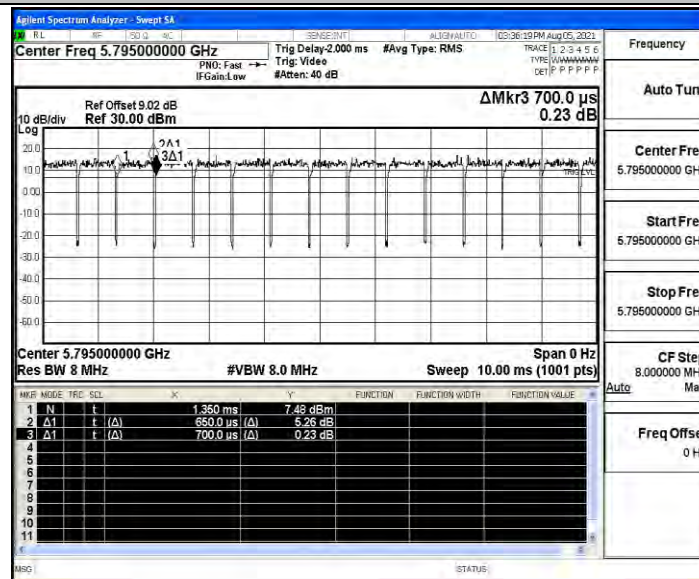


11AC40SISO_ANT3_5755

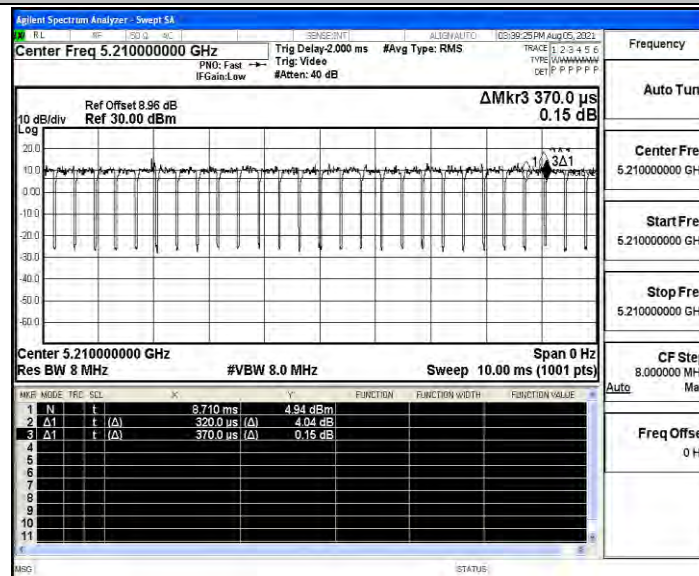




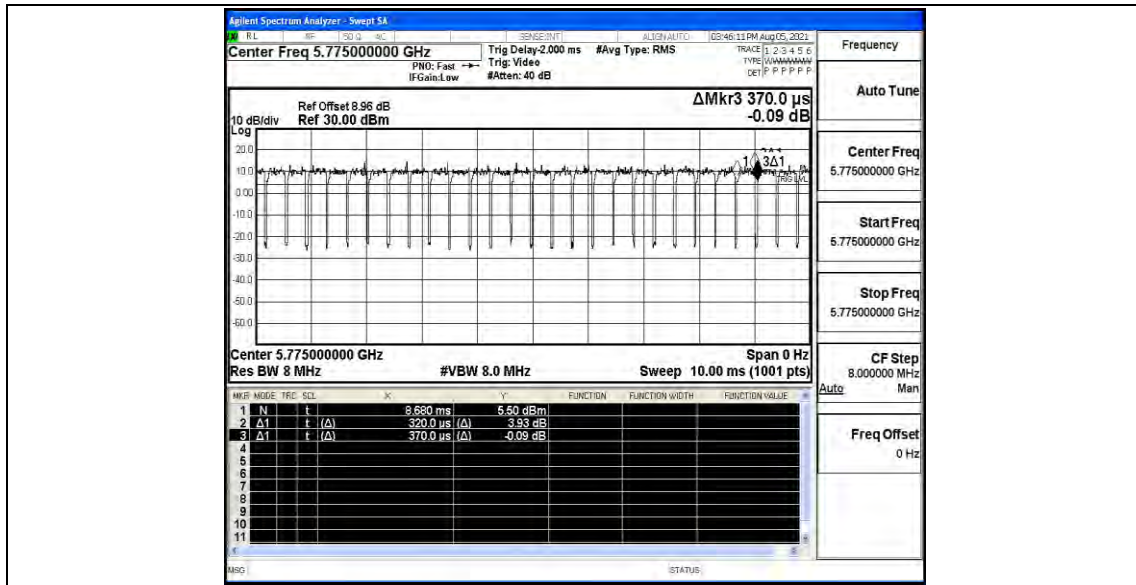
11AC40SISO_ANT3_5795



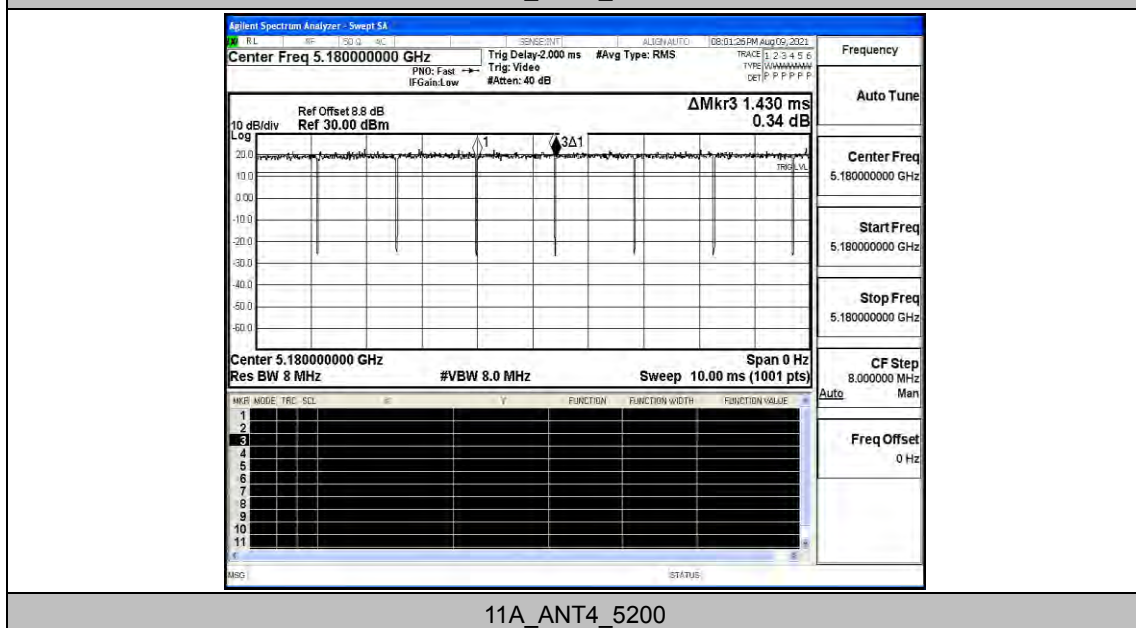
11AC80SISO_ANT3_5210



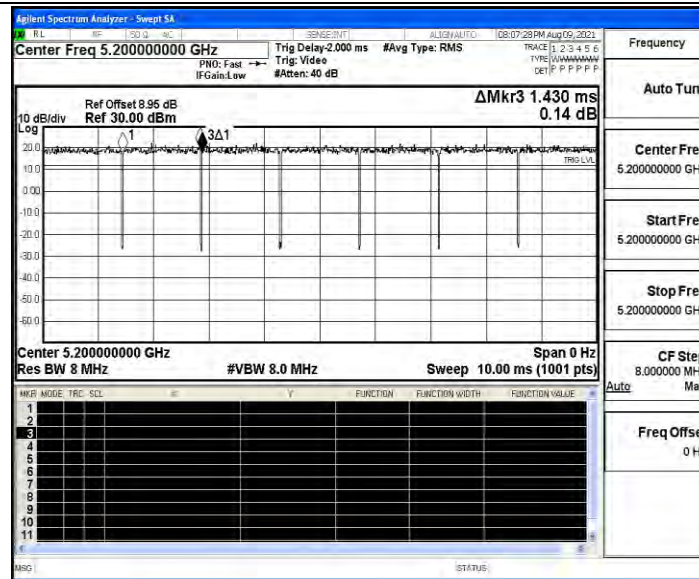
11AC80SISO_ANT3_5775



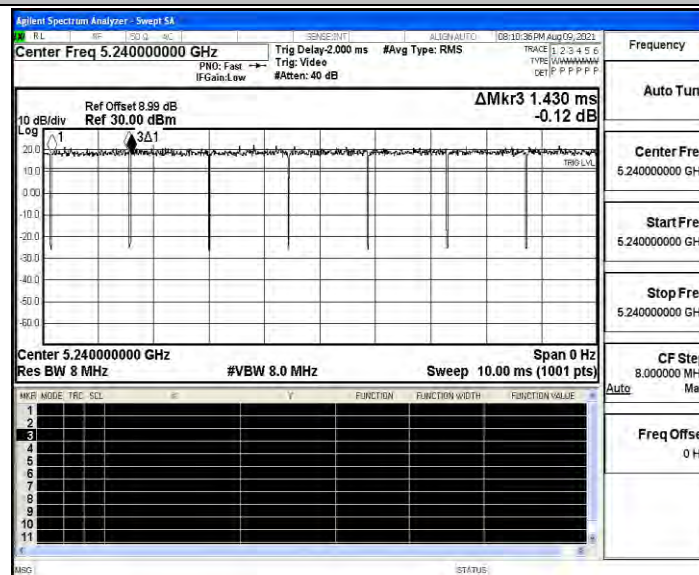
11A_ANT4_5180



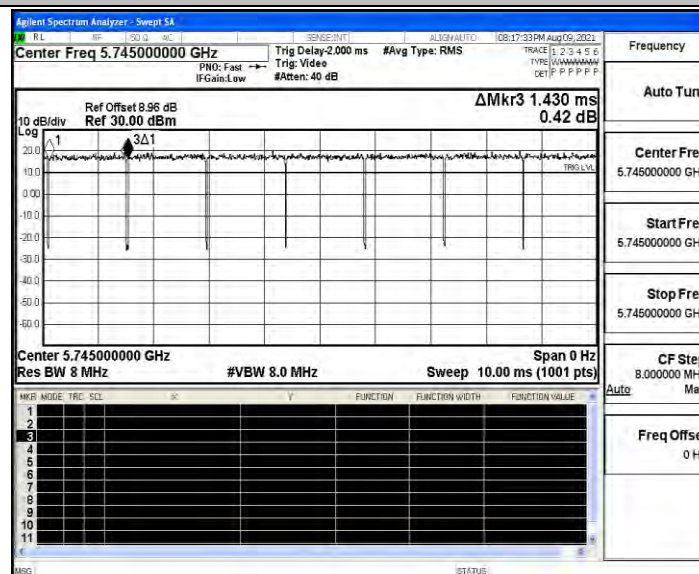
11A_ANT4_5200



11A_ANT4_5240



11A_ANT4_5745





Agilent Spectrum Analyzer - Sweep SA

IF L F IF F SQ G B C

SPAN: INT1 ALIGN: AUTO (08:20:28 AM Aug 09, 2021)

Center Freq 5.785000000 GHz Trig Delay: 2.000 ms #Avg Type: RMS TRACE 1 2 3 4 5 6

PNO: Fast IF Gain: Low Trig: Video #Atten: 40 dB TYPE: DANAMANA DET: P P P P P P

Auto Tune

Frequency

Auto Tune

Center Freq 5.785000000 GHz

Start Frequency 5.785000000 GHz

Stop Frequency 5.785000000 GHz

CF Stop 8.0000000 MHz

Auto Man

Freq Offset 0 Hz

Ref Offset 9 dB
Ref 30.00 dBm

$\Delta Mkr3$ 1.430 ms
0.03 dB

10 dB/div
Log

3dB

Center 5.785000000 GHz
Res BW 8 MHz #VBW 8.0 MHz Sweep 10.00 ms (1001 pts) Span 0 Hz

MNR MODE TRC SCL IF V FUNCTION FUNCTION WIDTH FUNCTION VALUE

1
2
3
4
5
6
7
8
9
10
11

MSG STATUS

Agilent Spectrum Analyzer - Sweep SA

IF IL RF IF3 Q MC TRIGGER: INT1 ALIGN: AUTO DATE: 22-10-2018 09:00:00

Center Freq 5.825000000 GHz Trig Delay: 2.000 ms #Avg Type: RMS TRACE 1 2 3 4 5 6
 PNO: Fast IF Gain: Low #Atten: 40 dB TYPE: DANAMAN DET: P P P P P

Ref Offset 9.08 dB ΔMkr3 1.430 ms
 Ref 30.00 dBm 0.85 dB

10 dB/div Log
 1 3Δ1
 0.00
 -10.0
 -20.0
 -30.0
 -40.0
 -50.0

Center 5.825000000 GHz Span 0 Hz
 Res BW 8 MHz #VBW 8.0 MHz Sweep 10.00 ms (1001 pts)

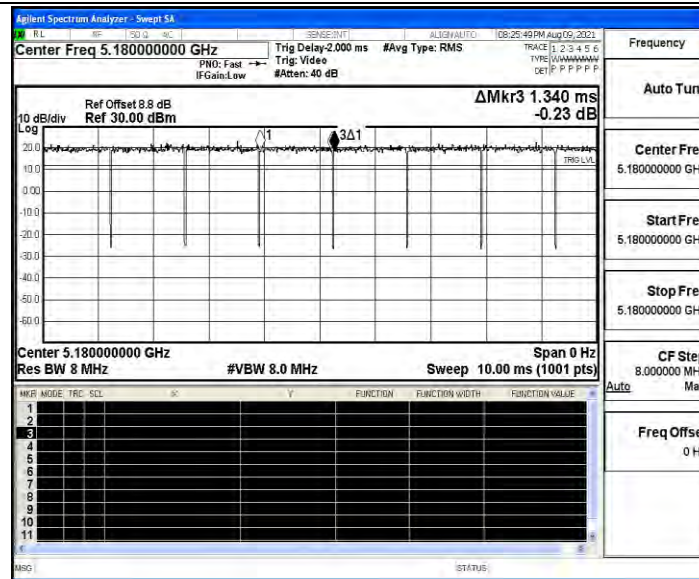
MNR	MODE	TRC	SCN	IF	V	FUNCTION	FUNCTION WIDTH	FUNCTION VALUE
1								
2								
3								
4								
5								
6								
7								
8								
9								
10								
11								

Auto Mar

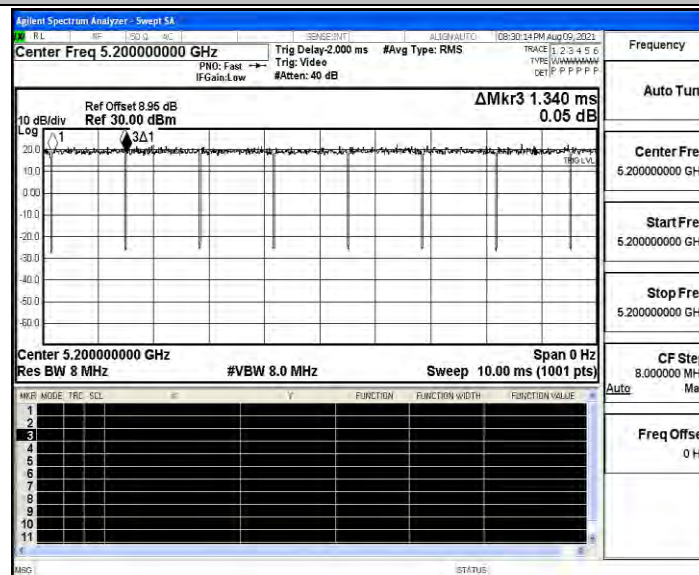
Freq Offset 0 Hz

STATUS

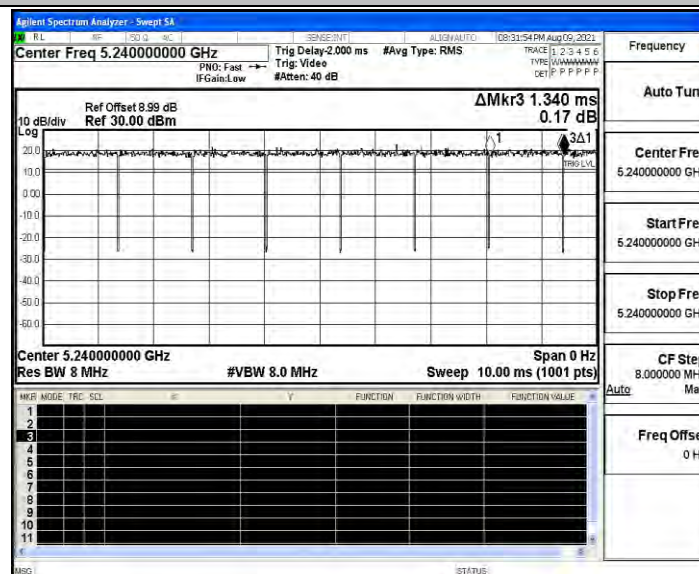
11N20SISO ANT4 5180



11N20SISO_ANT4_5200

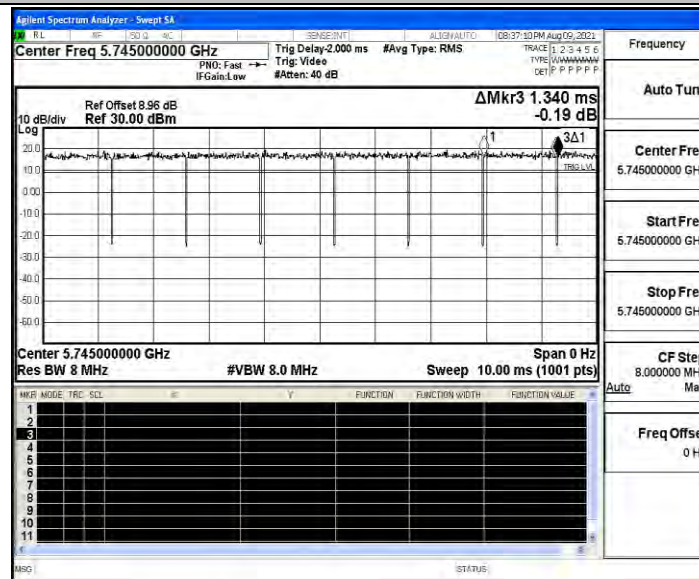


11N20SISO_ANT4_5240

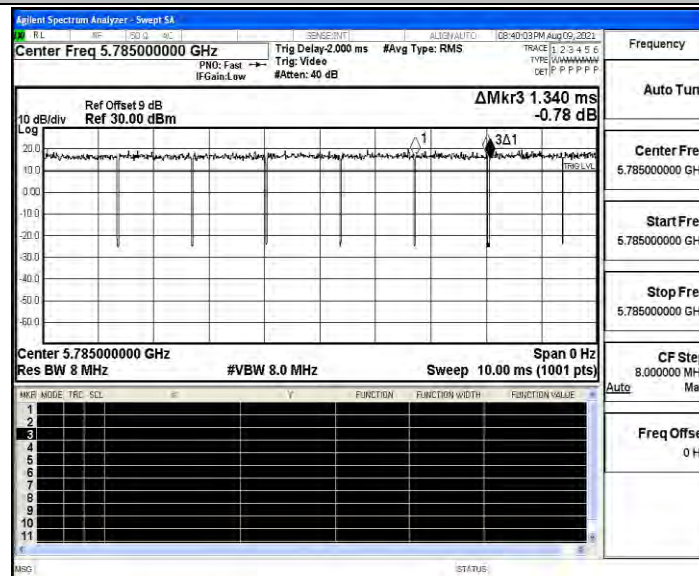




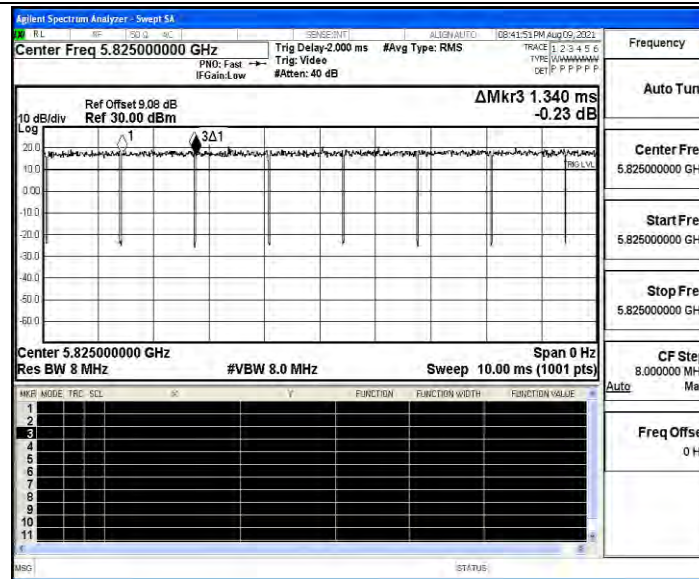
11N20SISO_ANT4_5745



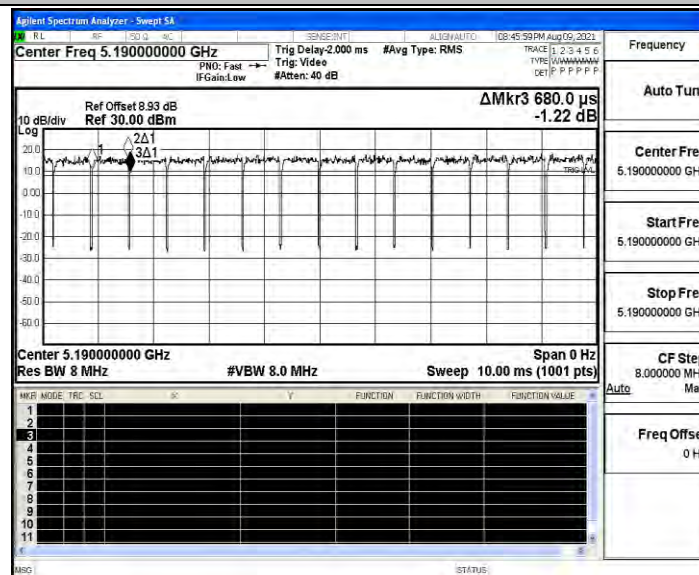
11N20SISO_ANT4_5785



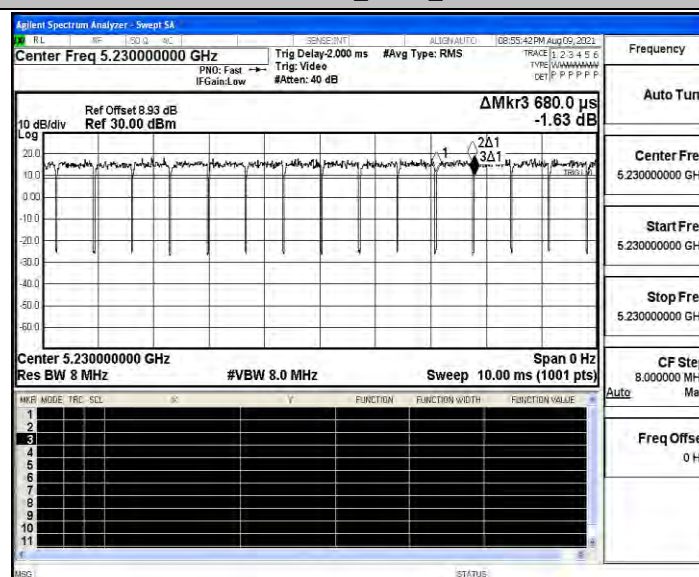
11N20SISO_ANT4_5825



11N40SISO_ANT4_5190

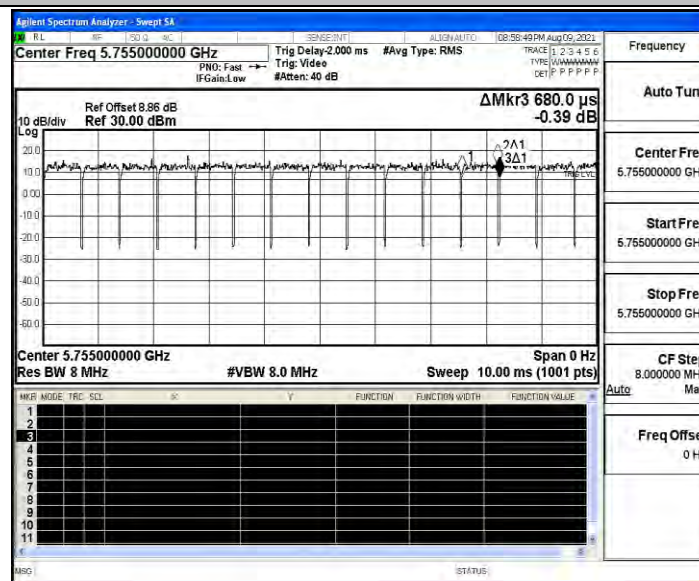


11N40SISO_ANT4_5230

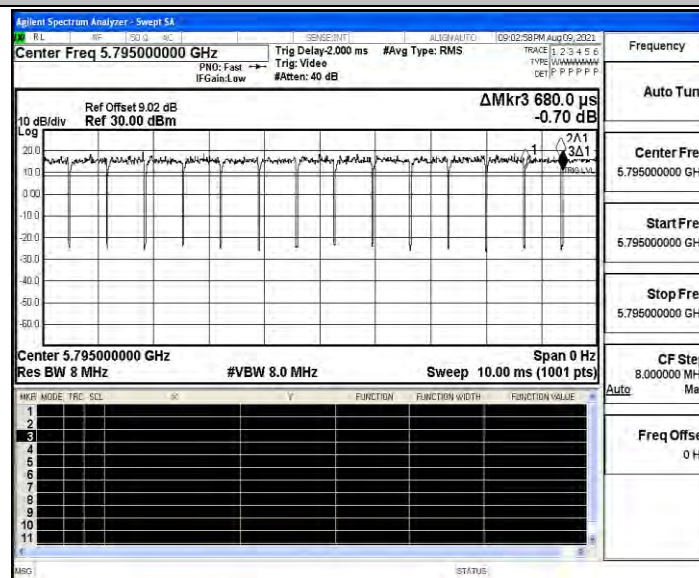




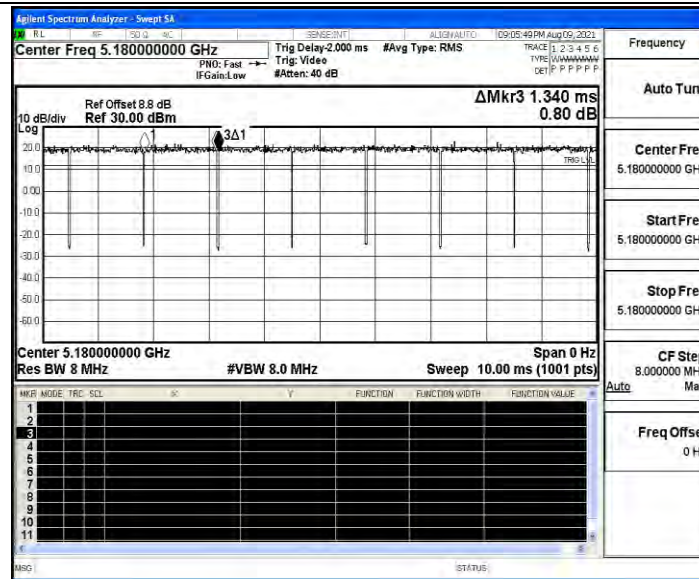
11N40SISO_ANT4_5755



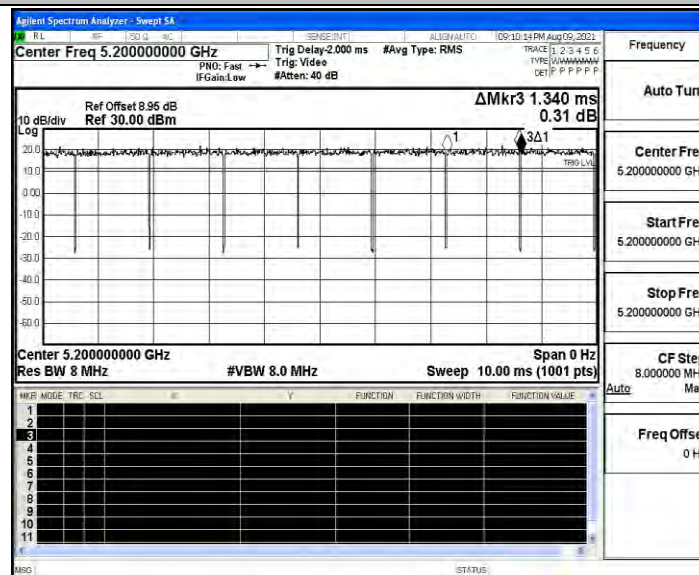
11N40SISO_ANT4_5795



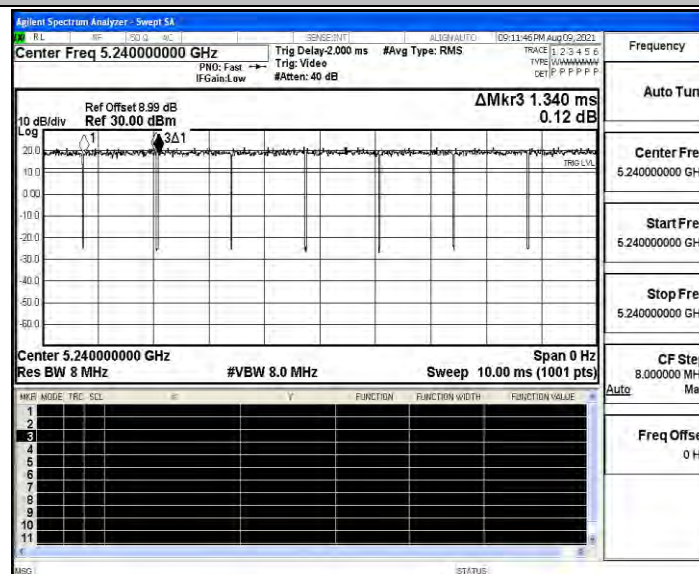
11AC20SISO_ANT4_5180



11AC20SISO_ANT4_5200

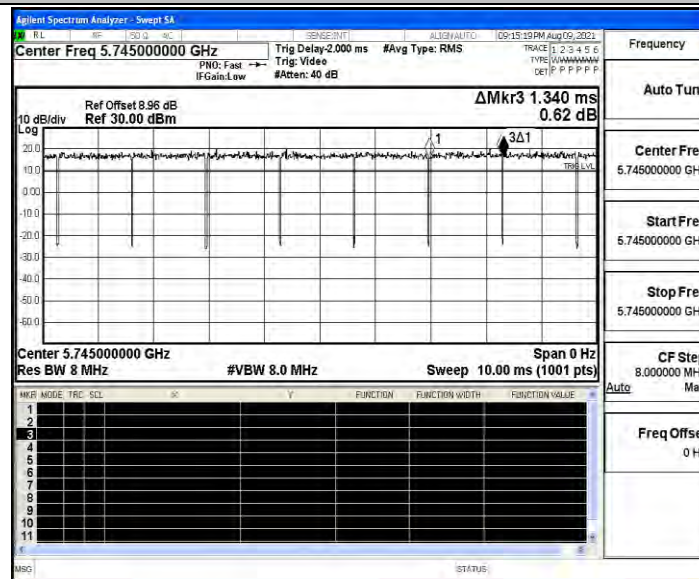


11AC20SISO_ANT4_5240

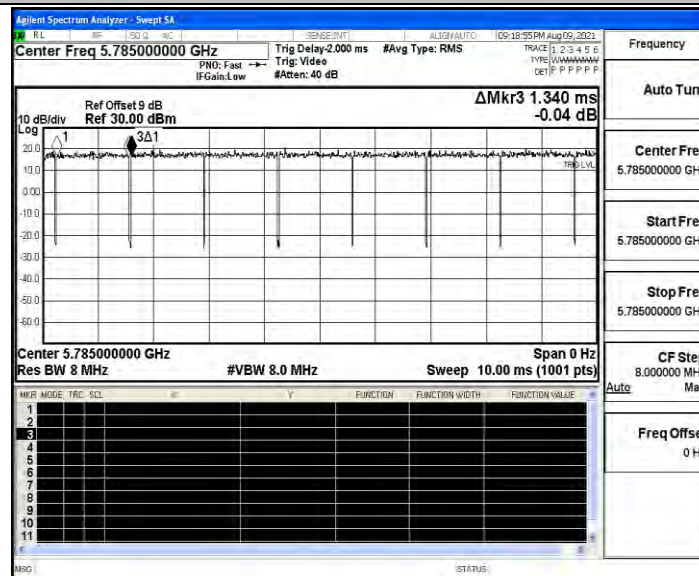




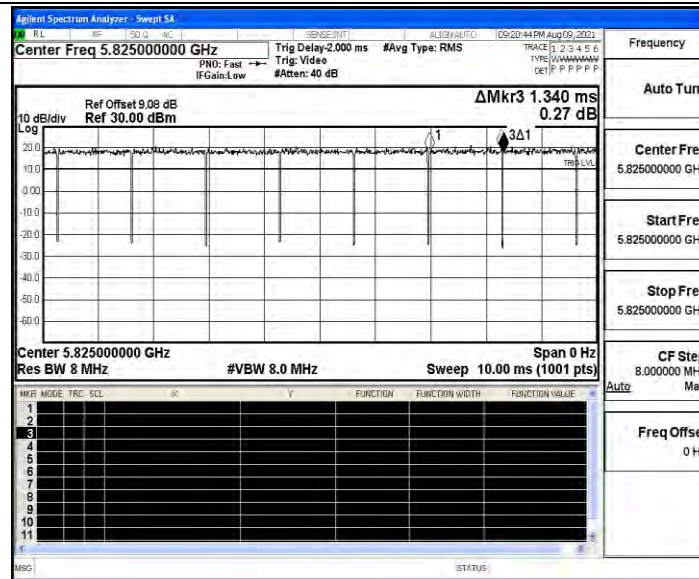
11AC20SISO_ANT4_5745



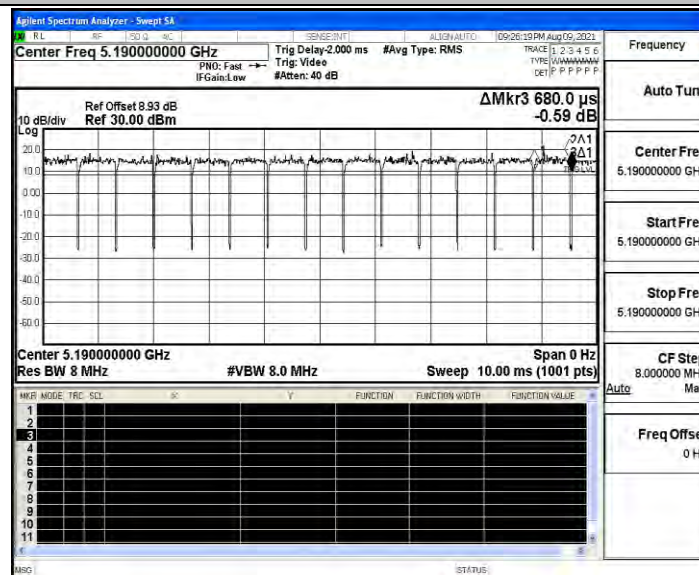
11AC20SISO_ANT4_5785



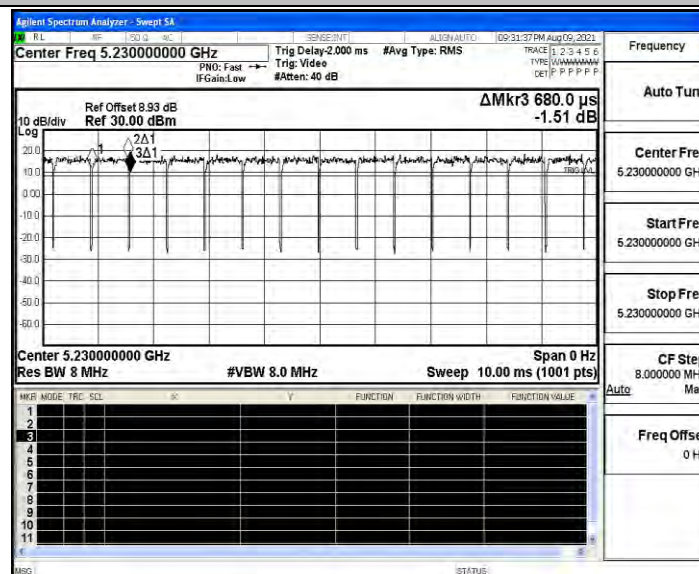
11AC20SISO_ANT4_5825



11AC40SISO_ANT4_5190

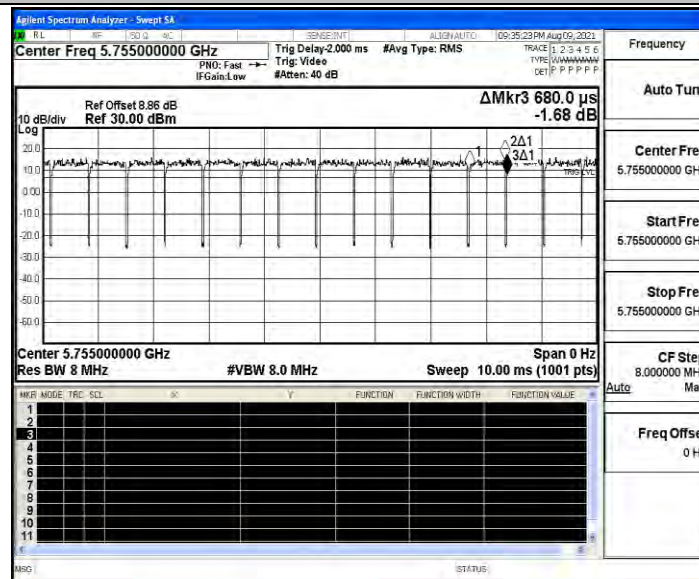


11AC40SISO_ANT4_5230

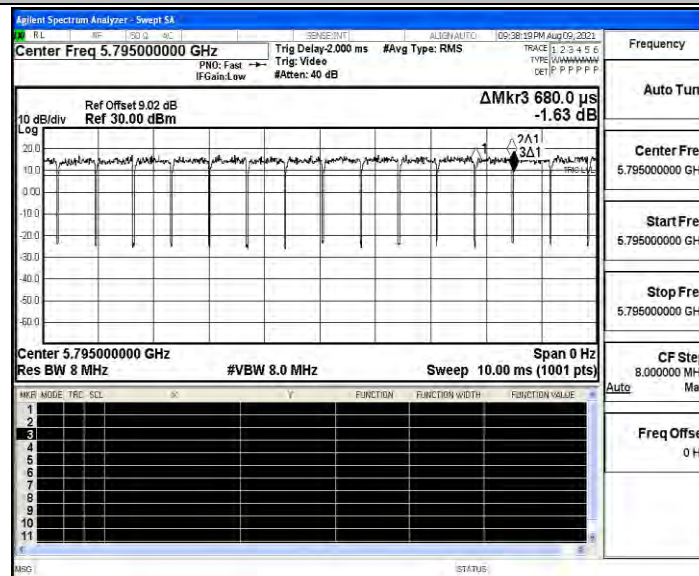




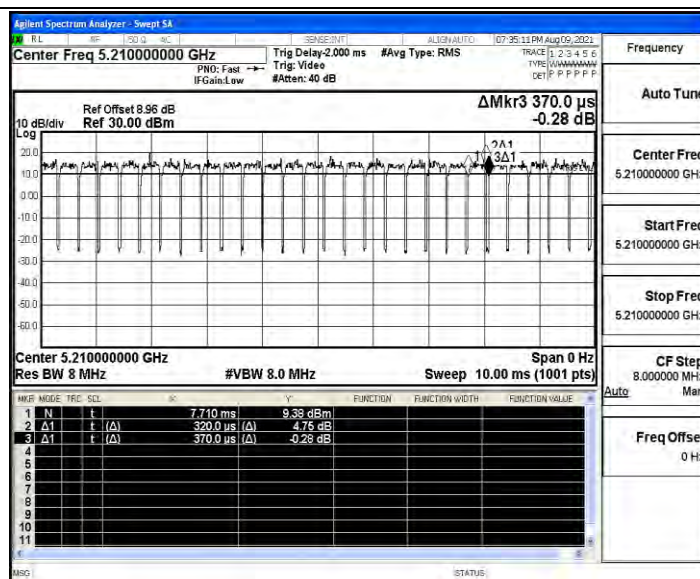
11AC40SISO_ANT4_5755



11AC40SISO_ANT4_5795



11AC80SISO_ANT4_5210



11AC80SISO ANT4 5775

