

CHILI2

Schematics

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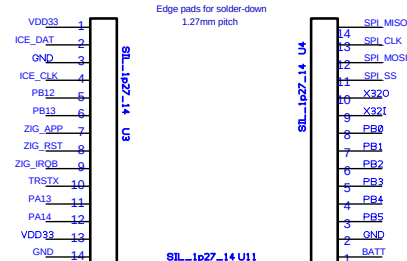
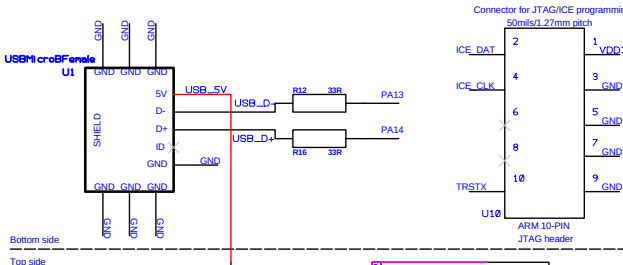
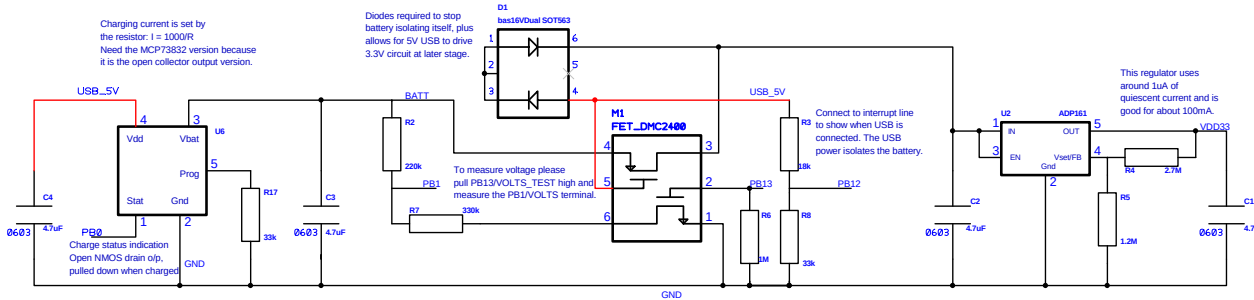
Model CHILI2D

Cascoda CHILI2D

Power nets:
 BATT: Battery (+) terminal
 BATT_SW: Switched output of battery
 USB_5V: Incoming 5V from USB
 REG_IN: Either USB or Battery feed to regulator
 VDD: Regulated power for circuit = 3.3V

Charging current is set by the resistor: $I = 1000/R$
Need the MCP73832 version because it is the open collector output version.

Diodes required to stop battery isolating itself, plus allows for 5V USB to drive 3.3V circuit at later stage



Project: Cascoda Chili

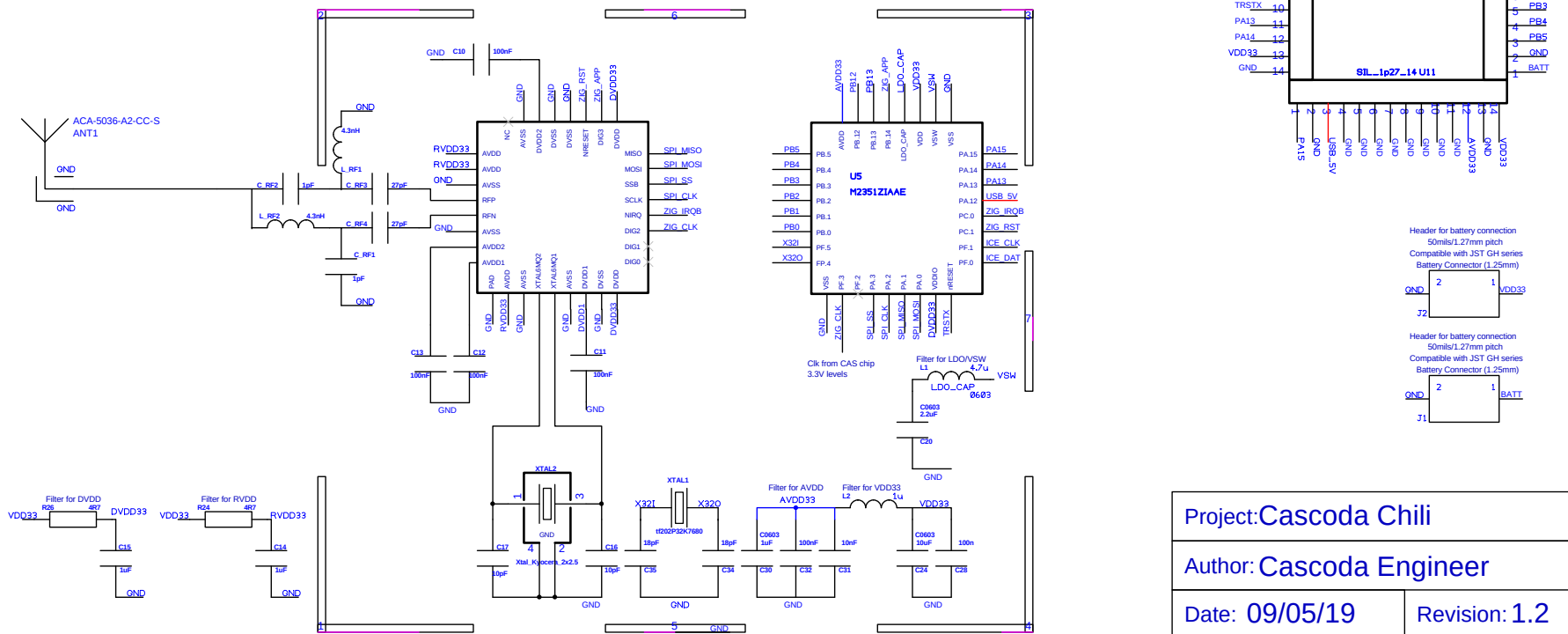
Author: Cascoda Engineer

Date: 09/05/19

Revision: 1.2

Model CHILI2S

Cascoda CHILI2S



Project: Cascoda Chili

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