

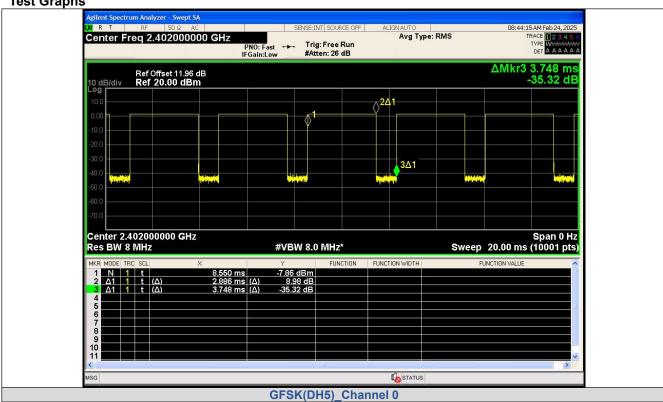
Report No.: MTEB25020179-R Page 54 of 63

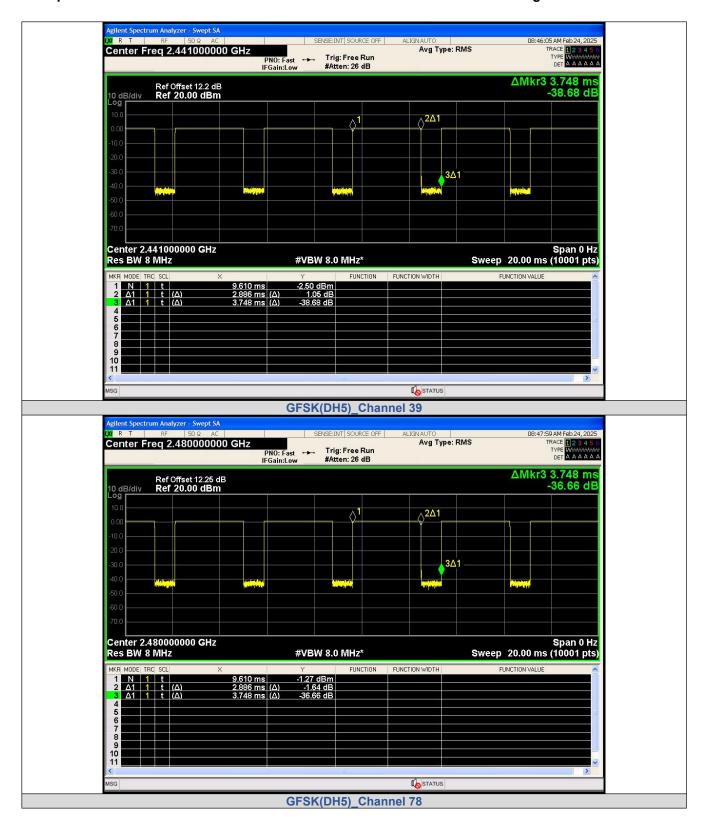
APPENDIX VI. Duty Cycle

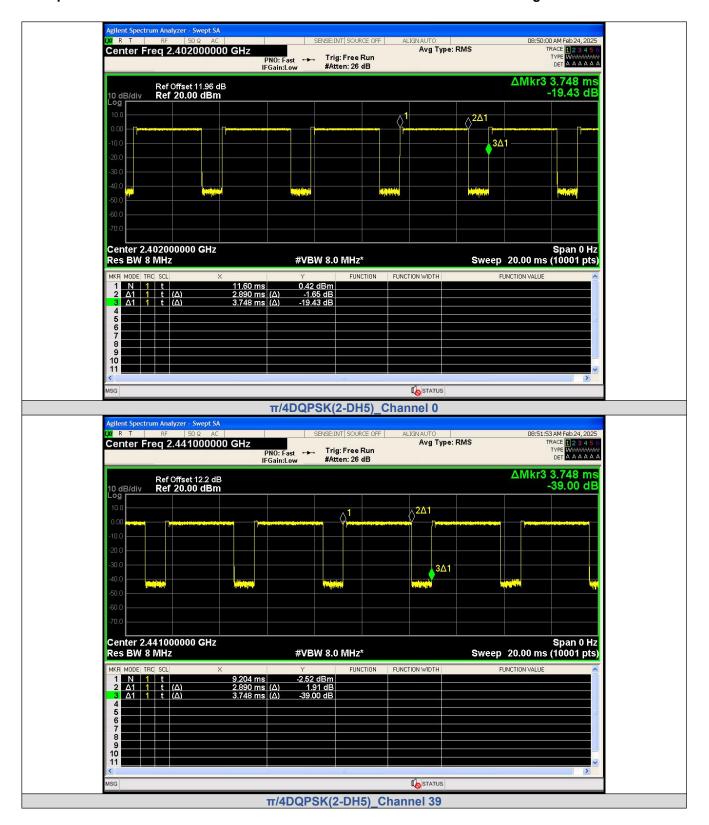
Test Result

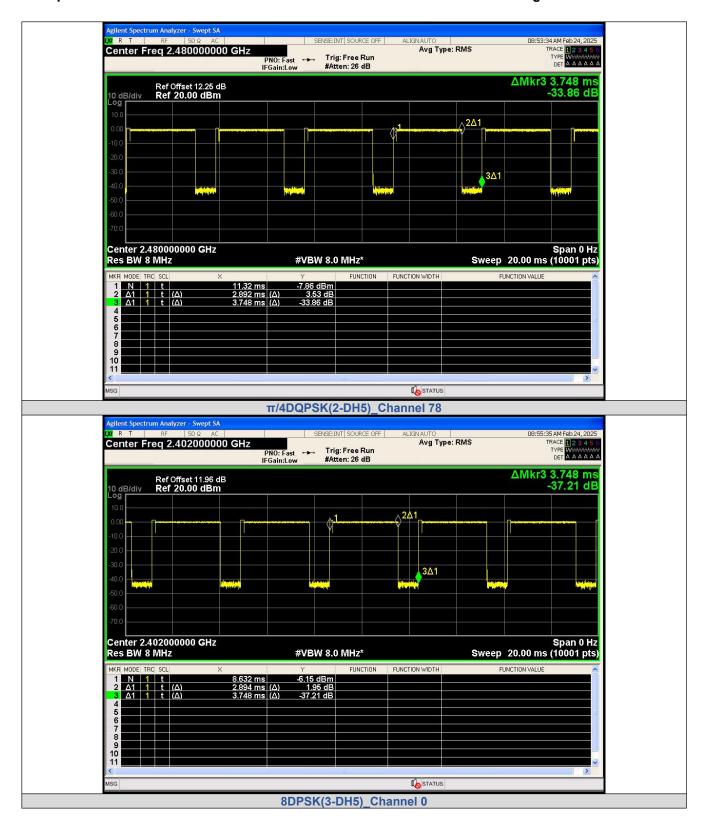
Modulation	Packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)
GFSK	DH5	0	2.886	3.748	77.00	0.7700	1.1351
		39	2.886	3.748	77.00	0.7700	1.1351
		78	2.886	3.748	77.00	0.7700	1.1351
π/4DQPSK	2-DH5	0	2.890	3.748	77.11	0.7711	1.1289
		39	2.890	3.748	77.11	0.7711	1.1289
		78	2.892	3.748	77.16	0.7716	1.1261
8DPSK	3-DH5	0	2.894	3.748	77.21	0.7721	1.1233
		39	2.892	3.748	77.16	0.7716	1.1261
		78	2.892	3.748	77.16	0.7716	1.1261

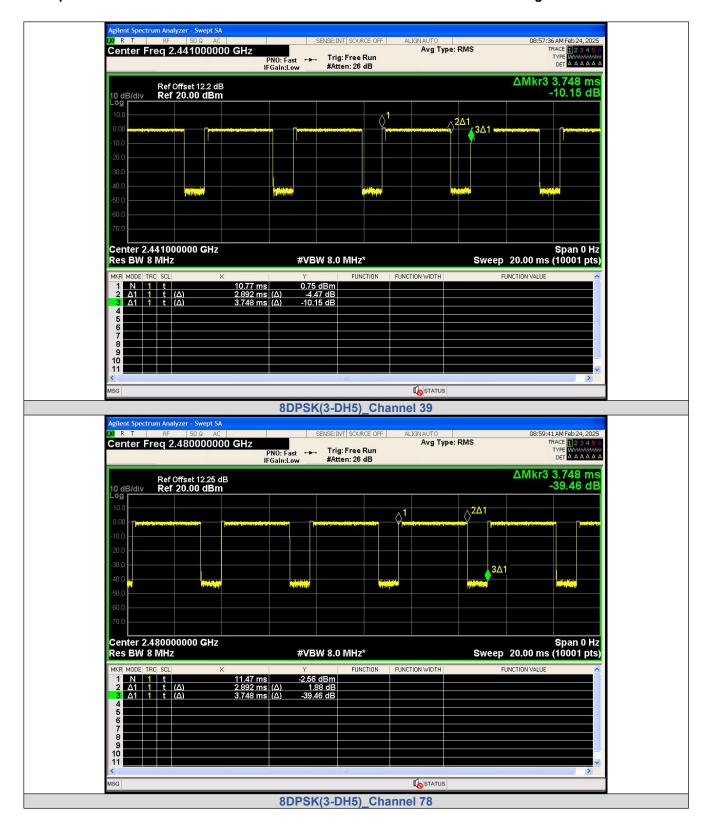












Report No.: MTEB25020179-R Page 59 of 63

APPENDIX VII. Dwell Time

Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	CH39 (2441MHz)	0.3828	318	121.73	< 400	PASS
	DH3		1.656	164	271.58		PASS
	DH5		2.880	109	313.92		PASS
π/4DQPSK	2-DH1		0.3912	318	124.4		PASS
	2-DH3		1.656	163	269.93		PASS
	2-DH5		2.880	108	311.04		PASS
8DPSK	3-DH1		0.3912	315	123.23		PASS
	3-DH3		1.656	162	268.27		PASS
	3-DH5		2.896	107	309.87		PASS

