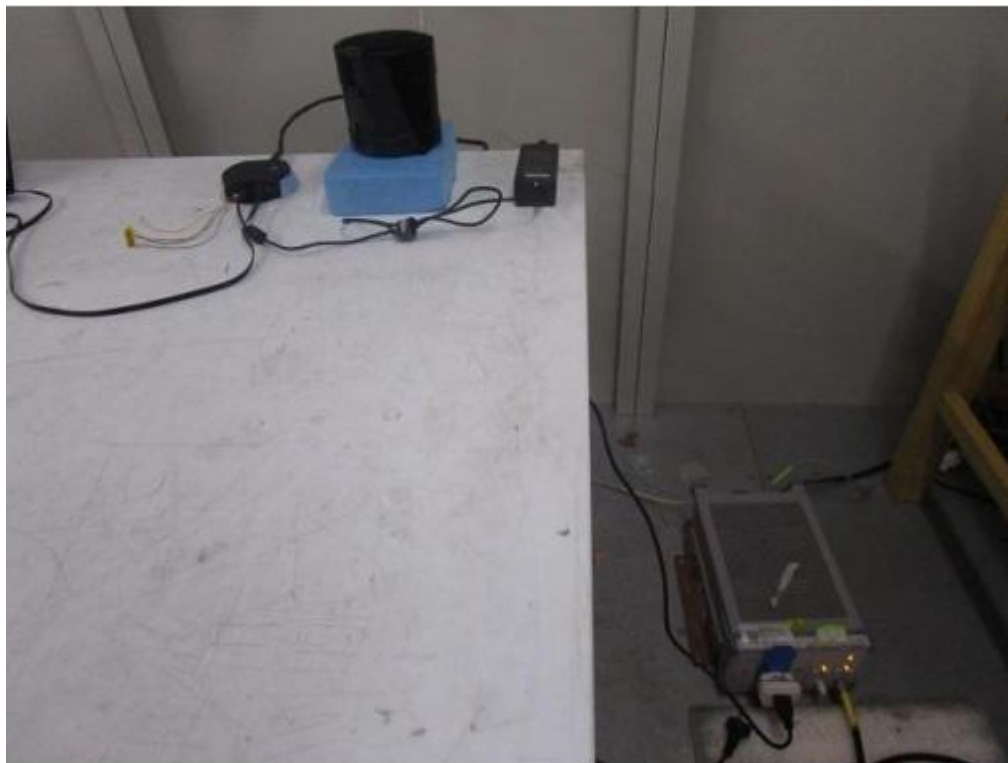
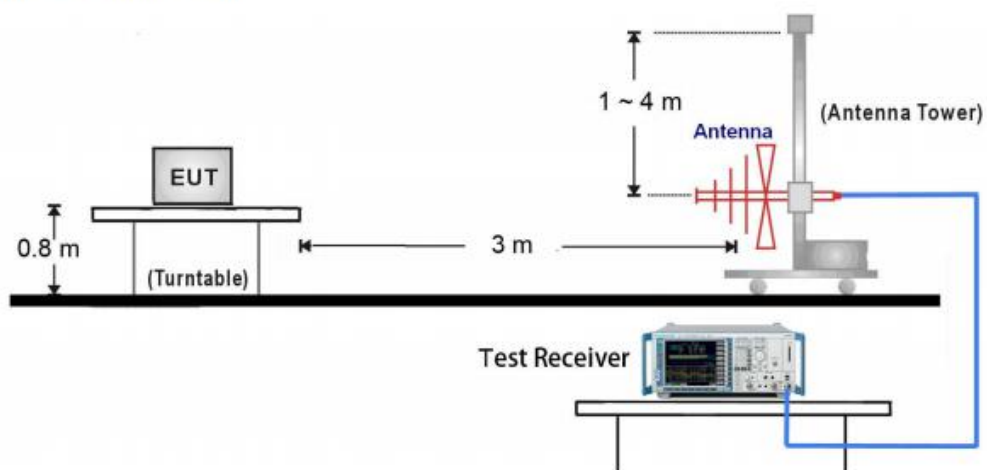


30MHz ~ 1GHz Test Setup:



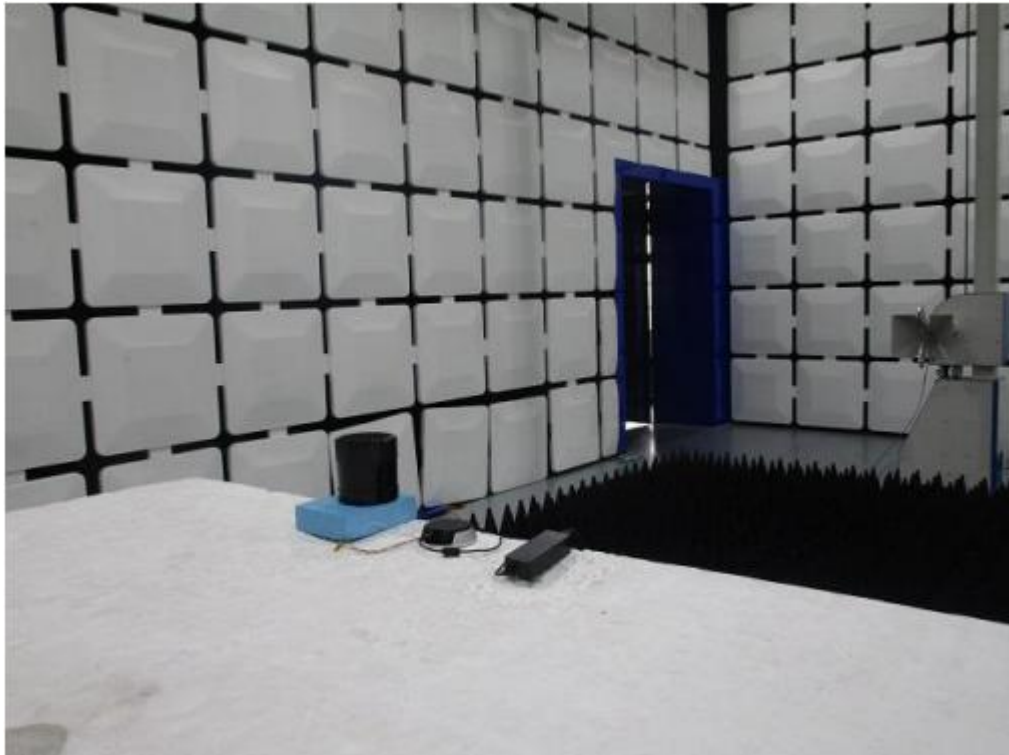
CE(Shape 1)



CE(Shape 2)



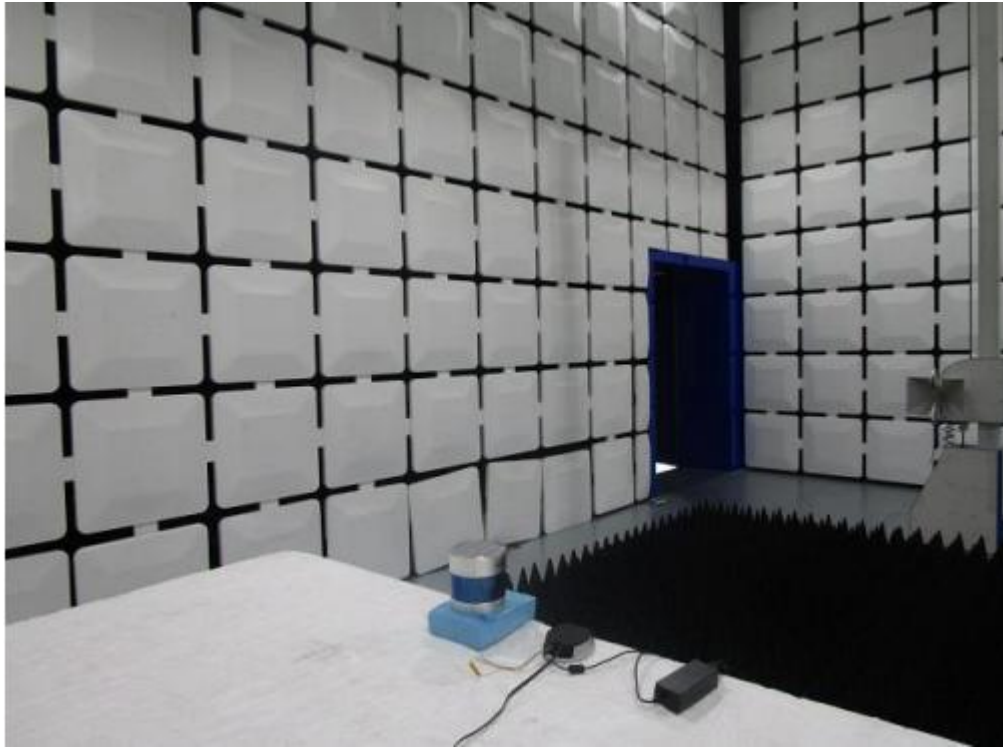
RE(Below 1G, Shape 1)



RE(Above 1G, Shape 1)



RE(Below 1G, Shape 2)



RE(Above 1G, Shape 2)