

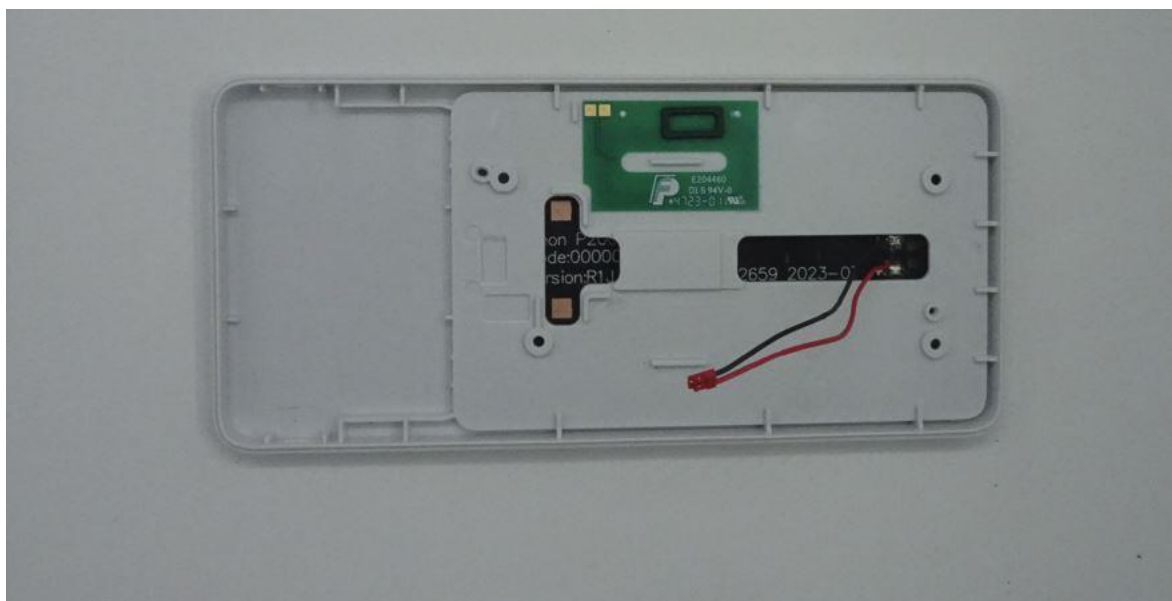
Appendix A: DUT Photographs



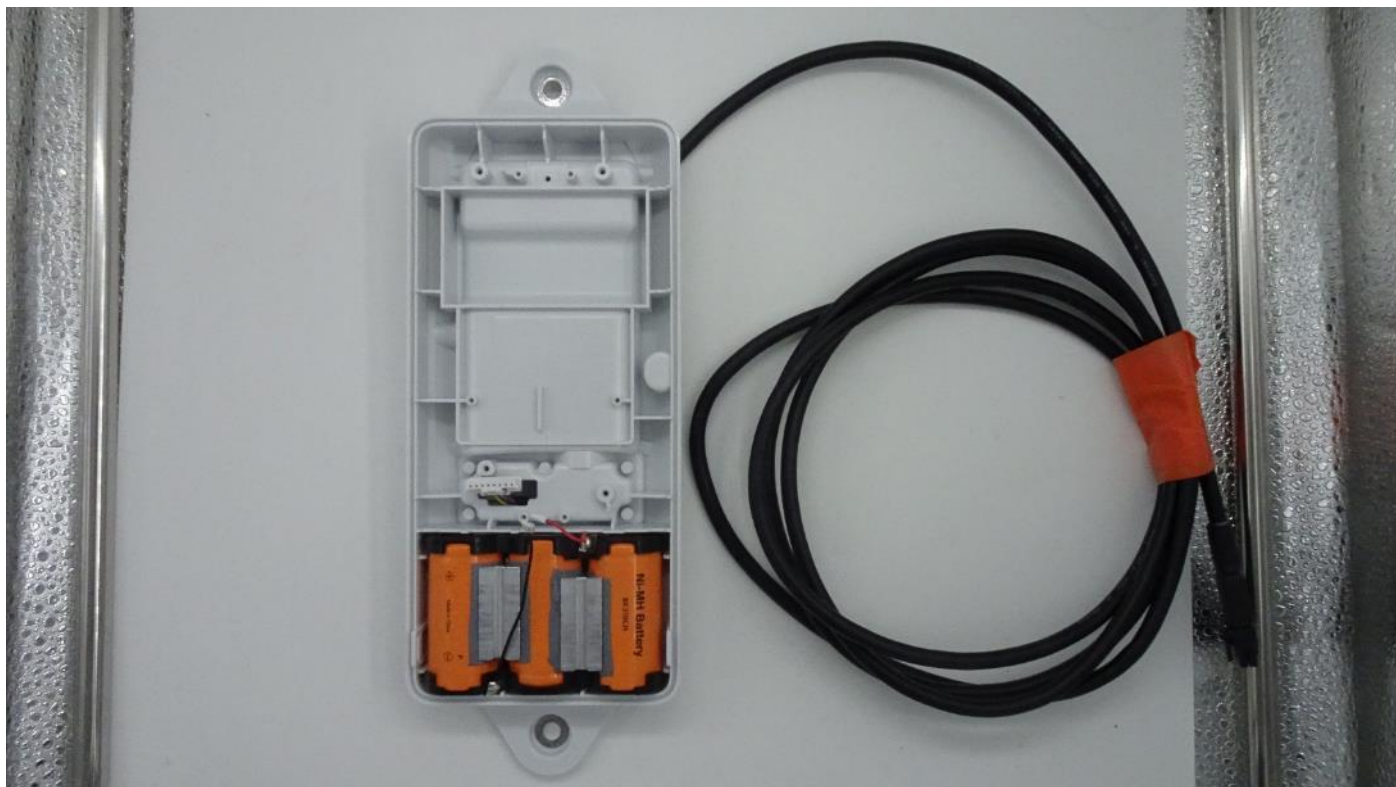
Picture 1 EUT Top view



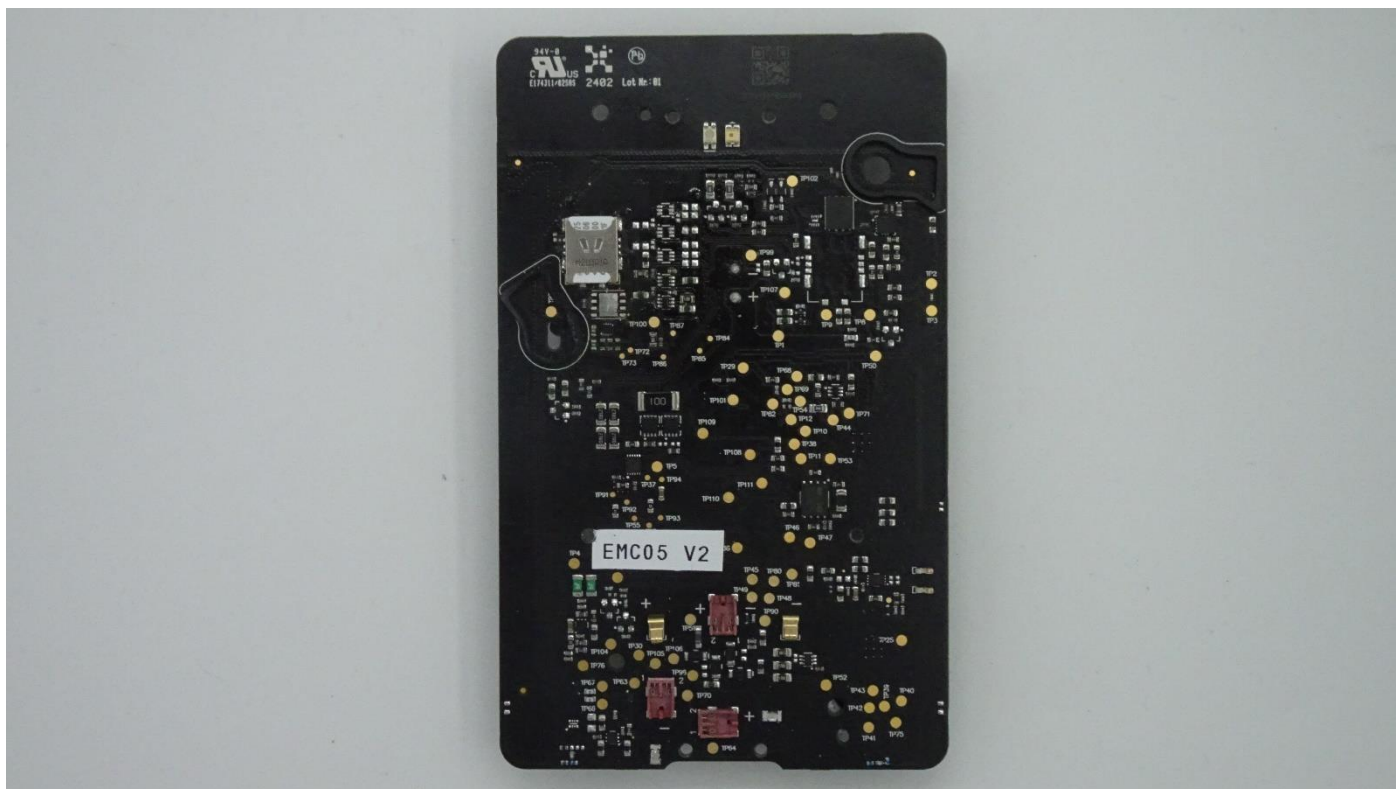
Picture 2 EUT Bottom view



Picture 3 EUT A cover inside



Picture 4 EUT B cover inside



Picture 5 EUT PCB Top

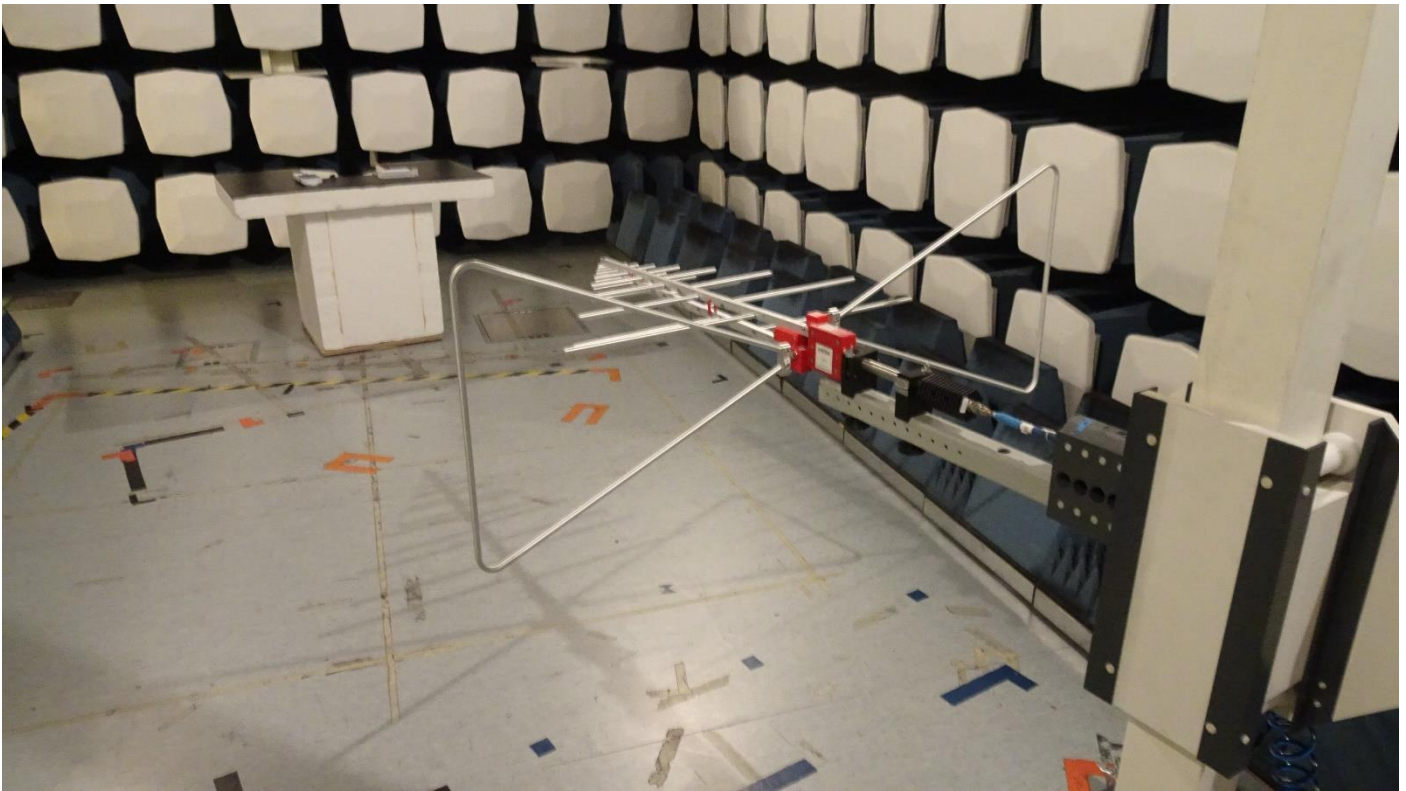


Picture 6 EUT PCB Bottom

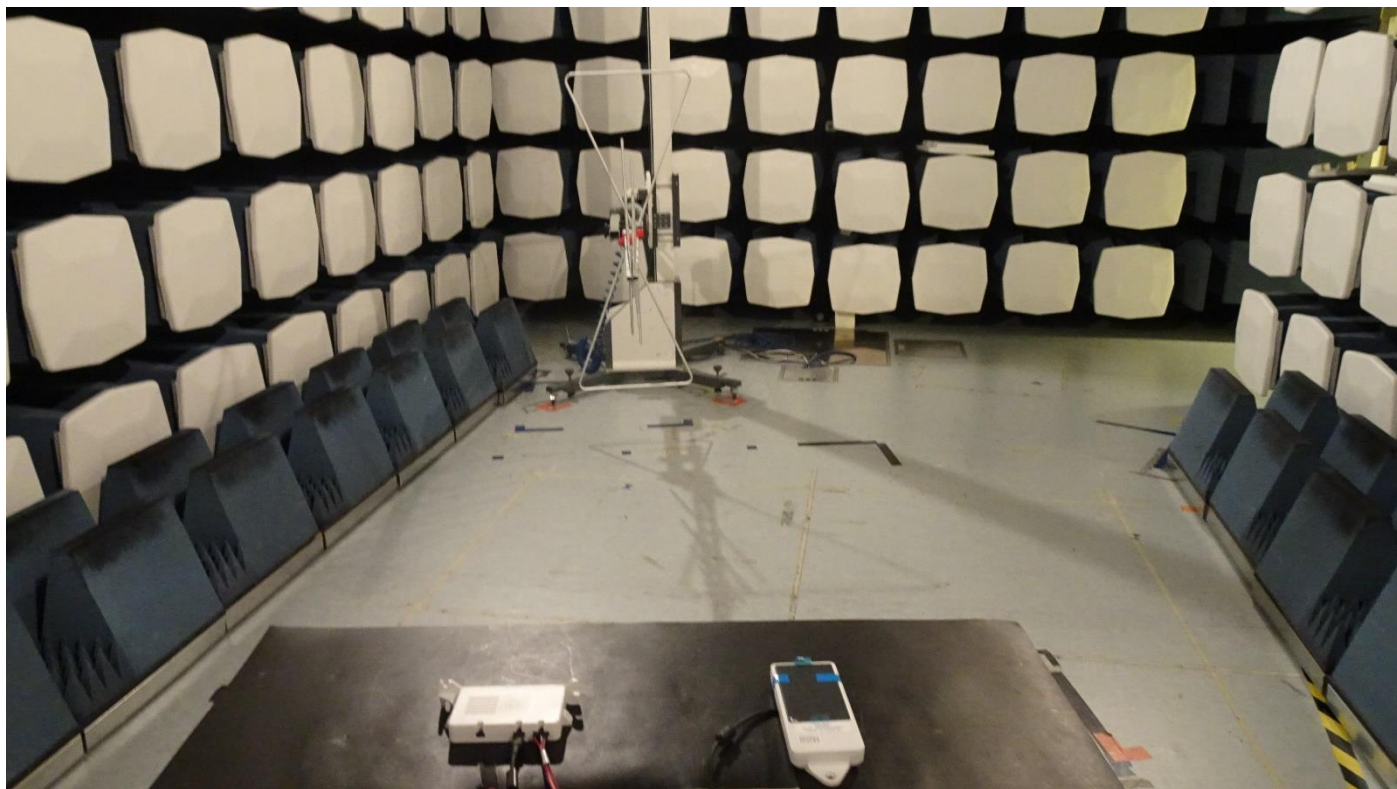


Picture 7 EUT Solar panel

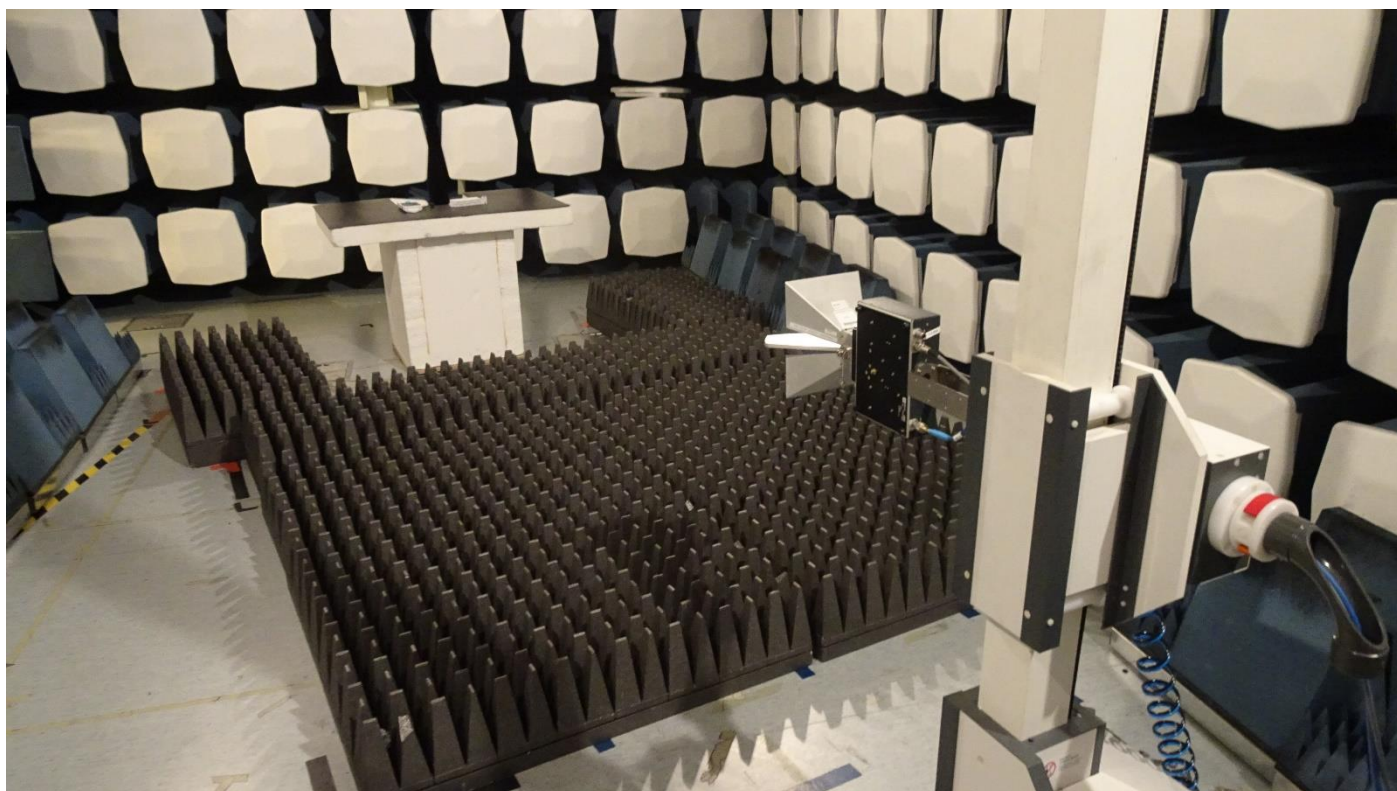
Appendix B: Test Setup Photographs



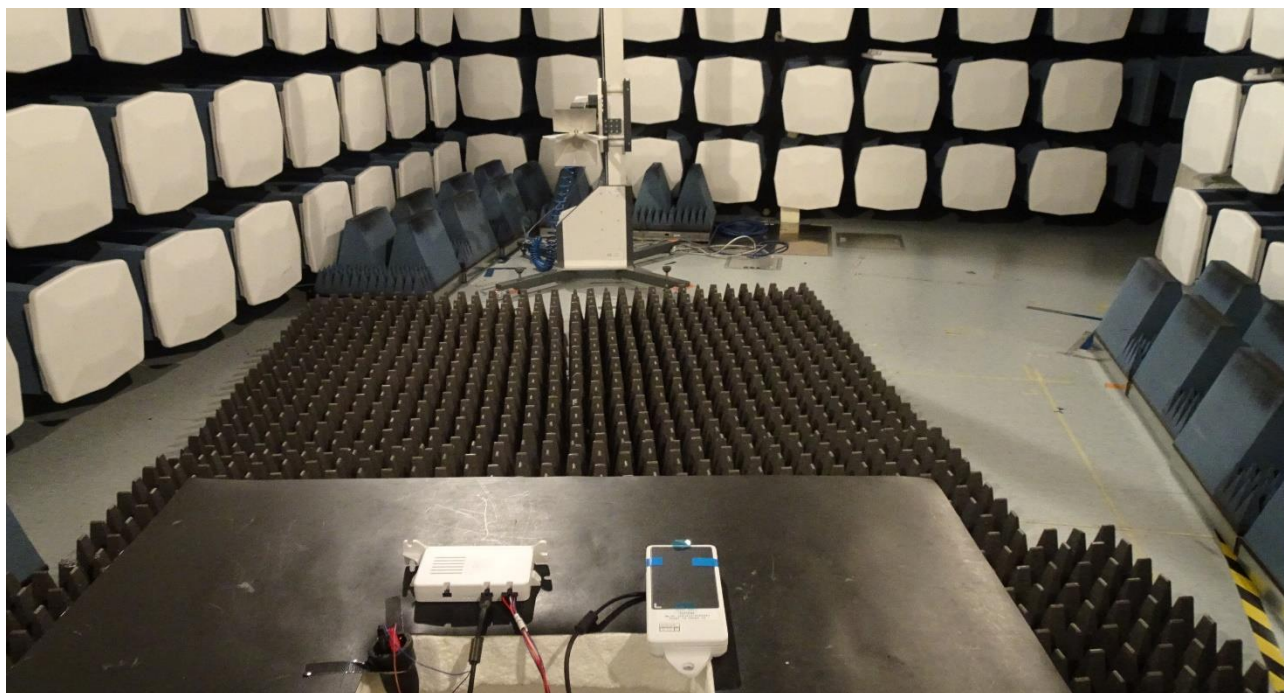
Picture 8 Radiated emissions, common setup 30 – 1000 MHz, front



Picture 9 Radiated emissions, common setup 30 – 1000 MHz, back



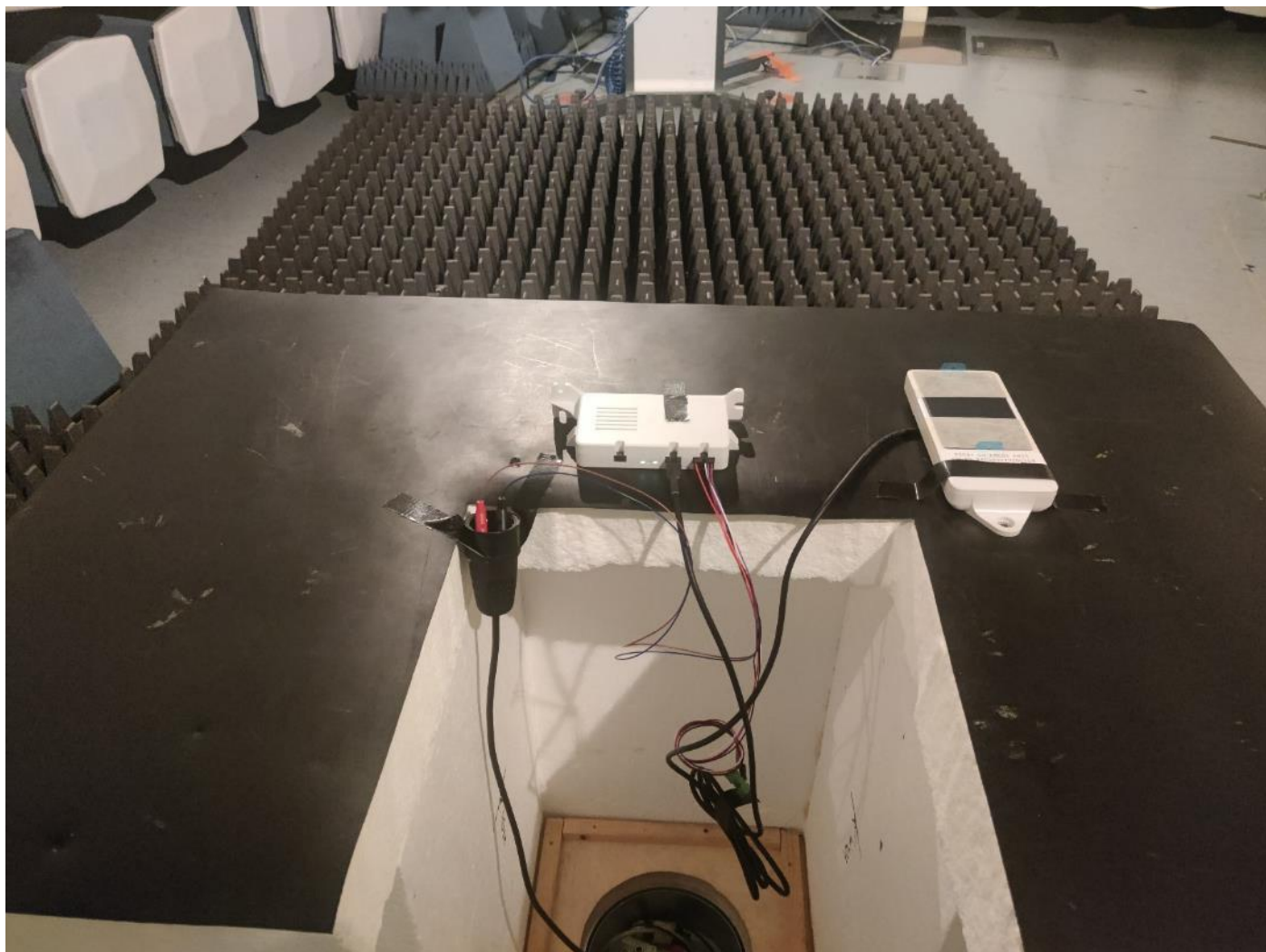
Picture 10 Radiated emissions, common setup 1 – 18 GHz, front



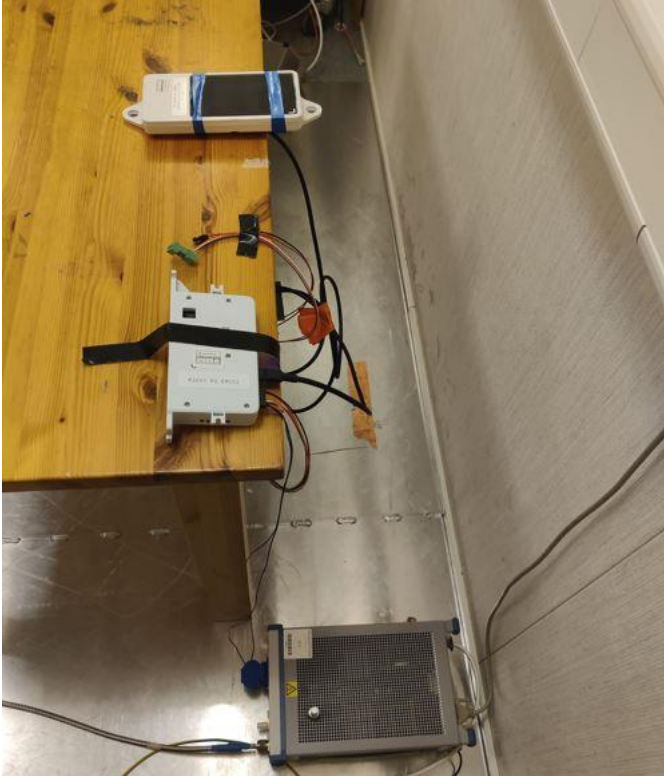
Picture 11 Radiated emissions, common setup 1 – 18 GHz, back



Picture 12 Radiated emissions, EUT setup 30 MHz – 18 GHz, front view



Picture 13 Radiated emissions, EUT setup 30 MHz – 18 GHz, back view



Picture 14 Conducted emissions, 150 kHz – 30 MHz