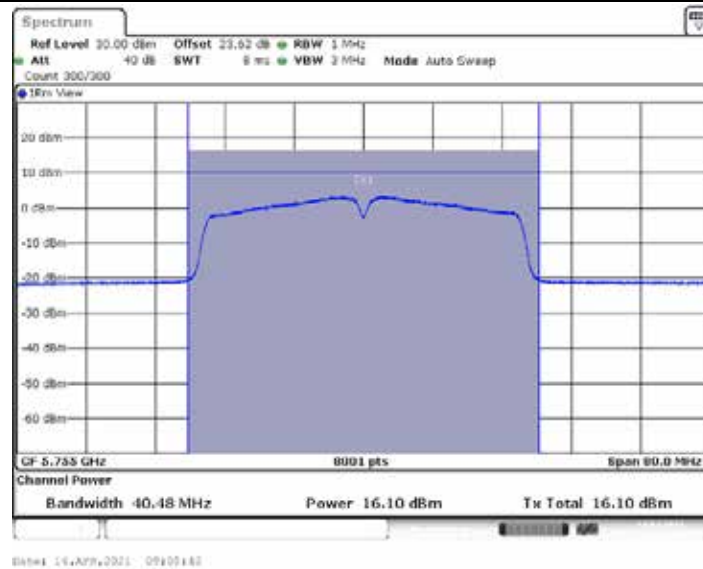


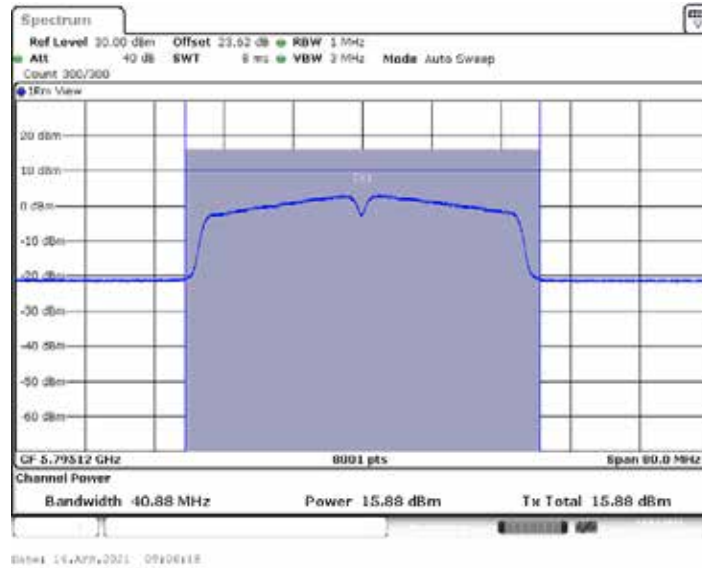
11AC40MIMO_Ant1_5755



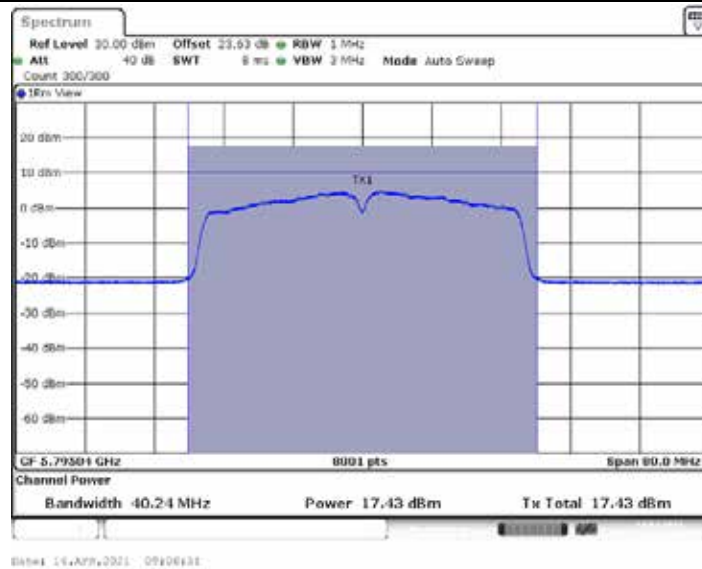
11AC40MIMO_Ant2_5755



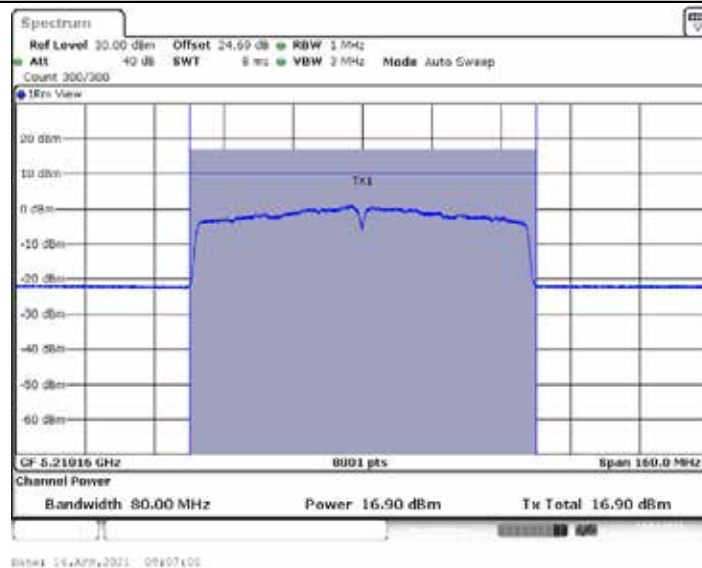
11AC40MIMO_Ant1_5795



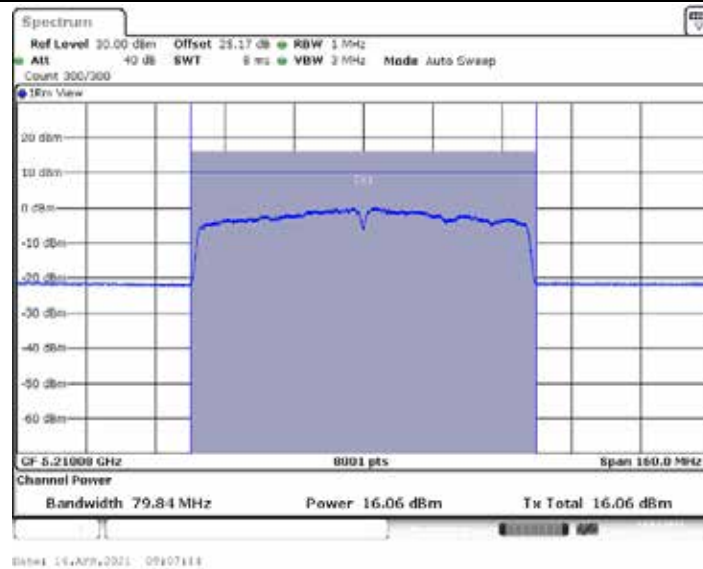
11AC40MIMO_Ant2_5795



11AC80MIMO_Ant1_5210



11AC80MIMO_Ant2_5210



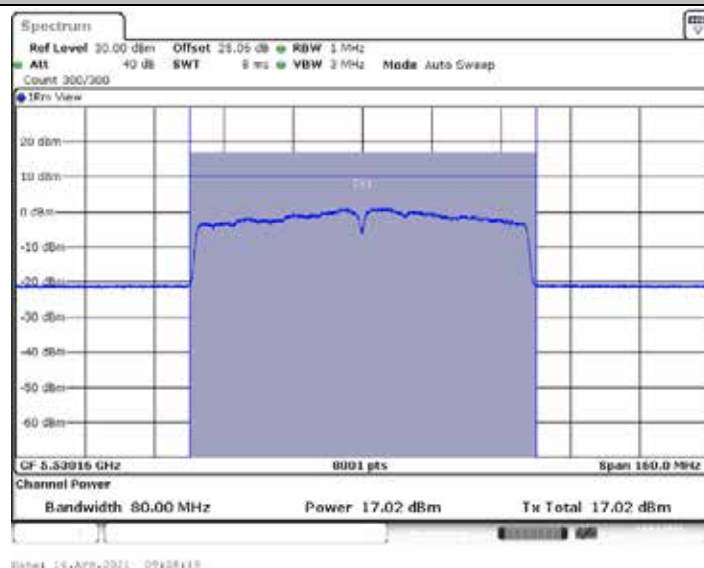
11AC80MIMO_Ant1_5290



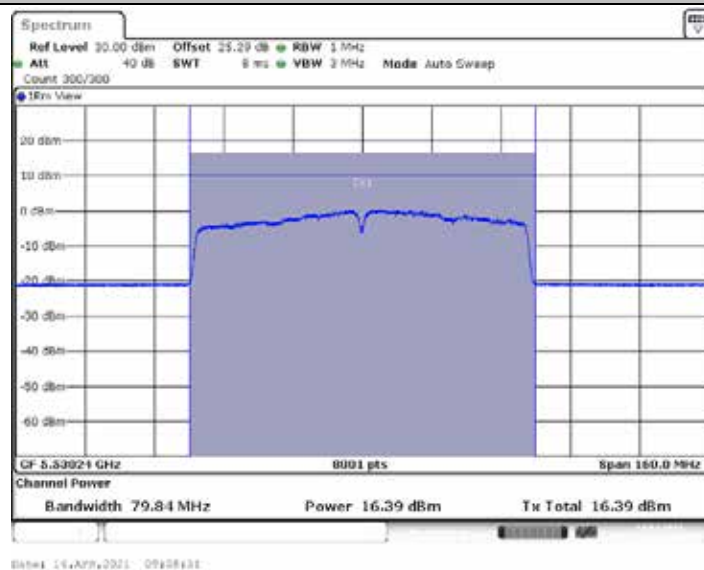
11AC80MIMO_Ant2_5290



11AC80MIMO_Ant1_5530



11AC80MIMO_Ant2_5530



11AC80MIMO_Ant1_5610



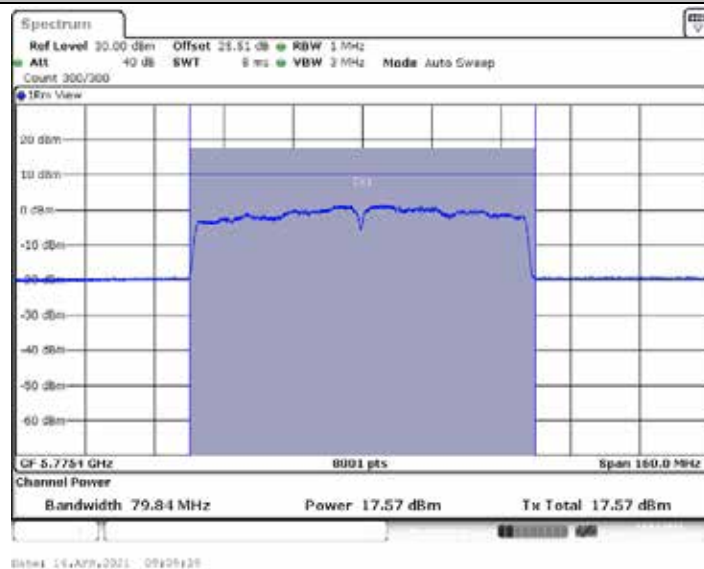
11AC80MIMO_Ant2_5610



11AC80MIMO_Ant1_5775



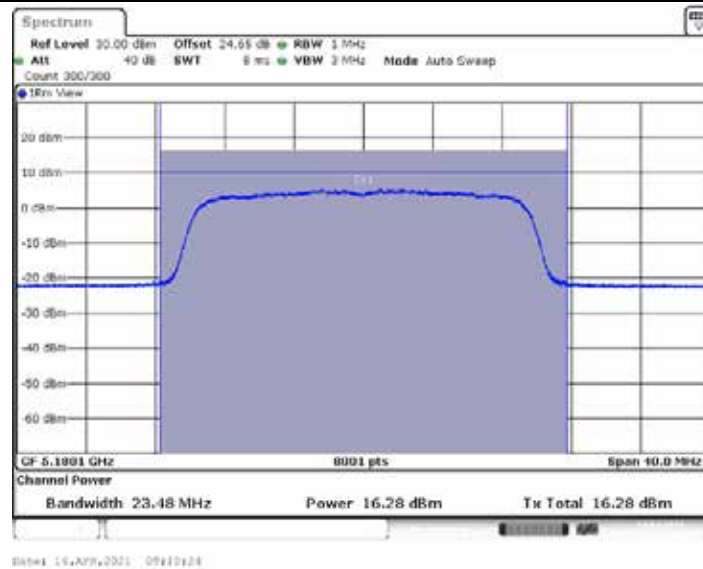
11AC80MIMO_Ant2_5775



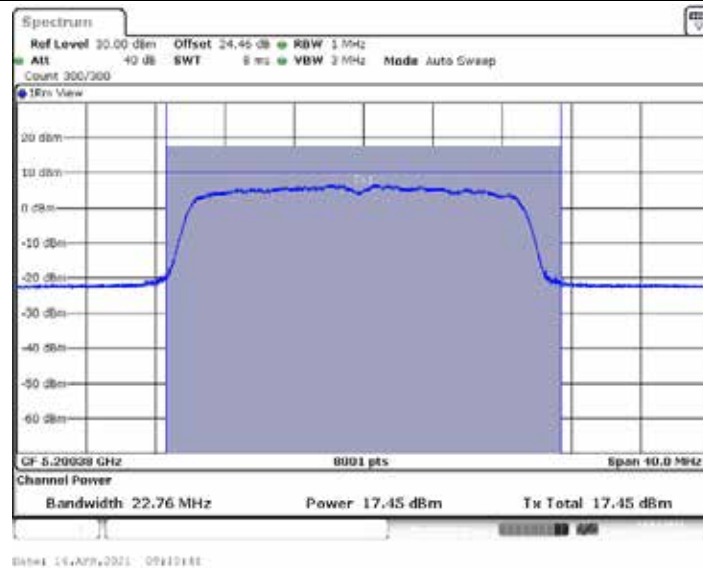
11AX20MIMO_Ant1_5180



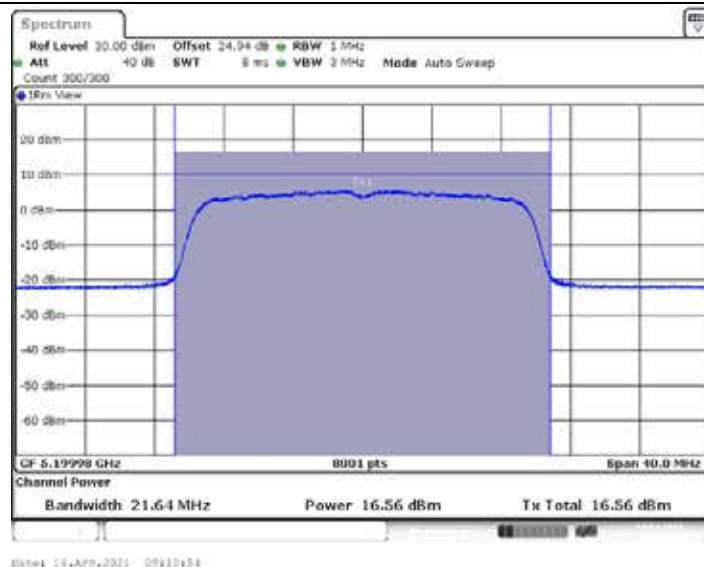
11AX20MIMO_Ant2_5180



11AX20MIMO_Ant1_5200



11AX20MIMO_Ant2_5200



11AX20MIMO_Ant1_5240



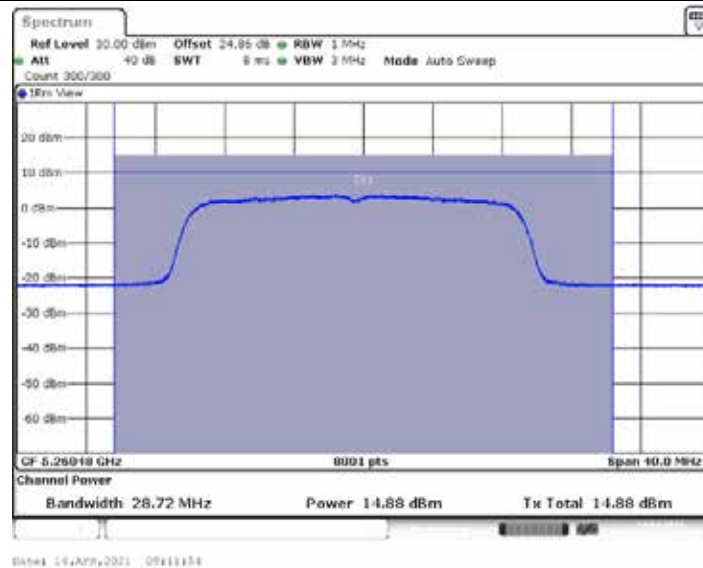
11AX20MIMO_Ant2_5240



11AX20MIMO_Ant1_5260



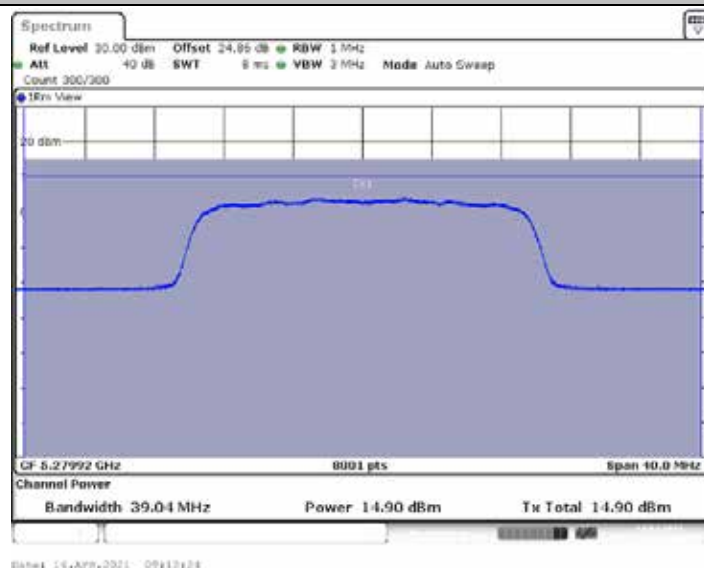
11AX20MIMO_Ant2_5260



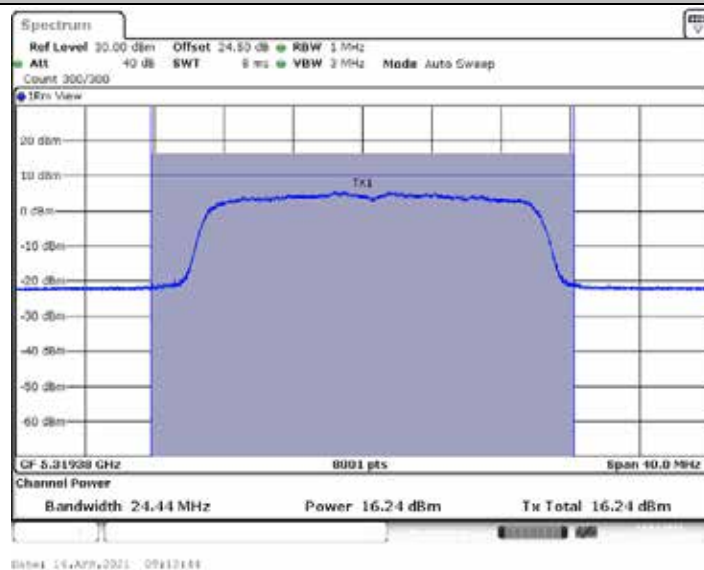
11AX20MIMO_Ant1_5280



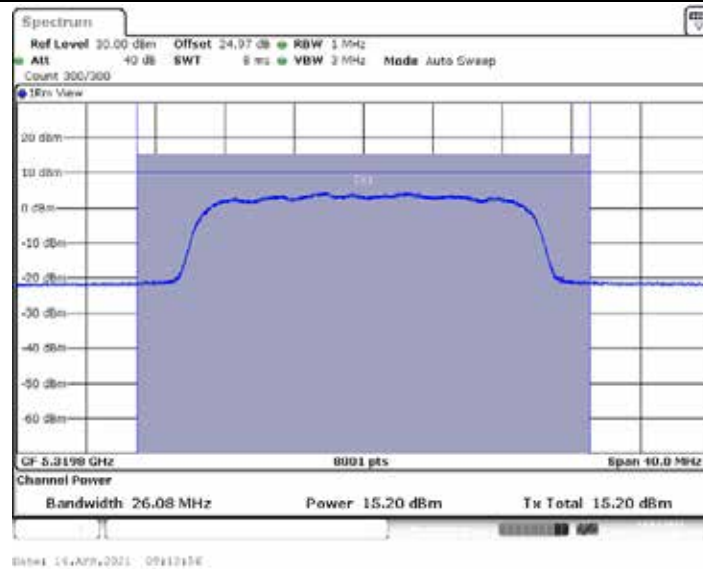
11AX20MIMO_Ant2_5280



11AX20MIMO_Ant1_5320



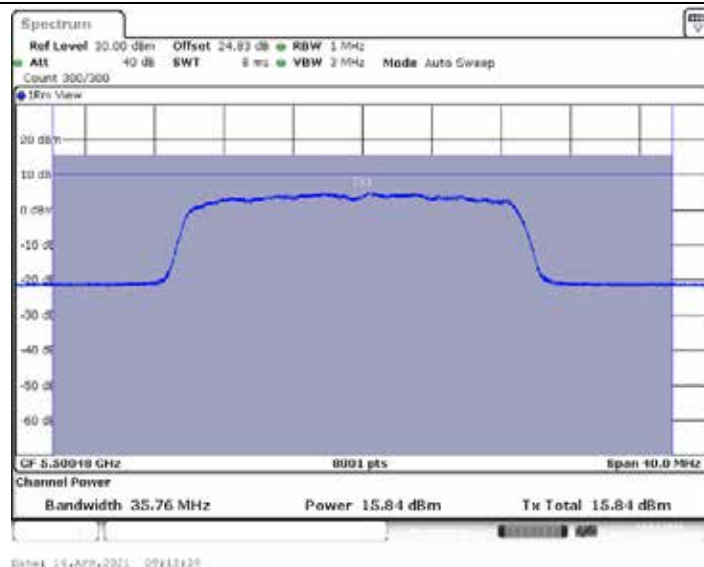
11AX20MIMO_Ant2_5320



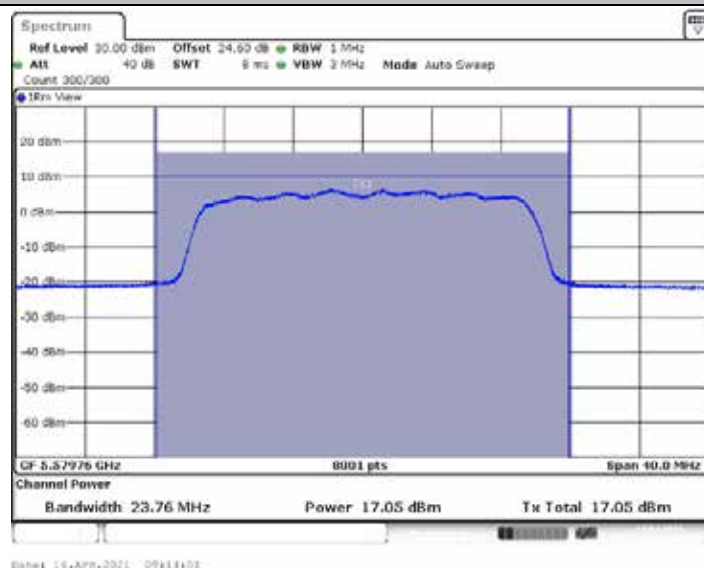
11AX20MIMO_Ant1_5500



11AX20MIMO_Ant2_5500



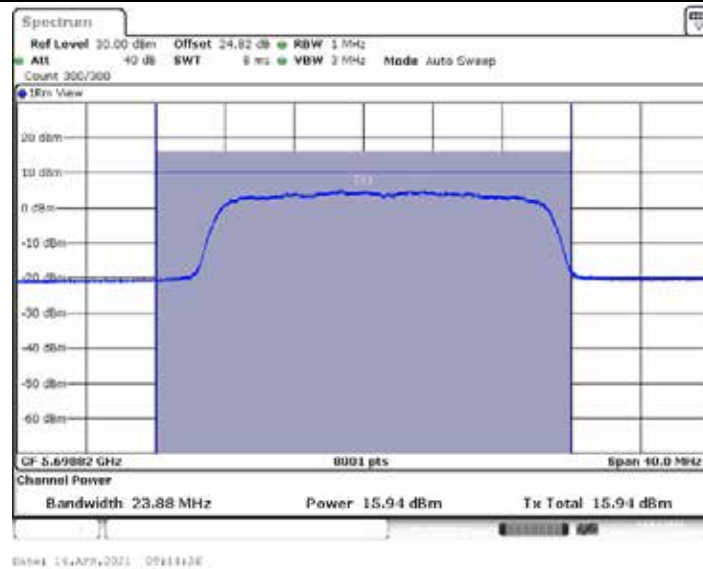
11AX20MIMO_Ant1_5580



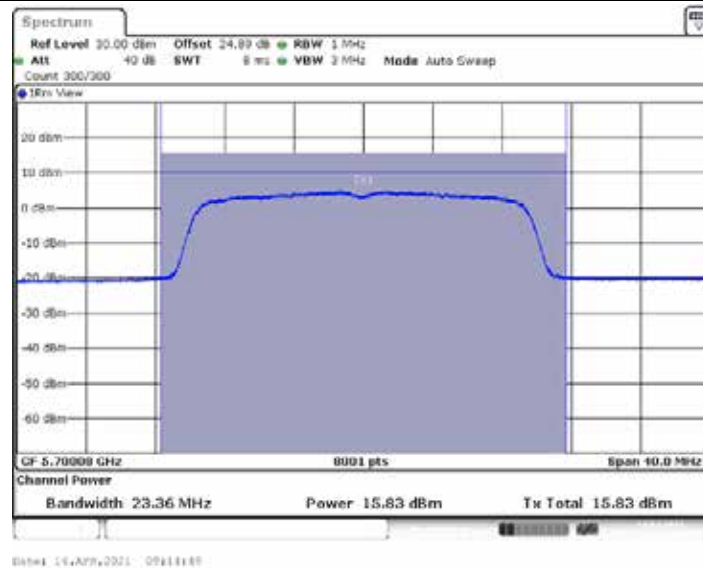
11AX20MIMO_Ant2_5580



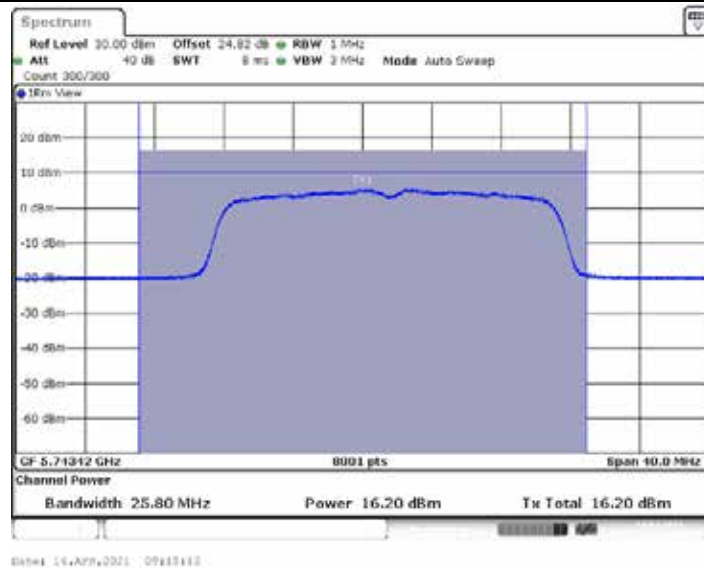
11AX20MIMO_Ant1_5700



11AX20MIMO_Ant2_5700



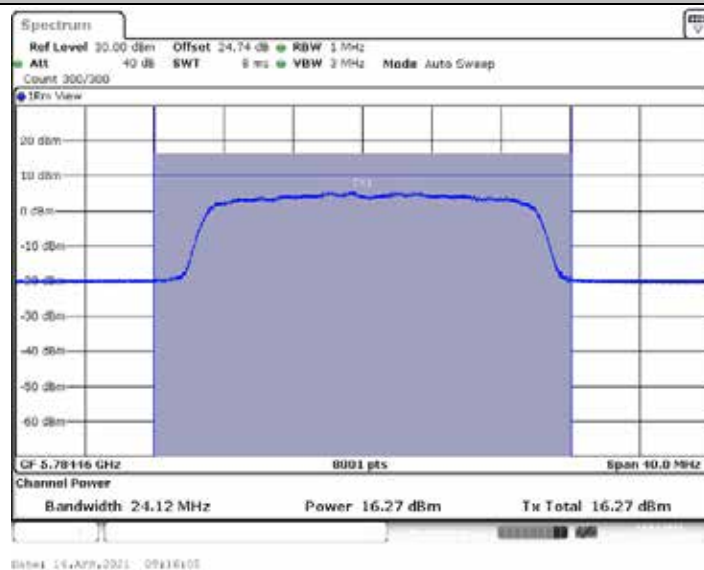
11AX20MIMO_Ant1_5745



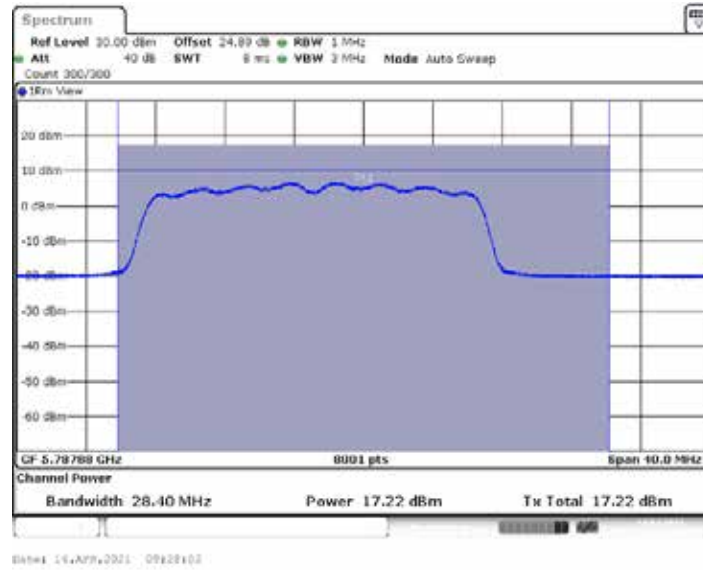
11AX20MIMO_Ant2_5745



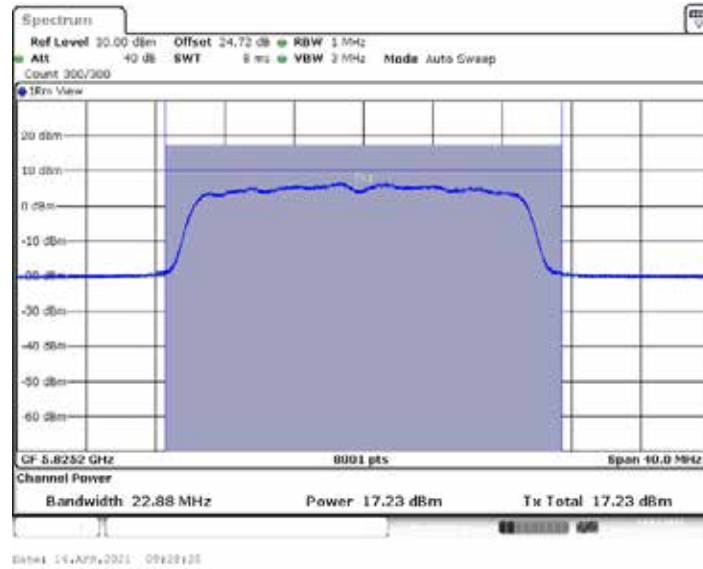
11AX20MIMO_Ant1_5785



11AX20MIMO_Ant2_5785



11AX20MIMO_Ant1_5825



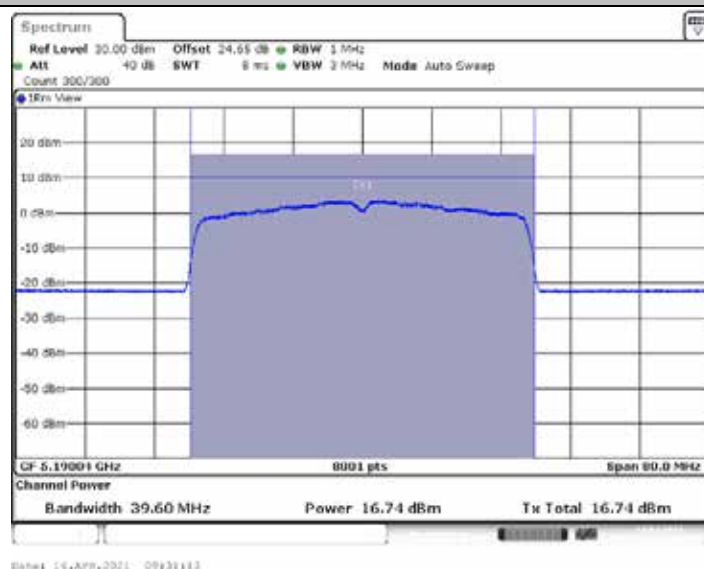
11AX20MIMO_Ant2_5825



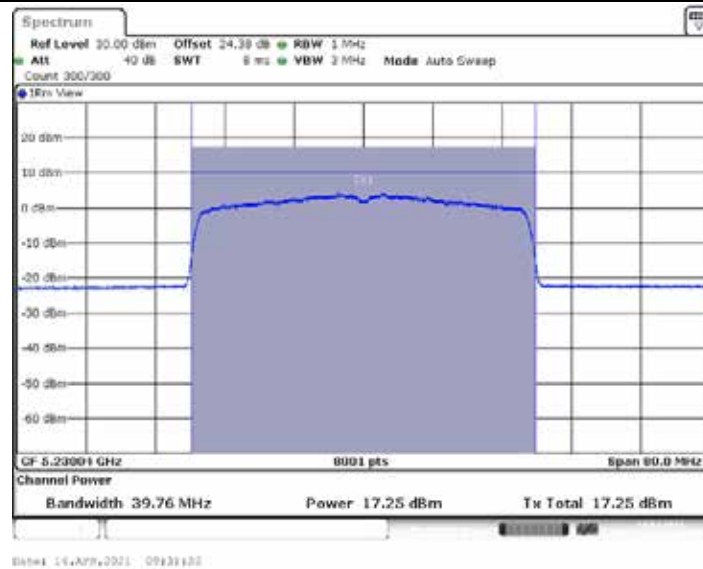
11AX40MIMO_Ant1_5190



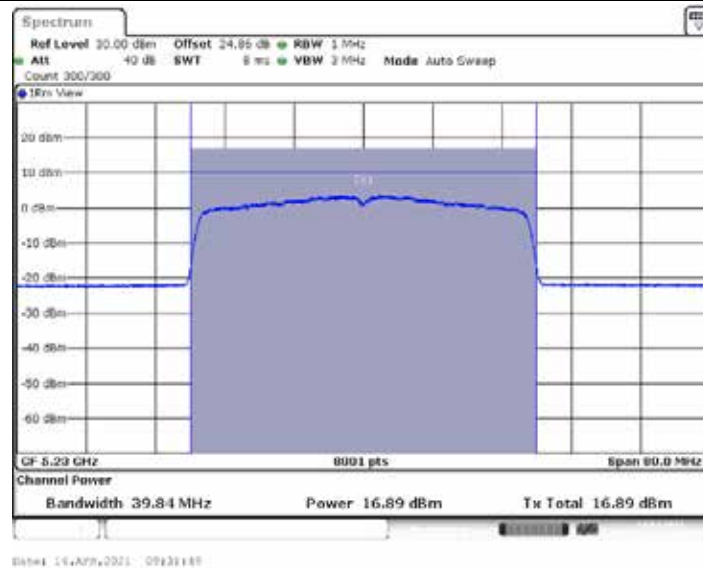
11AX40MIMO_Ant2_5190



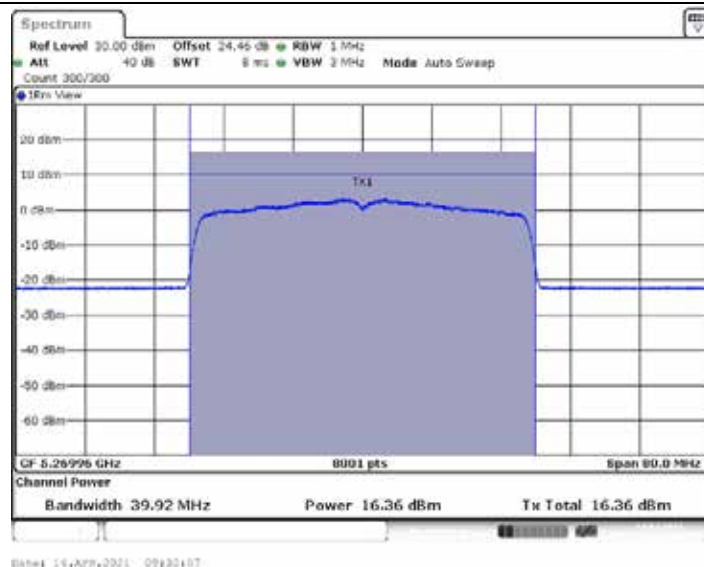
11AX40MIMO_Ant1_5230



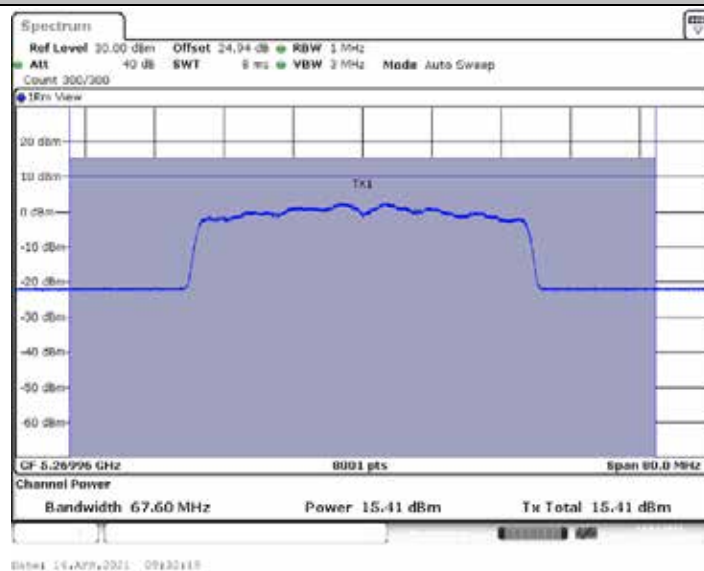
11AX40MIMO_Ant2_5230



11AX40MIMO_Ant1_5270



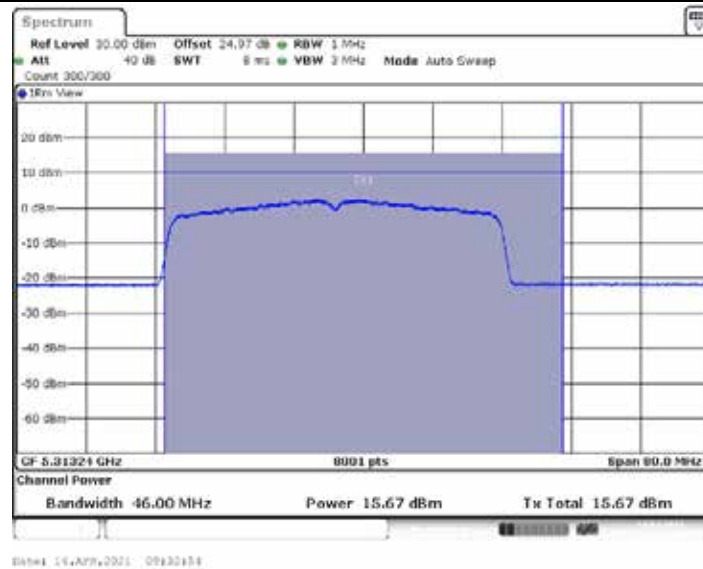
11AX40MIMO_Ant2_5270



11AX40MIMO_Ant1_5310



11AX40MIMO_Ant2_5310



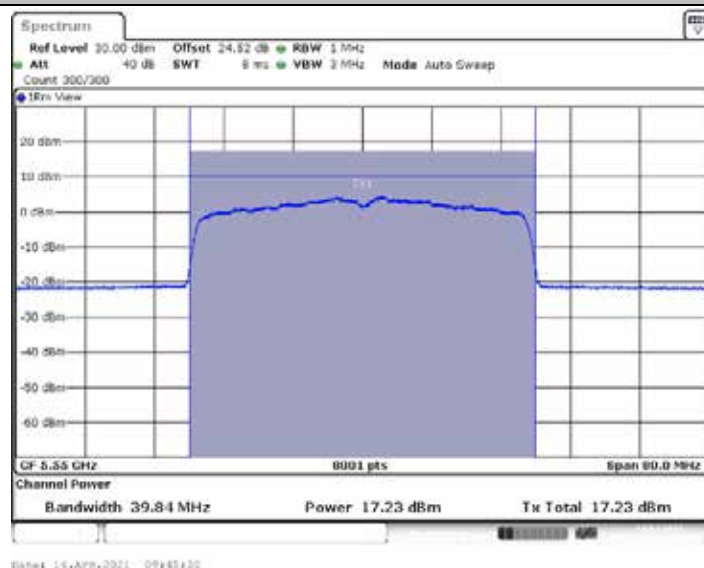
11AX40MIMO_Ant1_5510



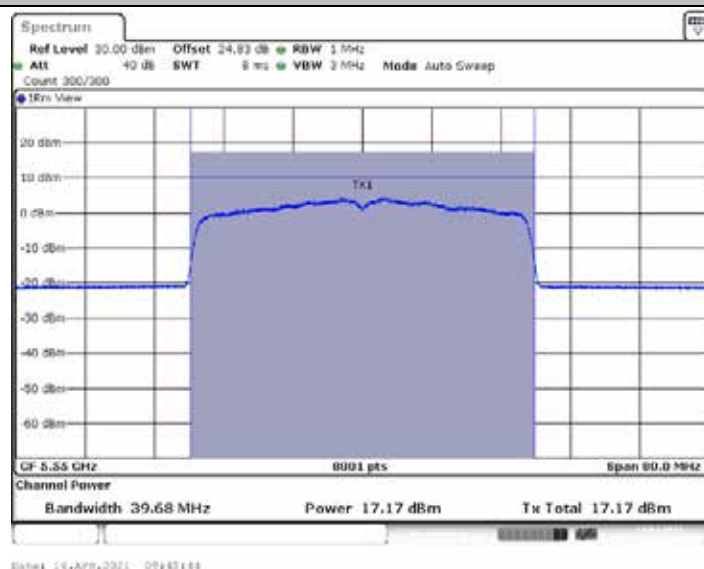
11AX40MIMO_Ant2_5510



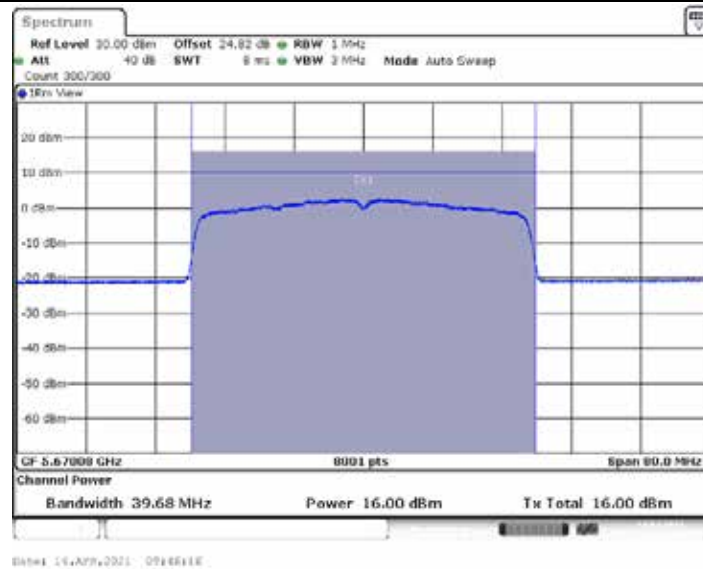
11AX40MIMO_Ant1_5550



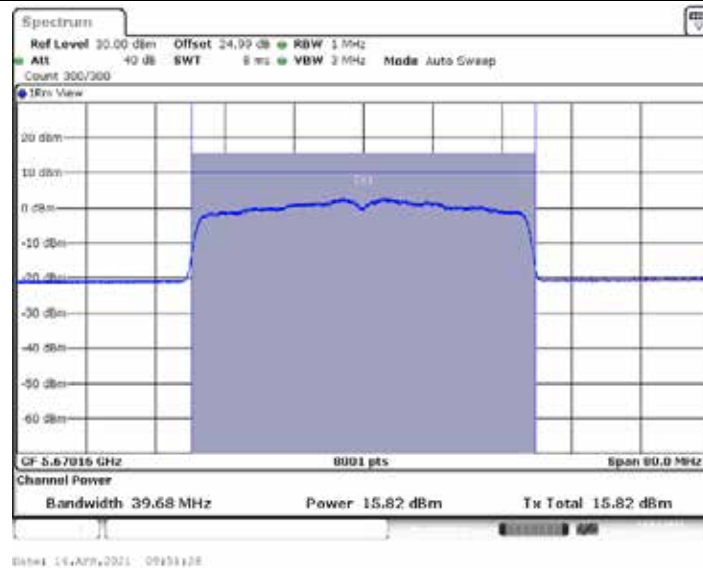
11AX40MIMO_Ant2_5550



11AX40MIMO_Ant1_5670



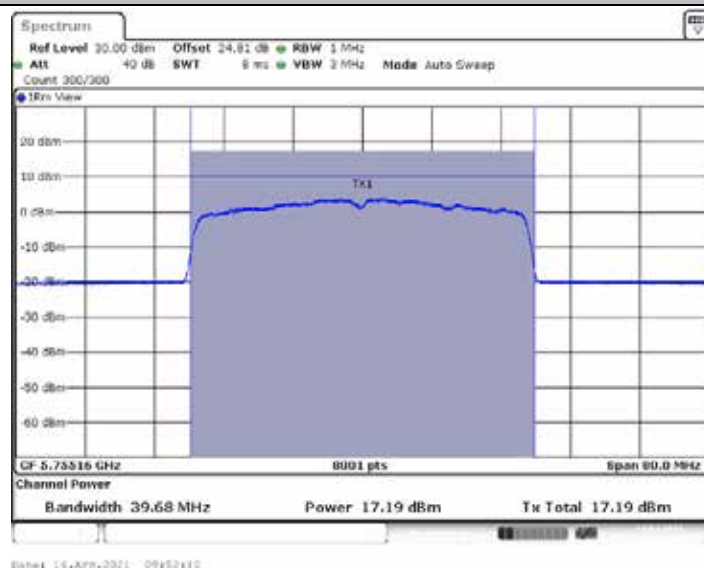
11AX40MIMO_Ant2_5670



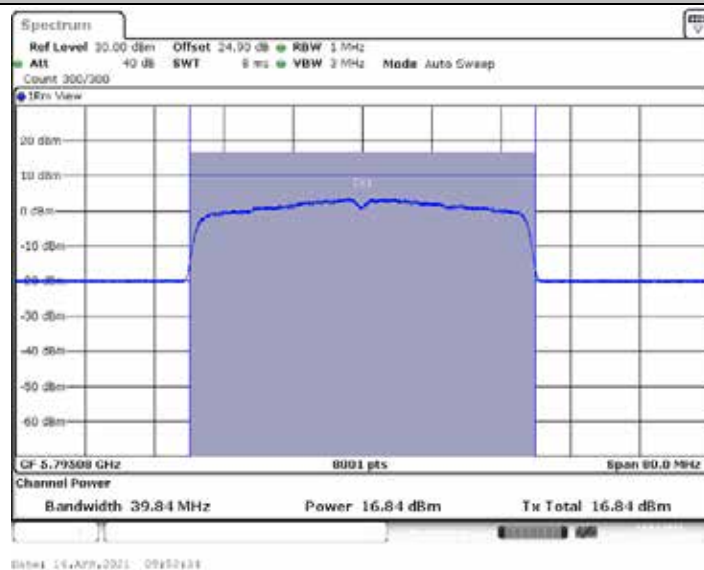
11AX40MIMO_Ant1_5755



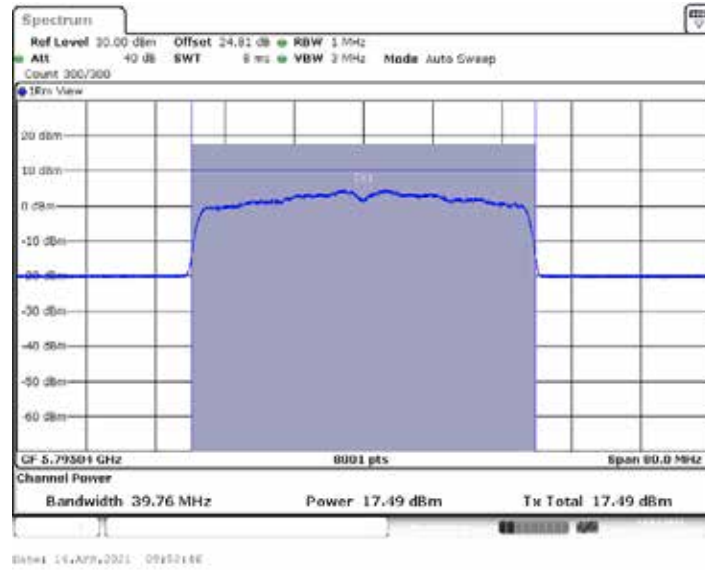
11AX40MIMO_Ant2_5755



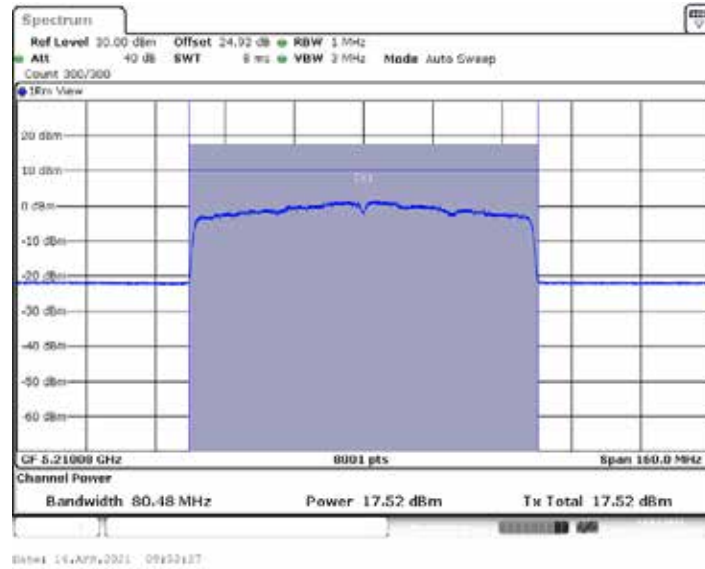
11AX40MIMO_Ant1_5795



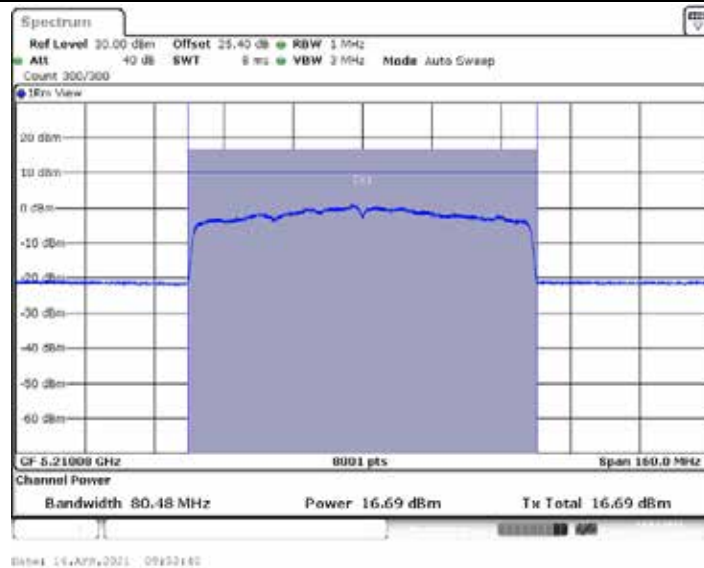
11AX40MIMO_Ant2_5795



11AX80MIMO_Ant1_5210



11AX80MIMO_Ant2_5210



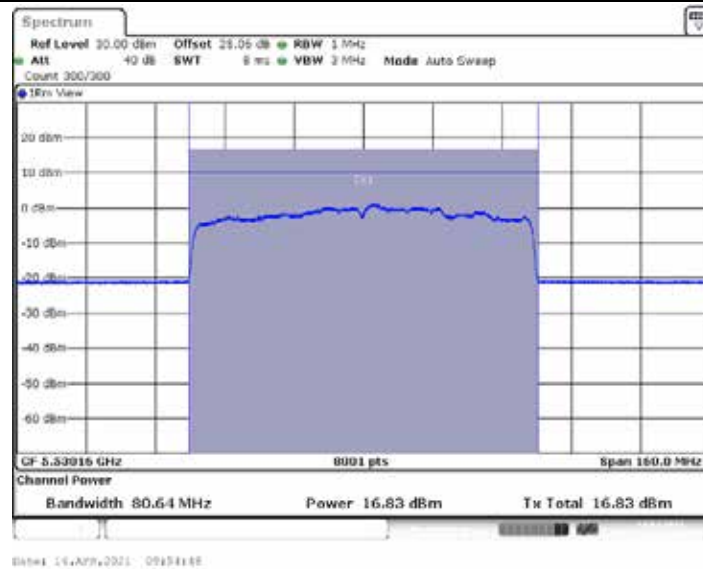
11AX80MIMO_Ant1_5290



11AX80MIMO_Ant2_5290



11AX80MIMO_Ant1_5530



11AX80MIMO_Ant2_5530



11AX80MIMO_Ant1_5610



11AX80MIMO_Ant2_5610



11AX80MIMO_Ant1_5775



11AX80MIMO_Ant2_5775



Date: 16 APR 2021 09:56:11

Appendix C: Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result [dBm/MHz]	Limit [dBm/MHz]	E.I.R.P [dBm/MHz]	E.I.R.P Limit [dBm/MHz]	Verdict
11A	Ant1	5180	6.3	<=11	6.30	<=10	PASS
	Ant2	5180	6.3	<=11	6.30	<=10	PASS
	Ant1	5200	6.46	<=11	6.46	<=10	PASS
	Ant2	5200	5.58	<=11	5.58	<=10	PASS
	Ant1	5240	5.87	<=11	5.87	<=10	PASS
	Ant2	5240	5.48	<=11	5.48	<=10	PASS
	Ant1	5260	5.07	<=11	---	---	PASS
	Ant2	5260	4.17	<=11	---	---	PASS
	Ant1	5280	4.89	<=11	---	---	PASS
	Ant2	5280	7.39	<=11	---	---	PASS
	Ant1	5320	5.56	<=11	---	---	PASS
	Ant2	5320	6.47	<=11	---	---	PASS
	Ant1	5500	5.64	<=11	---	---	PASS
	Ant2	5500	4.61	<=11	---	---	PASS
	Ant1	5580	5.8	<=11	---	---	PASS
	Ant2	5580	6.08	<=11	---	---	PASS
	Ant1	5700	5.16	<=11	---	---	PASS
	Ant2	5700	4.48	<=11	---	---	PASS
11N20SISO	Ant1	5180	6.28	<=11	6.28	<=10	PASS
	Ant2	5180	5.99	<=11	5.99	<=10	PASS
	Ant1	5200	6.46	<=11	6.46	<=10	PASS
	Ant2	5200	4.97	<=11	4.97	<=10	PASS
	Ant1	5240	6.07	<=11	6.07	<=10	PASS
	Ant2	5240	5.41	<=11	5.41	<=10	PASS
	Ant1	5260	4.83	<=11	---	---	PASS
	Ant2	5260	7.62	<=11	---	---	PASS
	Ant1	5280	4.94	<=11	---	---	PASS
	Ant2	5280	6.97	<=11	---	---	PASS
	Ant1	5320	5.34	<=11	---	---	PASS
	Ant2	5320	6.66	<=11	---	---	PASS
	Ant1	5500	4.75	<=11	---	---	PASS
	Ant2	5500	4.06	<=11	---	---	PASS
	Ant1	5580	5.67	<=11	---	---	PASS
	Ant2	5580	5.31	<=11	---	---	PASS
	Ant1	5700	4.24	<=11	---	---	PASS
	Ant2	5700	3.88	<=11	---	---	PASS

11N40SISO	Ant1	5190	4.36	<=11	4.36	<=10	PASS
	Ant2	5190	3.92	<=11	3.92	<=10	PASS
	Ant1	5230	3.81	<=11	3.81	<=10	PASS
	Ant2	5230	3.1	<=11	3.10	<=10	PASS
	Ant1	5270	2.17	<=11	---	---	PASS
	Ant2	5270	5.9	<=11	---	---	PASS
	Ant1	5310	2.75	<=11	---	---	PASS
	Ant2	5310	4.34	<=11	---	---	PASS
	Ant1	5510	2.66	<=11	---	---	PASS
	Ant2	5510	2.88	<=11	---	---	PASS
	Ant1	5550	3.24	<=11	---	---	PASS
	Ant2	5550	2.78	<=11	---	---	PASS
	Ant1	5670	2.25	<=11	---	---	PASS
	Ant2	5670	1.08	<=11	---	---	PASS
11AC20SISO	Ant1	5180	6.15	<=11	6.15	<=10	PASS
	Ant2	5180	5.74	<=11	5.74	<=10	PASS
	Ant1	5200	6.09	<=11	6.09	<=10	PASS
	Ant2	5200	5.65	<=11	5.65	<=10	PASS
	Ant1	5240	5.97	<=11	5.97	<=10	PASS
	Ant2	5240	5.38	<=11	5.38	<=10	PASS
	Ant1	5260	4.75	<=11	---	---	PASS
	Ant2	5260	7.08	<=11	---	---	PASS
	Ant1	5280	5.23	<=11	---	---	PASS
	Ant2	5280	7.37	<=11	---	---	PASS
	Ant1	5320	4.97	<=11	---	---	PASS
	Ant2	5320	6.23	<=11	---	---	PASS
	Ant1	5500	4.81	<=11	---	---	PASS
	Ant2	5500	3.96	<=11	---	---	PASS
	Ant1	5580	5.62	<=11	---	---	PASS
	Ant2	5580	6.12	<=11	---	---	PASS
	Ant1	5700	4.25	<=11	---	---	PASS
	Ant2	5700	2.91	<=11	---	---	PASS
11AC40SISO	Ant1	5190	4.54	<=11	4.54	<=10	PASS
	Ant2	5190	4.54	<=11	4.54	<=10	PASS
	Ant1	5230	3.58	<=11	3.58	<=10	PASS
	Ant2	5230	3.62	<=11	3.62	<=10	PASS
	Ant1	5270	3.11	<=11	---	---	PASS
	Ant2	5270	2.82	<=11	---	---	PASS
	Ant1	5310	3.75	<=11	---	---	PASS
	Ant2	5310	3.25	<=11	---	---	PASS
	Ant1	5510	2.45	<=11	---	---	PASS
	Ant2	5510	1.97	<=11	---	---	PASS
	Ant1	5550	2.98	<=11	---	---	PASS

	Ant2	5550	3.83	<=11	---	---	PASS
	Ant1	5670	1.86	<=11	---	---	PASS
	Ant2	5670	2.31	<=11	---	---	PASS
11AC80SISO	Ant1	5210	0.27	<=11	0.27	<=10	PASS
	Ant2	5210	-0.63	<=11	-0.63	<=10	PASS
	Ant1	5290	-1.02	<=11	---	---	PASS
	Ant2	5290	-1.81	<=11	---	---	PASS
	Ant1	5530	0.18	<=11	---	---	PASS
	Ant2	5530	-0.11	<=11	---	---	PASS
	Ant1	5610	0.16	<=11	---	---	PASS
	Ant2	5610	-0.21	<=11	---	---	PASS
11AX20SISO	Ant1	5180	6.64	<=11	6.64	<=10	PASS
	Ant2	5180	4.97	<=11	4.97	<=10	PASS
	Ant1	5200	6.5	<=11	6.50	<=10	PASS
	Ant2	5200	6.01	<=11	6.01	<=10	PASS
	Ant1	5240	6.5	<=11	6.50	<=10	PASS
	Ant2	5240	4.74	<=11	4.74	<=10	PASS
	Ant1	5260	4.93	<=11	---	---	PASS
	Ant2	5260	3.42	<=11	---	---	PASS
	Ant1	5280	5.27	<=11	---	---	PASS
	Ant2	5280	3.73	<=11	---	---	PASS
	Ant1	5320	5.07	<=11	---	---	PASS
	Ant2	5320	2.81	<=11	---	---	PASS
	Ant1	5500	4.75	<=11	---	---	PASS
	Ant2	5500	4.13	<=11	---	---	PASS
	Ant1	5580	6	<=11	---	---	PASS
	Ant2	5580	5.11	<=11	---	---	PASS
	Ant1	5700	4.54	<=11	---	---	PASS
	Ant2	5700	3.99	<=11	---	---	PASS
11AX40SISO	Ant1	5190	4.29	<=11	4.29	<=10	PASS
	Ant2	5190	2.72	<=11	2.72	<=10	PASS
	Ant1	5230	3.74	<=11	3.74	<=10	PASS
	Ant2	5230	2.53	<=11	2.53	<=10	PASS
	Ant1	5270	3.26	<=11	---	---	PASS
	Ant2	5270	1.88	<=11	---	---	PASS
	Ant1	5310	2.65	<=11	---	---	PASS
	Ant2	5310	1.53	<=11	---	---	PASS
	Ant1	5510	2.9	<=11	---	---	PASS
	Ant2	5510	2.85	<=11	---	---	PASS
	Ant1	5550	3.29	<=11	---	---	PASS
	Ant2	5550	3.34	<=11	---	---	PASS
	Ant1	5670	1.75	<=11	---	---	PASS
	Ant2	5670	2.37	<=11	---	---	PASS

11AX80SISO	Ant1	5210	1.01	<=11	1.01	<=10	PASS
	Ant2	5210	2.73	<=11	2.73	<=10	PASS
	Ant1	5290	0.2	<=11	---	---	PASS
	Ant2	5290	-1.36	<=11	---	---	PASS
	Ant1	5530	0.54	<=11	---	---	PASS
	Ant2	5530	0.34	<=11	---	---	PASS
	Ant1	5610	-0.93	<=11	---	---	PASS
	Ant2	5610	0.61	<=11	---	---	PASS
11N20MIMO	Ant1	5180	6.95	<=11	6.95	<=10	PASS
	Ant2	5180	5.81	<=11	5.81	<=10	PASS
	total	5180	9.43	<=11	9.43	<=10	PASS
	Ant1	5200	6.9	<=11	6.90	<=10	PASS
	Ant2	5200	5.17	<=11	5.17	<=10	PASS
	total	5200	9.13	<=11	9.13	<=10	PASS
	Ant1	5240	6.3	<=11	6.30	<=10	PASS
	Ant2	5240	5.48	<=11	5.48	<=10	PASS
	total	5240	8.92	<=11	8.92	<=10	PASS
	Ant1	5260	4.88	<=11	---	---	PASS
	Ant2	5260	4.14	<=11	---	---	PASS
	total	5260	7.54	<=11	---	---	PASS
	Ant1	5280	4.75	<=11	---	---	PASS
	Ant2	5280	4.44	<=11	---	---	PASS
	total	5280	7.61	<=11	---	---	PASS
	Ant1	5320	5.23	<=11	---	---	PASS
	Ant2	5320	3.81	<=11	---	---	PASS
	total	5320	7.59	<=11	---	---	PASS
	Ant1	5500	5.49	<=11	---	---	PASS
	Ant2	5500	5.29	<=11	---	---	PASS
	total	5500	8.40	<=11	---	---	PASS
	Ant1	5580	5.41	<=11	---	---	PASS
	Ant2	5580	6.08	<=11	---	---	PASS
	total	5580	8.77	<=11	---	---	PASS
	Ant1	5700	4.34	<=11	---	---	PASS
	Ant2	5700	4	<=11	---	---	PASS
	total	5700	7.18	<=11	---	---	PASS
11N40MIMO	Ant1	5190	5.01	<=11	5.01	<=10	PASS
	Ant2	5190	4.4	<=11	4.40	<=10	PASS
	total	5190	7.73	<=11	7.73	<=10	PASS
	Ant1	5230	3.88	<=11	3.88	<=10	PASS
	Ant2	5230	3.53	<=11	3.53	<=10	PASS
	total	5230	6.72	<=11	6.72	<=10	PASS
	Ant1	5270	2.17	<=11	---	---	PASS
	Ant2	5270	2.54	<=11	---	---	PASS

	total	5270	5.37	<=11	---	---	PASS
	Ant1	5310	3.03	<=11	---	---	PASS
	Ant2	5310	2.17	<=11	---	---	PASS
	total	5310	5.63	<=11	---	---	PASS
	Ant1	5510	3.4	<=11	---	---	PASS
	Ant2	5510	3.36	<=11	---	---	PASS
	total	5510	6.39	<=11	---	---	PASS
	Ant1	5550	2.97	<=11	---	---	PASS
	Ant2	5550	4.63	<=11	---	---	PASS
	total	5550	6.89	<=11	---	---	PASS
	Ant1	5670	1.72	<=11	---	---	PASS
	Ant2	5670	1.24	<=11	---	---	PASS
	total	5670	4.50	<=11	---	---	PASS
11AC20MIMO	Ant1	5180	7.09	<=11	7.09	<=10	PASS
	Ant2	5180	6.56	<=11	6.56	<=10	PASS
	total	5180	9.84	<=11	9.84	<=10	PASS
	Ant1	5200	6.83	<=11	6.83	<=10	PASS
	Ant2	5200	6.24	<=11	6.24	<=10	PASS
	total	5200	9.56	<=11	9.56	<=10	PASS
	Ant1	5240	6.32	<=11	6.32	<=10	PASS
	Ant2	5240	5.81	<=11	5.81	<=10	PASS
	total	5240	9.08	<=11	9.08	<=10	PASS
	Ant1	5260	5.13	<=11	---	---	PASS
	Ant2	5260	4.58	<=11	---	---	PASS
	total	5260	7.87	<=11	---	---	PASS
	Ant1	5280	5.05	<=11	---	---	PASS
	Ant2	5280	4.45	<=11	---	---	PASS
	total	5280	7.77	<=11	---	---	PASS
	Ant1	5320	4.81	<=11	---	---	PASS
	Ant2	5320	3.57	<=11	---	---	PASS
	total	5320	7.24	<=11	---	---	PASS
	Ant1	5500	5.46	<=11	---	---	PASS
	Ant2	5500	5.12	<=11	---	---	PASS
	total	5500	8.30	<=11	---	---	PASS
	Ant1	5580	5.67	<=11	---	---	PASS
	Ant2	5580	5.96	<=11	---	---	PASS
	total	5580	8.83	<=11	---	---	PASS
	Ant1	5700	4.95	<=11	---	---	PASS
	Ant2	5700	4.98	<=11	---	---	PASS
	total	5700	7.98	<=11	---	---	PASS
11AC40MIMO	Ant1	5190	4.85	<=11	4.85	<=10	PASS
	Ant2	5190	3.81	<=11	3.81	<=10	PASS
	total	5190	7.37	<=11	7.37	<=10	PASS

	Ant1	5230	3.62	<=11	3.62	<=10	PASS
	Ant2	5230	3.24	<=11	3.24	<=10	PASS
	total	5230	6.44	<=11	6.44	<=10	PASS
	Ant1	5270	2.57	<=11	---	---	PASS
	Ant2	5270	1.83	<=11	---	---	PASS
	total	5270	5.23	<=11	---	---	PASS
	Ant1	5310	2.87	<=11	---	---	PASS
	Ant2	5310	2.06	<=11	---	---	PASS
	total	5310	5.49	<=11	---	---	PASS
	Ant1	5510	3.48	<=11	---	---	PASS
	Ant2	5510	3.78	<=11	---	---	PASS
	total	5510	6.64	<=11	---	---	PASS
	Ant1	5550	3.79	<=11	---	---	PASS
	Ant2	5550	3.36	<=11	---	---	PASS
	total	5550	6.59	<=11	---	---	PASS
	Ant1	5670	2.24	<=11	---	---	PASS
	Ant2	5670	2.81	<=11	---	---	PASS
	total	5670	5.54	<=11	---	---	PASS
11AC80MIMO	Ant1	5210	0.23	<=11	0.23	<=10	PASS
	Ant2	5210	0.54	<=11	0.54	<=10	PASS
	total	5210	3.40	<=11	3.40	<=10	PASS
	Ant1	5290	-0.52	<=11	---	---	PASS
	Ant2	5290	-1.96	<=11	---	---	PASS
	total	5290	1.83	<=11	---	---	PASS
	Ant1	5530	0	<=11	---	---	PASS
	Ant2	5530	1.04	<=11	---	---	PASS
	total	5530	3.56	<=11	---	---	PASS
	Ant1	5610	0.64	<=11	---	---	PASS
	Ant2	5610	1.3	<=11	---	---	PASS
	total	5610	3.99	<=11	---	---	PASS
11AX20MIMO	Ant1	5180	6.37	<=11	6.37	<=10	PASS
	Ant2	5180	5.87	<=11	5.87	<=10	PASS
	total	5180	9.14	<=11	9.14	<=10	PASS
	Ant1	5200	6.89	<=11	6.89	<=10	PASS
	Ant2	5200	5.83	<=11	5.83	<=10	PASS
	total	5200	9.40	<=11	9.40	<=10	PASS
	Ant1	5240	5.58	<=11	5.58	<=10	PASS
	Ant2	5240	5.06	<=11	5.06	<=10	PASS
	total	5240	8.34	<=11	8.34	<=10	PASS
	Ant1	5260	5.79	<=11	---	---	PASS
	Ant2	5260	3.98	<=11	---	---	PASS
	total	5260	7.99	<=11	---	---	PASS
	Ant1	5280	4.73	<=11	---	---	PASS

	Ant2	5280	4.83	<=11	---	---	PASS
	total	5280	7.79	<=11	---	---	PASS
	Ant1	5320	5.79	<=11	---	---	PASS
	Ant2	5320	4.05	<=11	---	---	PASS
	total	5320	8.02	<=11	---	---	PASS
	Ant1	5500	5.9	<=11	---	---	PASS
	Ant2	5500	5.12	<=11	---	---	PASS
	total	5500	8.54	<=11	---	---	PASS
	Ant1	5580	6.07	<=11	---	---	PASS
	Ant2	5580	6.02	<=11	---	---	PASS
	total	5580	9.06	<=11	---	---	PASS
	Ant1	5700	5.28	<=11	---	---	PASS
	Ant2	5700	4.14	<=11	---	---	PASS
	total	5700	7.76	<=11	---	---	PASS
	Ant1	5745	2.31	<=30	---	---	PASS
	Ant2	5745	3.68	<=30	---	---	PASS
	total	5745	6.06	<=30	---	---	PASS
	Ant1	5785	2.8	<=30	---	---	PASS
	Ant2	5785	3.28	<=30	---	---	PASS
	total	5785	6.06	<=30	---	---	PASS
11AX40MIMO	Ant1	5190	4.6	<=11	4.60	<=10	PASS
	Ant2	5190	3.69	<=11	3.69	<=10	PASS
	total	5190	7.18	<=11	7.18	<=10	PASS
	Ant1	5230	3.12	<=11	3.12	<=10	PASS
	Ant2	5230	3.1	<=11	3.10	<=10	PASS
	total	5230	6.12	<=11	6.12	<=10	PASS
	Ant1	5270	2.65	<=11	---	---	PASS
	Ant2	5270	2.07	<=11	---	---	PASS
	total	5270	5.38	<=11	---	---	PASS
	Ant1	5310	2.8	<=11	---	---	PASS
	Ant2	5310	2.6	<=11	---	---	PASS
	total	5310	5.71	<=11	---	---	PASS
	Ant1	5510	3.46	<=11	---	---	PASS
	Ant2	5510	3.12	<=11	---	---	PASS
	total	5510	6.30	<=11	---	---	PASS
	Ant1	5550	3.58	<=11	---	---	PASS
	Ant2	5550	3.54	<=11	---	---	PASS
	total	5550	6.57	<=11	---	---	PASS
	Ant1	5670	2.85	<=11	---	---	PASS
	Ant2	5670	1.19	<=11	---	---	PASS
	total	5670	5.11	<=11	---	---	PASS
11AX80MIMO	Ant1	5210	1.02	<=11	1.02	<=10	PASS
	Ant2	5210	0.05	<=11	0.05	<=10	PASS

	total	5210	3.57	<=11	3.57	<=10	PASS
	Ant1	5290	-0.47	<=11	---	---	PASS
	Ant2	5290	-1.05	<=11	---	---	PASS
	total	5290	2.26	<=11	---	---	PASS
	Ant1	5530	0.39	<=11	---	---	PASS
	Ant2	5530	1.24	<=11	---	---	PASS
	total	5530	3.85	<=11	---	---	PASS
	Ant1	5610	1.47	<=11	---	---	PASS
	Ant2	5610	1.59	<=11	---	---	PASS
	total	5610	4.54	<=11	---	---	PASS

TestMode	Antenna	Channel	Result [dBm/500KHz]	Limit [dBm/500KHz]	E.I.R.P [dBm/500KHz]	E.I.R.P Limit [dBm/500KHz]	Verdict
11A	Ant1	5745	2.05	<=30	---	---	PASS
	Ant2	5745	2.42	<=30	---	---	PASS
	Ant1	5785	2.31	<=30	---	---	PASS
	Ant2	5785	1.64	<=30	---	---	PASS
	Ant1	5825	2.64	<=30	---	---	PASS
	Ant2	5825	4.18	<=30	---	---	PASS
11N20SISO	Ant1	5745	1.03	<=30	---	---	PASS
	Ant2	5745	1.3	<=30	---	---	PASS
	Ant1	5785	1.79	<=30	---	---	PASS
	Ant2	5785	1.97	<=30	---	---	PASS
	Ant1	5825	2.58	<=30	---	---	PASS
	Ant2	5825	2.06	<=30	---	---	PASS
11N40SISO	Ant1	5755	-0.76	<=30	---	---	PASS
	Ant2	5755	1.11	<=30	---	---	PASS
	Ant1	5795	0.05	<=30	---	---	PASS
	Ant2	5795	0.97	<=30	---	---	PASS
11AC20SISO	Ant1	5745	2.05	<=30	---	---	PASS
	Ant2	5745	2.04	<=30	---	---	PASS
	Ant1	5785	2.36	<=30	---	---	PASS
	Ant2	5785	1.86	<=30	---	---	PASS
	Ant1	5825	2.3	<=30	---	---	PASS
	Ant2	5825	3.14	<=30	---	---	PASS
11AC40SISO	Ant1	5755	-0.15	<=30	---	---	PASS
	Ant2	5755	1.41	<=30	---	---	PASS
	Ant1	5795	-0.04	<=30	---	---	PASS
	Ant2	5795	-0.88	<=30	---	---	PASS
11AC80SISO	Ant1	5775	-3.51	<=30	---	---	PASS
	Ant2	5775	-3.29	<=30	---	---	PASS

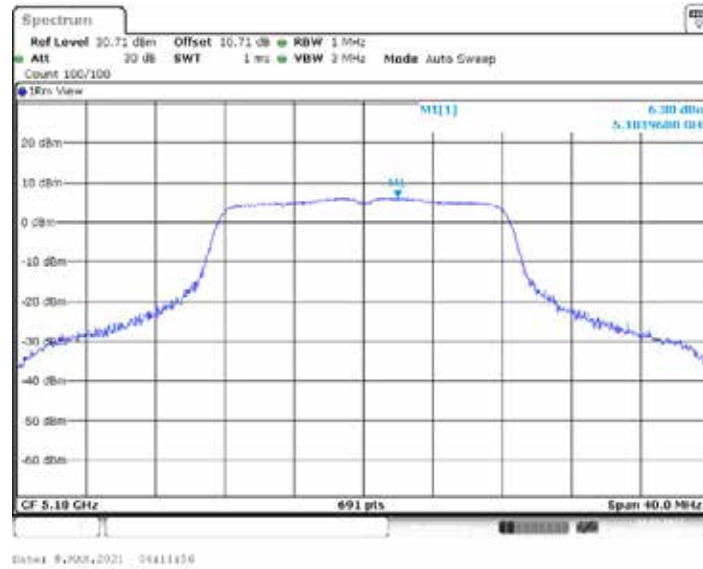
11AX20SISO	Ant1	5745	1.74	<=30	---	---	PASS
	Ant2	5745	2.48	<=30	---	---	PASS
	Ant1	5785	1.51	<=30	---	---	PASS
	Ant2	5785	3.16	<=30	---	---	PASS
	Ant1	5825	3.61	<=30	---	---	PASS
	Ant2	5825	3.68	<=30	---	---	PASS
11AX40SISO	Ant1	5755	0.08	<=30	---	---	PASS
	Ant2	5755	0.12	<=30	---	---	PASS
	Ant1	5795	-0.01	<=30	---	---	PASS
	Ant2	5795	0.17	<=30	---	---	PASS
11AX80SISO	Ant1	5775	-3.68	<=30	---	---	PASS
	Ant2	5775	-2.98	<=30	---	---	PASS
11N20MIMO	Ant1	5745	2.03	<=30	---	---	PASS
	Ant2	5745	3.59	<=30	---	---	PASS
	total	5745	5.89	<=30	---	---	PASS
	Ant1	5785	2.56	<=30	---	---	PASS
	Ant2	5785	2.65	<=30	---	---	PASS
	total	5785	5.62	<=30	---	---	PASS
	Ant1	5825	2.82	<=30	---	---	PASS
	Ant2	5825	2.93	<=30	---	---	PASS
	total	5825	5.89	<=30	---	---	PASS
11N40MIMO	Ant1	5755	-0.04	<=30	---	---	PASS
	Ant2	5755	1.35	<=30	---	---	PASS
	total	5755	3.72	<=30	---	---	PASS
	Ant1	5795	-0.16	<=30	---	---	PASS
	Ant2	5795	1.38	<=30	---	---	PASS
	total	5795	3.69	<=30	---	---	PASS
11AC20MIMO	Ant1	5745	2.21	<=30	---	---	PASS
	Ant2	5745	3.2	<=30	---	---	PASS
	total	5745	5.74	<=30	---	---	PASS
	Ant1	5785	1.98	<=30	---	---	PASS
	Ant2	5785	2.62	<=30	---	---	PASS
	total	5785	5.32	<=30	---	---	PASS
	Ant1	5825	2.21	<=30	---	---	PASS
	Ant2	5825	4.04	<=30	---	---	PASS
	total	5825	6.23	<=30	---	---	PASS
11AC40MIMO	Ant1	5755	-0.74	<=30	---	---	PASS
	Ant2	5755	0.32	<=30	---	---	PASS
	total	5755	2.83	<=30	---	---	PASS
	Ant1	5795	1.24	<=30	---	---	PASS
	Ant2	5795	1.15	<=30	---	---	PASS
	total	5795	4.21	<=30	---	---	PASS
11AC80MIMO	Ant1	5775	-3.02	<=30	---	---	PASS

O	Ant2	5775	-2.55	<=30	---	---	PASS
	total	5775	0.23	<=30	---	---	PASS
11AX20MIMO	Ant1	5745	2.31	<=30	---	---	PASS
	Ant2	5745	3.68	<=30	---	---	PASS
	total	5745	6.06	<=30	---	---	PASS
	Ant1	5785	2.8	<=30	---	---	PASS
	Ant2	5785	3.28	<=30	---	---	PASS
	total	5785	6.06	<=30	---	---	PASS
	Ant1	5825	3.15	<=30	---	---	PASS
	Ant2	5825	2.84	<=30	---	---	PASS
	total	5825	6.01	<=30	---	---	PASS
11AX40MIMO	Ant1	5755	-0.31	<=30	---	---	PASS
	Ant2	5755	0.67	<=30	---	---	PASS
	total	5755	3.22	<=30	---	---	PASS
	Ant1	5795	1.22	<=30	---	---	PASS
	Ant2	5795	2.61	<=30	---	---	PASS
	total	5795	4.98	<=30	---	---	PASS
11AX80MIMO	Ant1	5775	-1.82	<=30	---	---	PASS
	Ant2	5775	-3.3	<=30	---	---	PASS
	total	5775	0.51	<=30	---	---	PASS

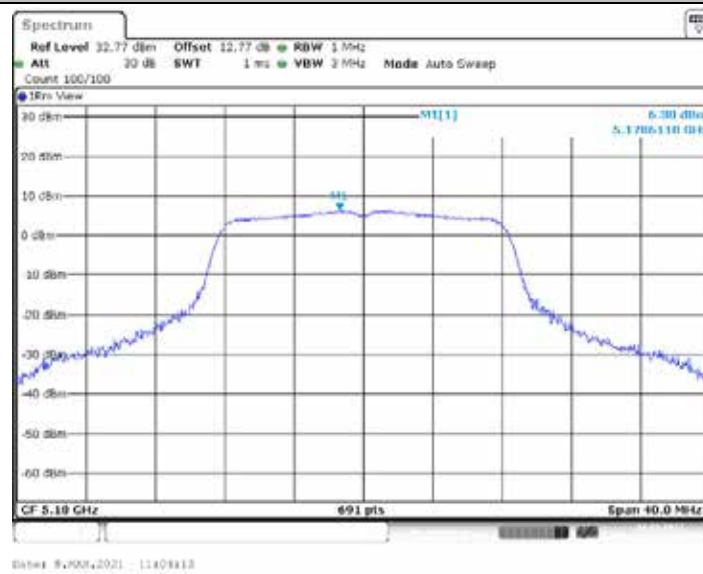
Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.
2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs

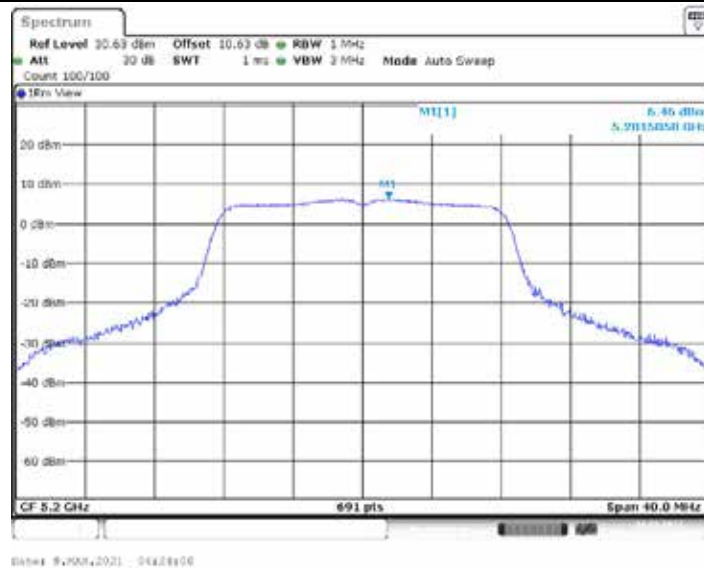
11A_Ant1_5180



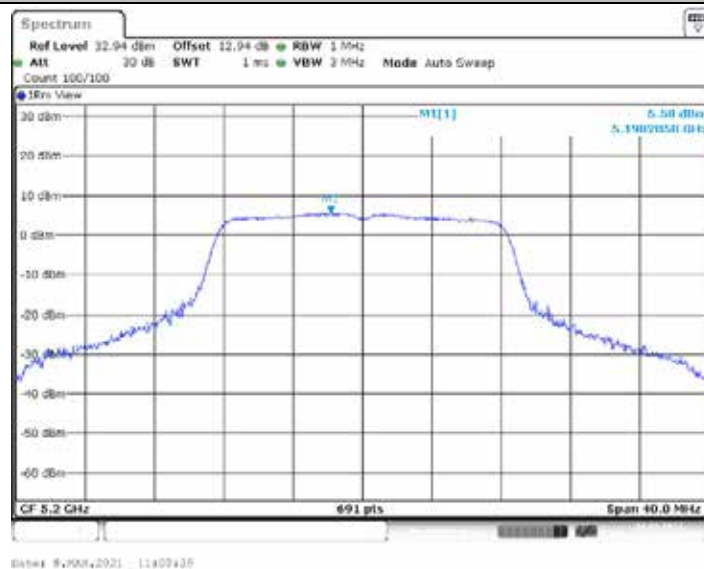
11A_Ant2_5180



11A_Ant1_5200



11A_Ant2_5200



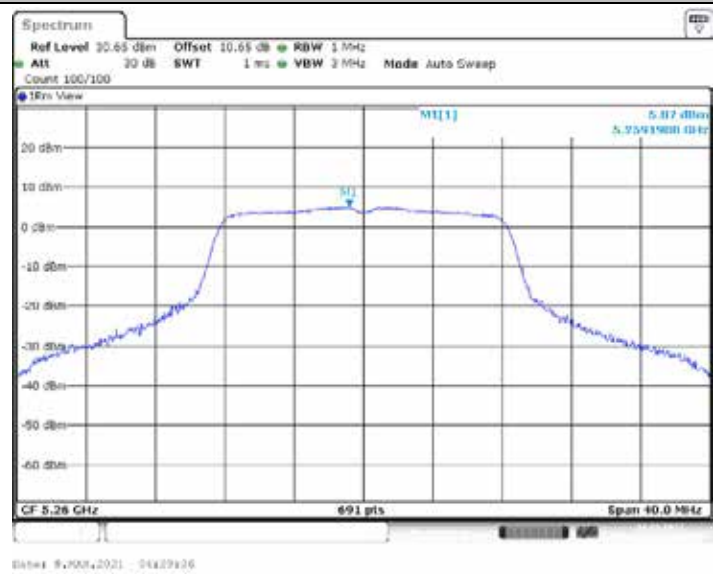
11A_Ant1_5240



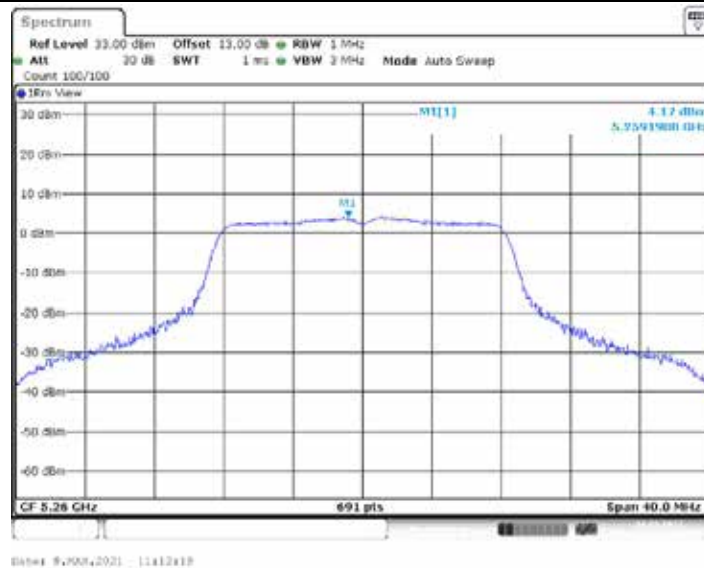
11A_Ant2_5240



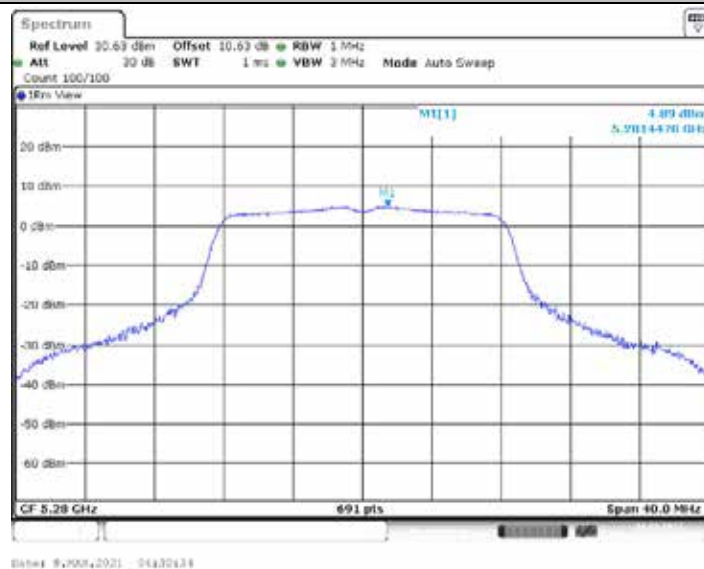
11A_Ant1_5260



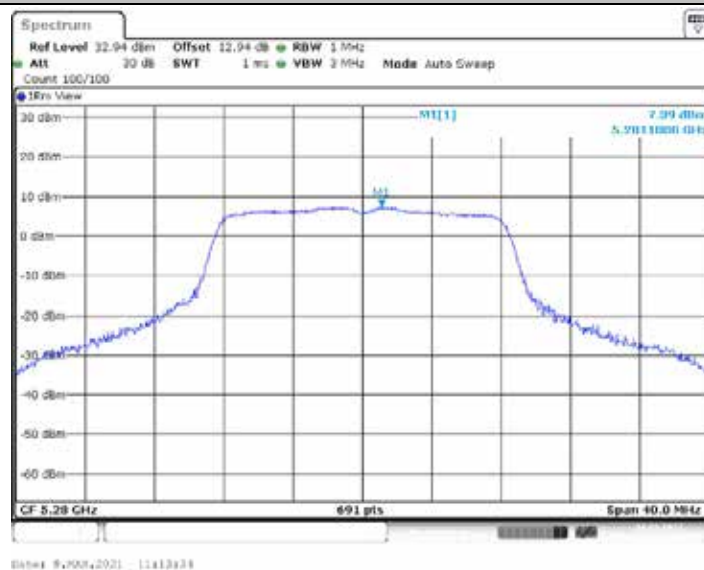
11A_Ant2_5260



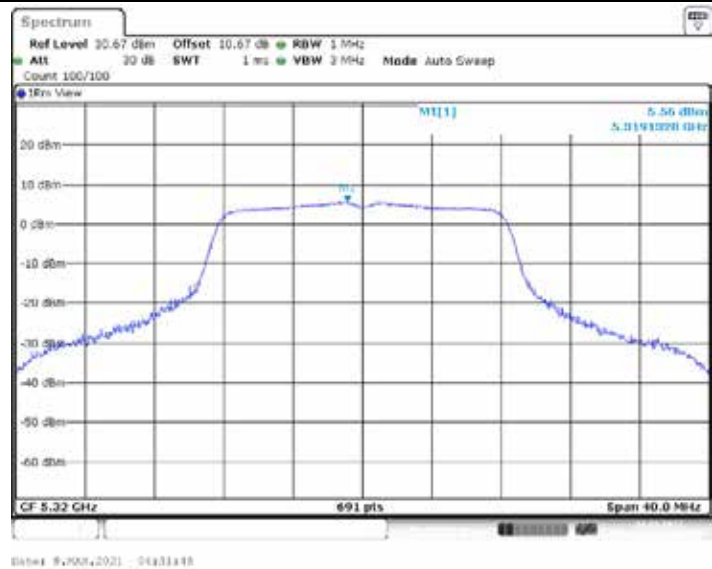
11A_Ant1_5280



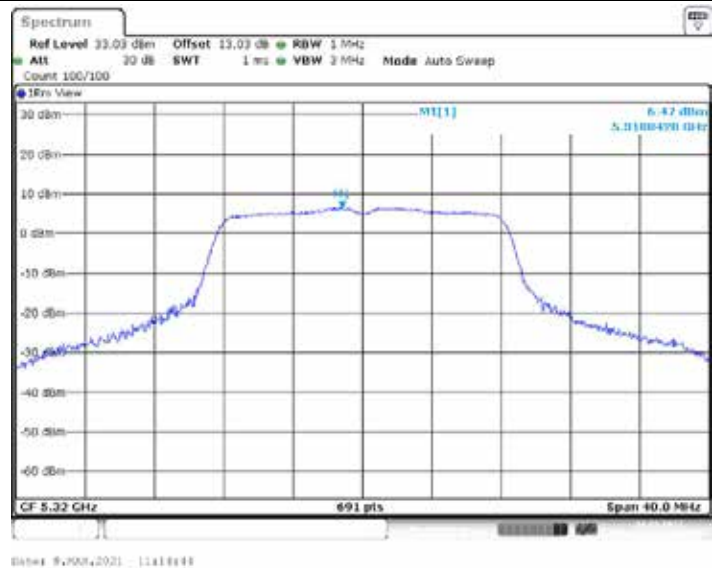
11A_Ant2_5280



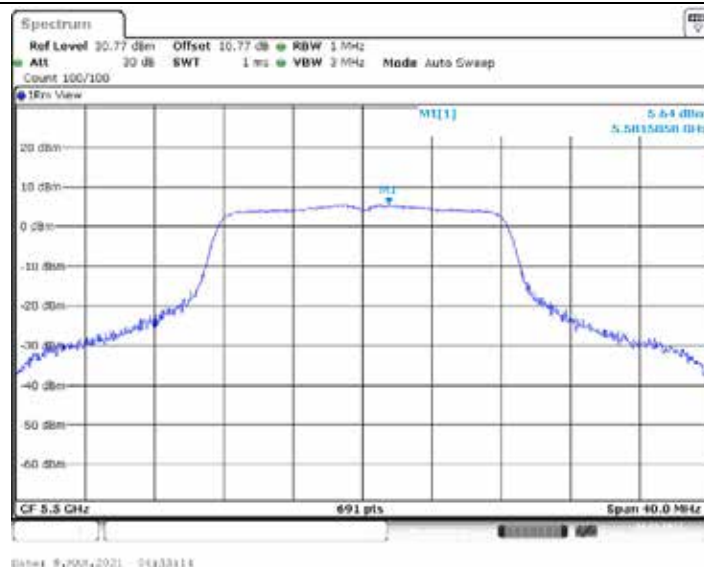
11A_Ant1_5320



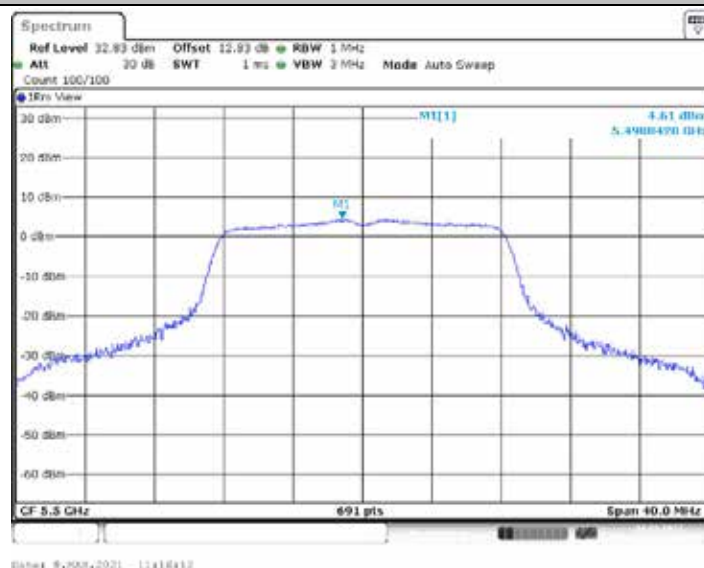
11A_Ant2_5320



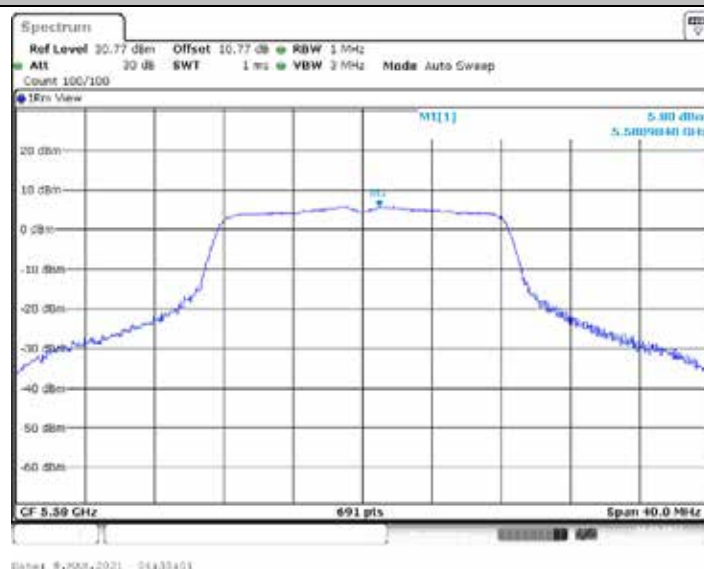
11A_Ant1_5500



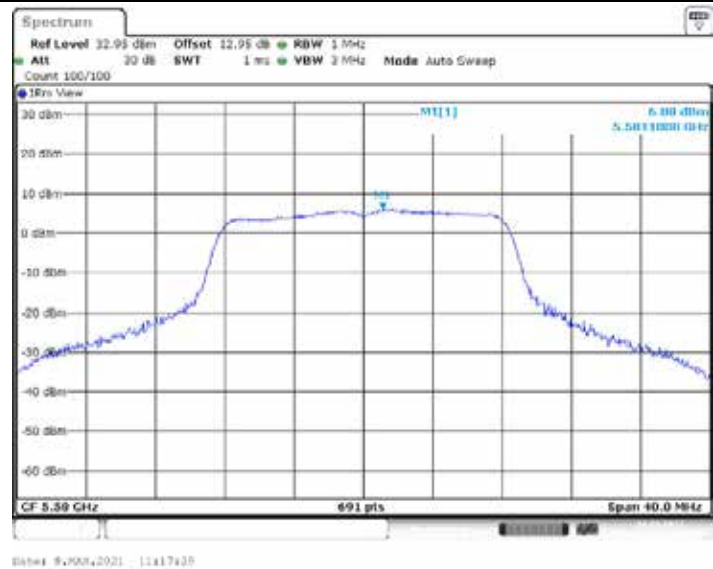
11A_Ant2_5500



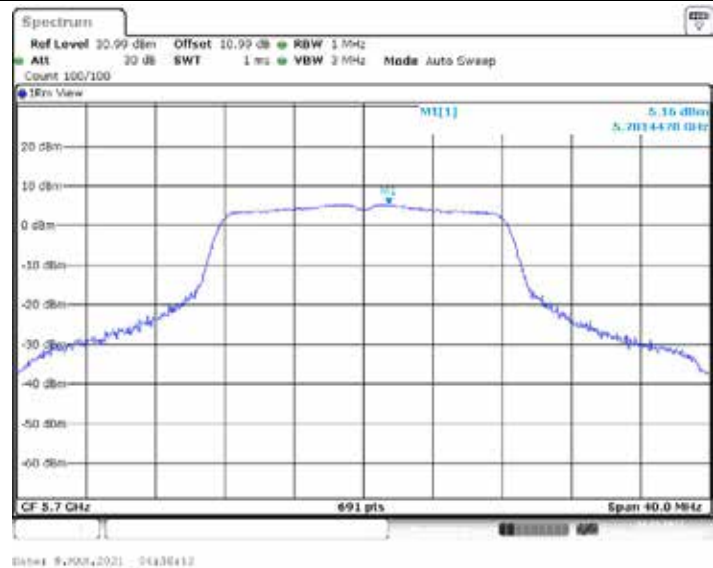
11A_Ant1_5580



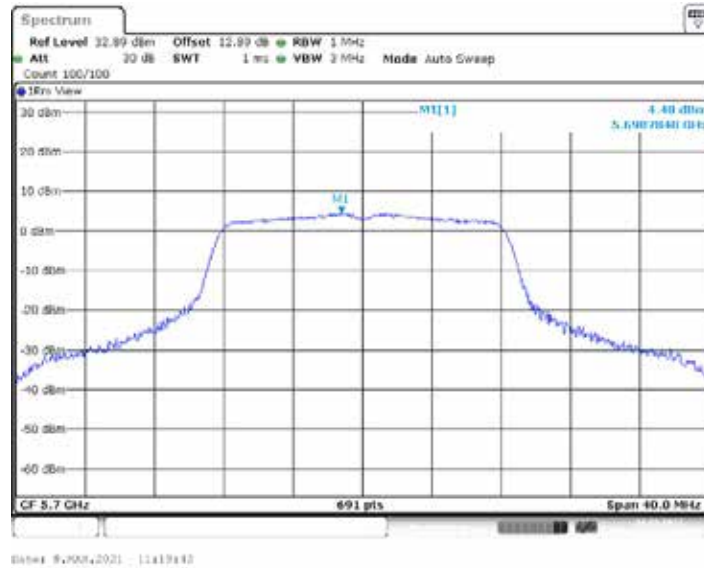
11A_Ant2_5580



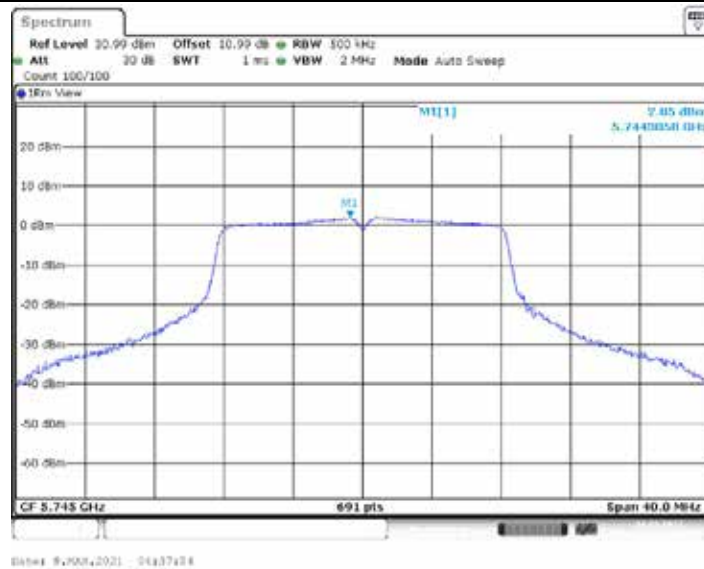
11A_Ant1_5700



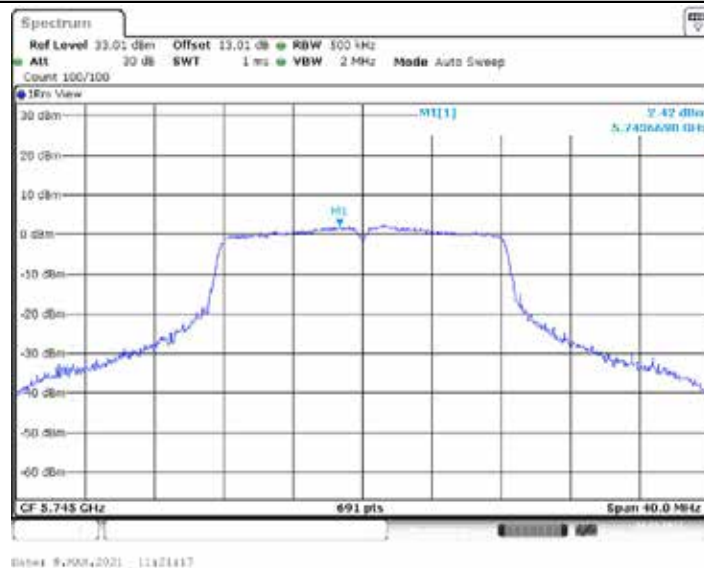
11A_Ant2_5700

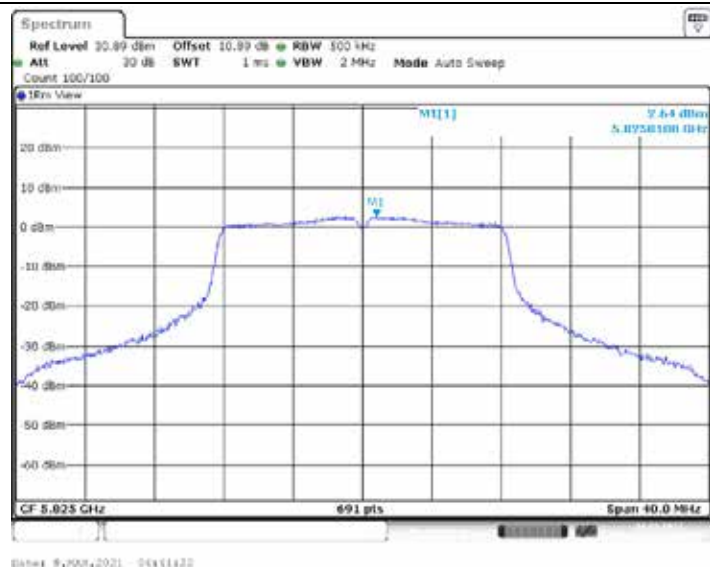


11A_Ant1_5745

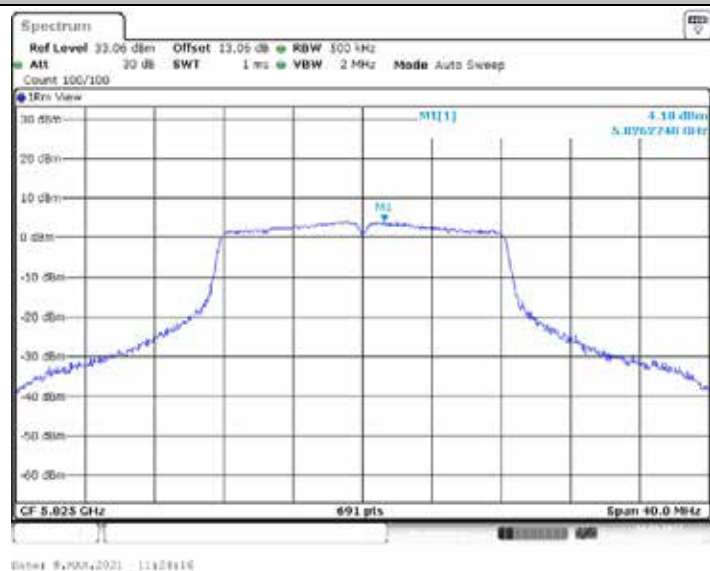


11A_Ant2_5745

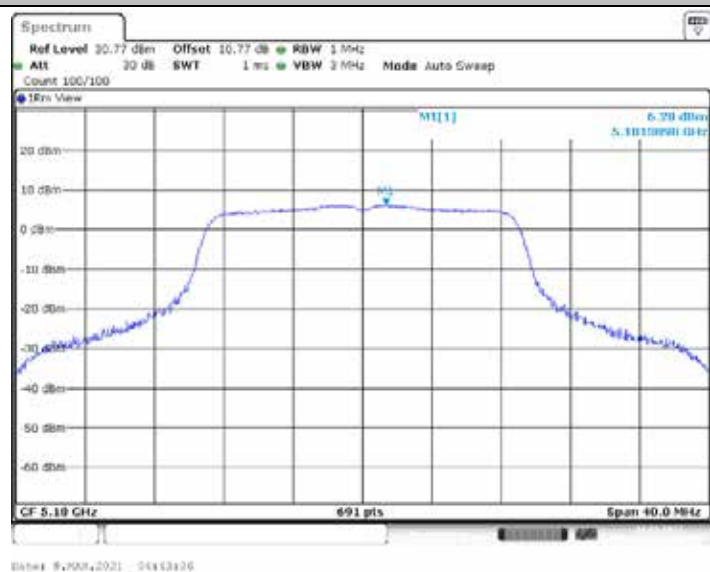




11A_Ant2_5825



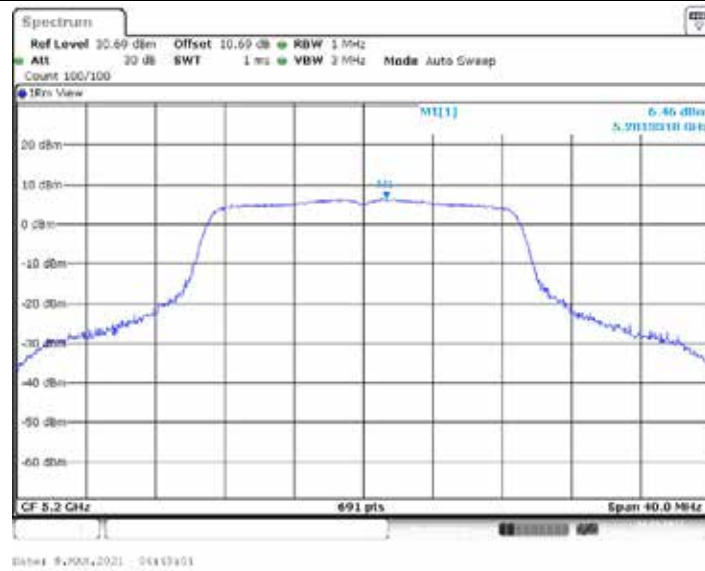
11N20SISO_Ant1_5180



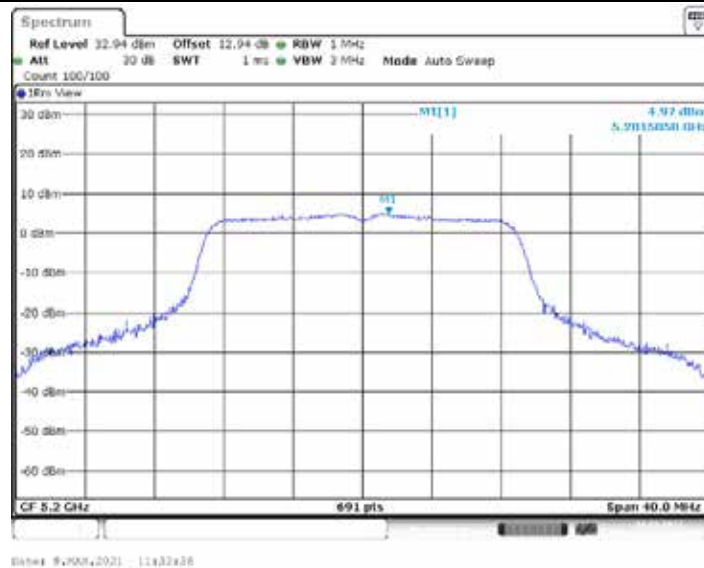
11N20SISO_Ant2_5180



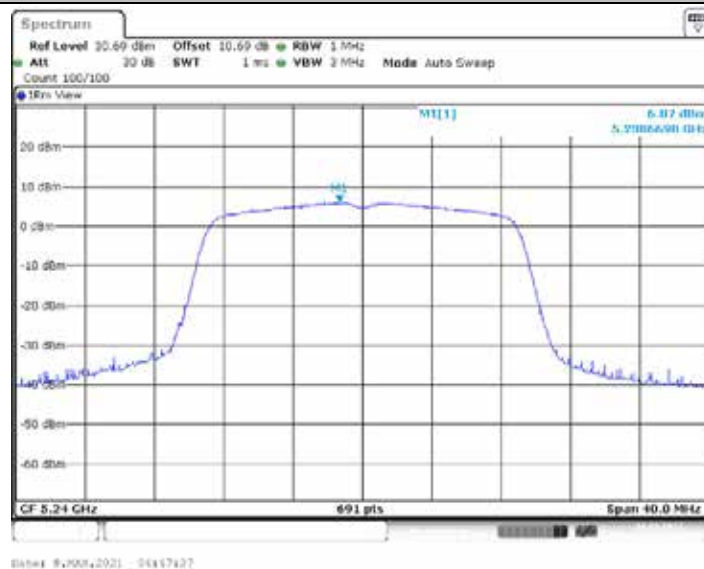
11N20SISO_Ant1_5200



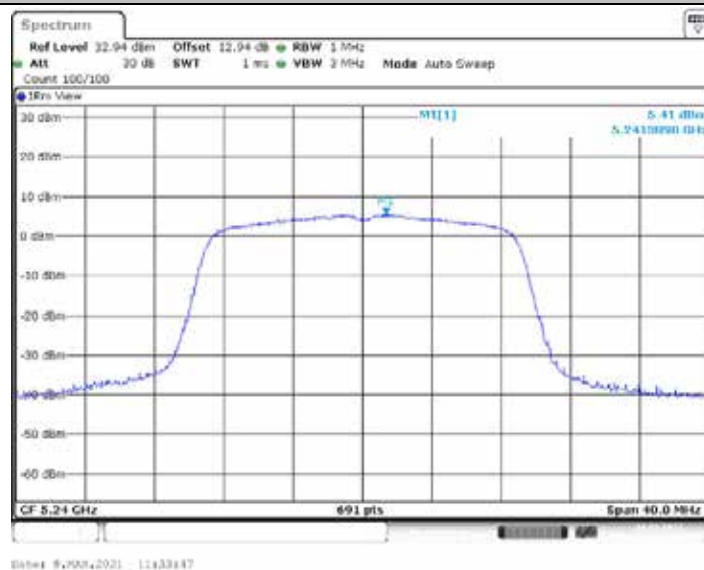
11N20SISO_Ant2_5200



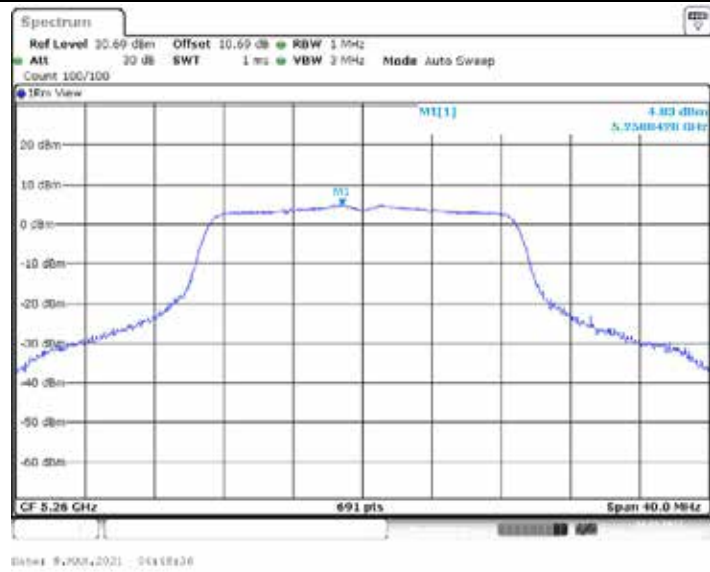
11N20SISO_Ant1_5240



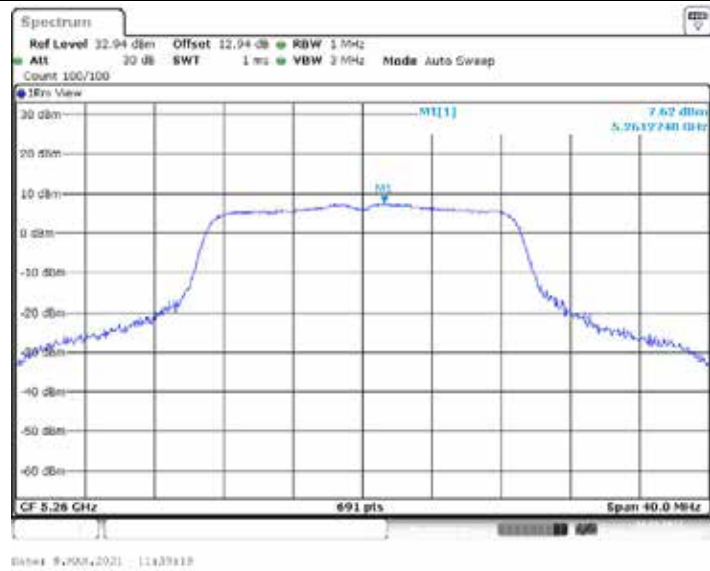
11N20SISO_Ant2_5240



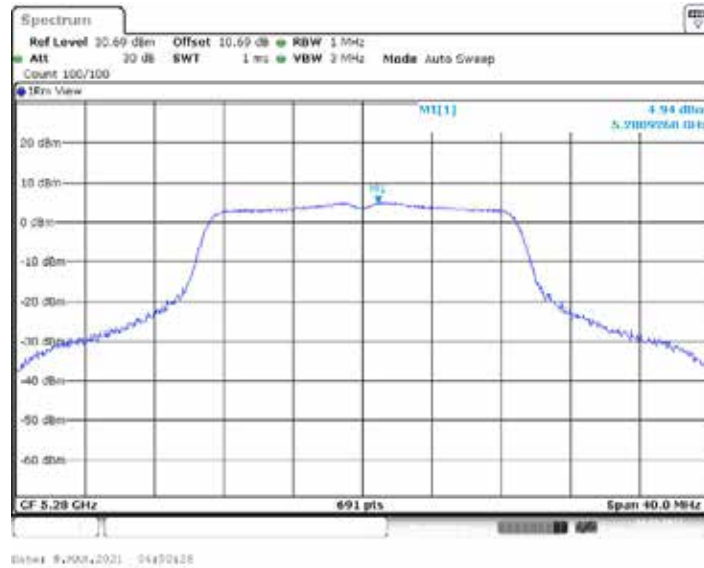
11N20SISO_Ant1_5260



11N20SISO_Ant2_5260



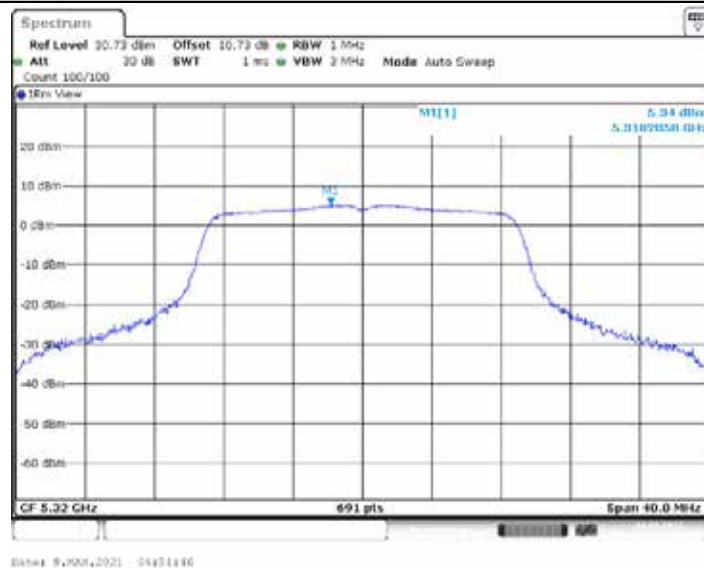
11N20SISO_Ant1_5280



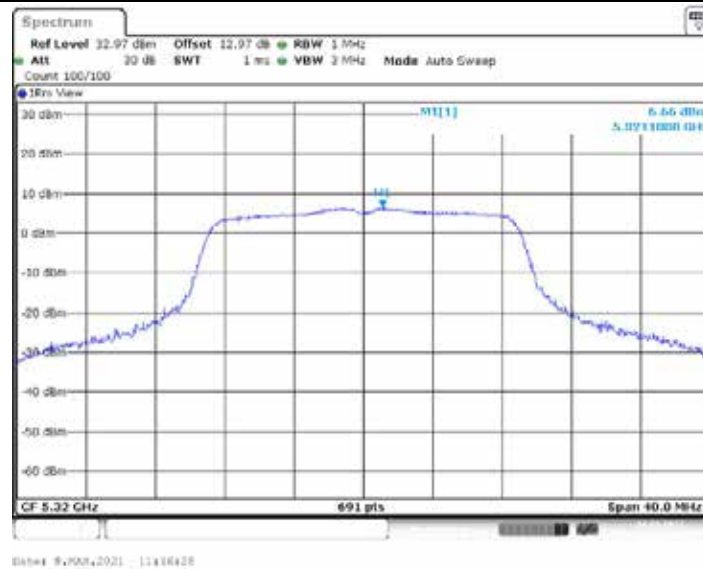
11N20SISO_Ant2_5280



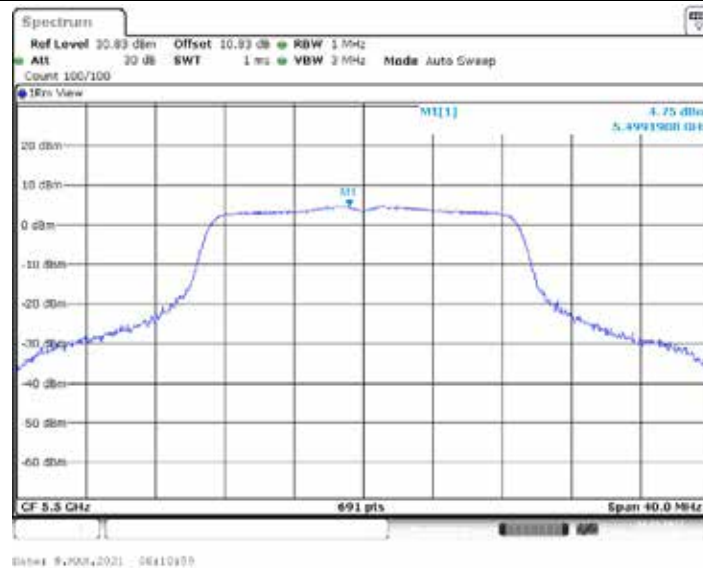
11N20SISO_Ant1_5320



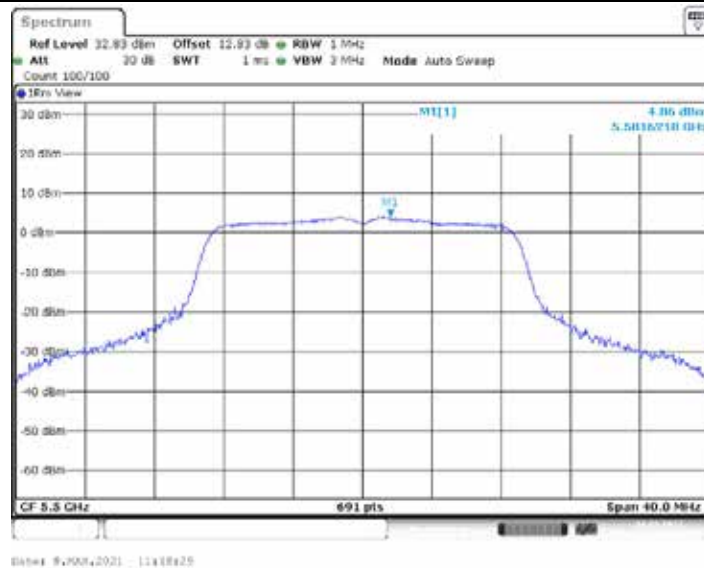
11N20SISO_Ant2_5320



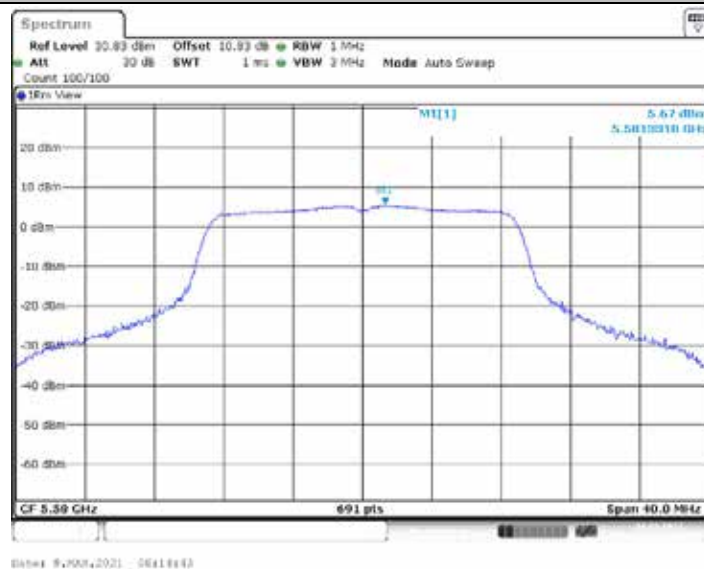
11N20SISO_Ant1_5500



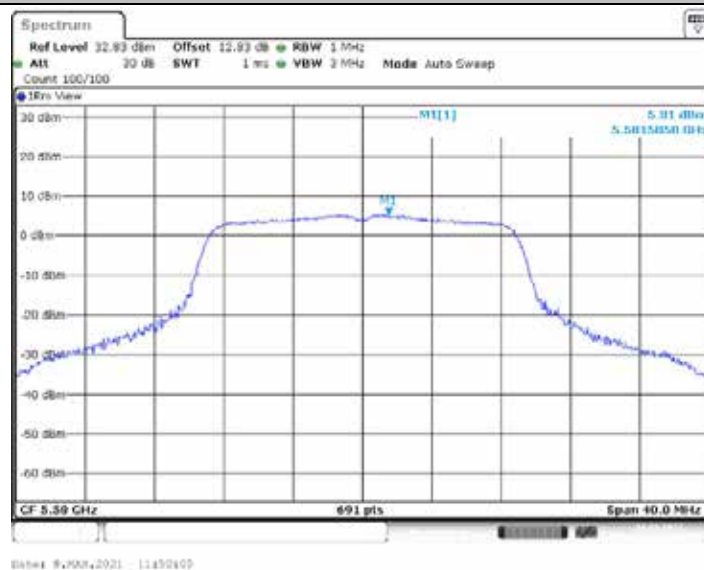
11N20SISO_Ant2_5500



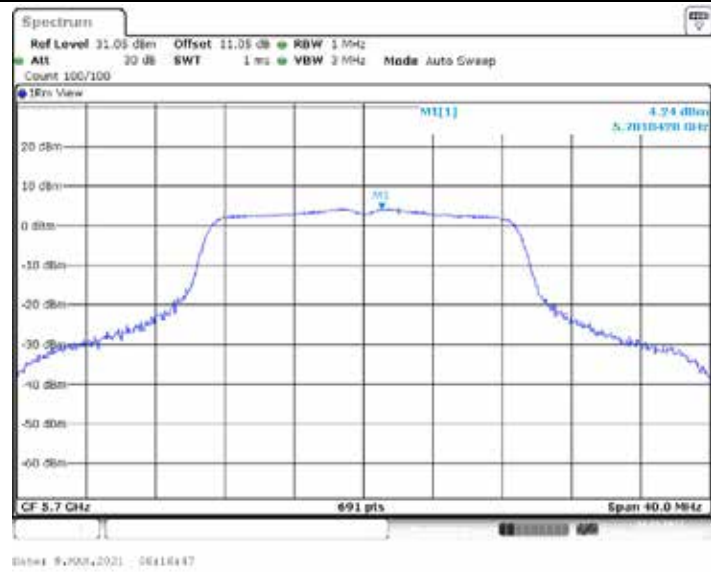
11N20SISO_Ant1_5580



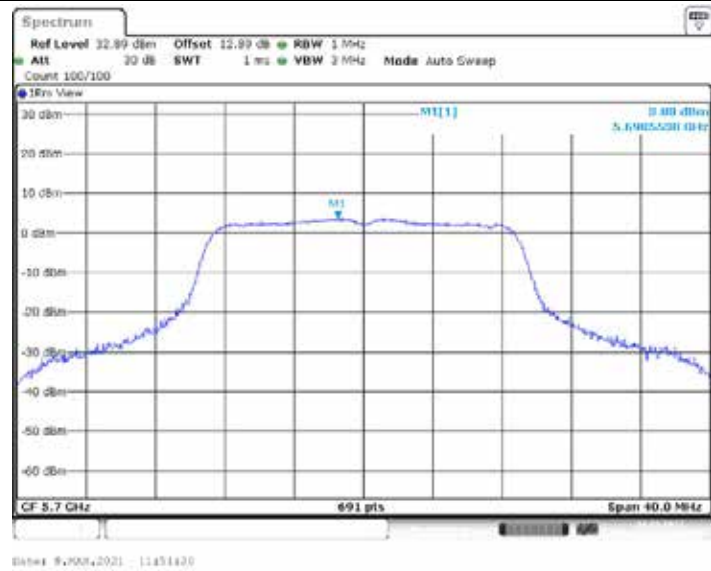
11N20SISO_Ant2_5580



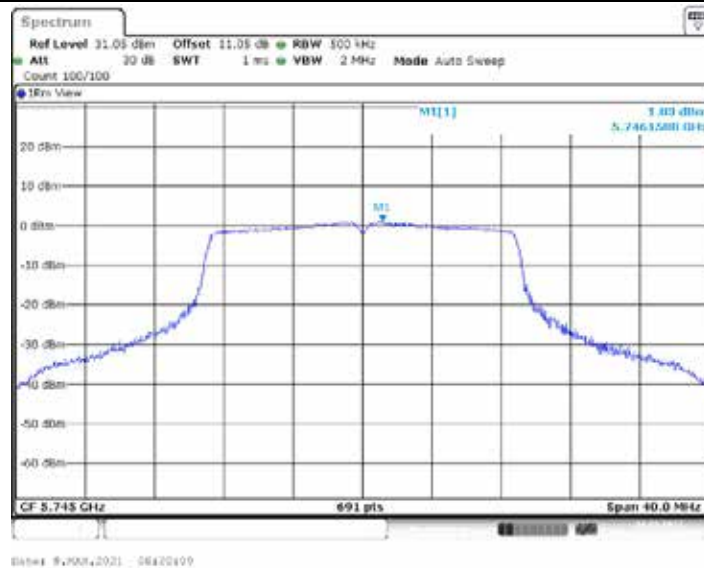
11N20SISO_Ant1_5700



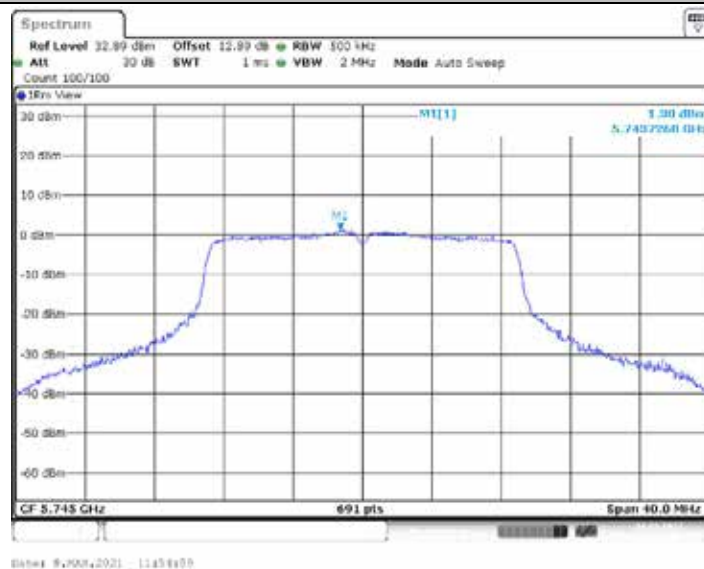
11N20SISO_Ant2_5700



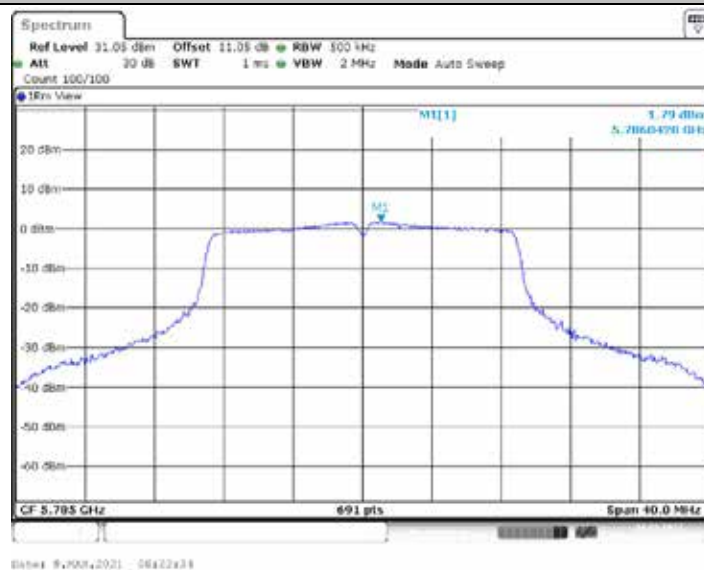
11N20SISO_Ant1_5745



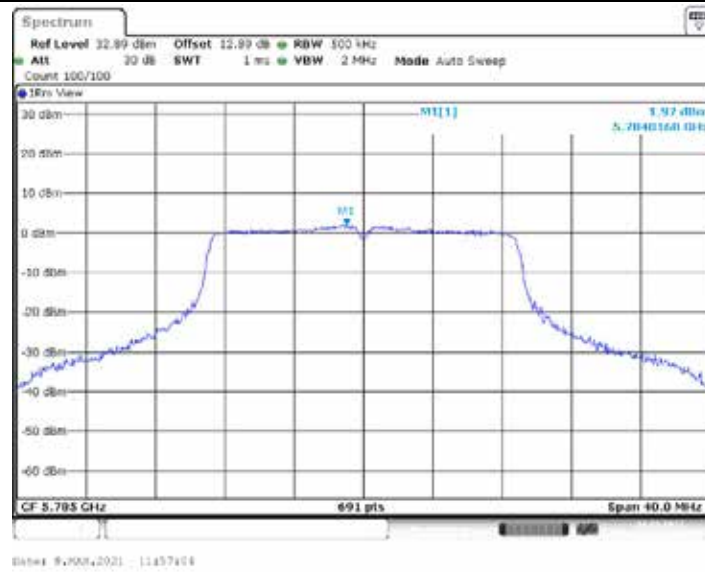
11N20SISO_Ant2_5745



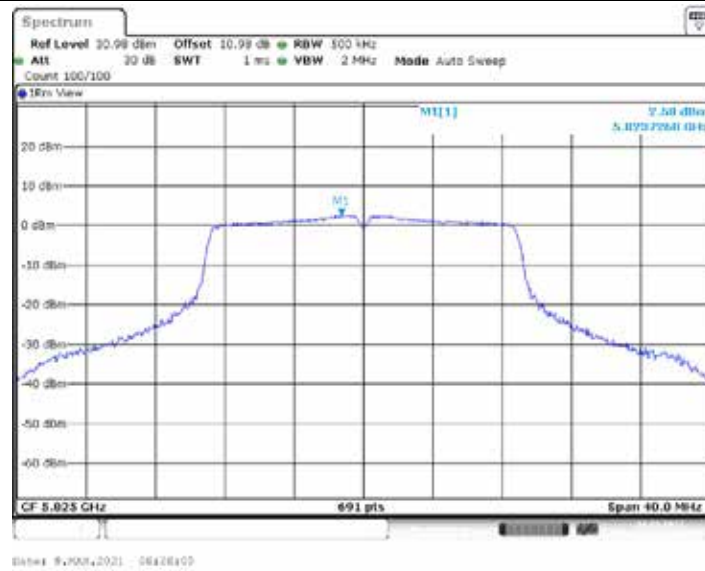
11N20SISO_Ant1_5785



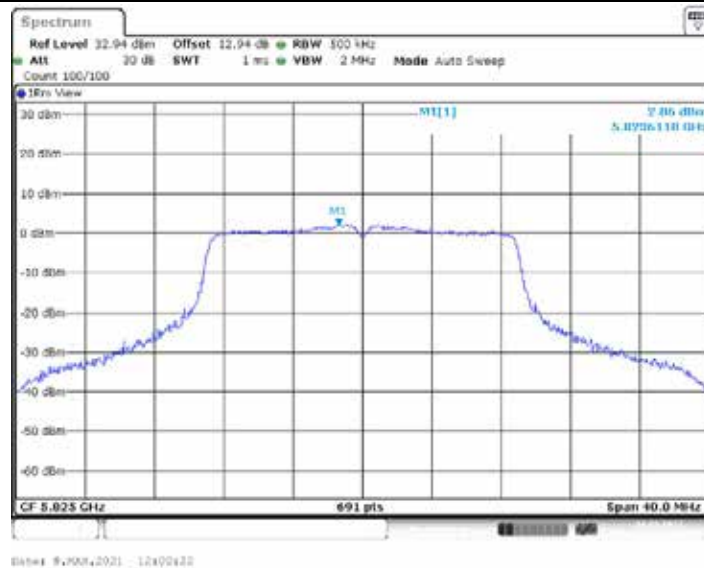
11N20SISO_Ant2_5785



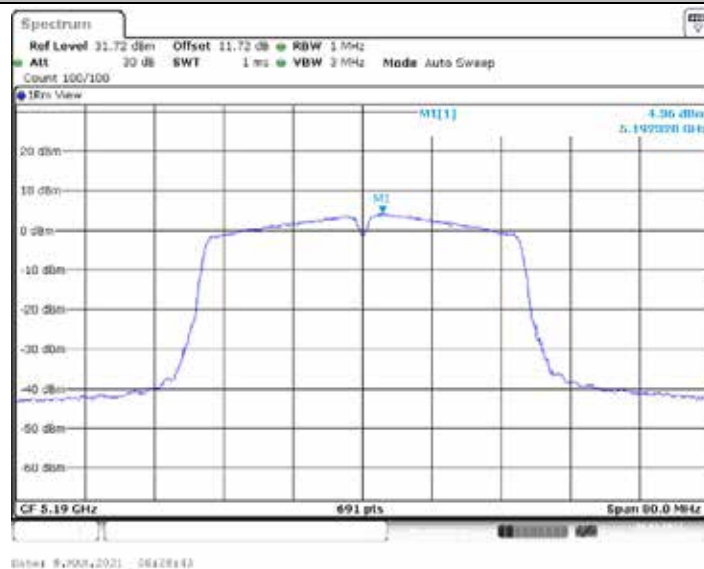
11N20SISO_Ant1_5825



11N20SISO_Ant2_5825



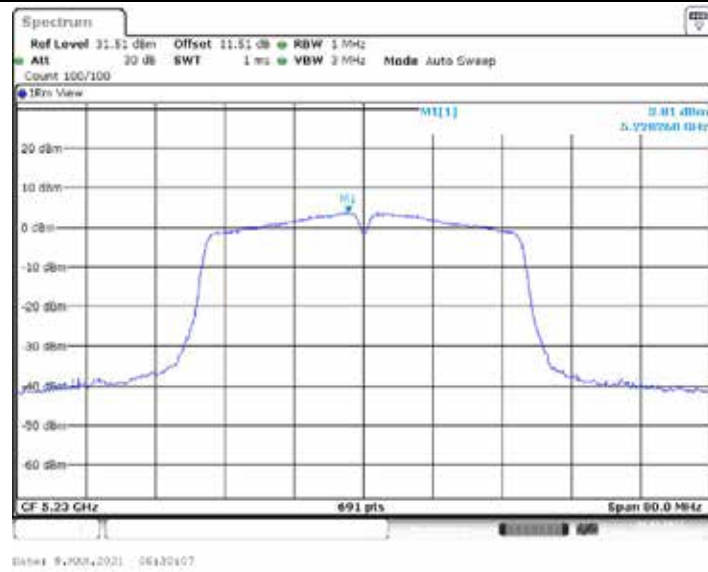
11N40SISO_Ant1_5190



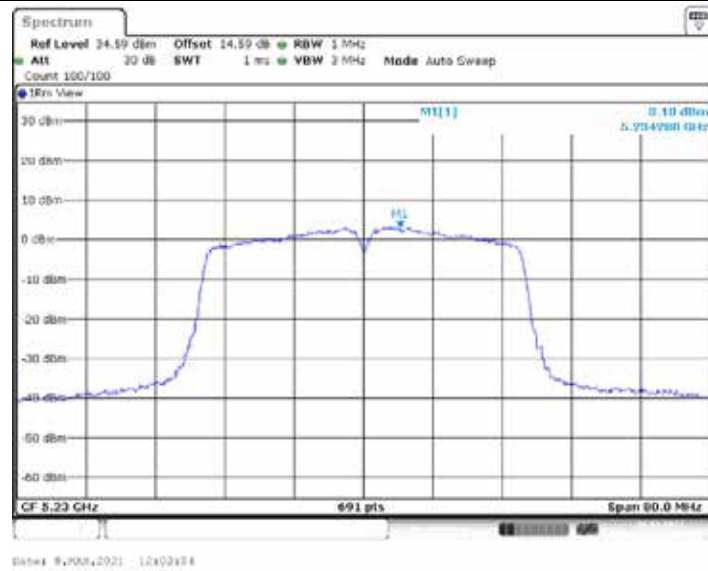
11N40SISO_Ant2_5190



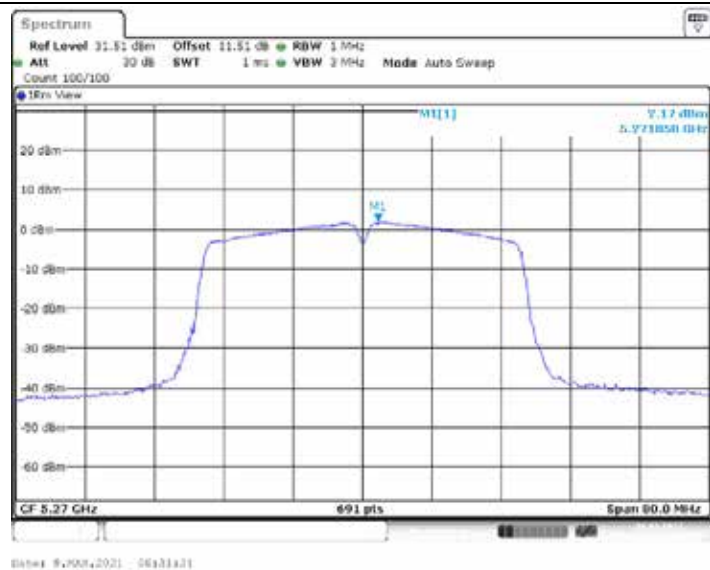
11N40SISO_Ant1_5230



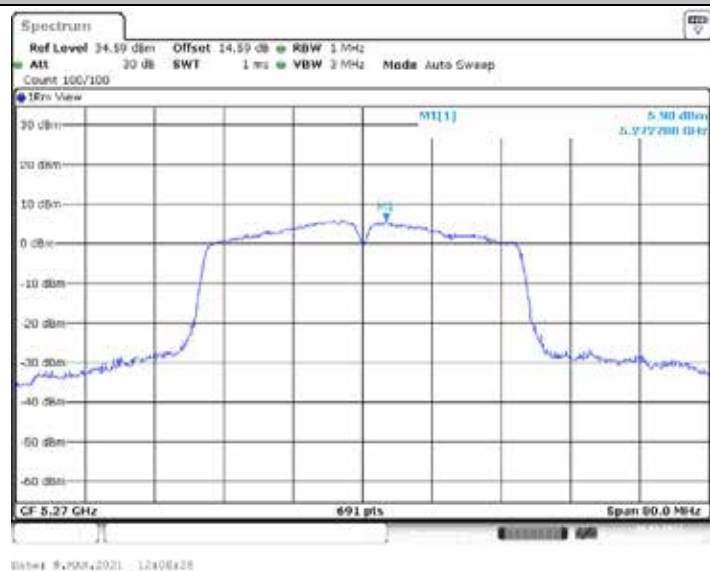
11N40SISO_Ant2_5230



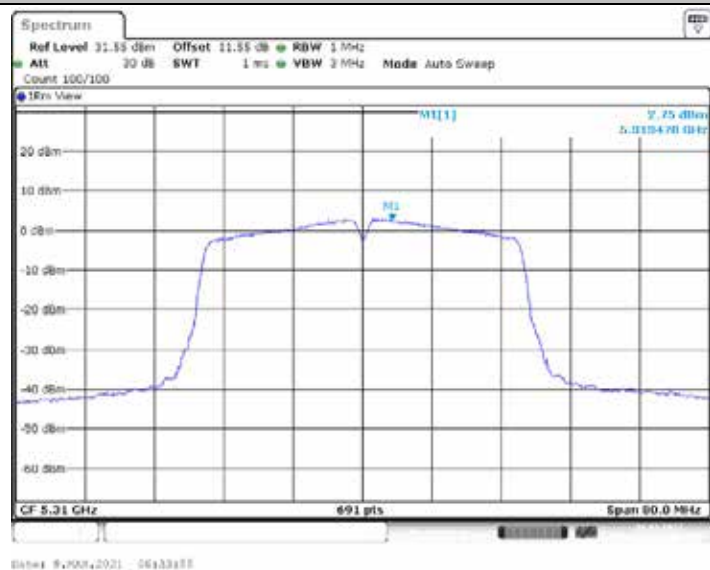
11N40SISO_Ant1_5270



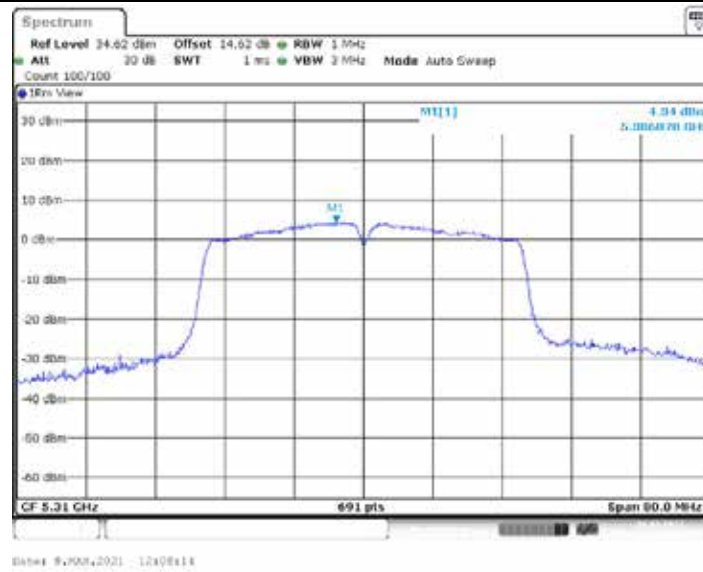
11N40SISO_Ant2_5270



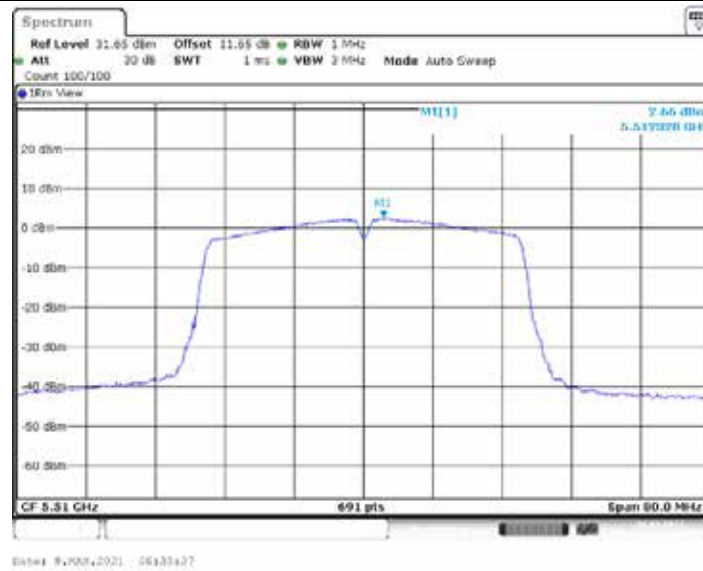
11N40SISO_Ant1_5310



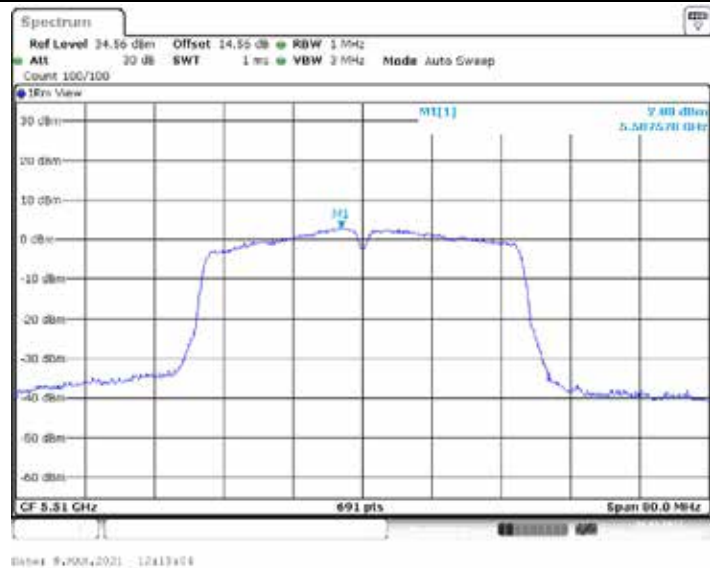
11N40SISO_Ant2_5310



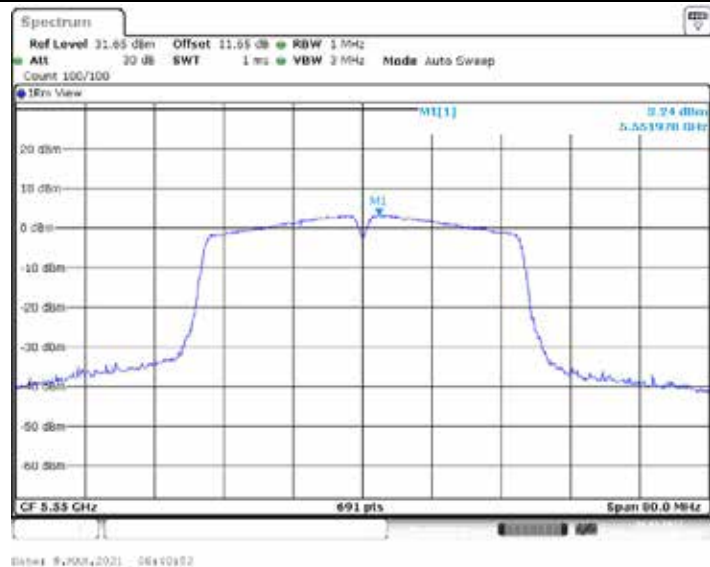
11N40SISO_Ant1_5510



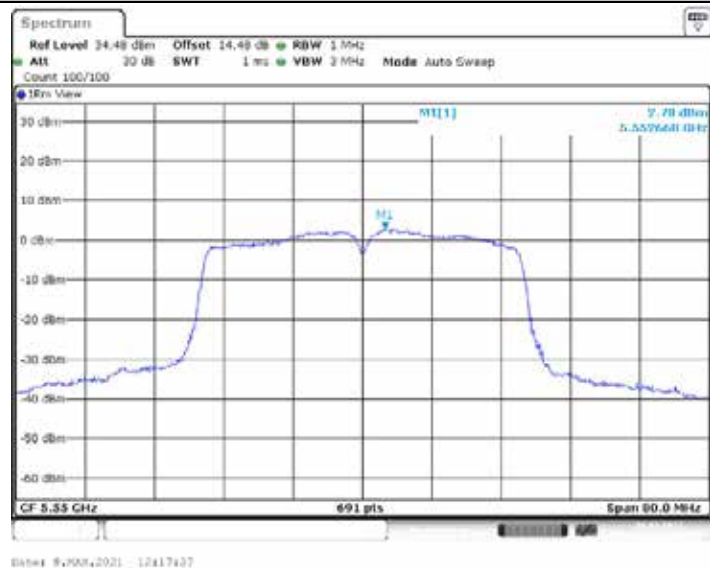
11N40SISO_Ant2_5510



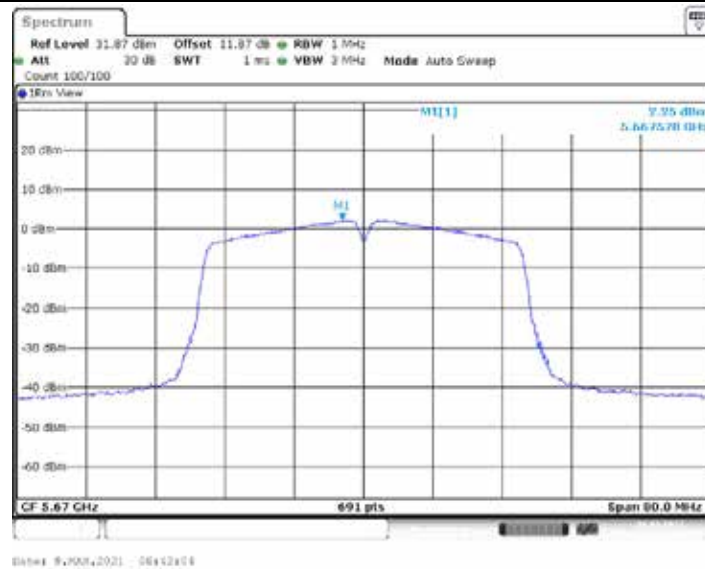
11N40SISO_Ant1_5550



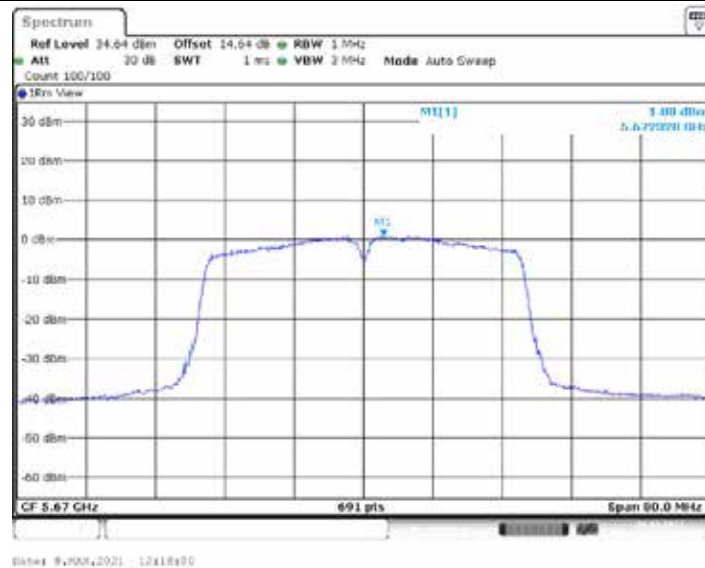
11N40SISO_Ant2_5550



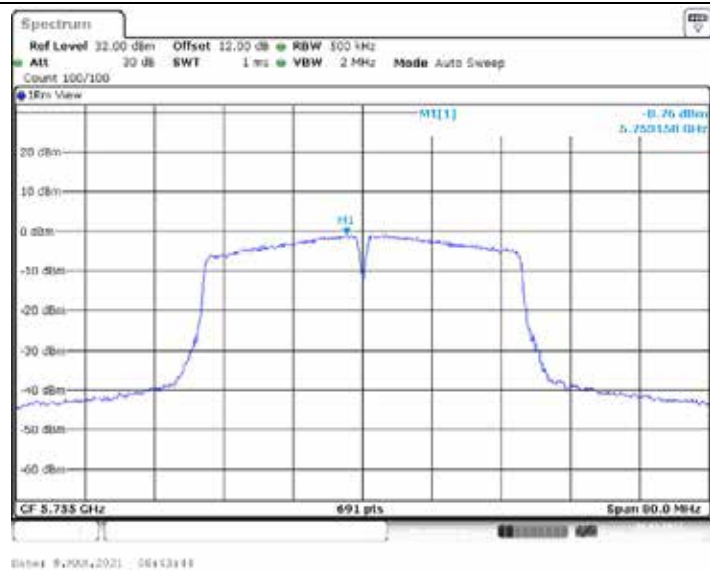
11N40SISO_Ant1_5670



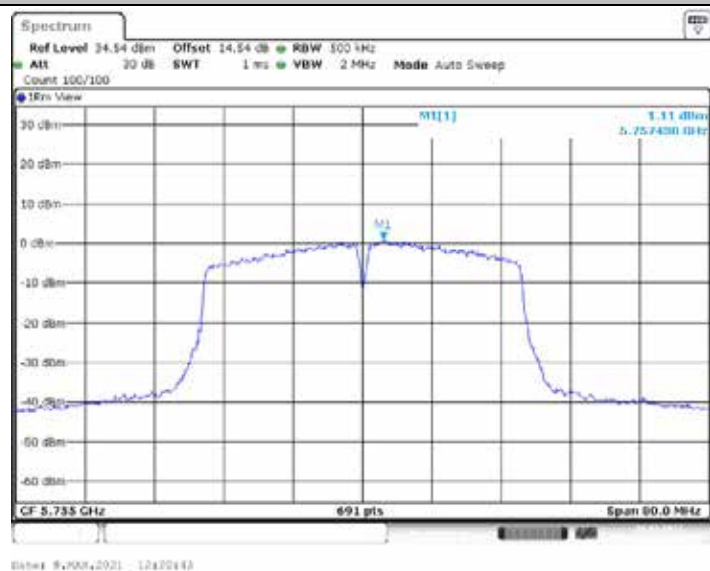
11N40SISO_Ant2_5670



11N40SISO_Ant1_5755



11N40SISO_Ant2_5755



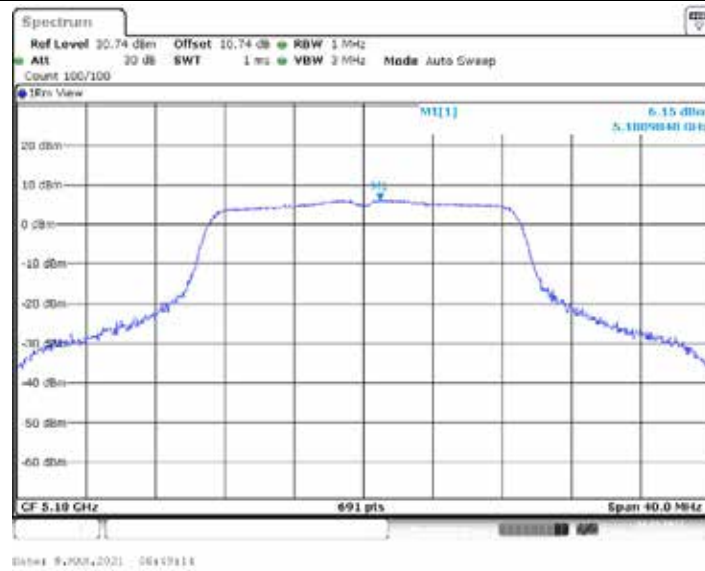
11N40SISO_Ant1_5795



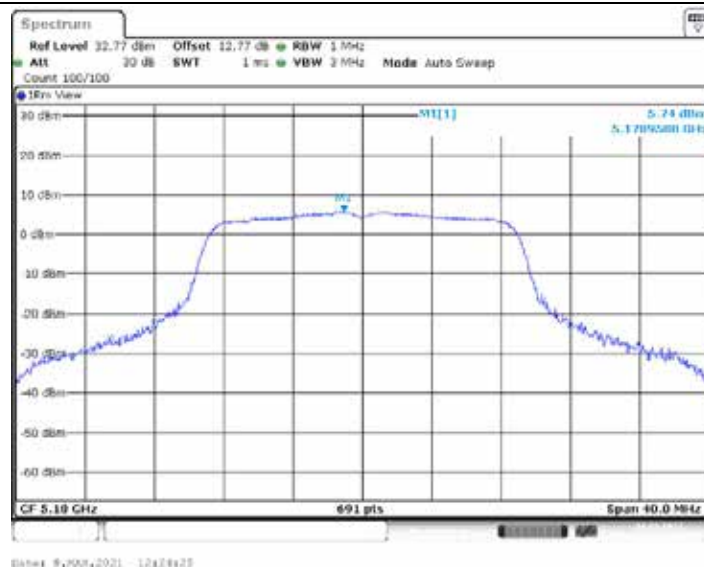
11N40SISO_Ant2_5795



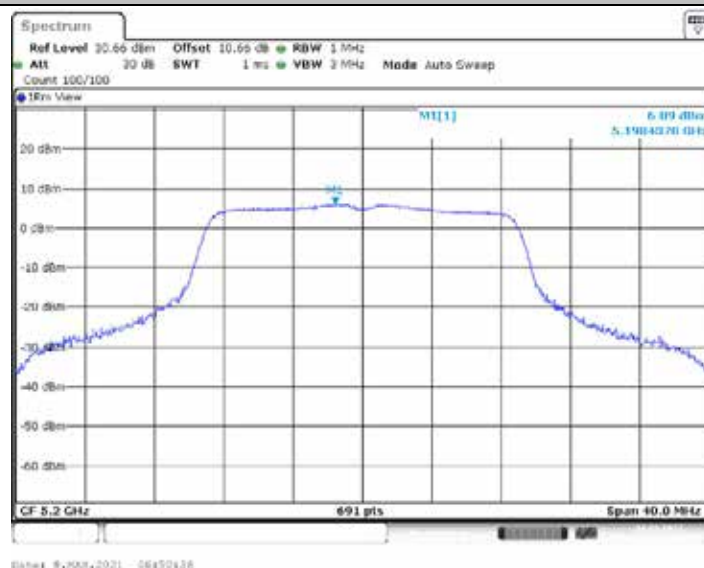
11AC20SISO_Ant1_5180



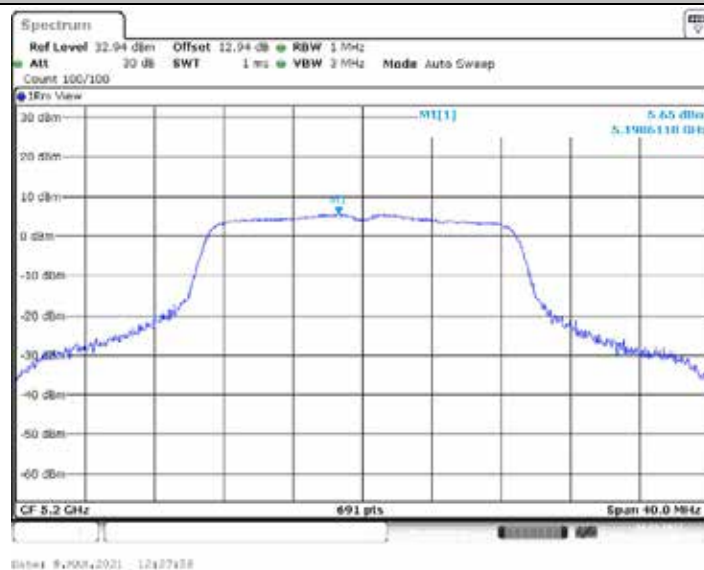
11AC20SISO_Ant2_5180



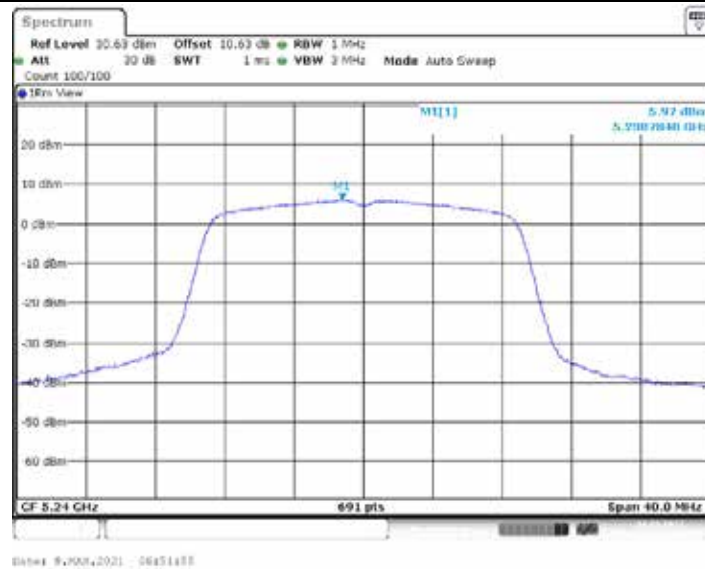
11AC20SISO_Ant1_5200



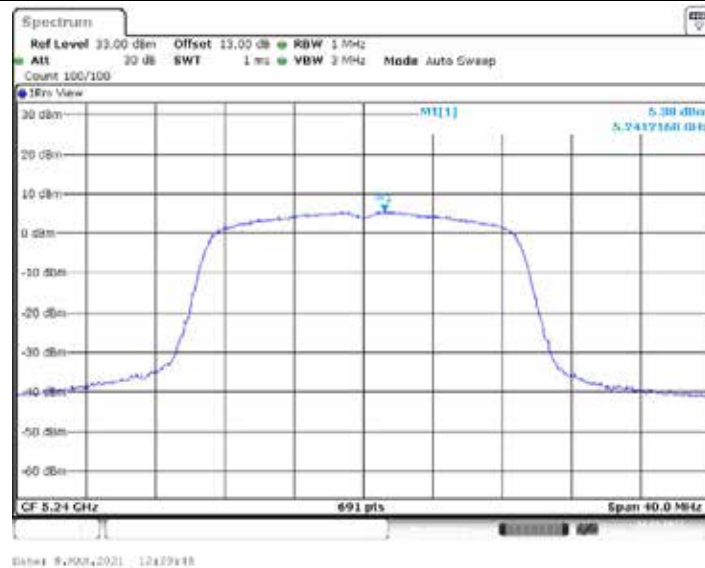
11AC20SISO_Ant2_5200



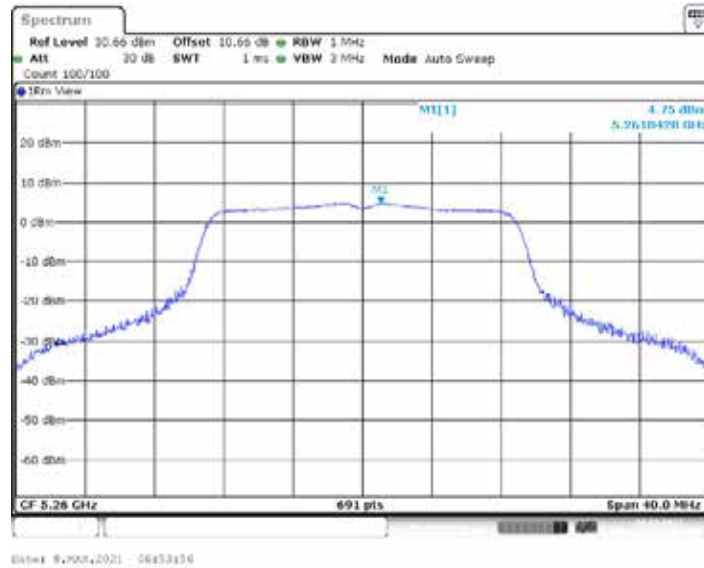
11AC20SISO_Ant1_5240



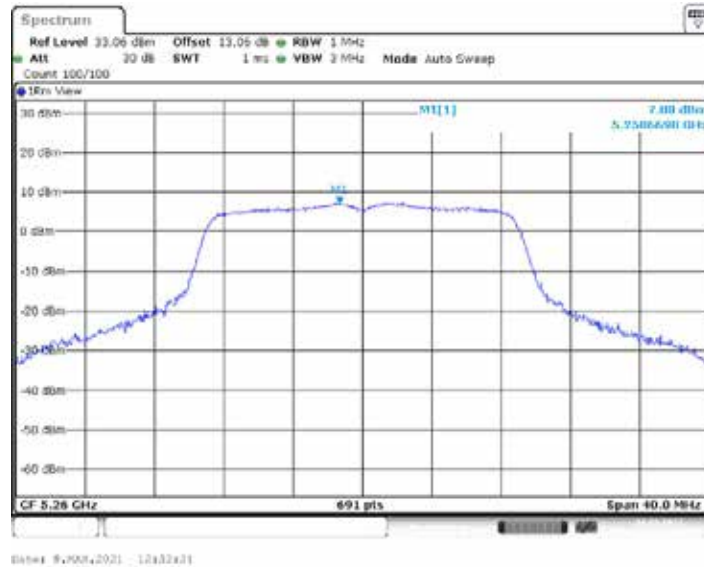
11AC20SISO_Ant2_5240



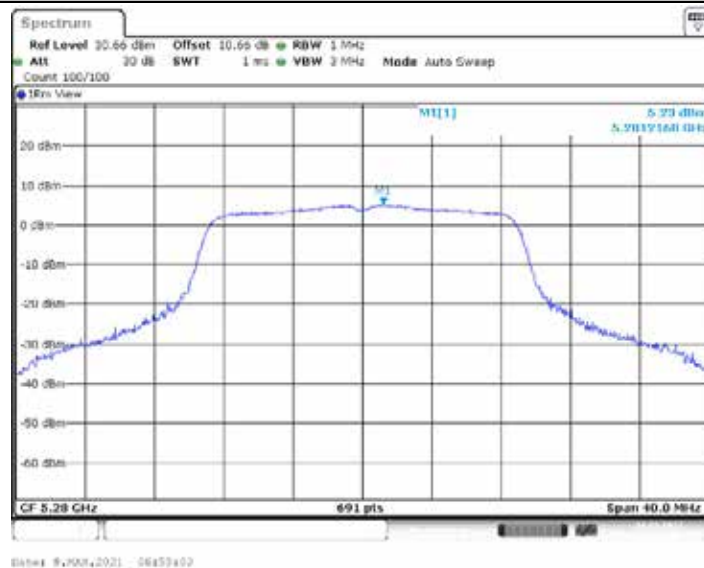
11AC20SISO_Ant1_5260



11AC20SISO_Ant2_5260



11AC20SISO_Ant1_5280



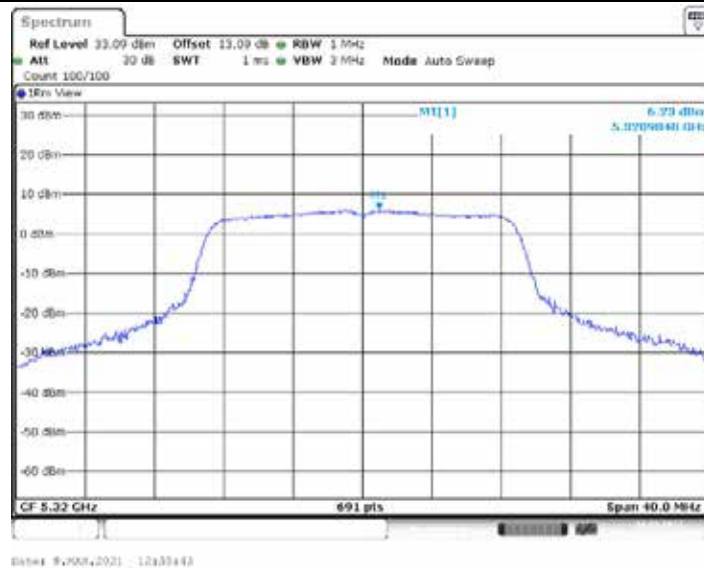
11AC20SISO_Ant2_5280



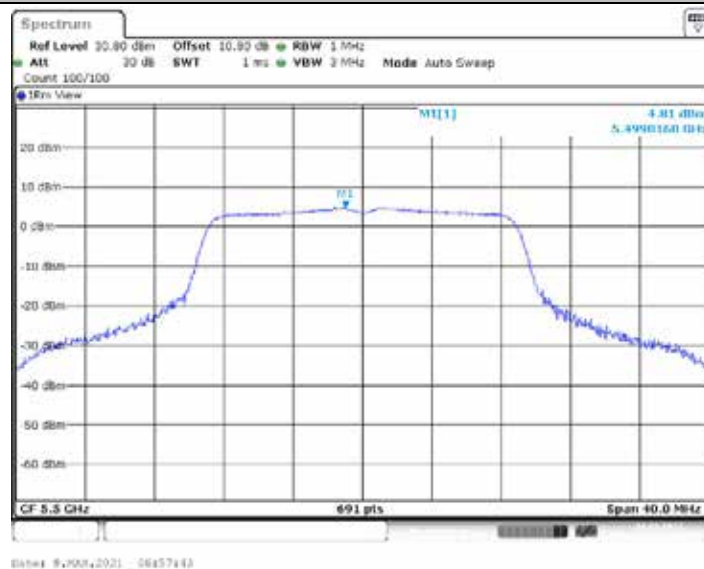
11AC20SISO_Ant1_5320



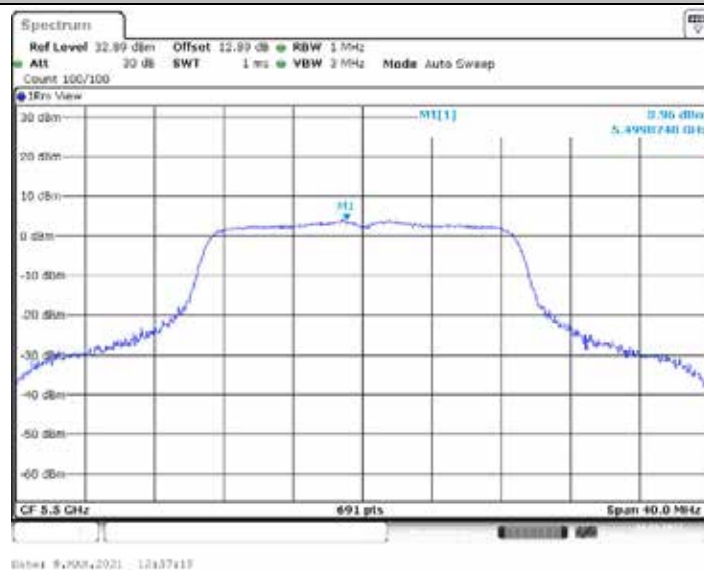
11AC20SISO_Ant2_5320



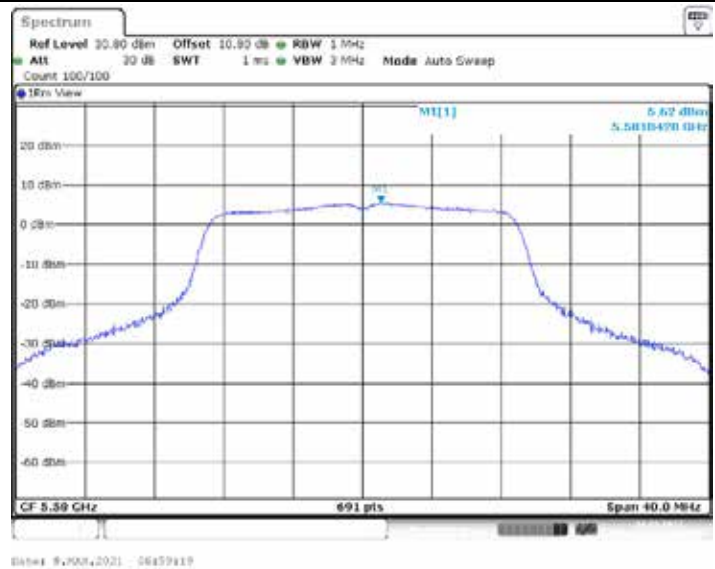
11AC20SISO_Ant1_5500



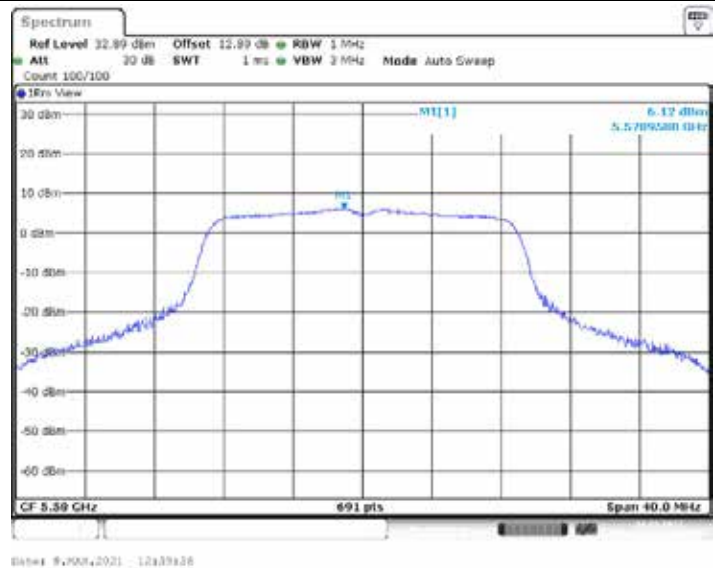
11AC20SISO_Ant2_5500



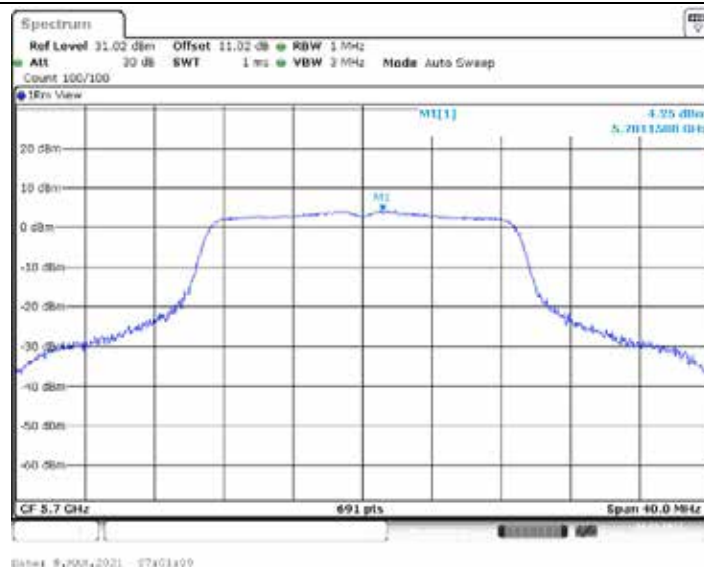
11AC20SISO_Ant1_5580



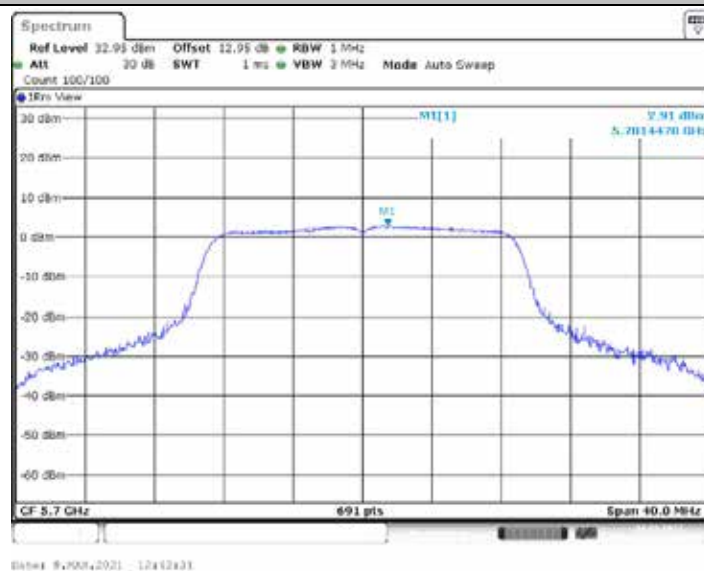
11AC20SISO_Ant2_5580



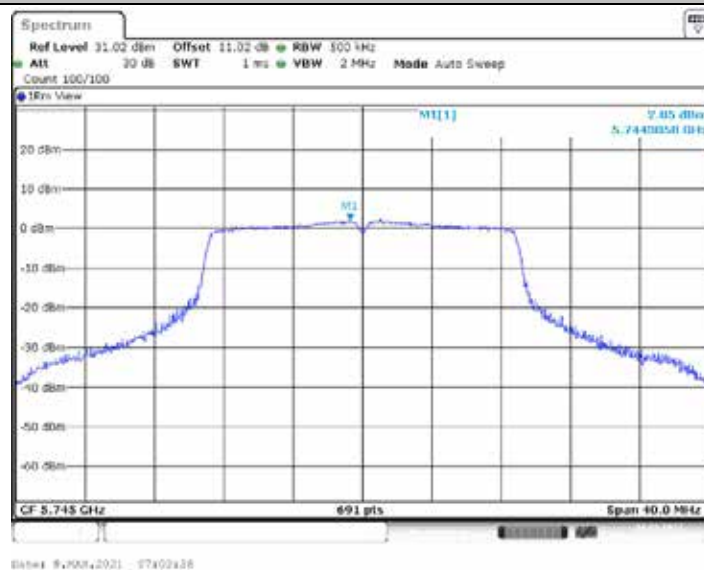
11AC20SISO_Ant1_5700



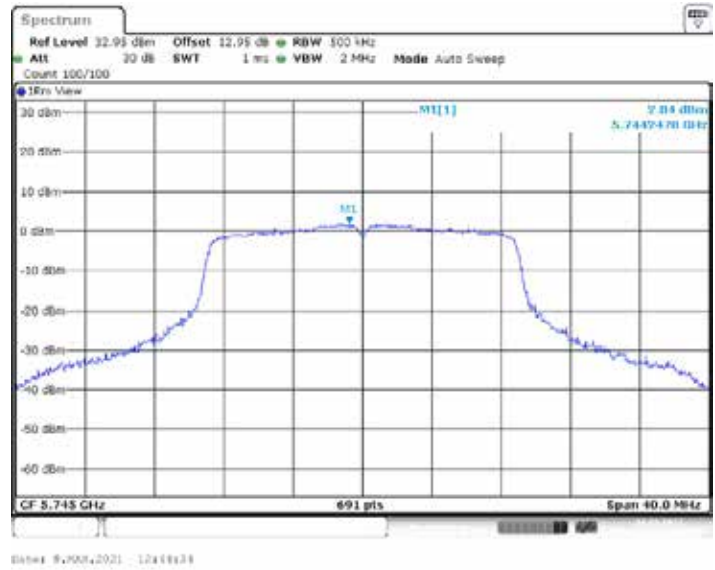
11AC20SISO_Ant2_5700



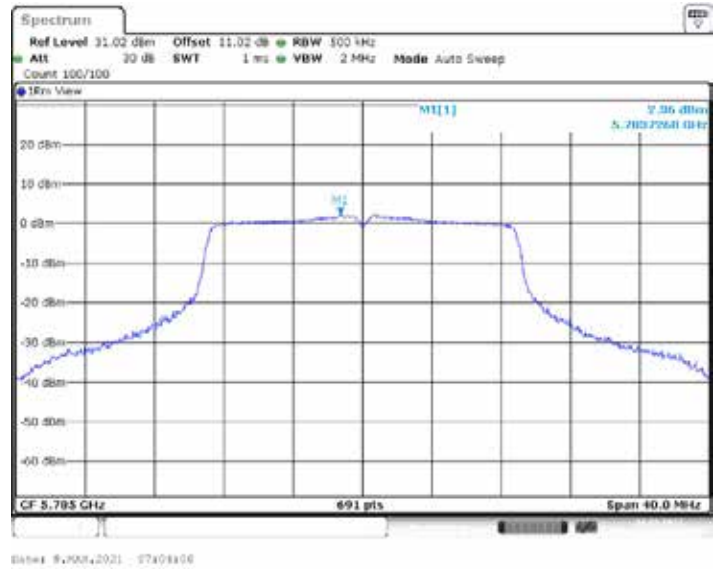
11AC20SISO Ant1 5745



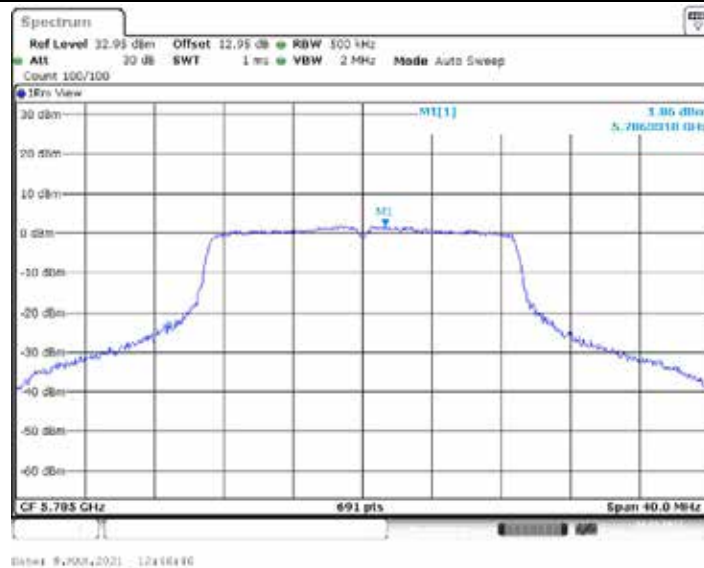
11AC20SISO_Ant2_5745



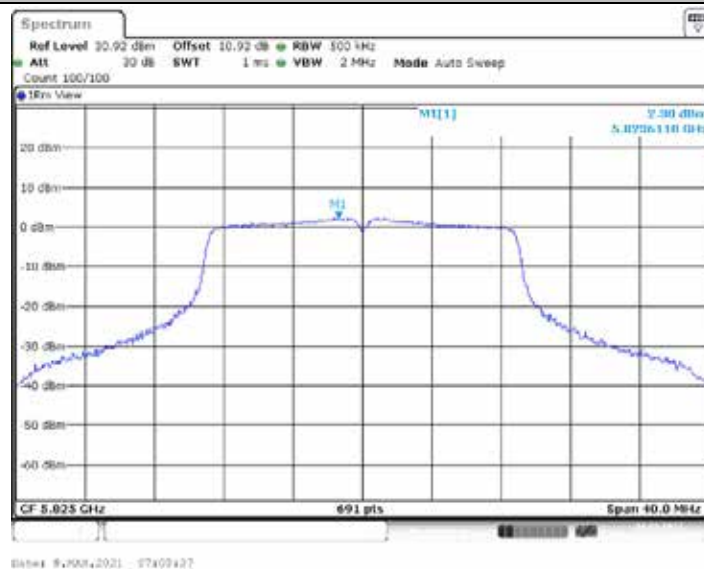
11AC20SISO_Ant1_5785



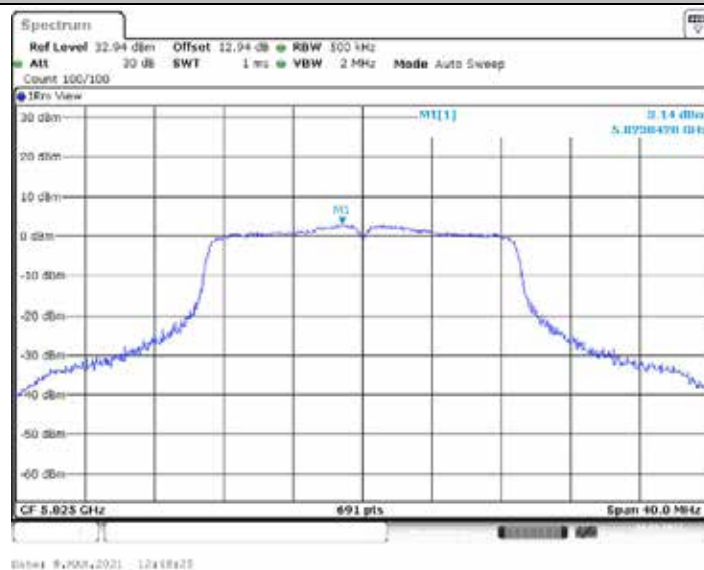
11AC20SISO_Ant2_5785

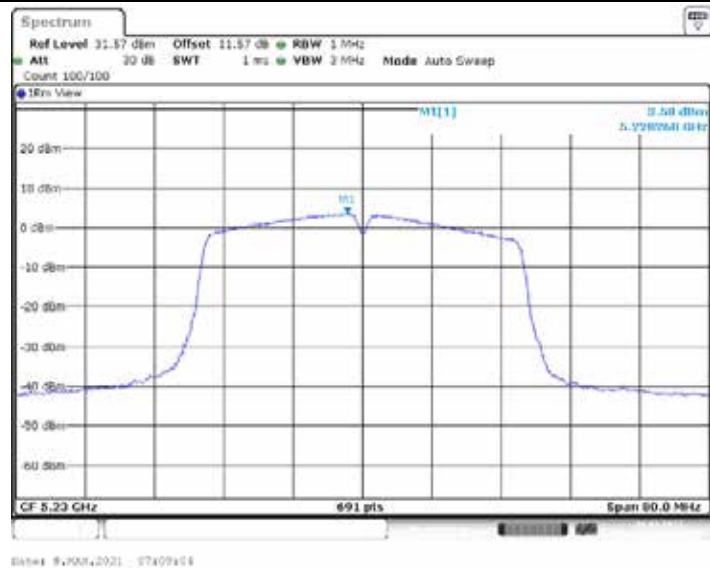


11AC20SISO_Ant1_5825

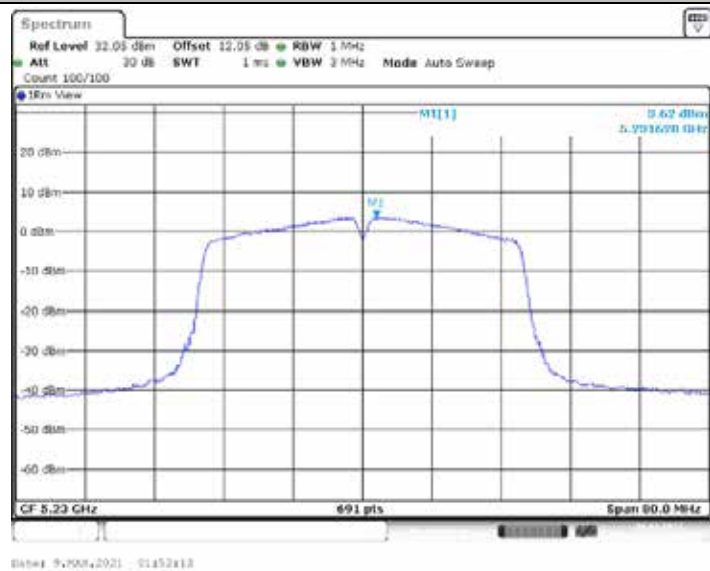


11AC20SISO_Ant2_5825

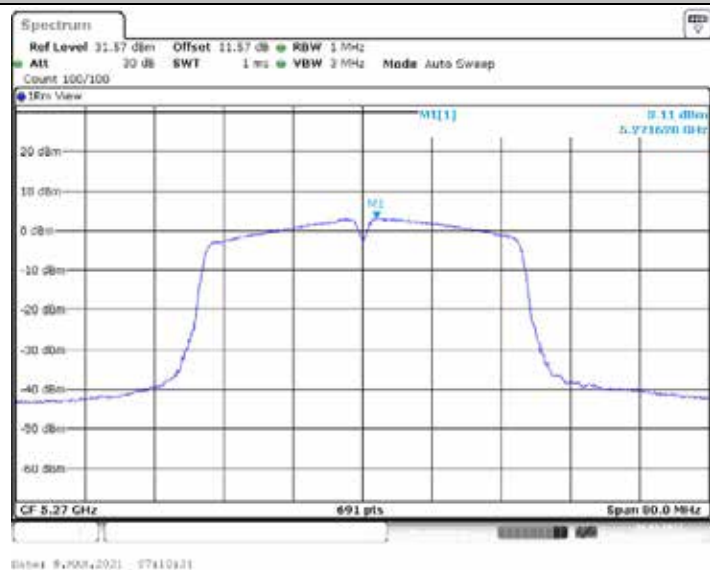




11AC40SISO_Ant2_5230



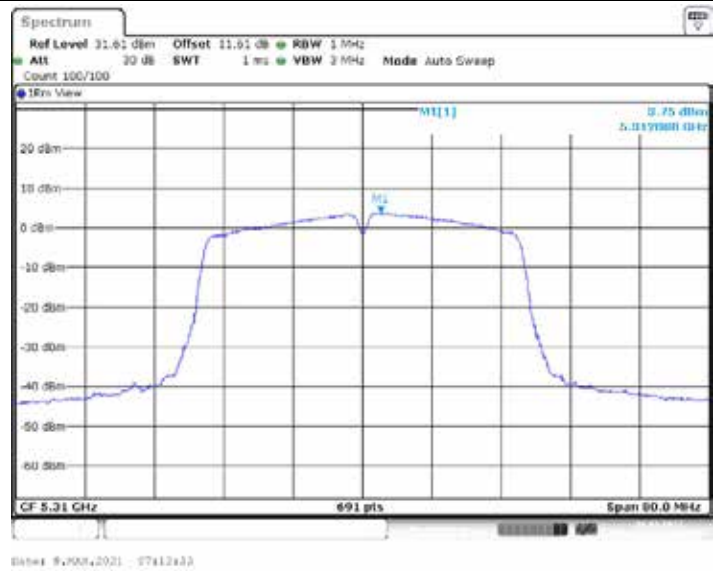
11AC40SISO_Ant1_5270



11AC40SISO_Ant2_5270



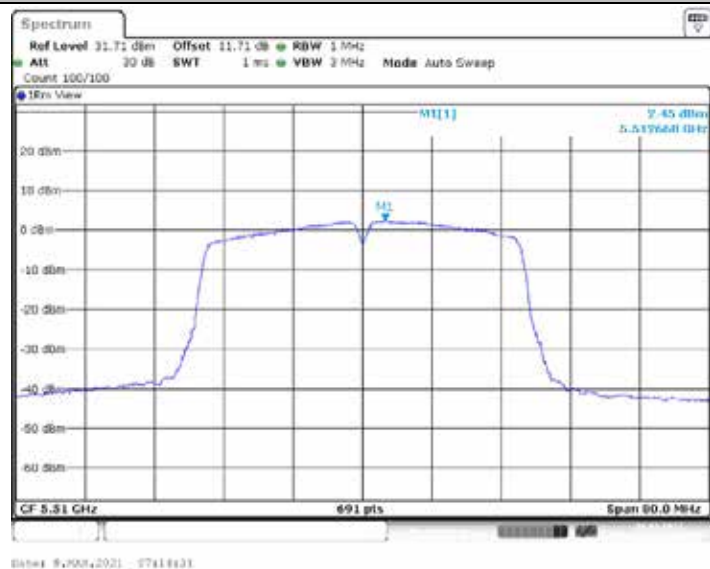
11AC40SISO_Ant1_5310



11AC40SISO_Ant2_5310



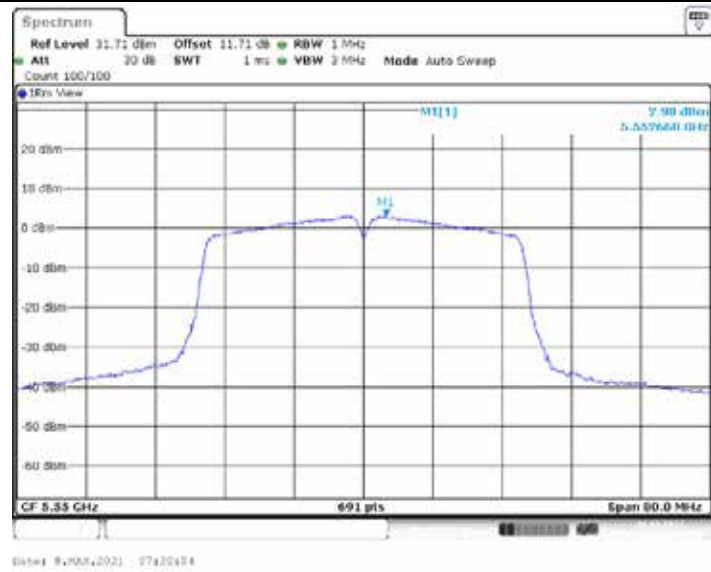
11AC40SISO_Ant1_5510



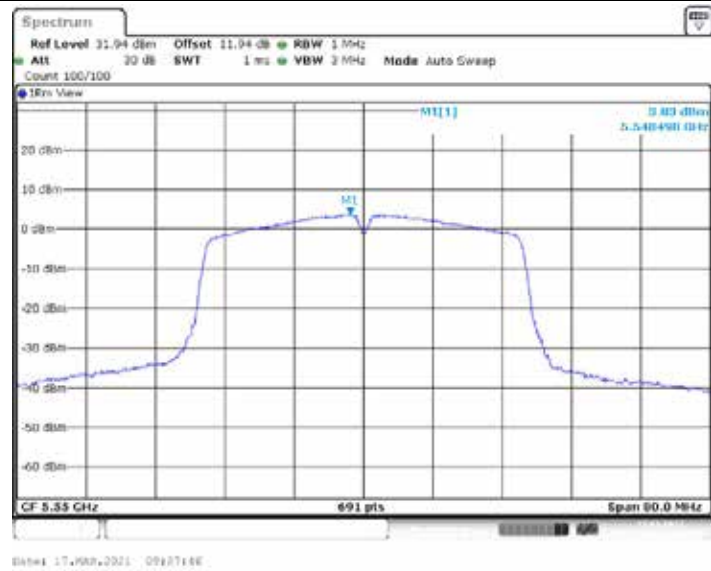
11AC40SISO_Ant2_5510



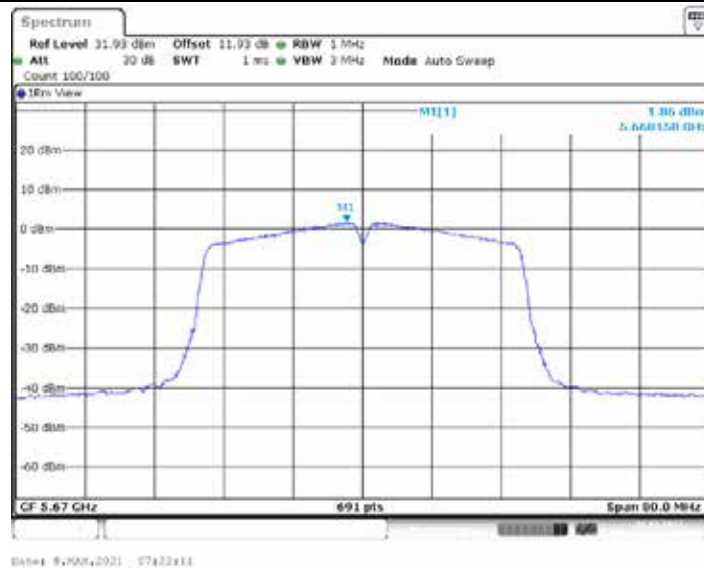
11AC40SISO_Ant1_5550



11AC40SISO_Ant2_5550



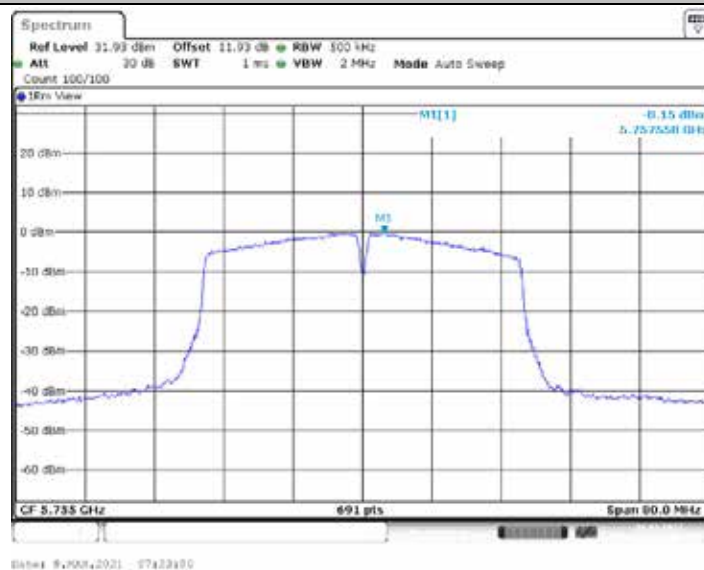
11AC40SISO_Ant1_5670



11AC40SISO_Ant2_5670



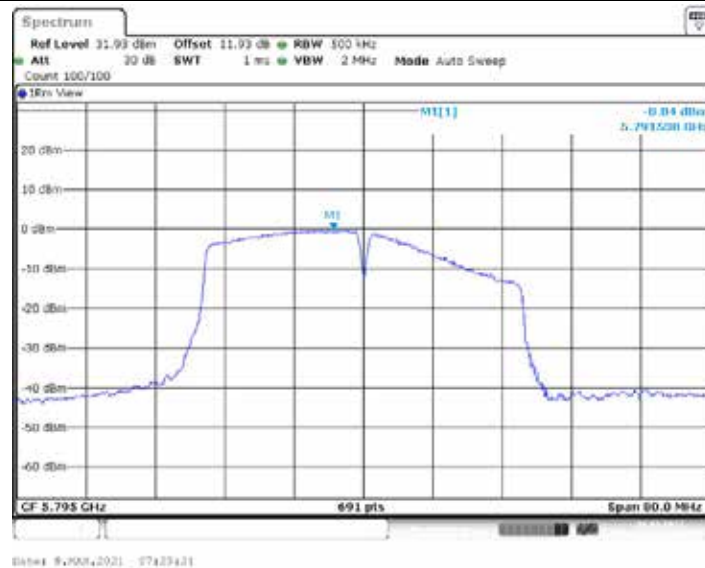
11AC40SISO_Ant1_5755



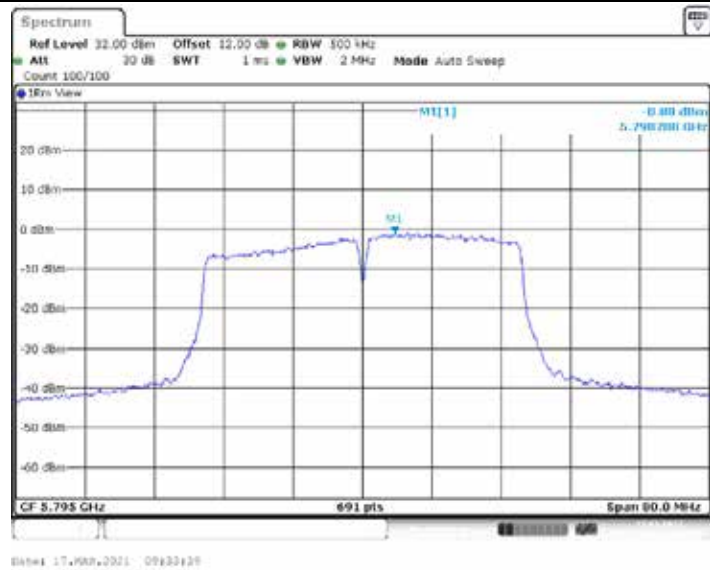
11AC40SISO_Ant2_5755



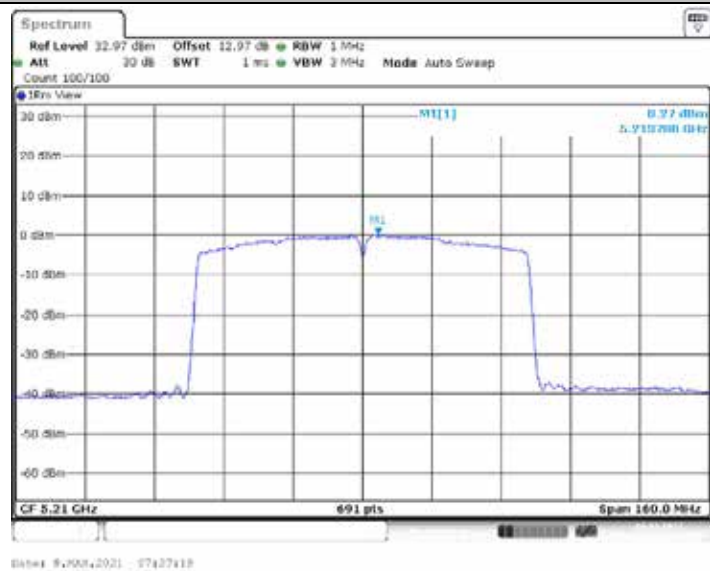
11AC40SISO_Ant1_5795



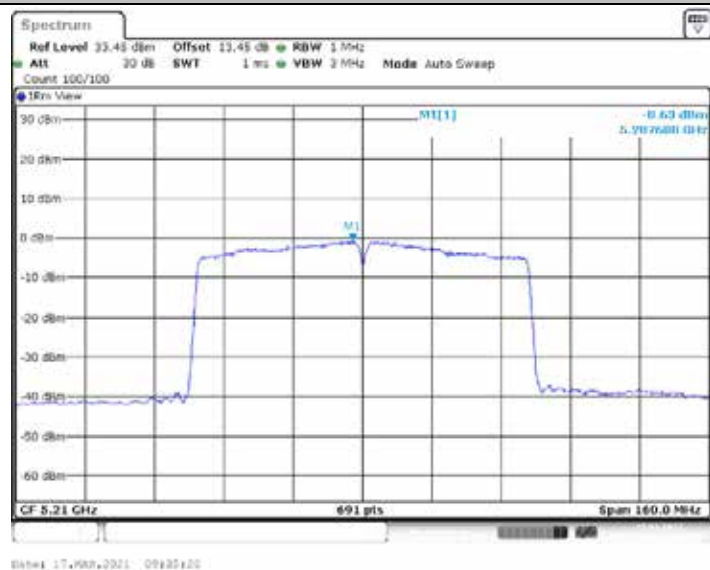
11AC40SISO_Ant2_5795



11AC80SISO_Ant1_5210



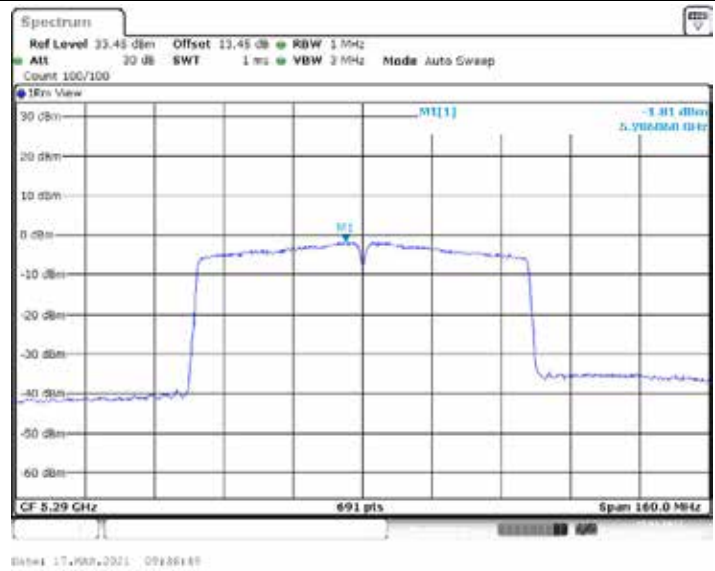
11AC80SISO_Ant2_5210



11AC80SISO_Ant1_5290



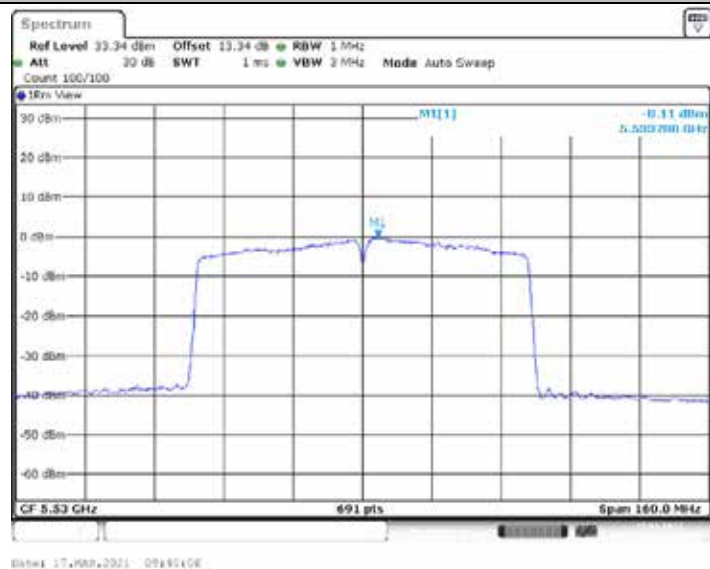
11AC80SISO_Ant2_5290



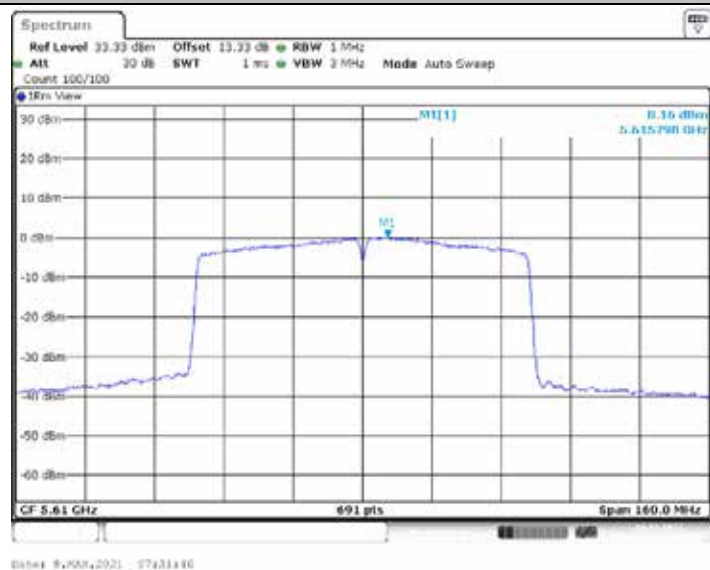
11AC80SISO_Ant1_5530



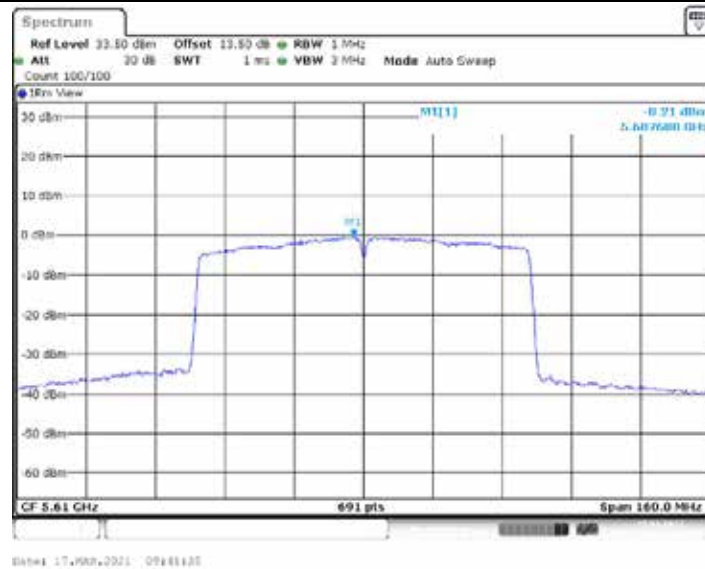
11AC80SISO_Ant2_5530



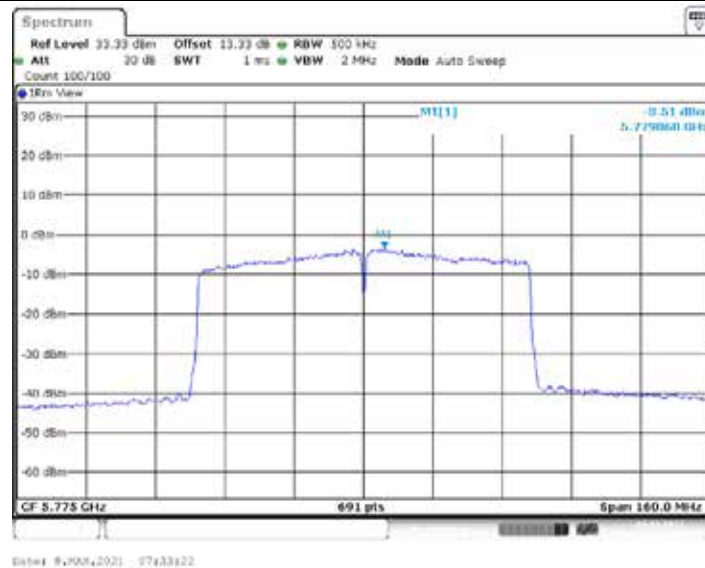
11AC80SISO_Ant1_5610



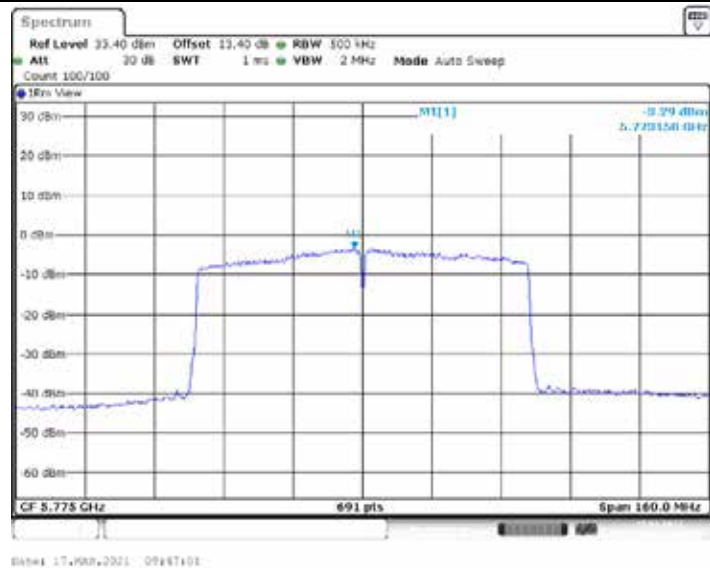
11AC80SISO_Ant2_5610



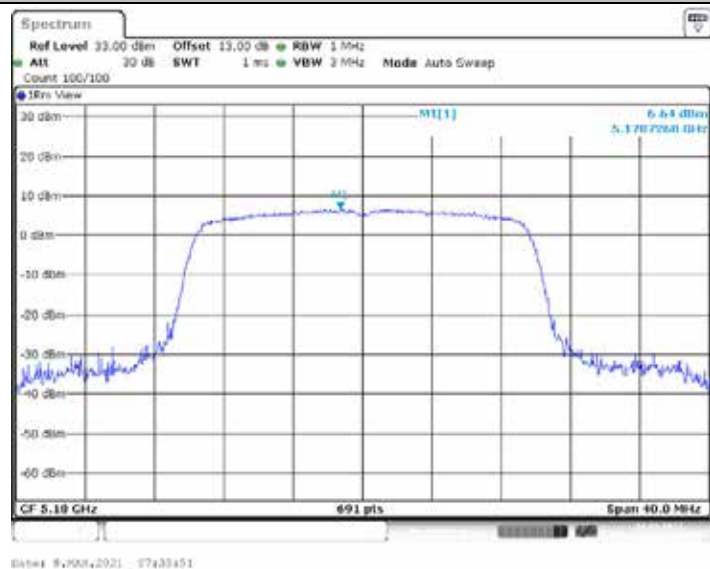
11AC80SISO_Ant1_5775



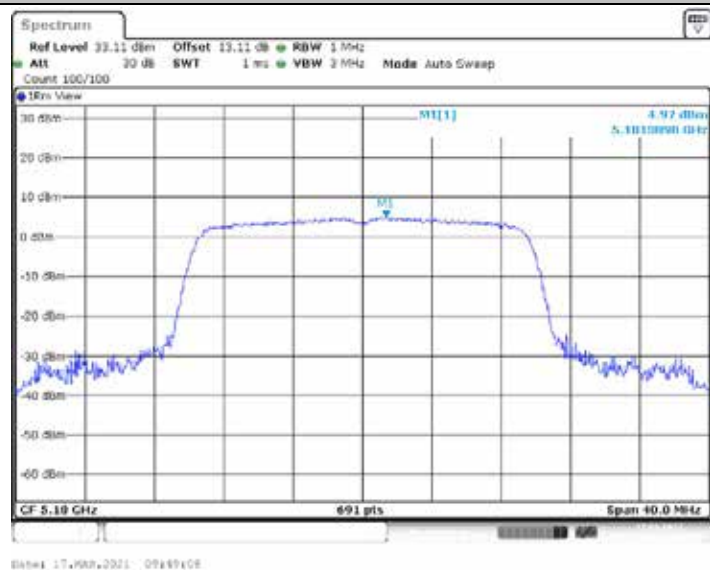
11AC80SISO_Ant2_5775



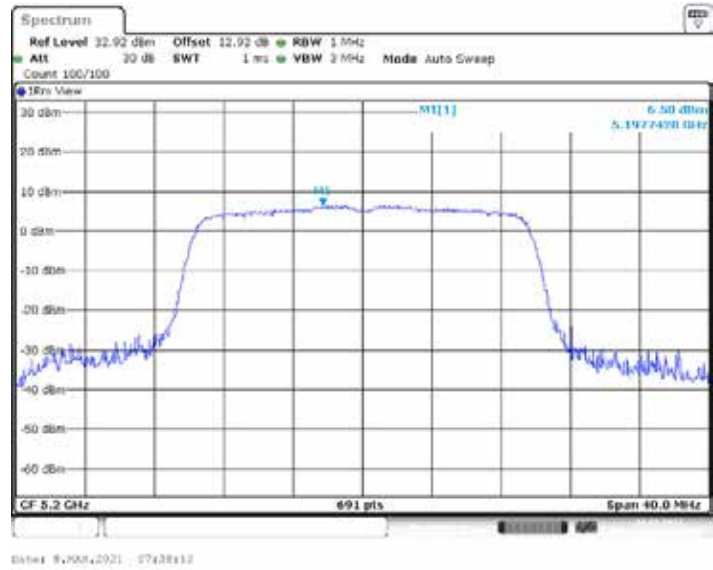
11AX20SISO_Ant1_5180



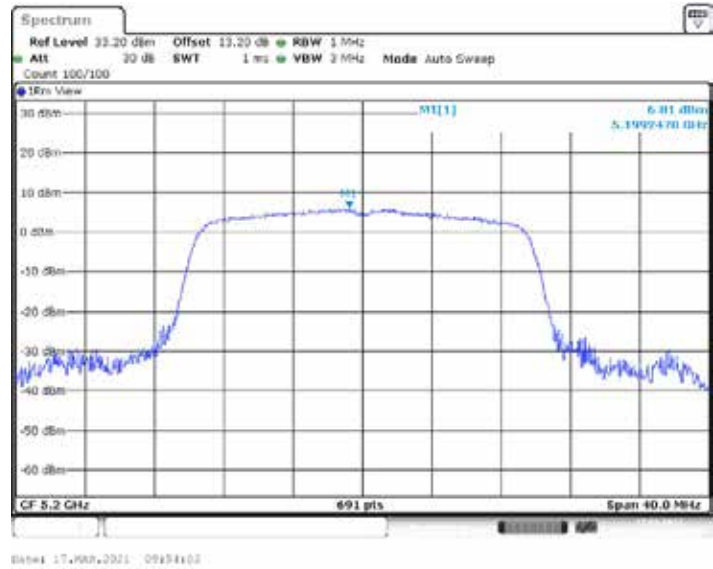
11AX20SISO_Ant2_5180



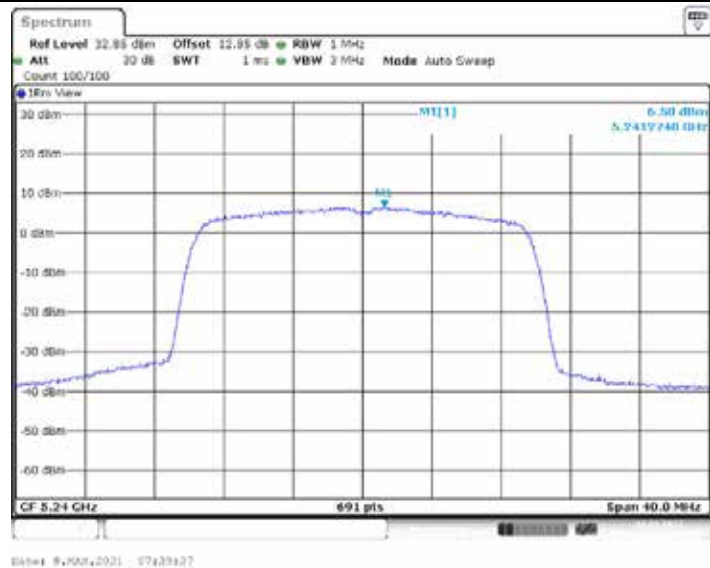
11AX20SISO_Ant1_5200



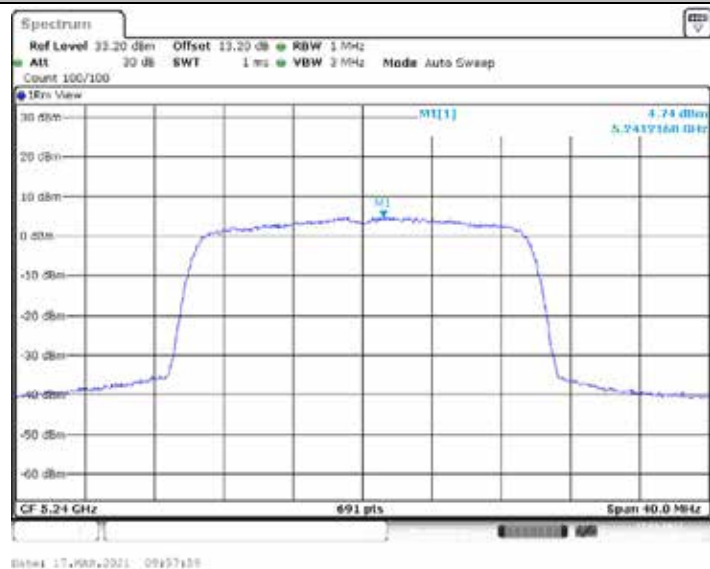
11AX20SISO_Ant2_5200



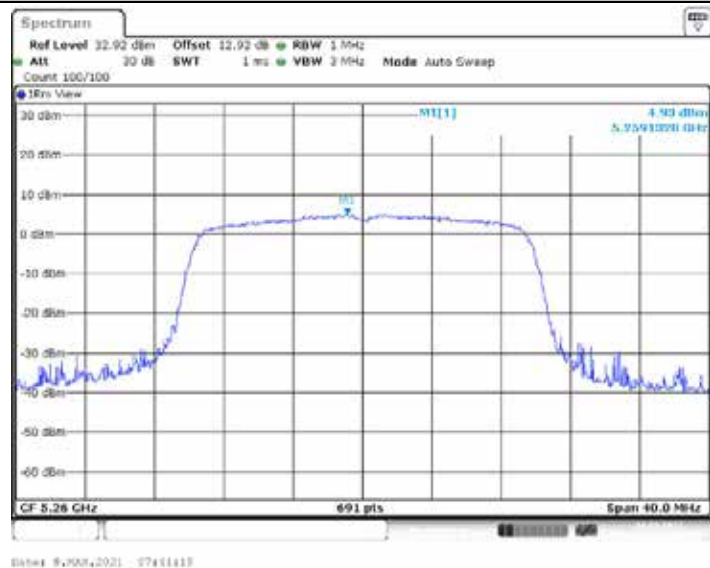
11AX20SISO_Ant1_5240



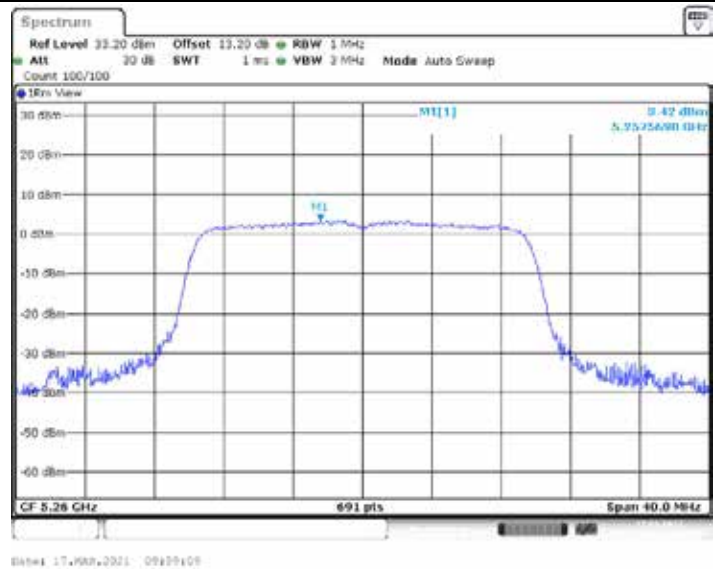
11AX20SISO_Ant2_5240



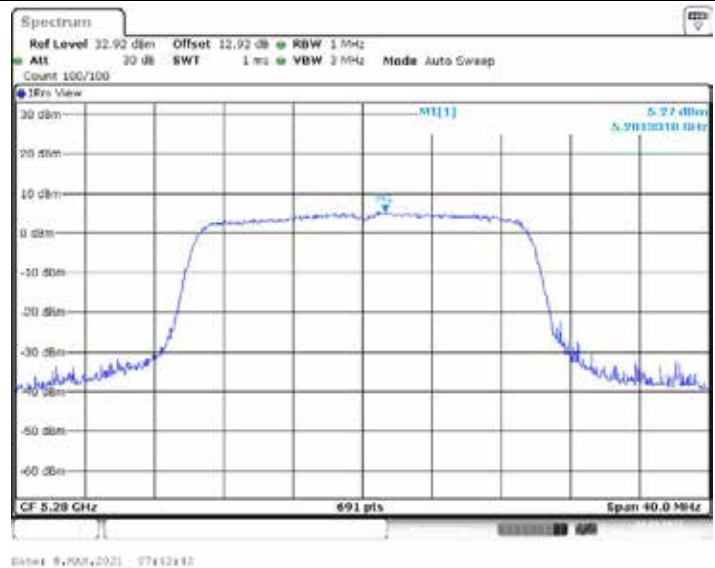
11AX20SISO_Ant1_5260



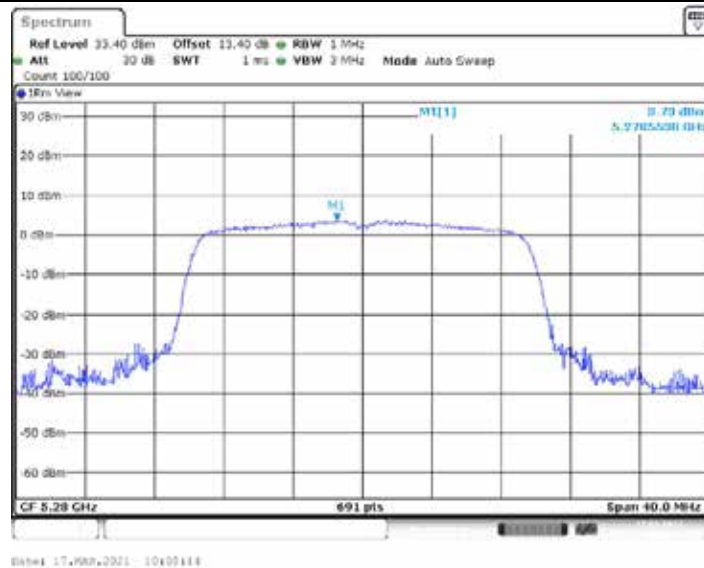
11AX20SISO_Ant2_5260



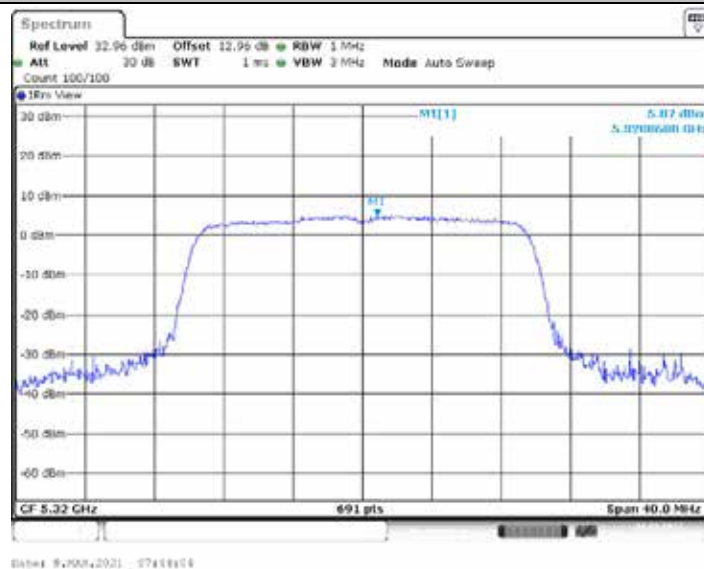
11AX20SISO_Ant1_5280



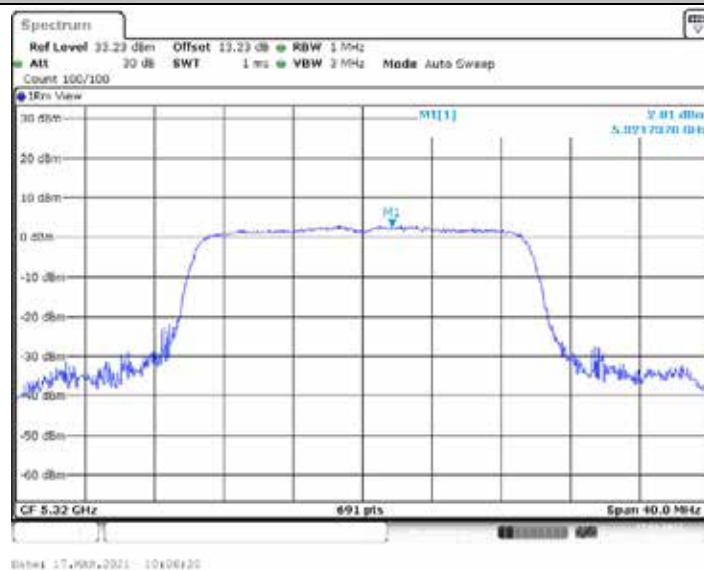
11AX20SISO_Ant2_5280



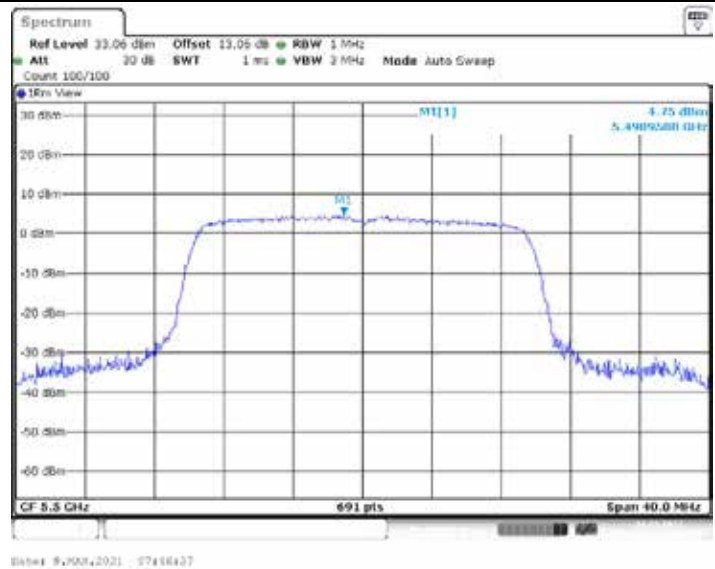
11AX20SISO_Ant1_5320



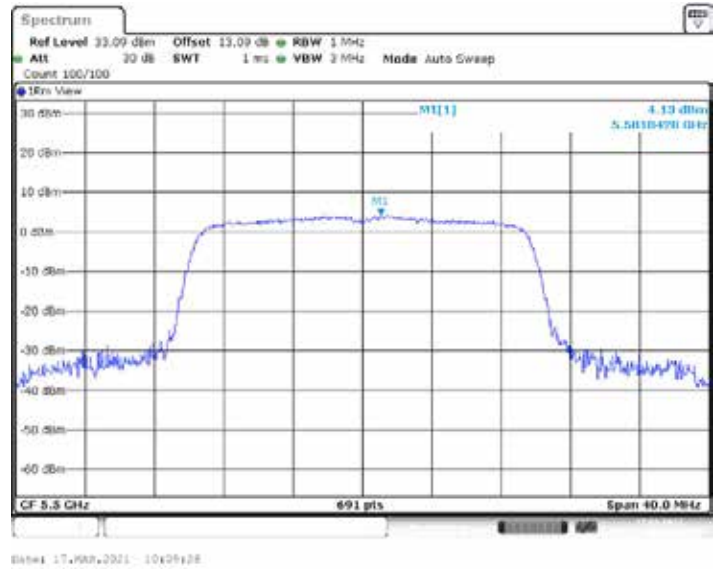
11AX20SISO_Ant2_5320



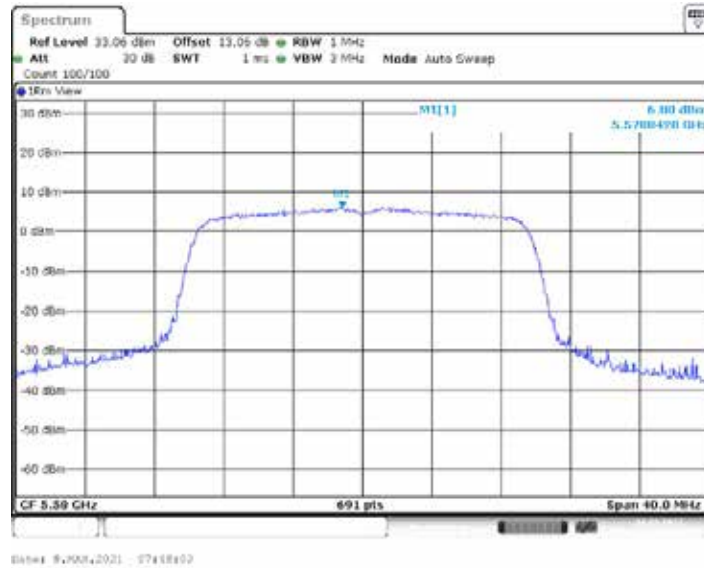
11AX20SISO_Ant1_5500



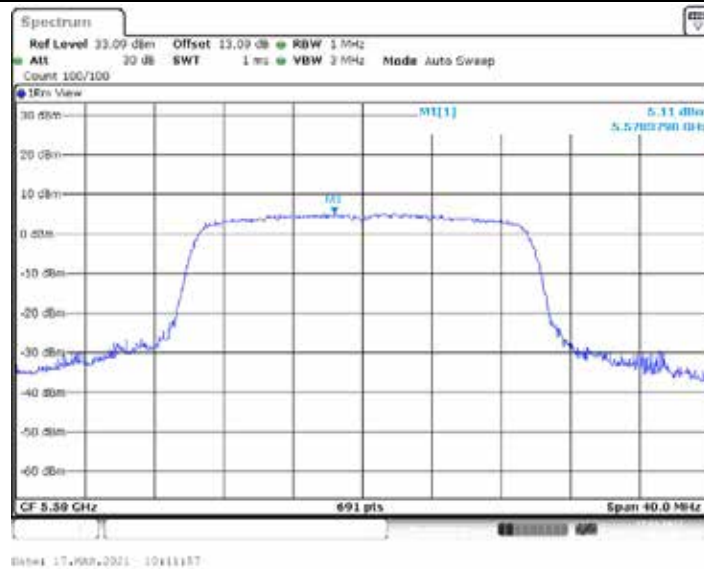
11AX20SISO_Ant2_5500



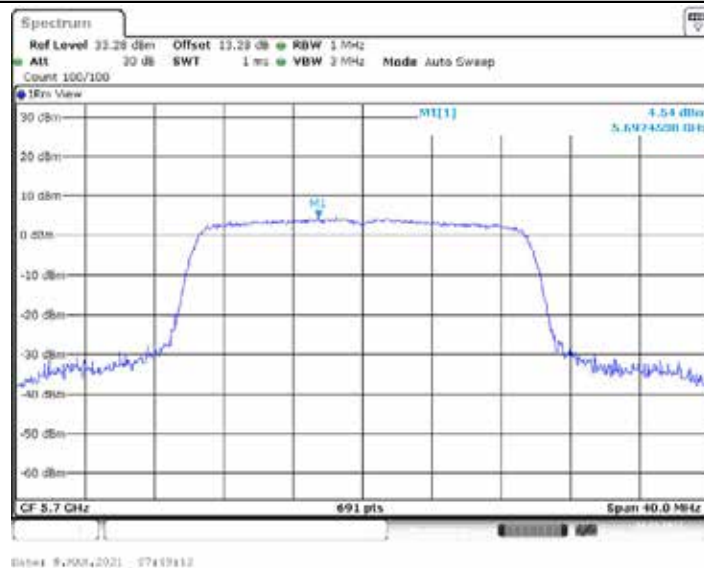
11AX20SISO_Ant1_5580



11AX20SISO_Ant2_5580



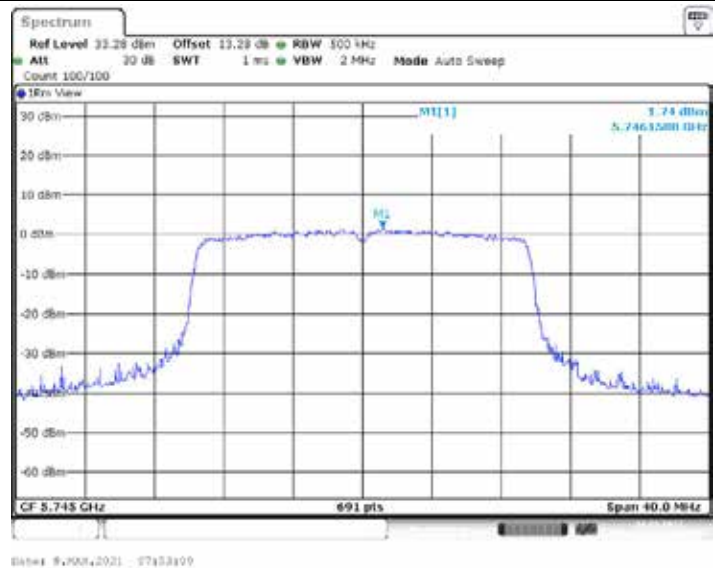
11AX20SISO_Ant1_5700



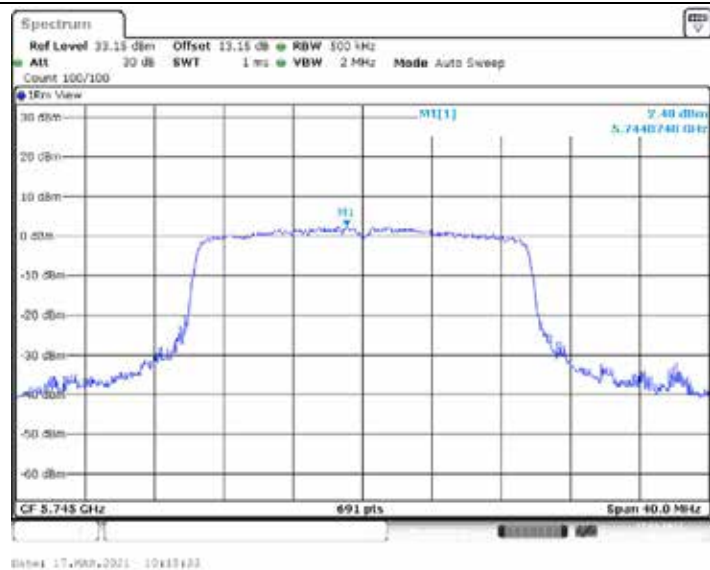
11AX20SISO_Ant2_5700



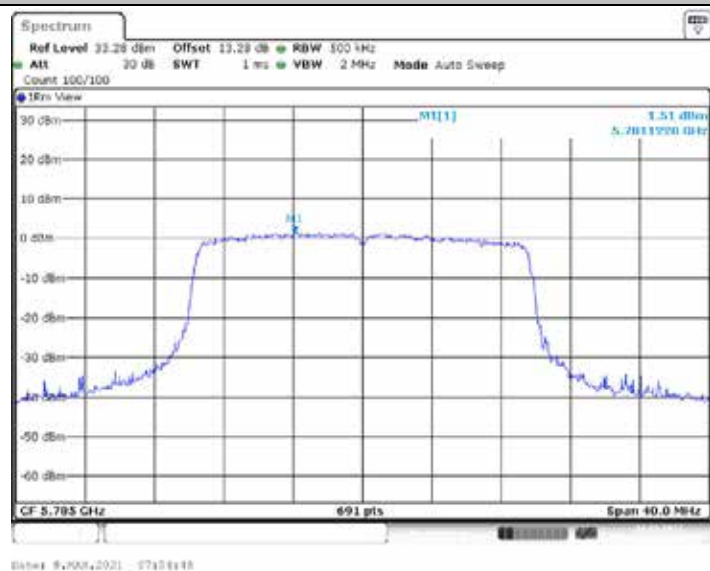
11AX20SISO_Ant1_5745



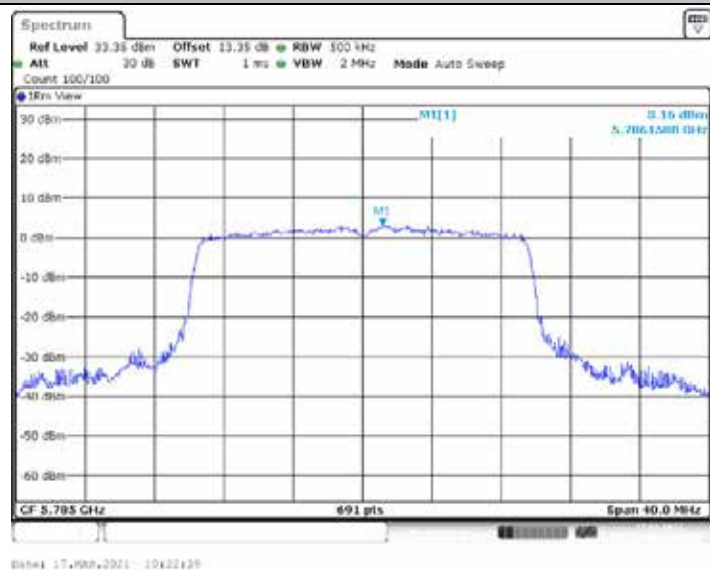
11AX20SISO_Ant2_5745



11AX20SISO_Ant1_5785



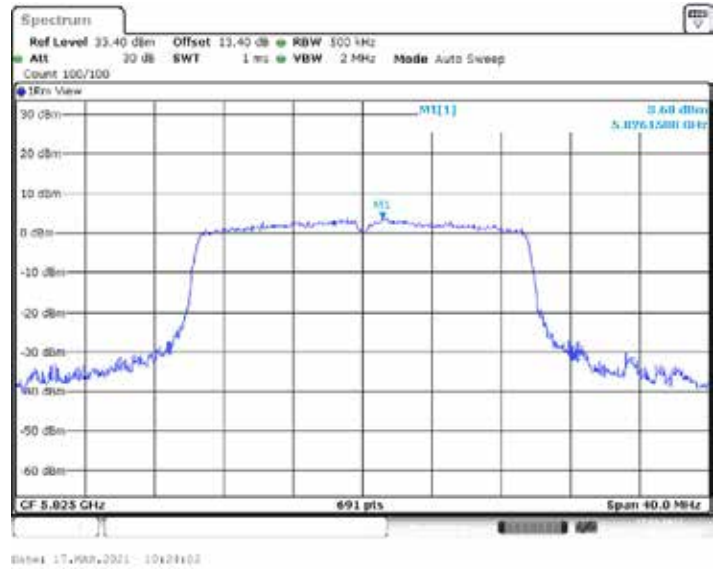
11AX20SISO_Ant2_5785



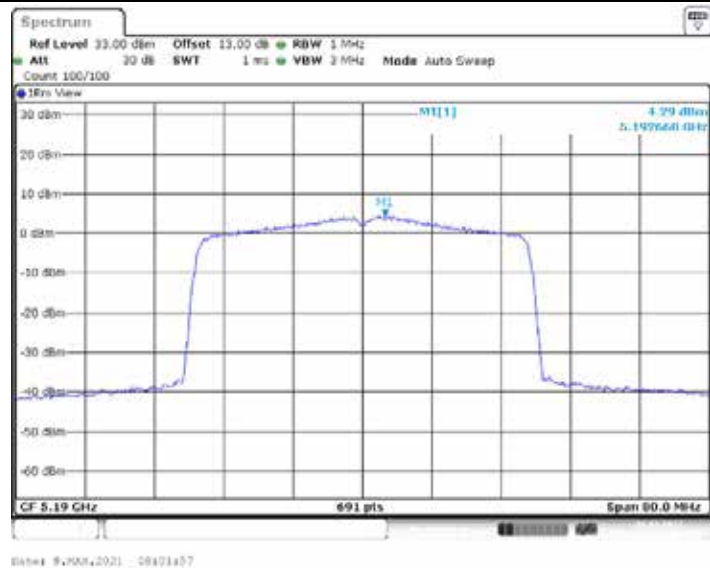
11AX20SISO_Ant1_5825



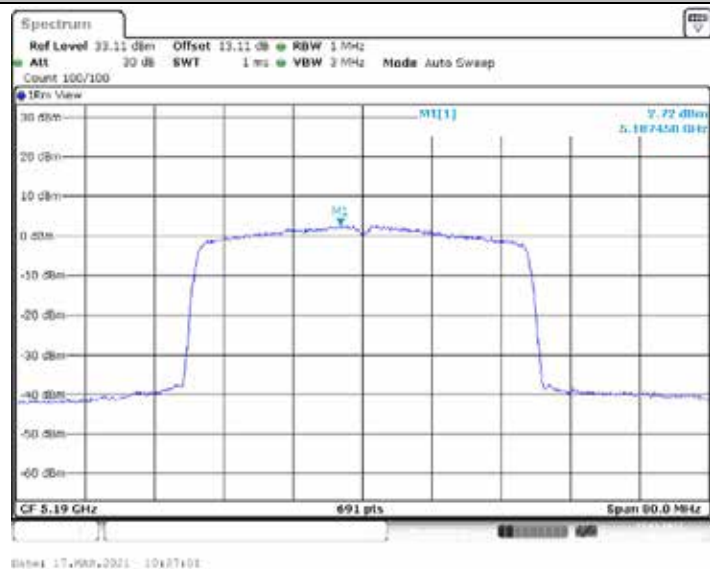
11AX20SISO_Ant2_5825



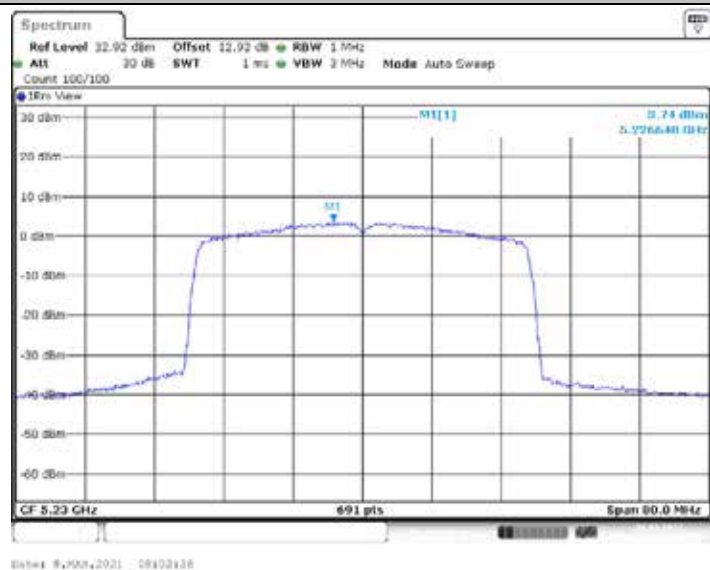
11AX40SISO_Ant1_5190



11AX40ISO_Ant2_5190



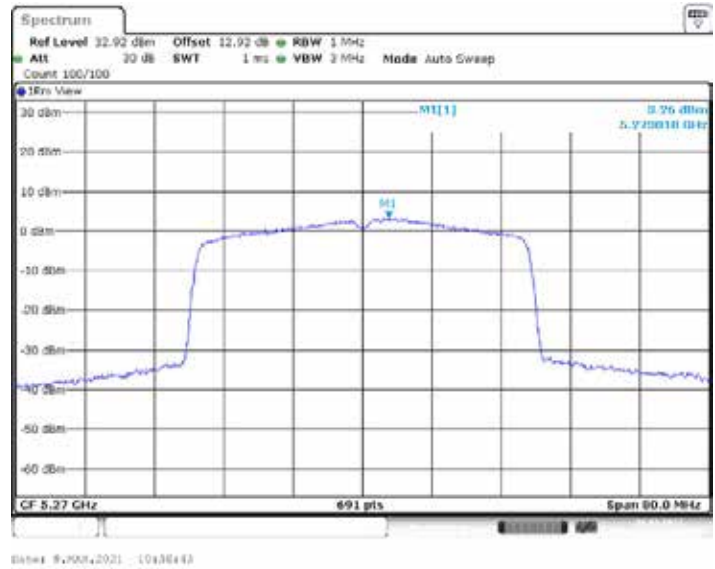
11AX40ISO_Ant1_5230



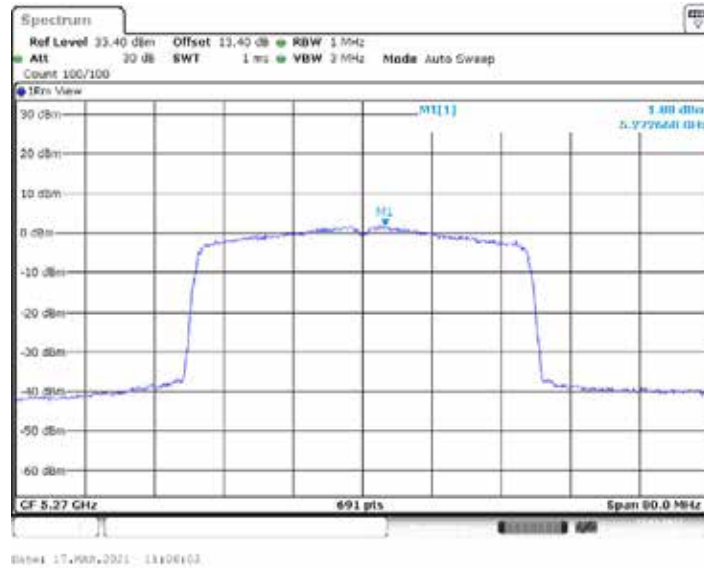
11AX40SISO_Ant2_5230



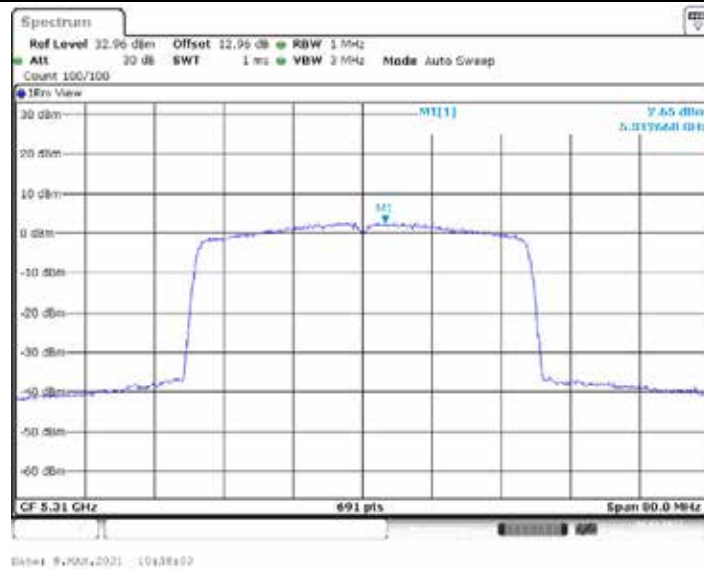
11AX40SISO_Ant1_5270



11AX40SISO_Ant2_5270



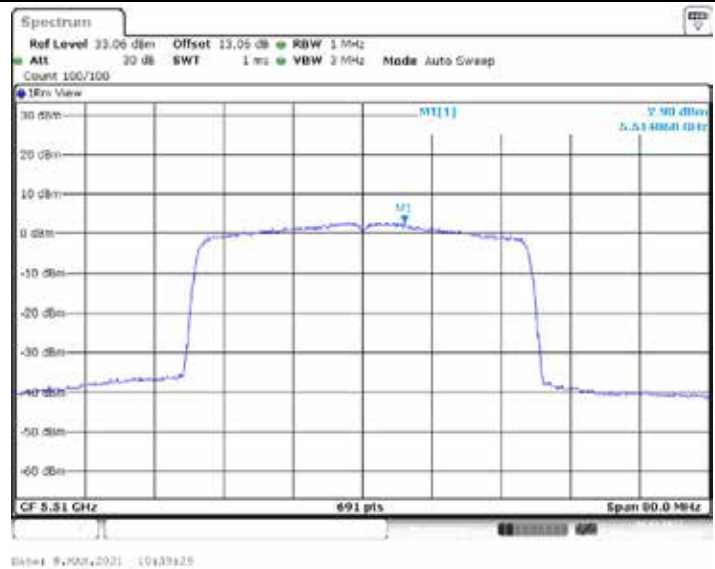
11AX40SISO_Ant1_5310



11AX40SISO_Ant2_5310



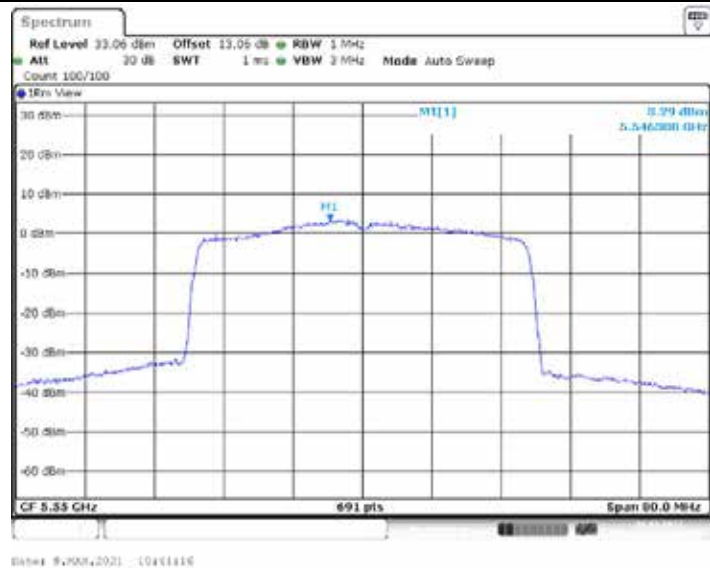
11AX40SISO_Ant1_5510



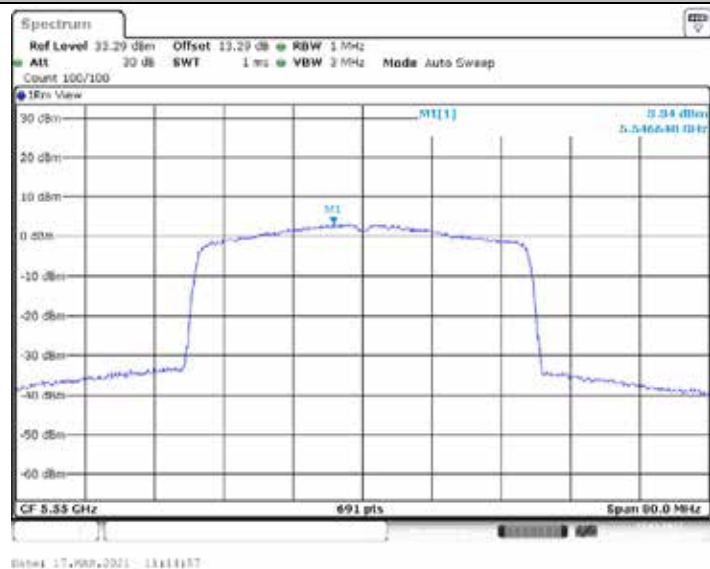
11AX40SISO_Ant2_5510



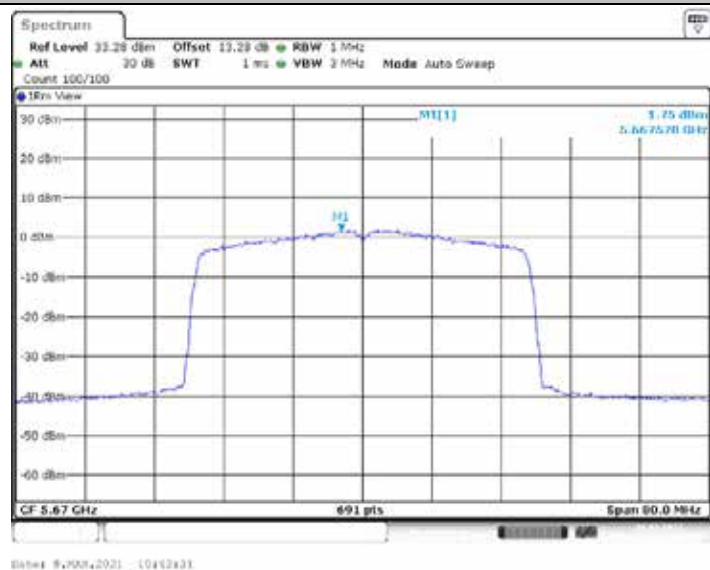
11AX40SISO_Ant1_5550



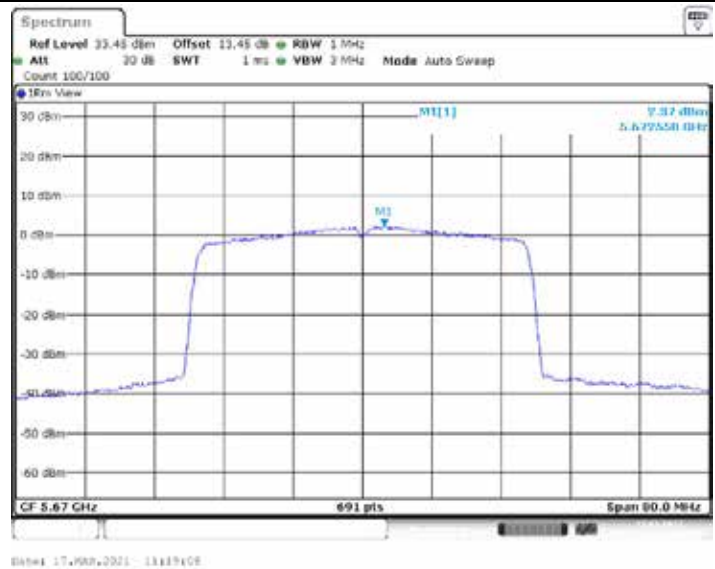
11AX40ISO_Ant2_5550



11AX40ISO_Ant1_5670



11AX40SISO_Ant2_5670



11AX40SISO_Ant1_5755



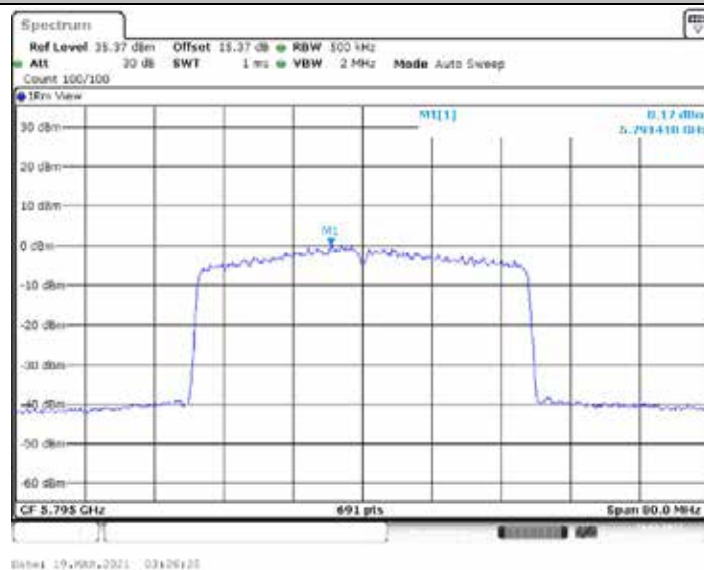
11AX40SISO_Ant2_5755



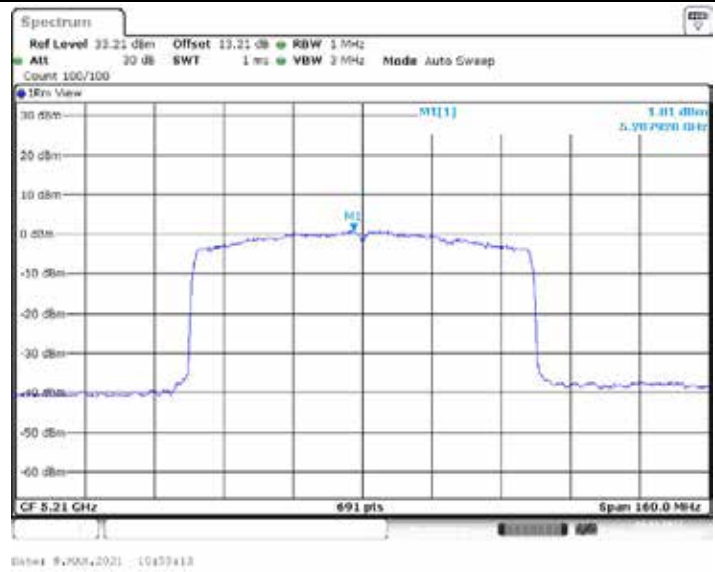
11AX40ISO_Ant1_5795



11AX40ISO_Ant2_5795



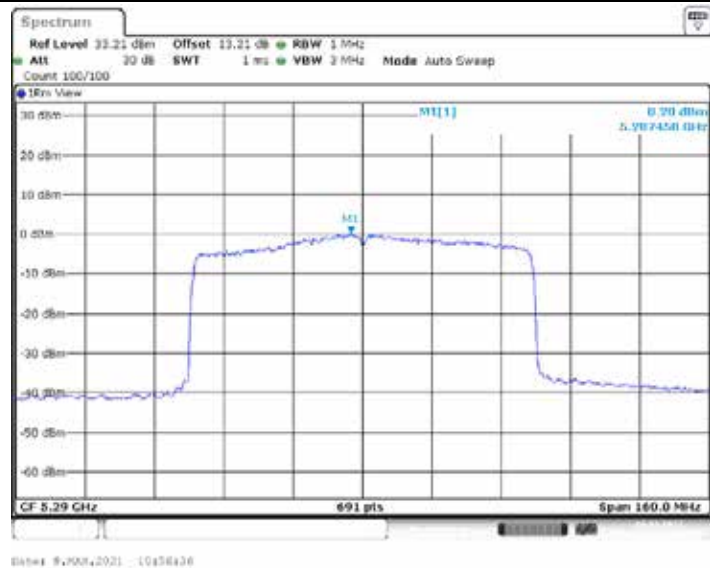
11AX80SISO_Ant1_5210



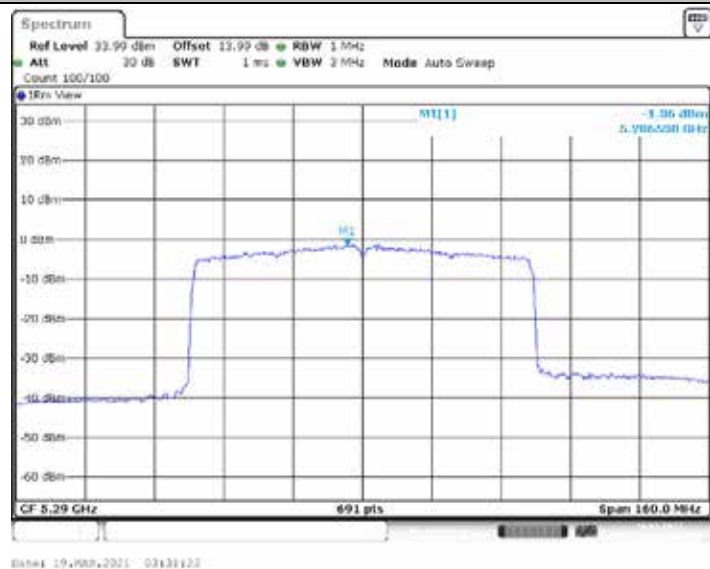
11AX80SISO_Ant2_5210



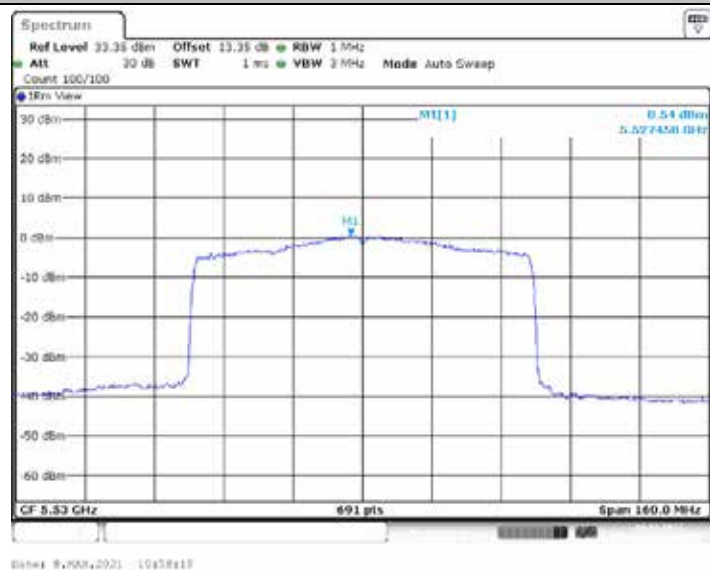
11AX80SISO_Ant1_5290



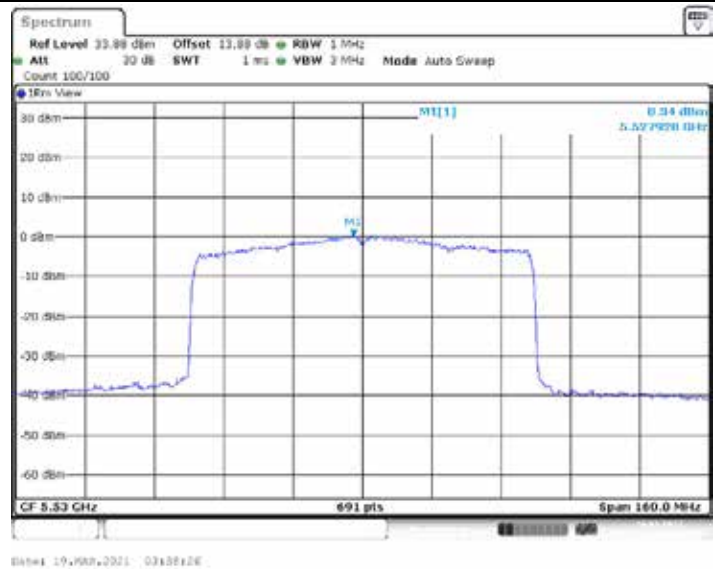
11AX80ISO_Ant2_5290



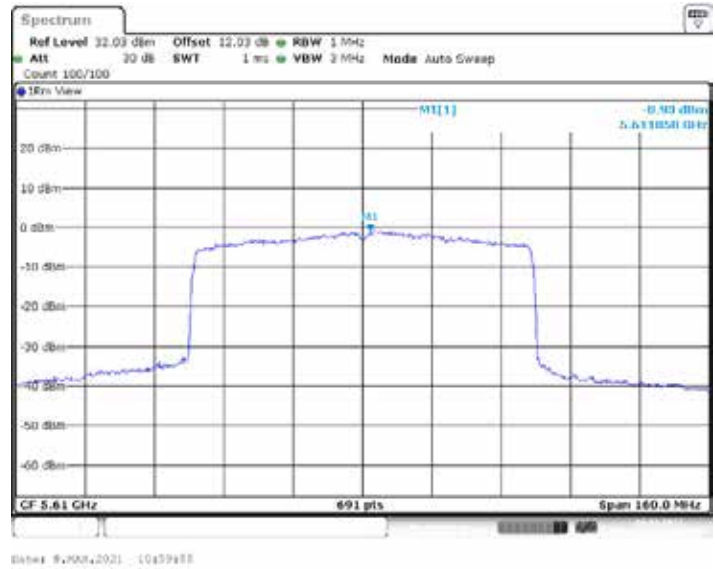
11AX80ISO_Ant1_5530



11AX80SISO_Ant2_5530



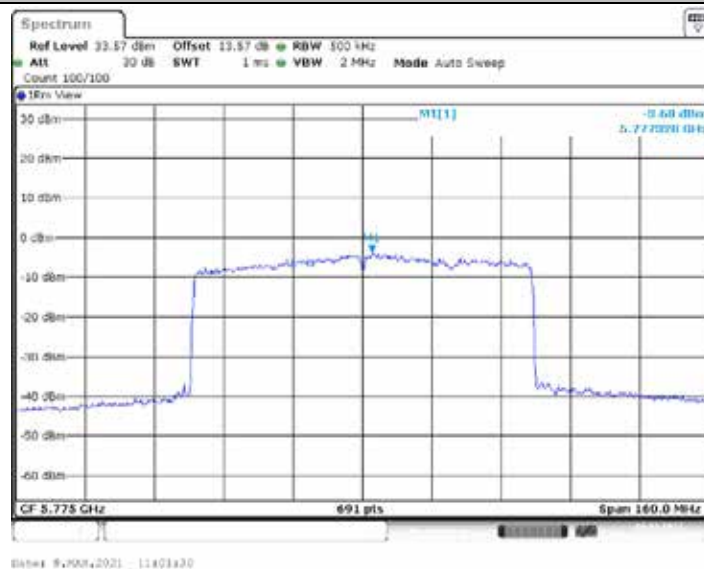
11AX80SISO_Ant1_5610



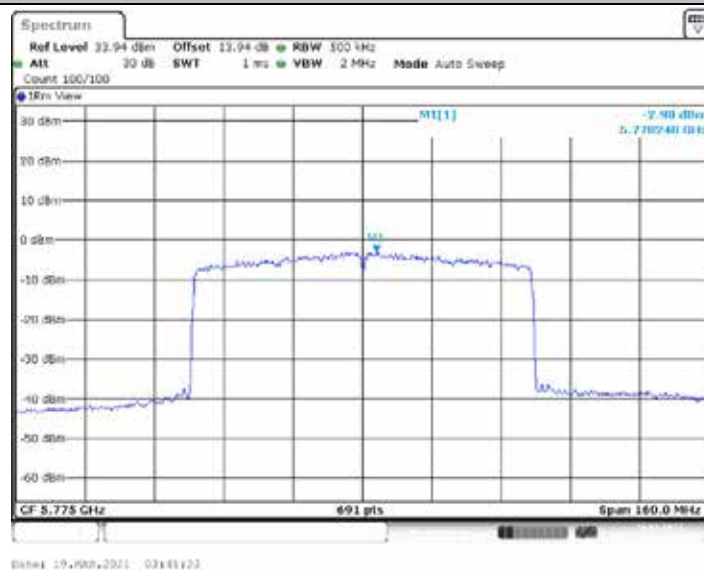
11AX80SISO_Ant2_5610



11AX80ISO_Ant1_5775



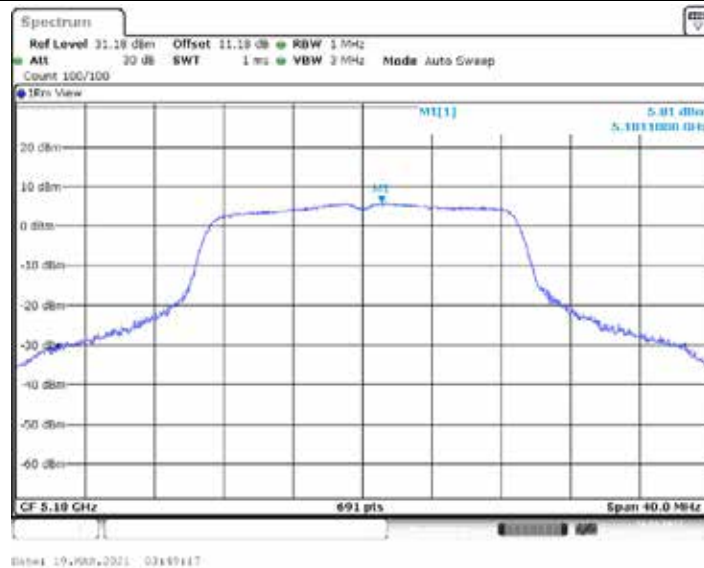
11AX80ISO_Ant2_5775



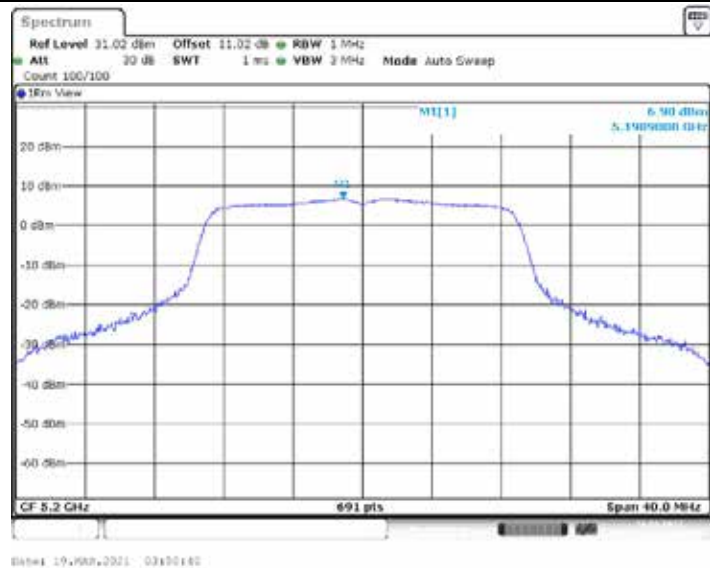
11N20MIMO_Ant1_5180



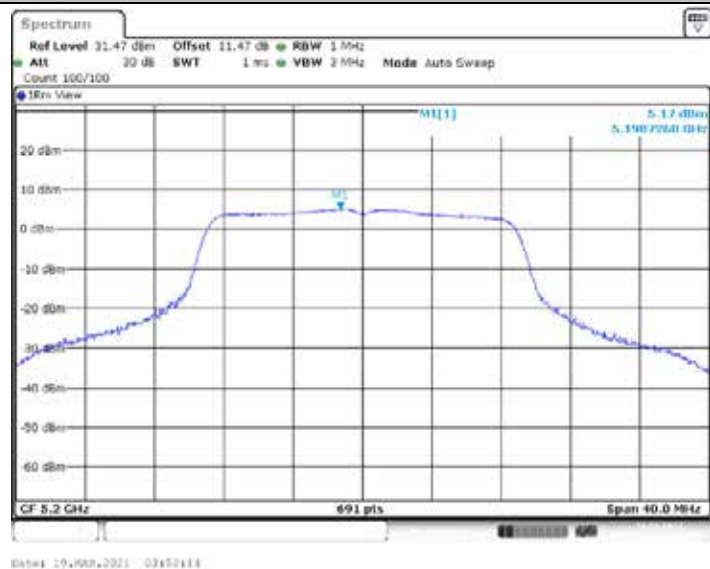
11N20MIMO_Ant2_5180



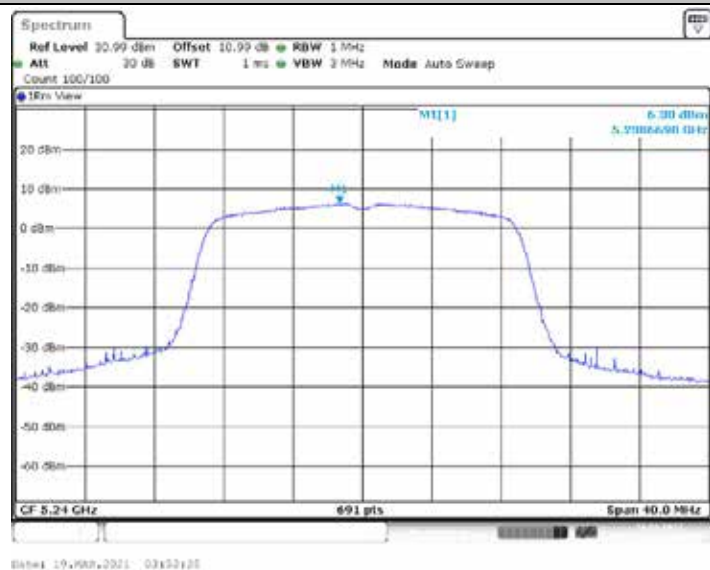
11N20MIMO_Ant1_5200



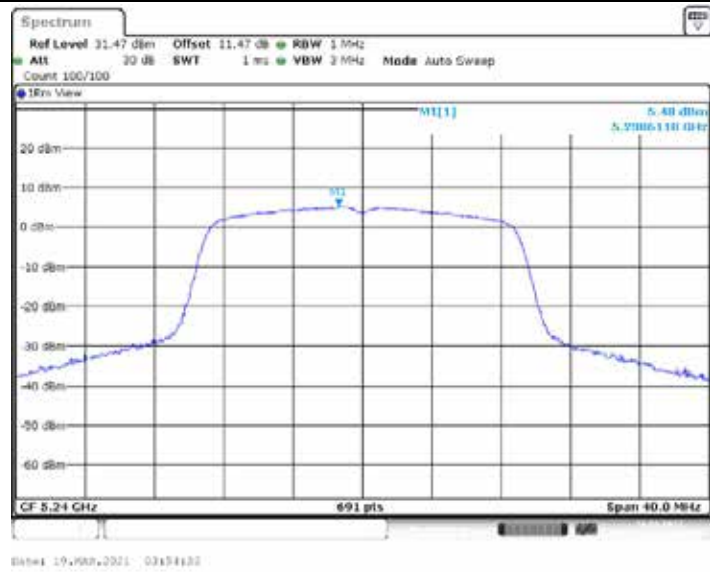
11N20MIMO_Ant2_5200



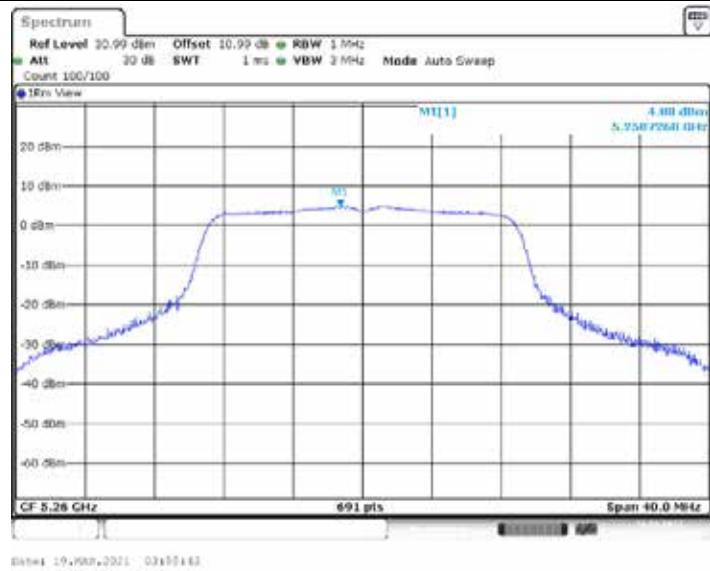
11N20MIMO_Ant1_5240



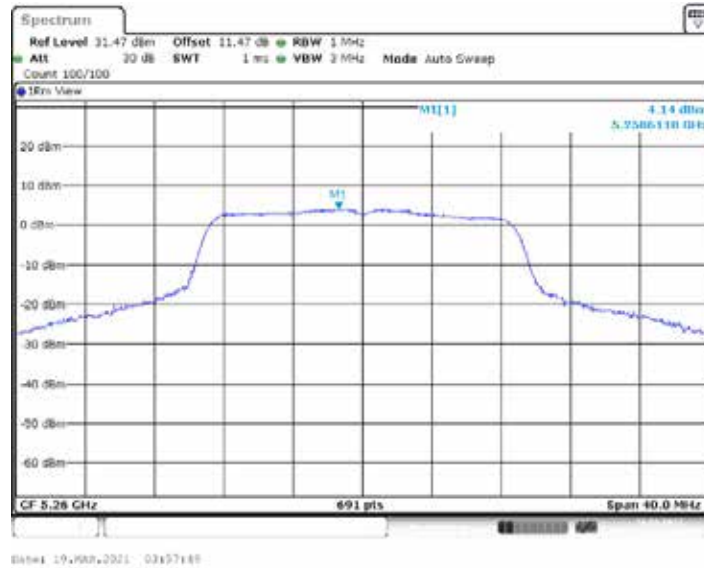
11N20MIMO_Ant2_5240



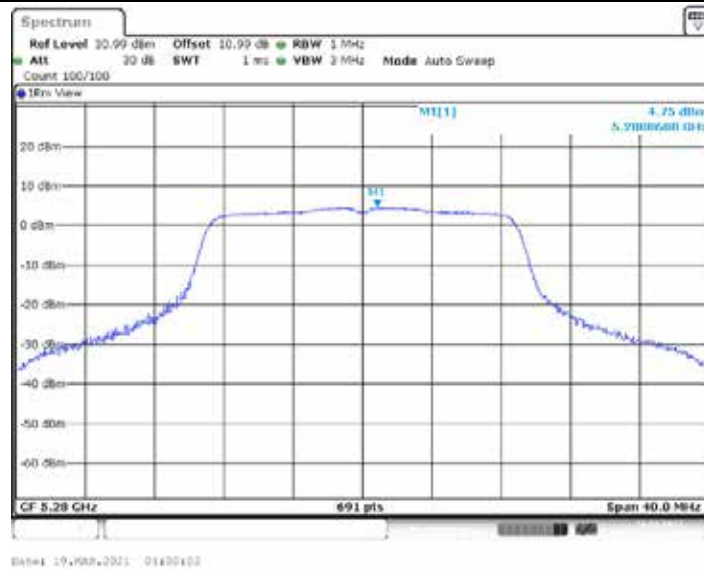
11N20MIMO_Ant1_5260



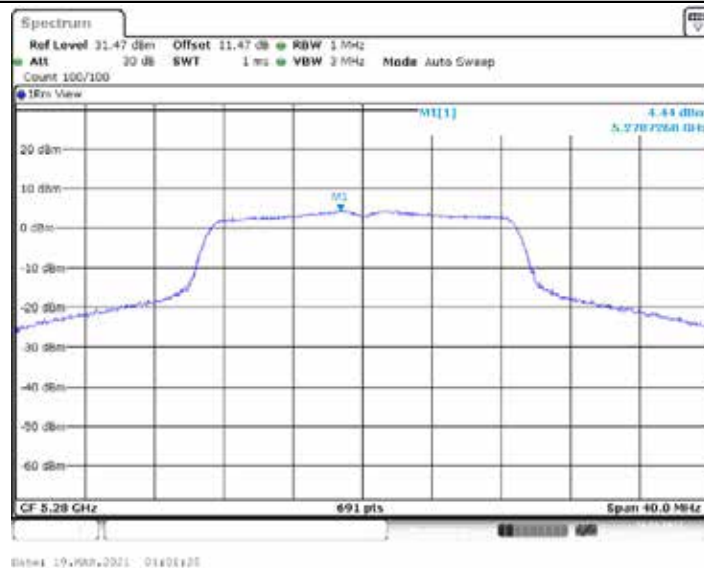
11N20MIMO_Ant2_5260



11N20MIMO_Ant1_5280



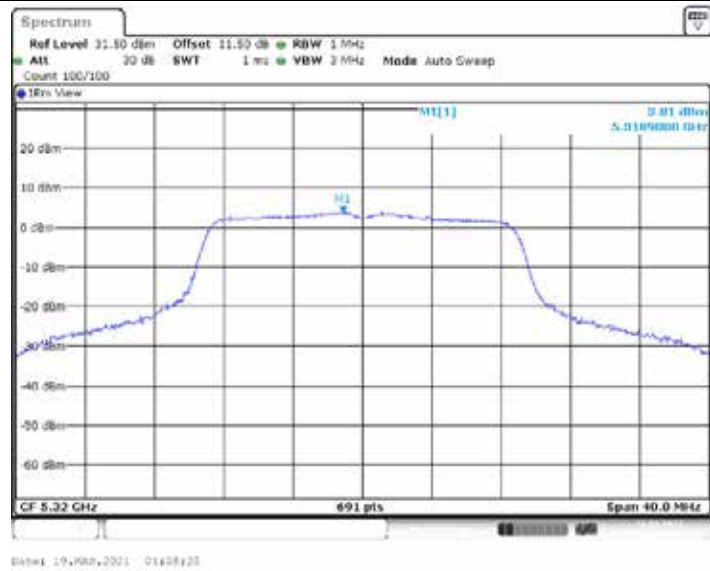
11N20MIMO_Ant2_5280



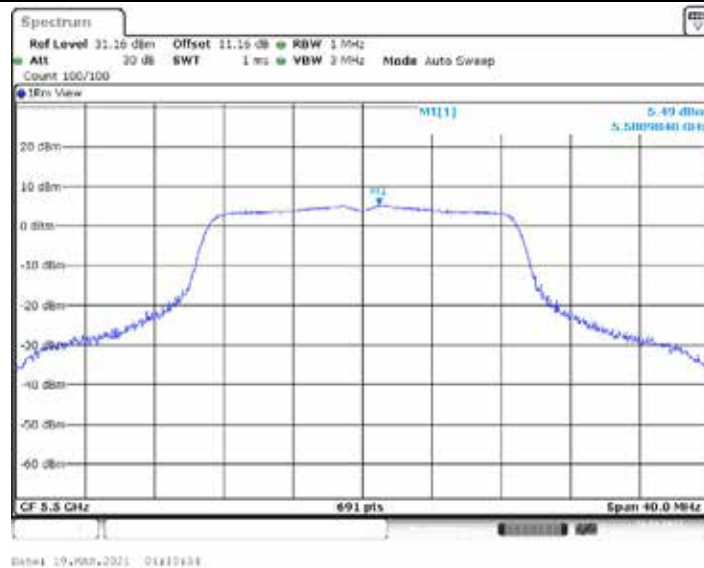
11N20MIMO_Ant1_5320



11N20MIMO_Ant2_5320



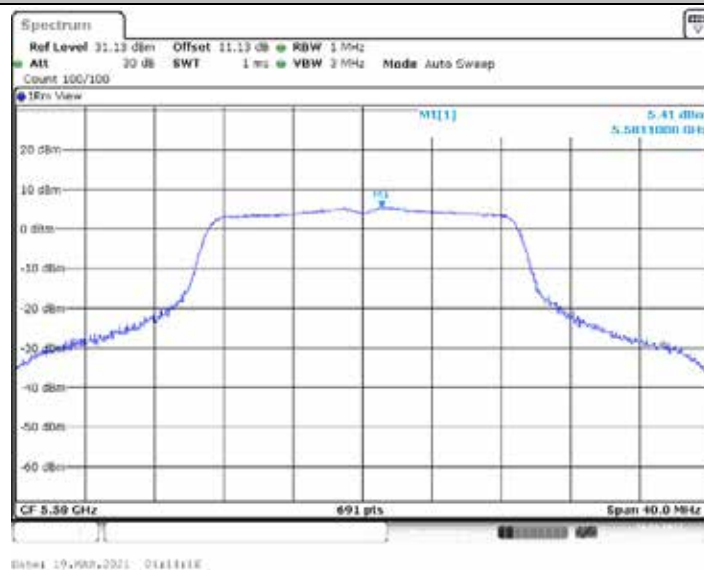
11N20MIMO_Ant1_5500



11N20MIMO_Ant2_5500



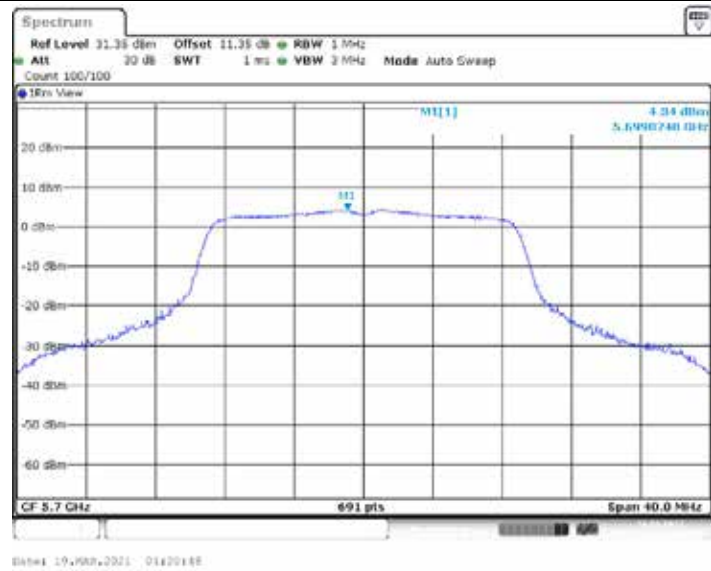
11N20MIMO_Ant1_5580



11N20MIMO_Ant2_5580



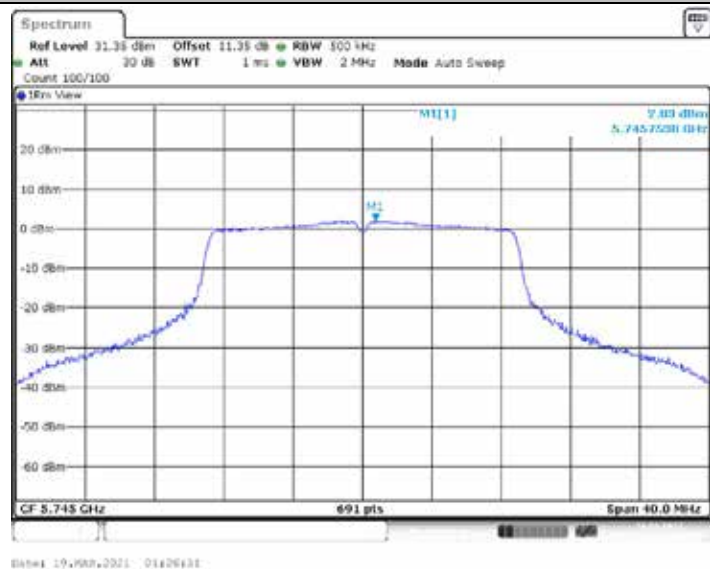
11N20MIMO_Ant1_5700



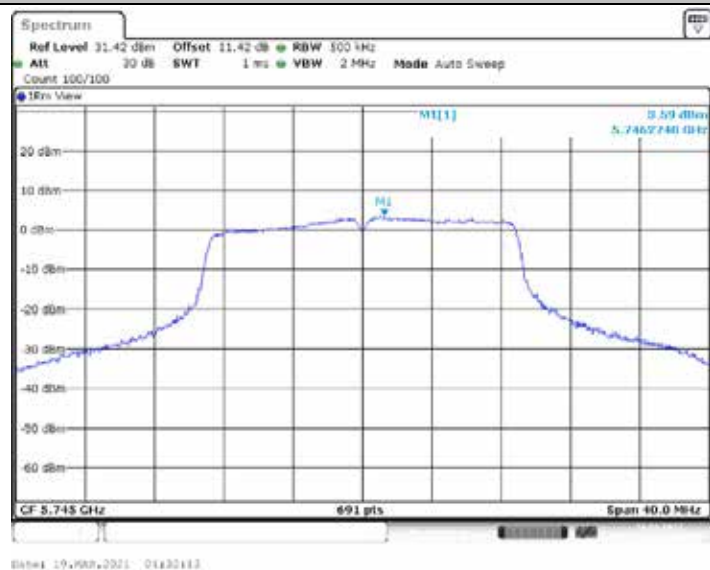
11N20MIMO_Ant2_5700



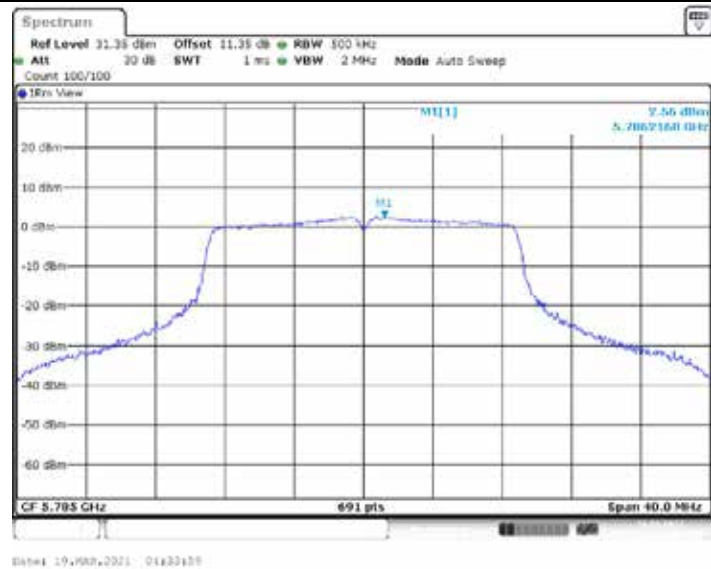
11N20MIMO_Ant1_5745



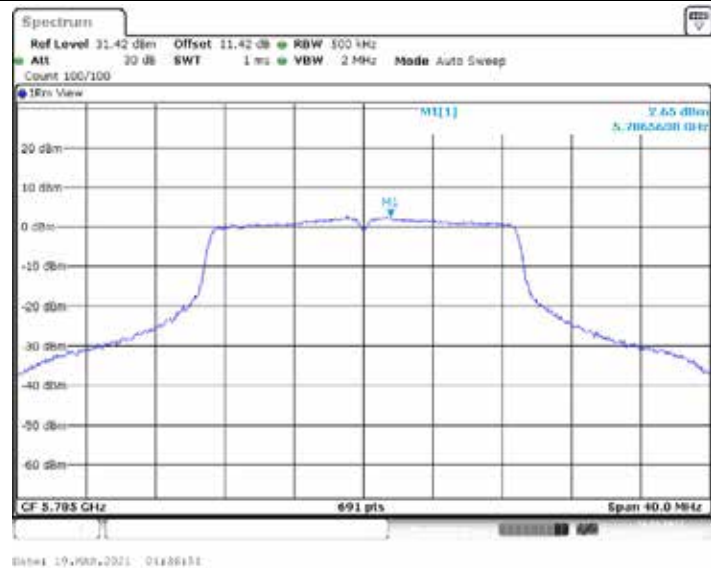
11N20MIMO_Ant2_5745



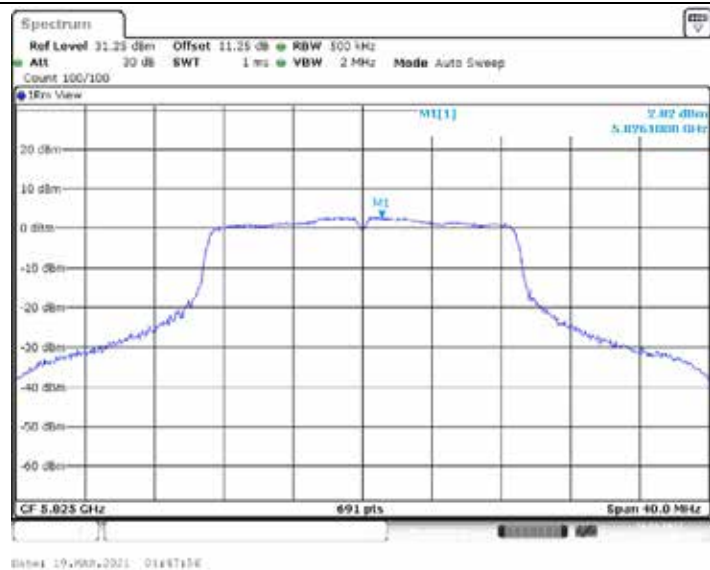
11N20MIMO_Ant1_5785



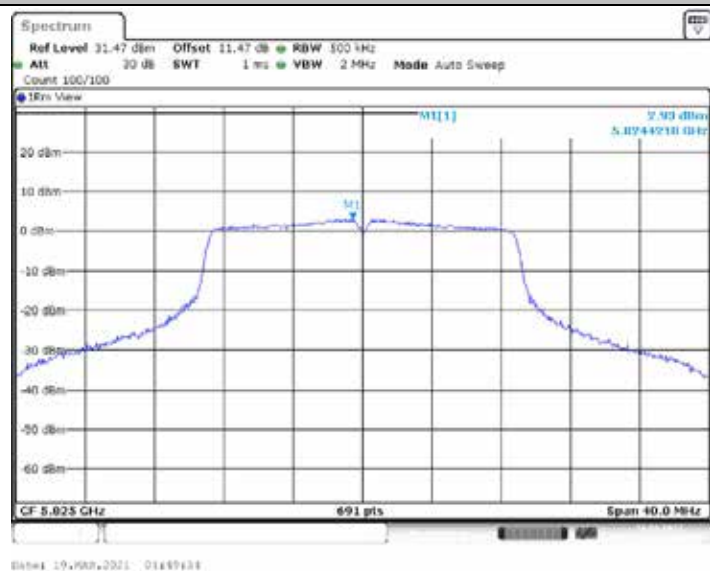
11N20MIMO_Ant2_5785



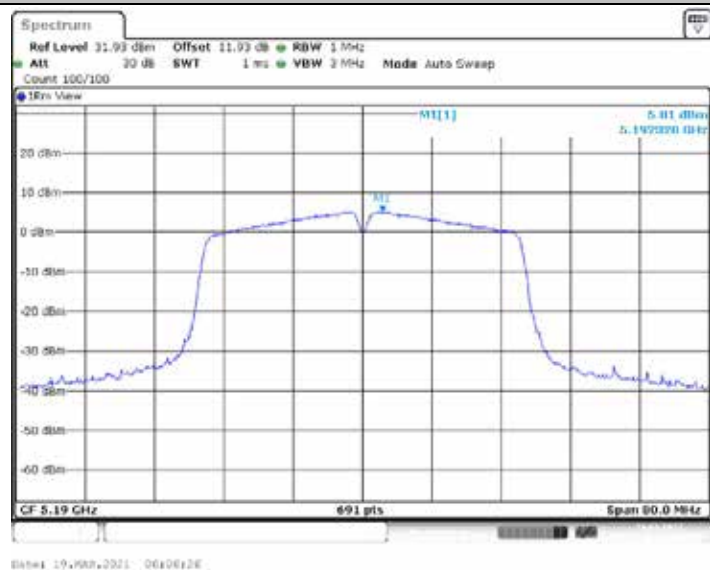
11N20MIMO_Ant1_5825



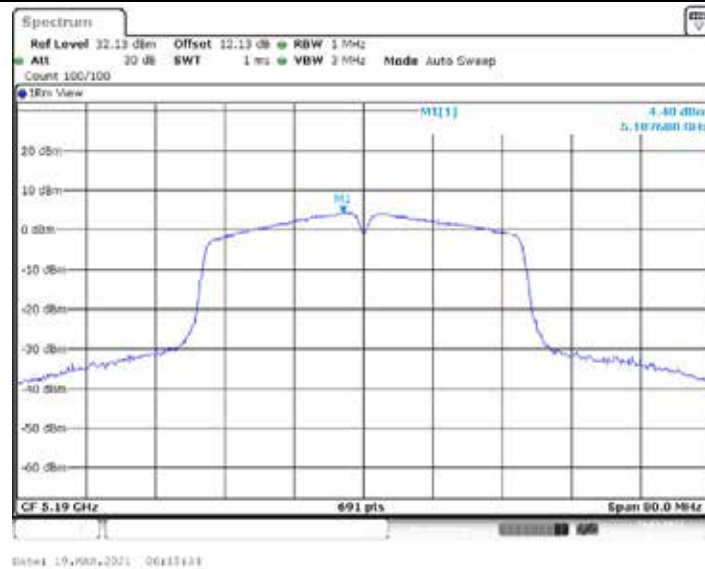
11N20MIMO_Ant2_5825



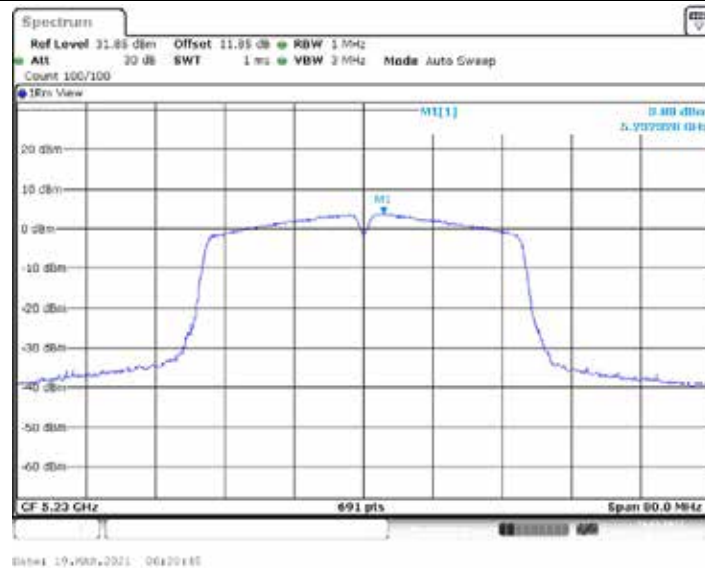
11N40MIMO_Ant1_5190



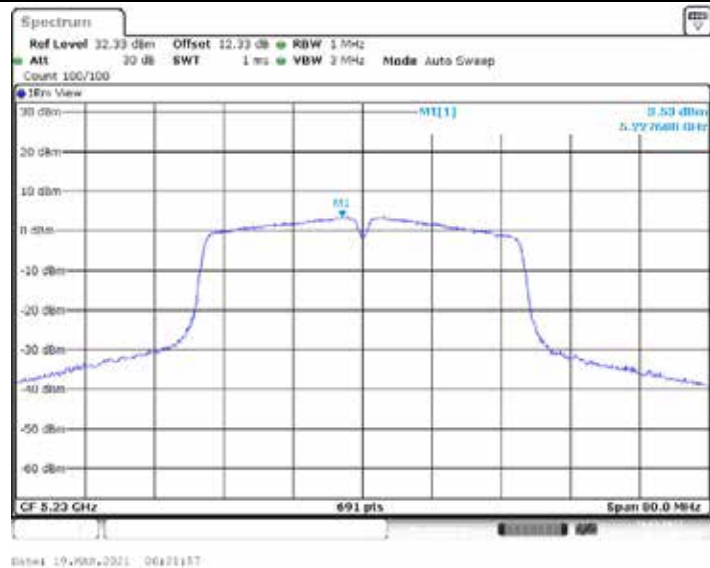
11N40MIMO_Ant2_5190



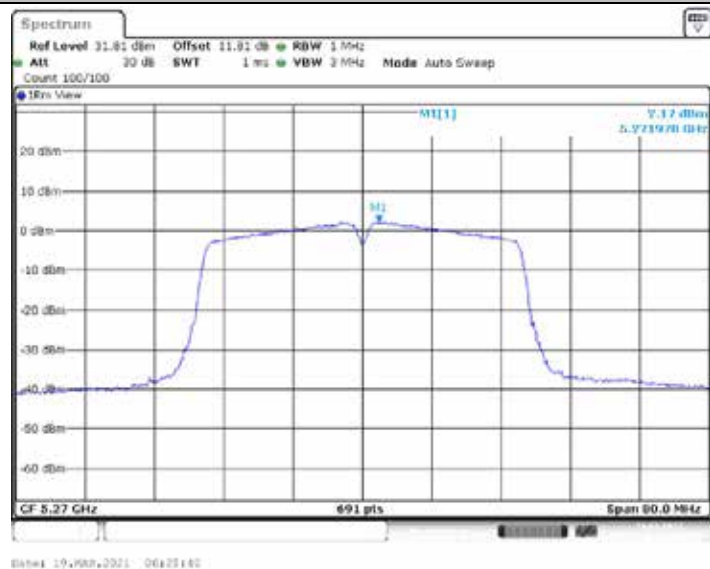
11N40MIMO_Ant1_5230



11N40MIMO_Ant2_5230



11N40MIMO_Ant1_5270



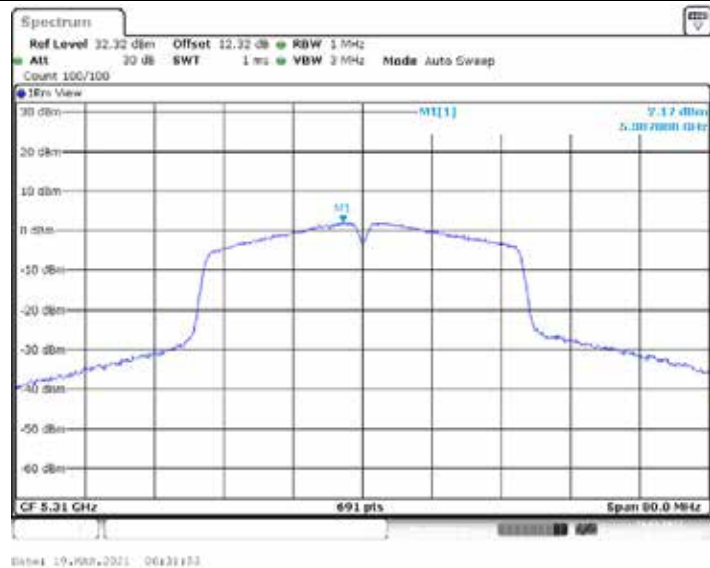
11N40MIMO_Ant2_5270



11N40MIMO_Ant1_5310



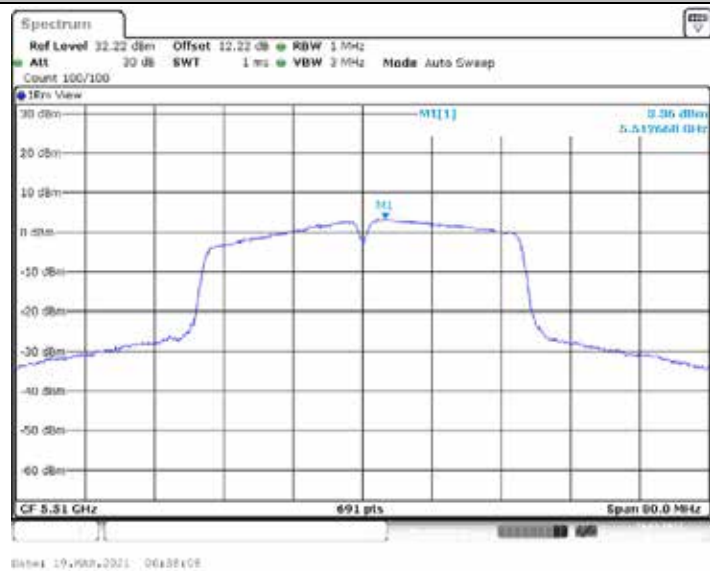
11N40MIMO_Ant2_5310



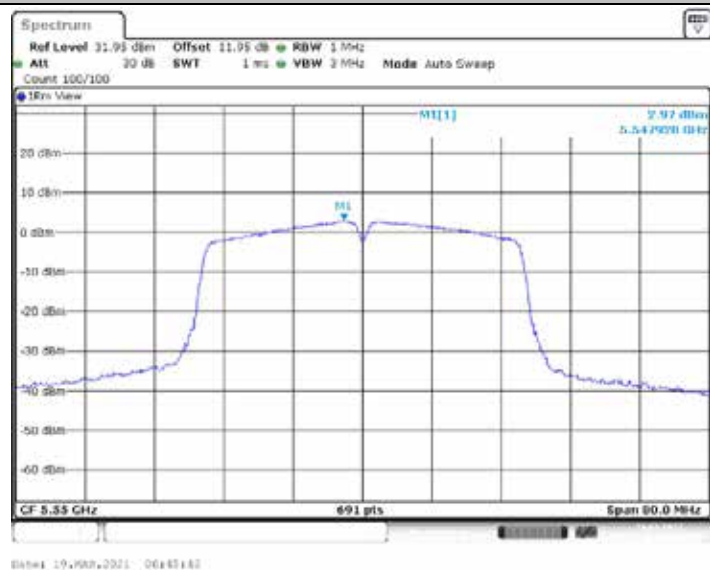
11N40MIMO_Ant1_5510



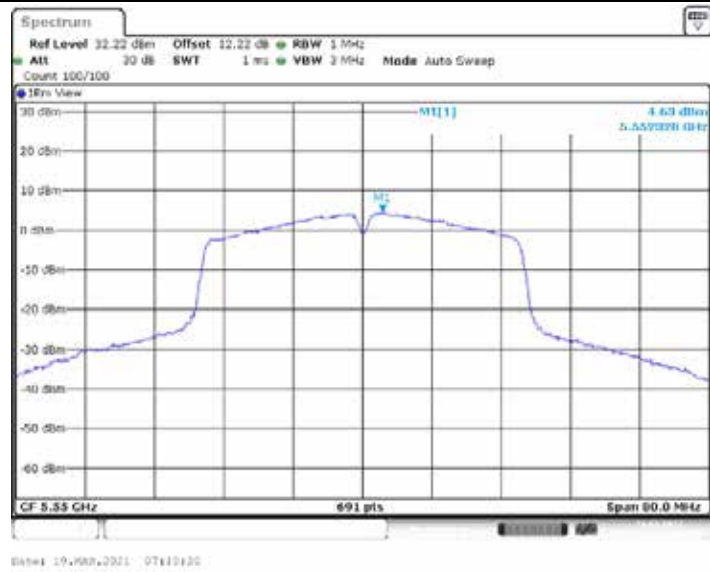
11N40MIMO_Ant2_5510



11N40MIMO_Ant1_5550



11N40MIMO_Ant2_5550



11N40MIMO_Ant1_5670



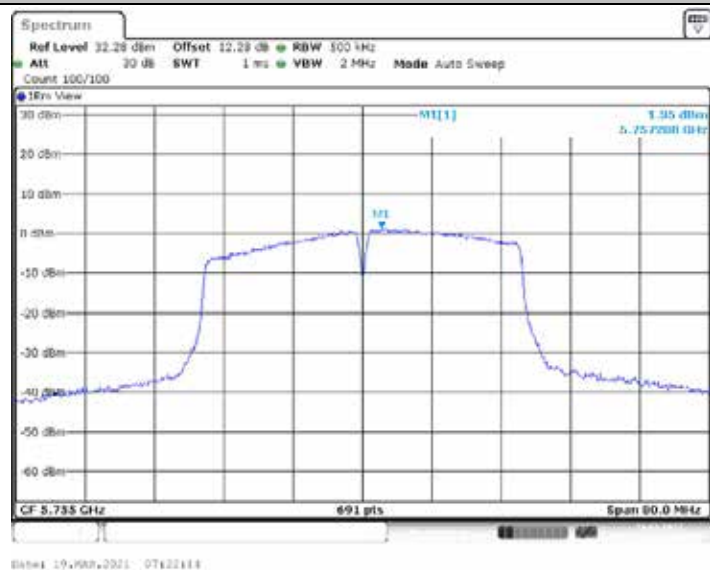
11N40MIMO_Ant2_5670



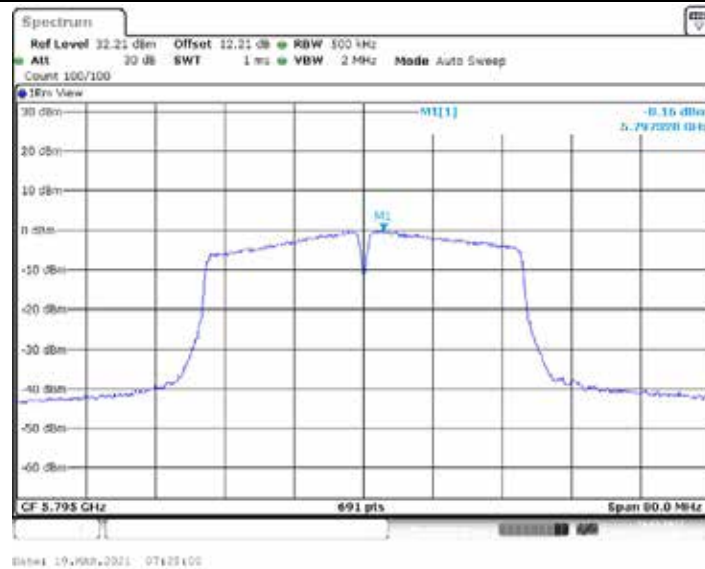
11N40MIMO_Ant1_5755



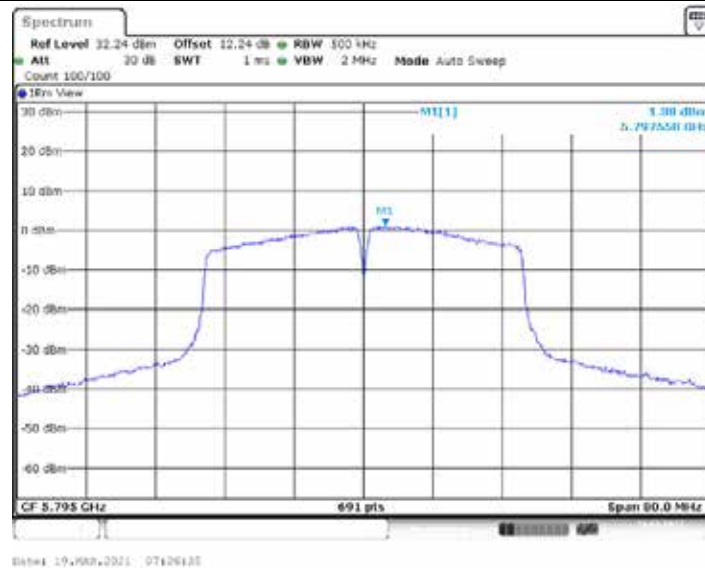
11N40MIMO_Ant2_5755



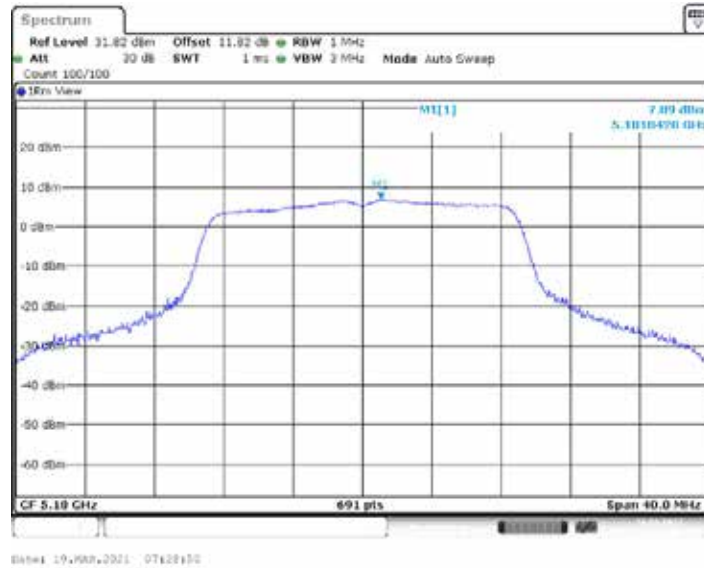
11N40MIMO_Ant1_5795



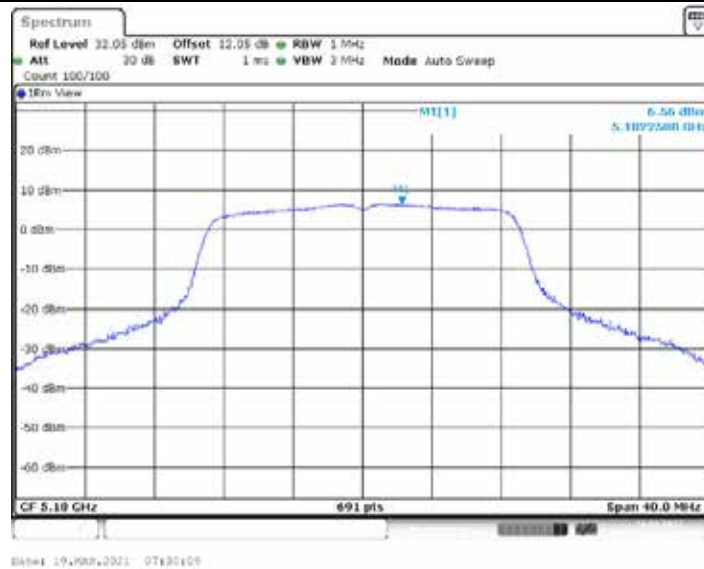
11N40MIMO_Ant2_5795



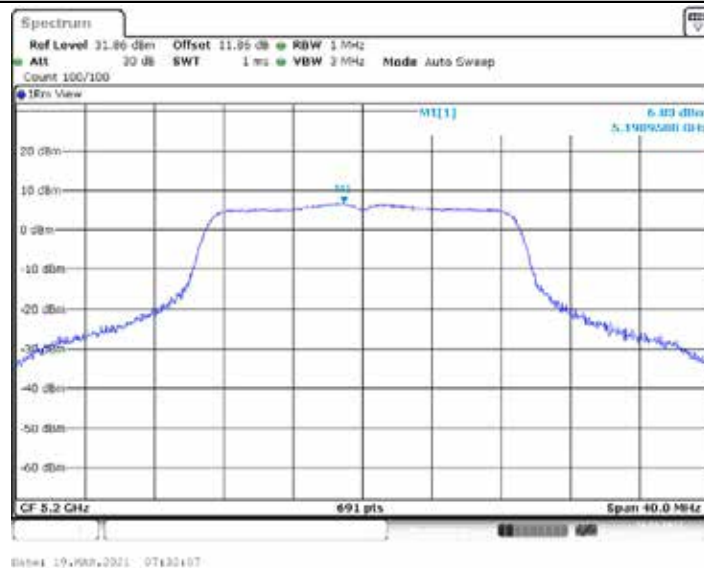
11AC20MIMO_Ant1_5180



11AC20MIMO_Ant2_5180



11AC20MIMO_Ant1_5200



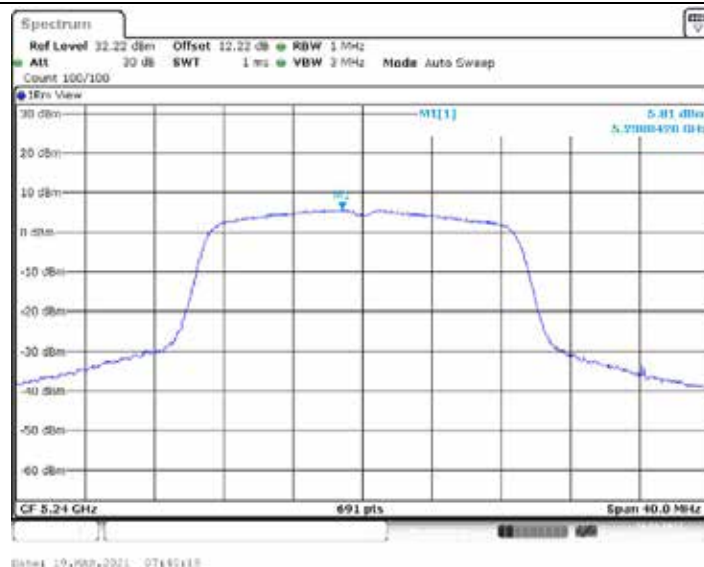
11AC20MIMO_Ant2_5200



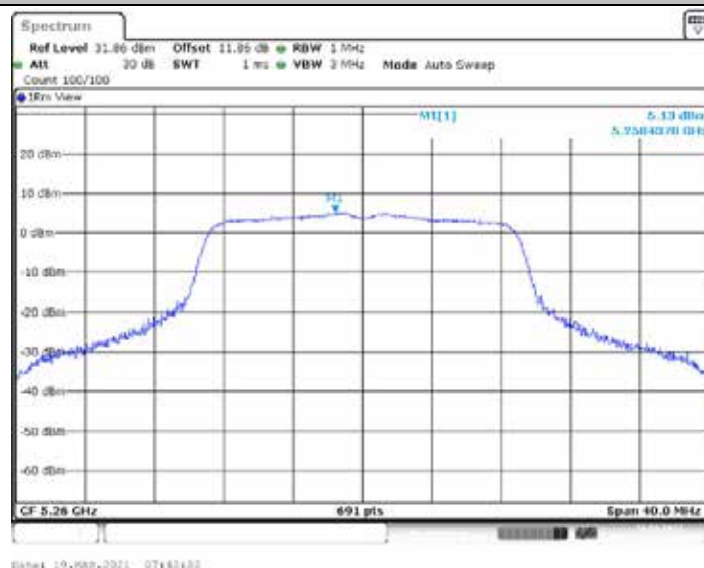
11AC20MIMO_Ant1_5240



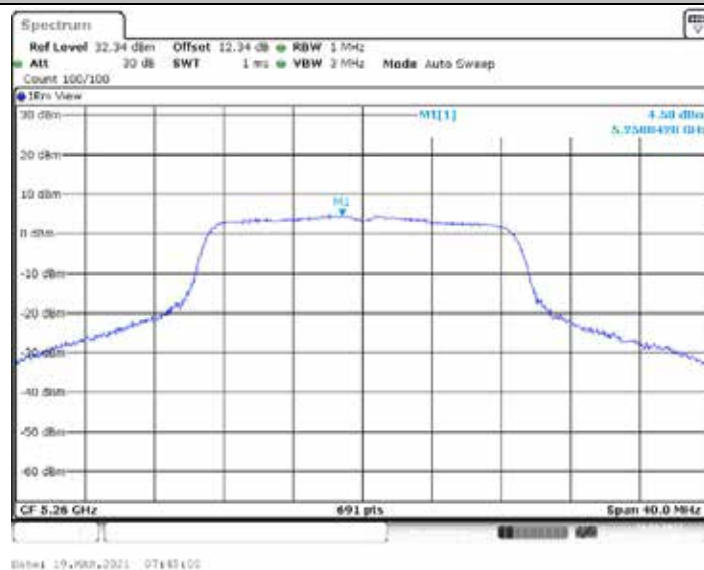
11AC20MIMO_Ant2_5240



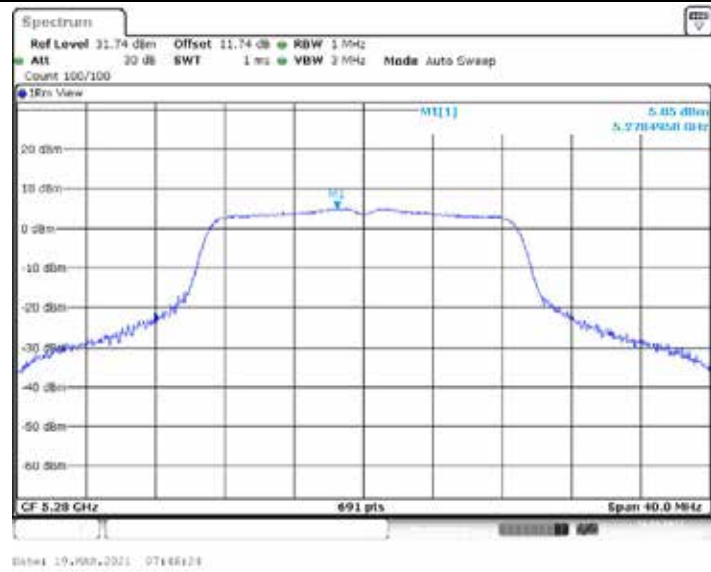
11AC20MIMO_Ant1_5260



11AC20MIMO_Ant2_5260



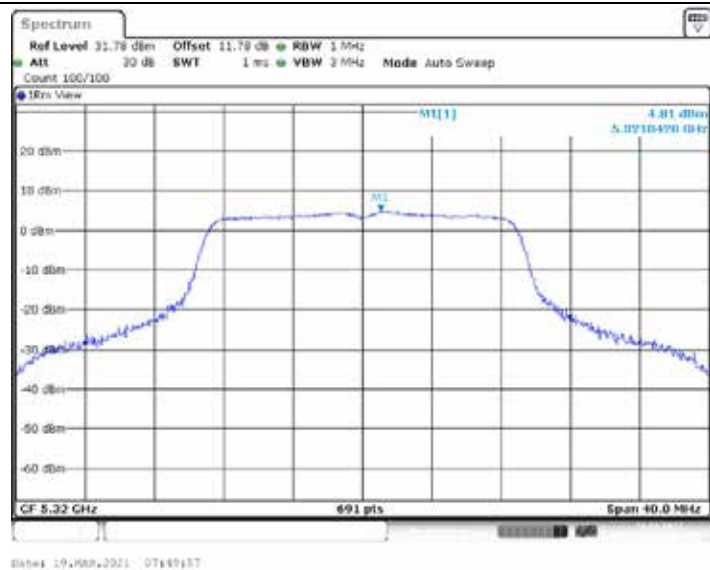
11AC20MIMO_Ant1_5280



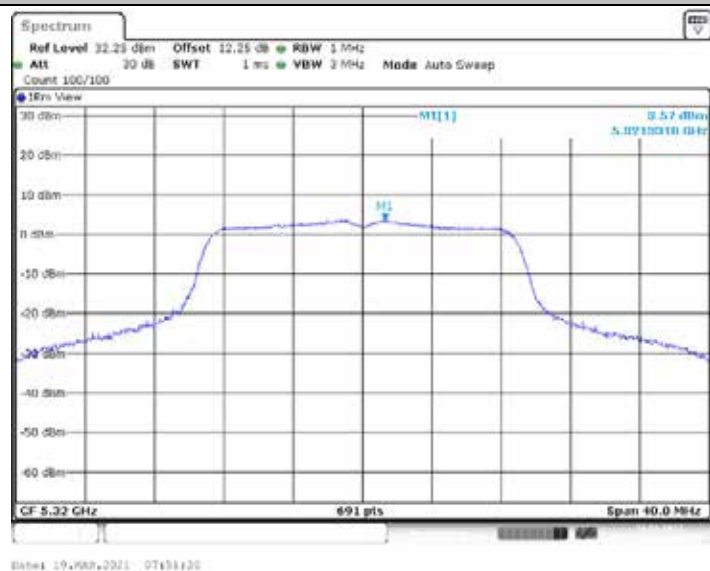
11AC20MIMO_Ant2_5280



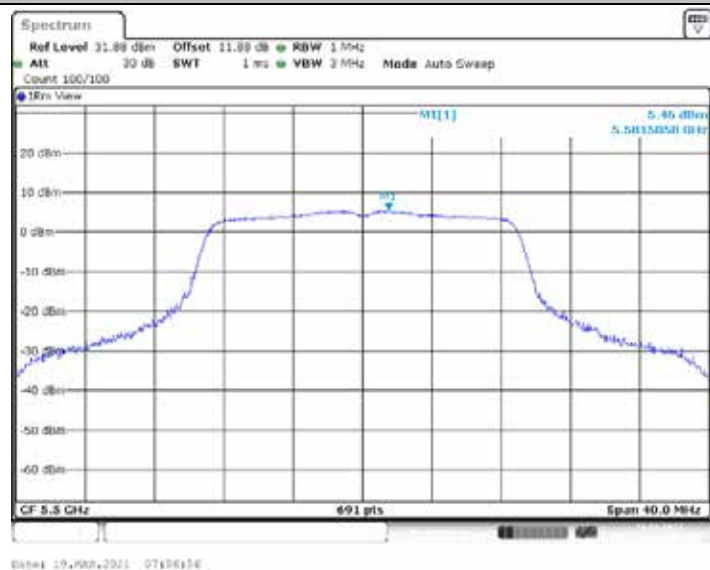
11AC20MIMO_Ant1_5320



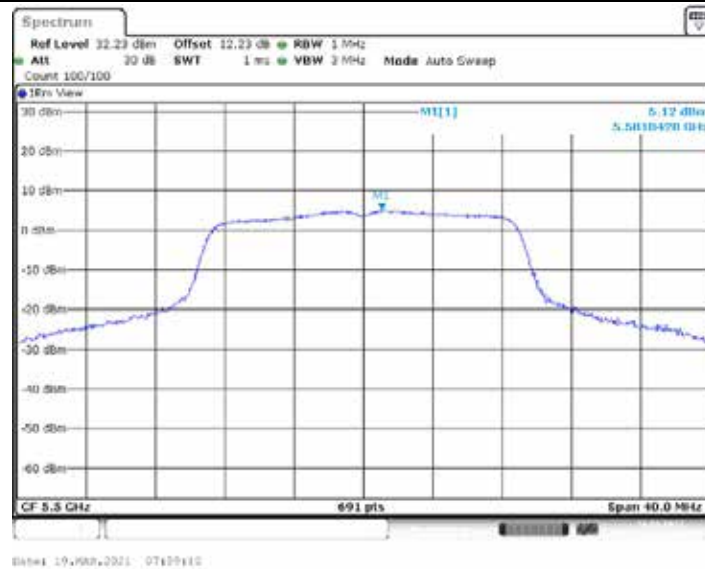
11AC20MIMO_Ant2_5320



11AC20MIMO_Ant1_5500



11AC20MIMO_Ant2_5500



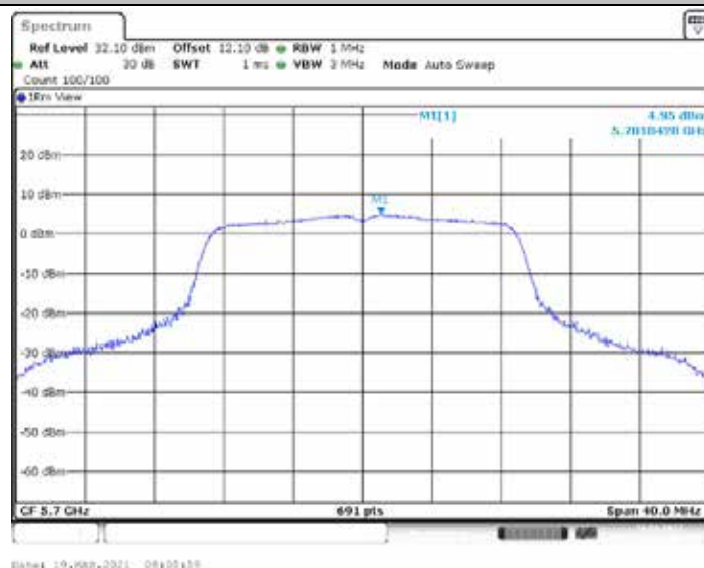
11AC20MIMO_Ant1_5580



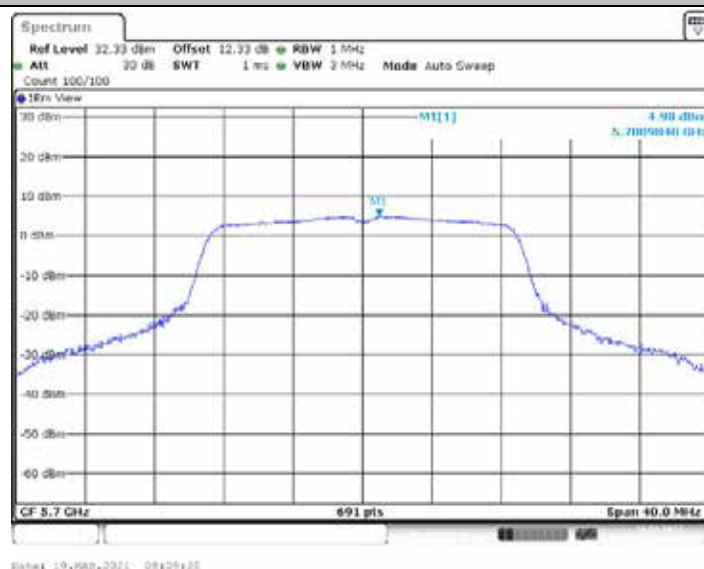
11AC20MIMO_Ant2_5580



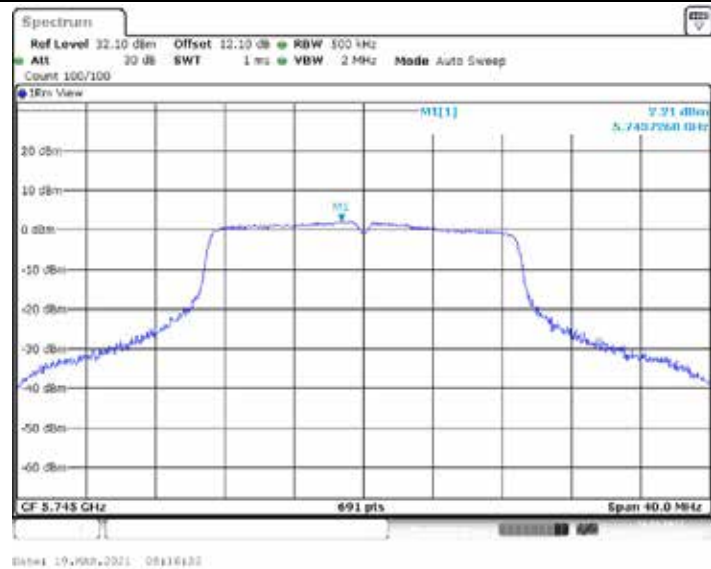
11AC20MIMO_Ant1_5700



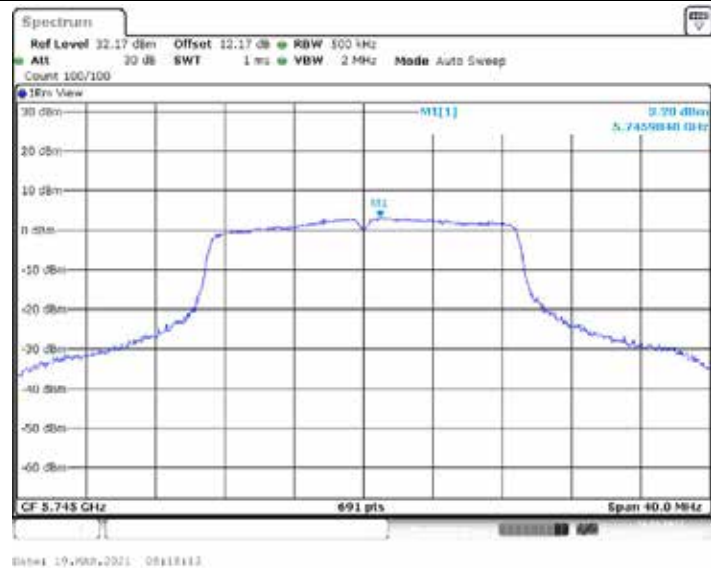
11AC20MIMO_Ant2_5700



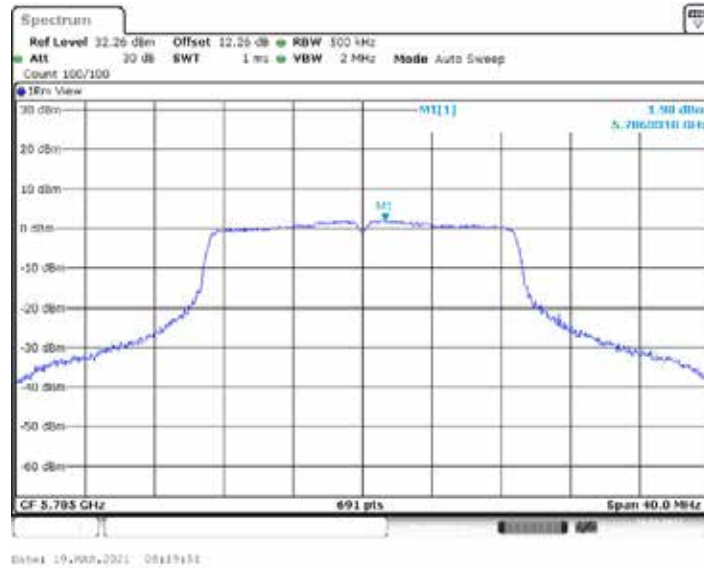
11AC20MIMO_Ant1_5745



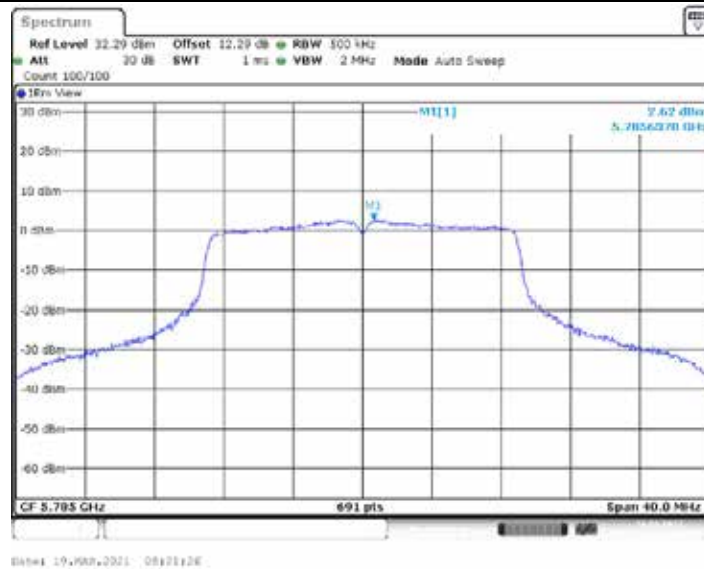
11AC20MIMO_Ant2_5745



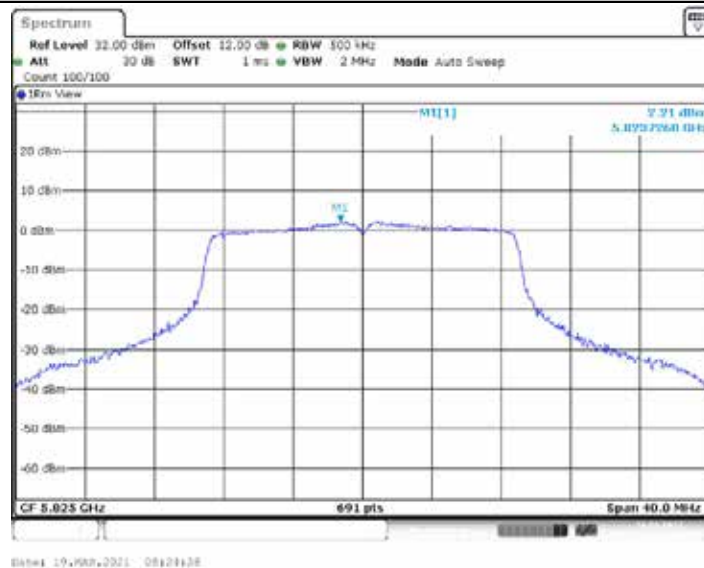
11AC20MIMO_Ant1_5785



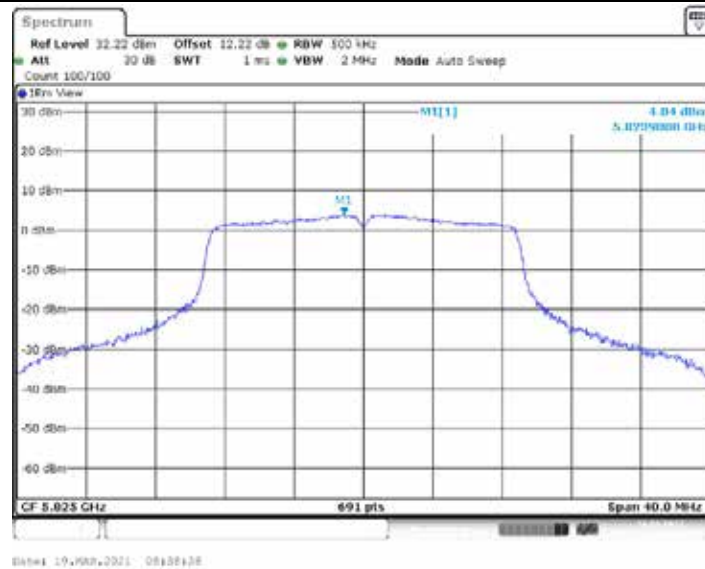
11AC20MIMO_Ant2_5785



11AC20MIMO_Ant1_5825



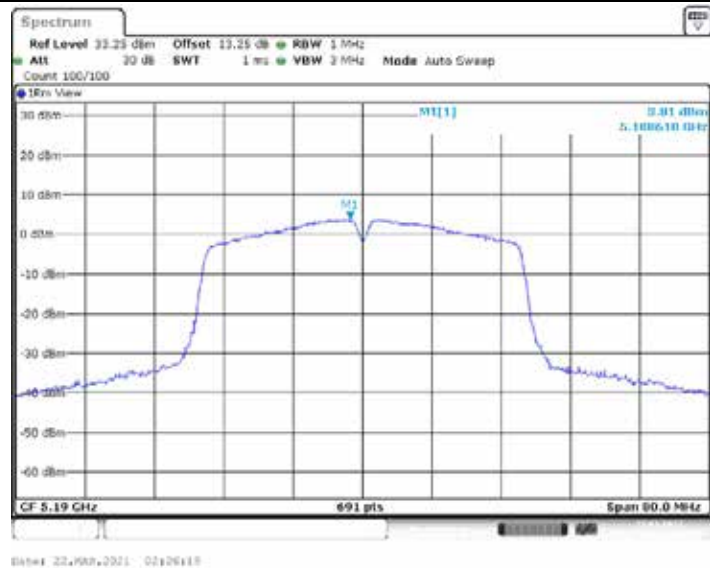
11AC20MIMO_Ant2_5825



11AC40MIMO_Ant1_5190



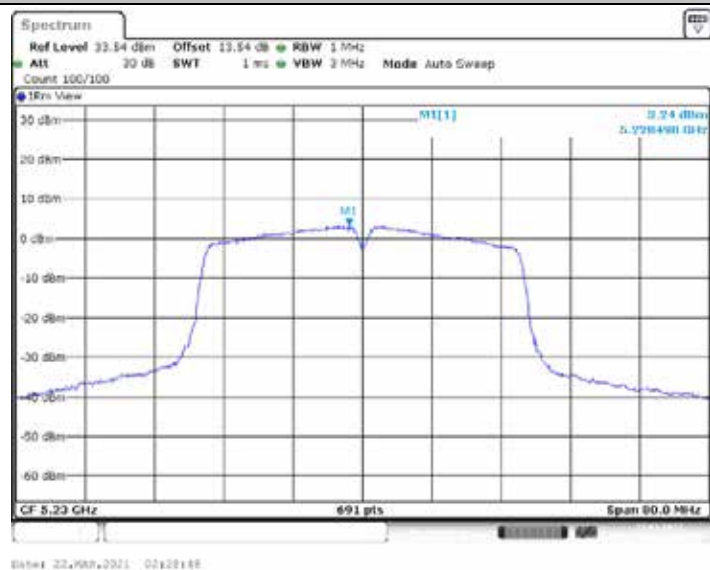
11AC40MIMO_Ant2_5190



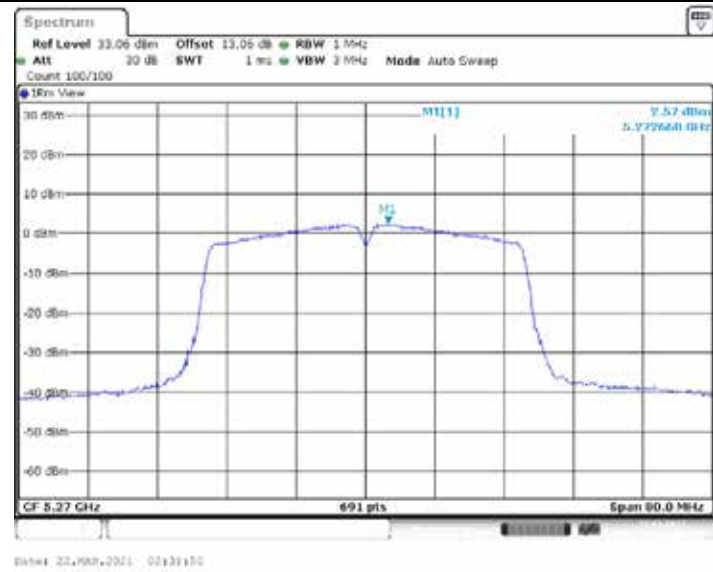
11AC40MIMO_Ant1_5230



11AC40MIMO_Ant2_5230



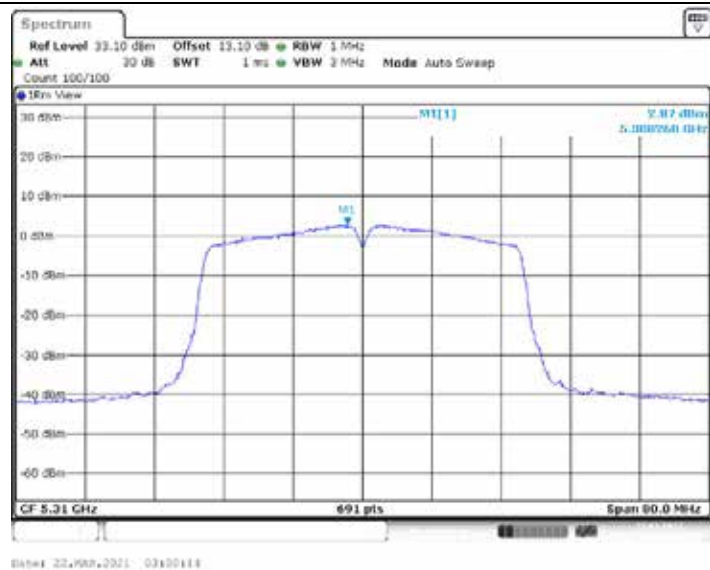
11AC40MIMO_Ant1_5270



11AC40MIMO_Ant2_5270



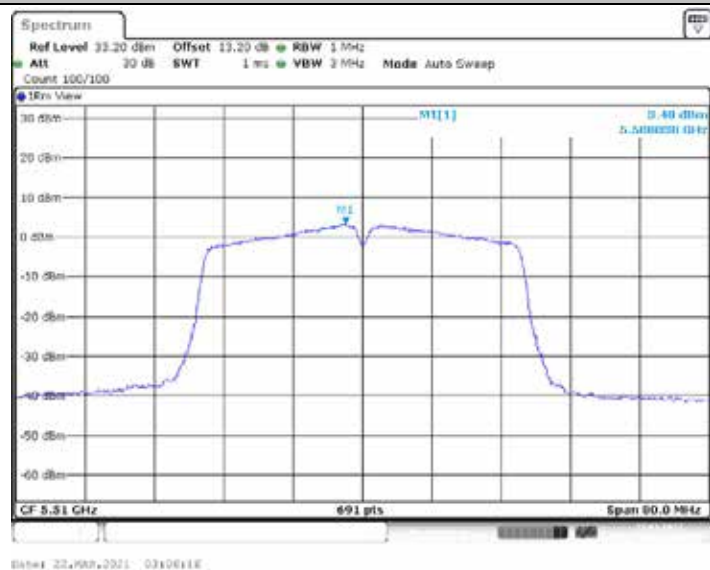
11AC40MIMO_Ant1_5310



11AC40MIMO_Ant2_5310



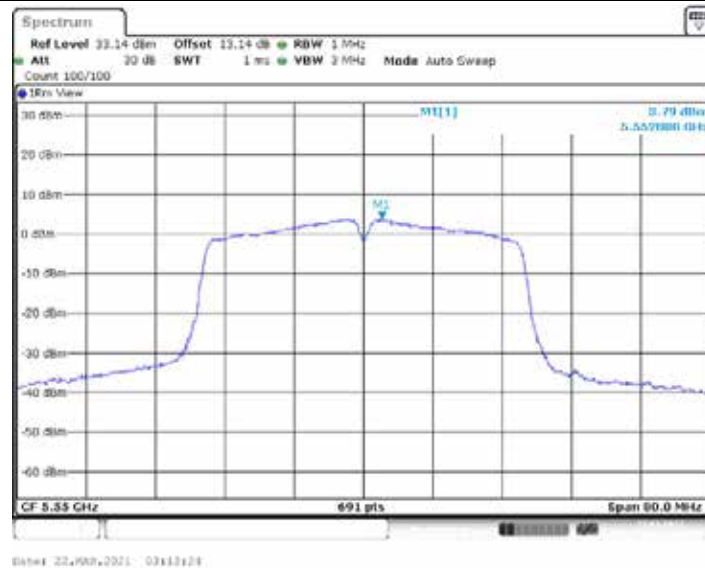
11AC40MIMO_Ant1_5510



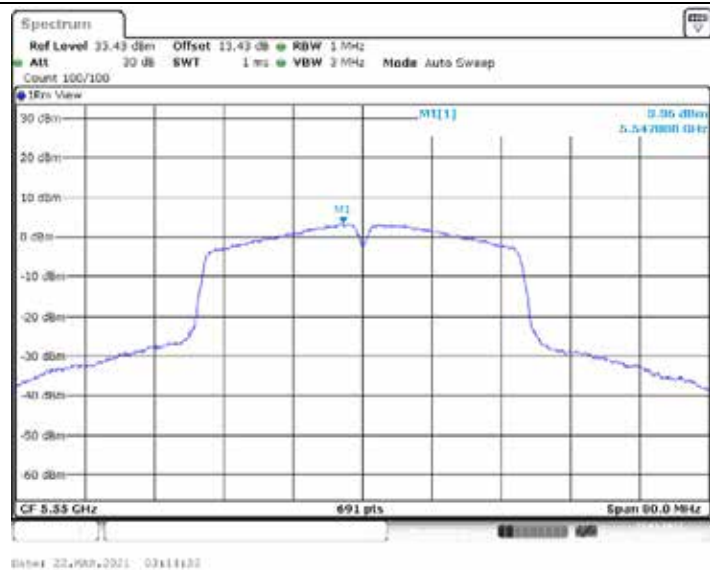
11AC40MIMO_Ant2_5510



11AC40MIMO_Ant1_5550



11AC40MIMO_Ant2_5550



11AC40MIMO_Ant1_5670



11AC40MIMO_Ant2_5670

