

User Manual

GEM Hemochron 100A

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1 SCOPE

This document describes key hardware aspects of the Accriva Diagnostics GEM Hemochron 100A M.2 module providing either SDIO or USB2.0 bus interface for WLAN connection and UART/SDIO/USB2.0 for Bluetooth® (including Low Energy or LE) connection. This document is intended to assist device manufacturers and related parties with the integration of this module into their host devices

2 INTRODUCTION

2.1 General Description

The GEM Hemochron 100A module is a dual band 2x2 802.11ac WLAN plus Bluetooth 4.2 dual mode adapter; it complies with M.2 2230 E-Key standard. The module provides both simultaneous and independent operation of the following:

- IEEE 802.11ac (Wave 2), 2x2 receive Multi-User MIMO spatial stream multiplexing with data rates up to MCS9 (866.7 Mbps)
- Bluetooth (Class 1 and Class 2)
- Bluetooth 4.2 (with Low Energy or LE)
- Bluetooth 5 Ready
- Bluetooth *Smart Ready* operation
- Three-way coexistence for WLAN and Bluetooth

Internal coexistence arbitration and a Mobile Wireless System (MWS) serial transport interface provide the functionality for connecting an external Long Term Evolution (LTE).

The module integrates all WLAN and Bluetooth functionality into a single package which supports low-cost and simple implementation along with flexibility for platform-specific customization. In addition, it has low power consumption radio architecture and proprietary power save technologies to extended battery life.

On the DFS engine, the module supports 802.11h Dynamic Frequency Selection to detect the presence of radar signals; support is extended to 80 MHz mode under the 802.11ac channelization modes. In addition, the E-DFS (Enhanced DFS) scheme is designed to increase pulse detection rates for shorter (0.5 us, 0.8 us, 1 us), in-band DFS pulses. The scheme is designed to minimize the false-alarm rate for out-of-band DFS pulse.

There are two interfaces for WLAN function:

- SDIO 3.0 – Supports both 1-bit SDIO and 4-bit SDIO transfer modes at full clock range up to 208 MHz
- USB 2.0

In addition, there are three interfaces for Bluetooth function:

- SDIO
- USB 2.0
- High-Speed UART

The GEM Hemochron 100A module also provides a PCM interface for master or slave mode; with the option of an 8-bit or 16-bit width size.

Pins CON[0], CON[1], and CON[2] are used to configure the operation interface.

2.2 Part Numbers and Supported Interfaces

The following (Table 1) are the GEM Hemochron 100A part number and the interfaces each supports.

Table 1: Part numbers and supported interfaces

Part Number	Supported Interfaces	
	WLAN	Bluetooth
GEM Hemochron 100A	UART	UART

3 FEATURES SUMMARY

The Accriva Diagnostics GEM Hemochron 100A device features are described in Table 2.

Table 2: GEM Hemochron 100A of features

Feature	Description																				
Radio Front End	- Integrates the complete transmit/receive RF paths including band pass filter, diplexer, switches, reference crystal oscillator, and power manage unit (PMU) - Supports 20/40/80 MHz channel bandwidth - WLAN/Bluetooth share one antenna																				
Coexistence	Coexistence arbitration for WLAN, Bluetooth, and LTE operation																				
Power Management	Dynamic Voltage Scaling (DVS) and Adaptive Voltage Scaling (AVS) feature supports the latest Marvell SoC and processor power control scheme.																				
Pre-Calibration	RF system-tested and calibrated in production.																				
Sleep Clock	An external sleep clock of 32.768 KHz is required during power save mode.																				
Host Interface	- SDIO 3.0 (4-bit and 1-bit), SDR 12/25/50 mode (up to 100MHz), USB 2.0 or PCIe for WLAN - SDIO 3.0, USB 2.0, HS-UART for Bluetooth HCI (compatible with any upper layer Bluetooth stack) - PCM digital audio interface for Bluetooth audio application.																				
	<table><tr><th>Strap Value CONFIG_HOST[2-0]</th><th>WLAN</th><th>Bluetooth/ BLE</th><th>ROM Notes</th></tr><tr><td>1</td><td>SDIO</td><td>UART</td><td>-</td></tr><tr><td>2</td><td>SDIO</td><td>SDIO</td><td>-</td></tr><tr><td>100</td><td>USB 2.0</td><td>UART</td><td>Initial USB 2.0 PHY <i>and</i> COM PHY PCIe USB 2.0</td></tr><tr><td>101</td><td>USB 2.0</td><td>USB 2.0</td><td>Initial USB 2.0 PHY only</td></tr></table>	Strap Value CONFIG_HOST[2-0]	WLAN	Bluetooth/ BLE	ROM Notes	1	SDIO	UART	-	2	SDIO	SDIO	-	100	USB 2.0	UART	Initial USB 2.0 PHY <i>and</i> COM PHY PCIe USB 2.0	101	USB 2.0	USB 2.0	Initial USB 2.0 PHY only
	Strap Value CONFIG_HOST[2-0]	WLAN	Bluetooth/ BLE	ROM Notes																	
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	2	SDIO	SDIO	-																	
	100	USB 2.0	UART	Initial USB 2.0 PHY <i>and</i> COM PHY PCIe USB 2.0																	
101	USB 2.0	USB 2.0	Initial USB 2.0 PHY only																		
Reference Frequency	- Incorporates a 40 MHz reference frequency source in package - An external sleep clock is recommended for minimal current consumption. If no sleep clock input is provided, an internal sleep clock (derived from the reference clock) is used. An approximate 50 uA current increase on the 3.3V rail.																				
Advanced WLAN	- A-MPDU RX (de-aggregation) and TX (aggregation) supports 802.11ac single-MPDU A-MPDU - Multi-BSS/Station - Transmit rate adaption, transmit power control																				

Feature	Description
	- Modulation and coding scheme (MCS): 802.11ac—MCS0-9 Nsts=1 and 2 802.11n—MCS0-15 - Dynamic frequency selection (DFS) – Radar detection 20/40/80 MHz channel bandwidths support - On-chip gain selectable LNA with optimized noise figure and power consumption - Internal PA with optimized gain distribution for linearity and noise performance - Support Wild variety of WLAN encryption: TKIP/WEP/AES
Advanced Bluetooth	- Bluetooth 4.2 (BDR/EDR/LE), Bluetooth Class 1 - Bluetooth 5 ready - Supports the following data rates: 1 Mbps (GFSK), 2 Mbps ($\pi/4$-DQPSK), 3 Mbps (8-DPSK) - Digital audio interface with PCM/TDM interface for voice application - Adaptive Frequency Hopping (AFH) using Package Error Rate (PER) - Standard SDIO or UART HCI transport layer - WLAN/Bluetooth coexistence protocol support - Shared LNA with WLAN/Bluetooth - Encryption (AES) support

4 SPECIFICATIONS

Table 3: Specifications

Table 3: Specifications

Feature	Description																				
Physical Interface	84-pin LGA package (including 16 thermal ground pad under the package)																				
Wi-Fi Interface	1-bit or 4-bit Secure Digital I/O; PCIe v3.0 Gen1/Gen2 (2.5/5 Gbps); USB 2.0																				
Bluetooth/ BLE Interface	Host Controller Interface (HCI) using High Speed UART, SDIO, USB 2.0																				
	<table><tr><th>Strap Value CONFIG_HOST[2-0]</th><th>WLAN</th><th>Bluetooth /BLE</th><th>ROM Notes</th></tr><tr><td>1</td><td>SDIO</td><td>UART</td><td>-</td></tr><tr><td>2</td><td>SDIO</td><td>SDIO</td><td>-</td></tr><tr><td>100</td><td>USB 2.0</td><td>UART</td><td>Initial USB 2.0 PHY <i>and</i> COM PHY PCIe USB 2.0</td></tr><tr><td>101</td><td>USB 2.0</td><td>USB 2.0</td><td>Initial USB 2.0 PHY only</td></tr></table>	Strap Value CONFIG_HOST[2-0]	WLAN	Bluetooth /BLE	ROM Notes	1	SDIO	UART	-	2	SDIO	SDIO	-	100	USB 2.0	UART	Initial USB 2.0 PHY <i>and</i> COM PHY PCIe USB 2.0	101	USB 2.0	USB 2.0	Initial USB 2.0 PHY only
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101	USB 2.0	USB 2.0	Initial USB 2.0 PHY only																		
Main Chip	Marvell 88W8997 (WLAN/BT); Marvell 88PG823 (PMU)																				
Input Voltage Requirements	DC 3.3 V ±10%																				
I/O Signalling Voltage	DC 3.3 V ± 10% or DC 1.8 V ± 10%																				
Peak Current consumption, VCC=VIO = 3.3 volts (At maximum transmit power setting)	MIMO 2x 2 operations. 802.11b (with BT in standby) @ 18 dBm 1 Mbps Transmit: XX mA Receive: XX mA 802.11g (with BT in standby) @ 18 dBm 6 Mbps Transmit: XX mA Receive: XX mA 802.11a (with BT in standby) @ 18 dBm 6 Mbps Transmit: XX mA Receive: XX mA																				
<i>Note: Reset refers to the radio are in reset, both Wi-fi and BT reset are</i>	802.11n (2.4 GHz/40MHz) (with BT in standby) @ 16 dBm MCS0 Transmit: XX mA Receive: XX mA 802.11n (5.0 GH/40MHz) (with BT in standby) @ 16 dBm MCS0																				

Feature	Description
<i>asserted.</i>	Transmit: XX mA Receive: XX mA 802.11ac (5.0 GH/80MHz) (with BT in standby) @ 14 dBm MCS0 Transmit: XX mA Receive: XX mA Bluetooth (with Wi-Fi in standby) Transmit: XX mA Receive: XX mA Reset: XXX mA
Operating Temperature	-30° to 85°C (-22° to 185°F)
Operating Humidity	10 to 90% (non-condensing)
Storage Temperature	-40° to 85°C (-40° to 185°F)
Storage Humidity	10 to 90% (non-condensing)
Maximum Electrostatic Discharge	Conductive 4KV; Air coupled 8KV follow EN61000-4-2
Size	22 mm (length) x 30 mm (width) x 3.3 mm (thickness)
Weight	TBD g
Wi-Fi Media	Direct Sequence-Spread Spectrum (DSSS) Complementary Code Keying (CCK) Orthogonal Frequency Divisional Multiplexing (OFDM)
Bluetooth Media	Frequency Hopping Spread Spectrum (FHSS)
Wi-Fi Media Access Protocol	Carrier sense multiple access with collision avoidance (CSMA/CA) A-MPDU Rx (De-aggregation) and Tx (aggregation) (802.11ac single-MPDU A-MPDU)
Network Architecture Types	Infrastructure and ad-hoc
Wi-Fi Standards	IEEE 802.11a, 802.11b, 802.11d*, 802.11e, 802.11g, 802.11h, 802.11i, 802.11k*, 802.11n, 802.11r, 802.11v*, 802.11ac * Summit version only
Bluetooth Standards	Bluetooth version 2.1 with Enhanced Data Rate Bluetooth 4.2 (Bluetooth Low Energy or BLE) Bluetooth 5 (coming soon)
Wi-Fi Data Rates Supported	Support 802.11 ac/a/b/g/n 2X2 MIMO MU-MIMO/802.11ac Wave 2 802.11b (DSSS, CCK) 1, 2, 5.5, 11 Mbps 802.11a/g (OFDM) 6, 9, 12, 18, 24, 36, 48, 54 Mbps 802.11n (OFDM, HT20/HT40, MCS 0-15) 802.11ac (OFDM, HT20, MCS0-8; OFDM HT40/HT80, MCS 0-9)

Feature	Description										
Modulation Table	BPSK, QPSK, CCK, 16-QAM, 64-QAM, and 256-QAM.										
802.11ac	HT	VHT	Spatial Streams	Modulation	Coding	20 MHz		40 MHz		80 MHz	
	MCS	MCS				No SGI	SGI	No SGI	SGI	No SGI	SGI
802.11n	Index	Index									
	0	0	1	BPSK	1/2	6.5	7.2	13.5	15	29.3	32.5
	1	1	1	QPSK	1/2	13	14.4	27	30	58.5	65
	2	2	1	QPSK	3/4	19.5	21.7	40.5	45	87.8	97.5
	3	3	1	16-QAM	1/2	26	28.9	54	60	117	130
	4	4	1	16-QAM	3/4	39	43.3	81	90	175.5	195
	5	5	1	64-QAM	2/3	52	57.8	108	120	234	260
	6	6	1	64-QAM	3/4	58.5	65	121.5	135	263.3	292.5
	7	7	1	64-QAM	5/6	65	72.2	135	150	292.5	325
		8	1	256-QAM	3/4	78	86.7	162	180	351	390
		9	1	256-QAM	5/6	N/A	N/A	180	200	390	433.3
	8	0	2	BPSK	1/2	13	14.4	27	30	58.5	65
	9	1	2	QPSK	1/2	26	28.9	54	60	117	130
	10	2	2	QPSK	3/4	39	43.3	81	90	175.5	195
	11	3	2	16-QAM	1/2	52	57.8	108	120	234	260
	12	4	2	16-QAM	3/4	78	86.7	162	180	351	390
	13	5	2	64-QAM	2/3	104	115.6	216	240	468	520
	14	6	2	64-QAM	3/4	117	130.3	243	270	526.5	585
	15	7	2	64-QAM	5/6	130	144.4	270	300	585	650
		8	2	256-QAM	3/4	156	173.3	324	360	702	180
		9	2	256-QAM	5/6	N/A	N/A	360	400	780	866.7
802.11ac/n Spatial Streams	2 (2x2 MIMO)										
Bluetooth Data Rates Supported	1, 2, 3 Mbps										
Bluetooth Modulation	GFSK@ 1 Mbps Pi/4-DQPSK@ 2 Mbps 8-DPSK@ 3 Mbps										
Regulatory Domain Support	FCC (Americas, Parts of Asia, and Middle East) ETSI (Europe, Middle East, Africa, and Parts of Asia) IC (Industry Canada)										
2.4 GHz Frequency Bands	ETSI: 2.4 GHz to 2.483 GHz FCC: 2.4 GHz to 2.473 GHz										
2.4 GHz Operating Channels (Wi-Fi)	ETSI: 13 (3 non-overlapping) FCC: 11 (3 non-overlapping)										

Feature	Description	
5 GHz Frequency Bands	ETSI	
	5.15 GHz to 5.35 GHz (Ch 36/40/44/48/52/56/60/64)	
	5.47 GHz to 5.725 GHz (Ch 100/104/108/112/116/120/124/128/132/136/140/144)	
	FCC	
	5.15 GHz to 5.35 GHz (Ch 36/40/44/48/52/56/60/64)	
	5.47 GHz to 5.725 GHz (Ch 100/104/108/112/116/120/124/128/132/136/140/144)	
	5.725 GHz to 5.825 GHz(Ch 149/153/157/161/165)	
5 GHz Operating Channels (Wi-Fi)	ETSI: 19 non-overlapping; FCC: 24 non-overlapping	
Transmit Power	802.11a	
Note: Transmit power on each channel varies according to individual country regulations. All values are nominal with +/-2 dBm tolerance at room temperature. Tolerance could be up to +/-2.5 dBm across operating temperature. Note: HT20 – 20 MHz-wide channels HT40 – 40 Mhz-wide channels HT80 – 80 MHz-wide channels	6 Mbps	18 dBm (63 mW)
	54 Mbps	16 dBm (40 mW)
	802.11b	
	1 Mbps	18 dBm (63 mW)
	11 Mbps	18 dBm (63 mW)
	802.11g	
	6 Mbps	18 dBm (63 mW)
	54 Mbps	16 dBm (40 mW)
	802.11n (2.4/5 GHz)	
	6.5 Mbps (MCS0-5/MCS8-13;HT20)	18 dBm (63 mW)
	65 Mbps (MCS6-7/MCS14-15;HT20)	16 dBm (40 mW)
	13.5Mbps(MCS0-5/MCS8-13;HT40)	16 dBm (40 mW)
	135Mbps (MCS6-7/MCS14-15;HT40)	14 dBm (25 mW)
	802.11ac (5 GHz)	
	6.5/13 Mbps (MCS0-6;Ntst=1,2;HT20)	18 dBm (63 mW)
	78/156 Mbps (MCS7-8;Ntst=1,2;HT20)	16 dBm (40 mW)
	13.5/27Mbps (MCS0-6;Ntst=1,2;HT40)	16 dBm (40 mW)
	180/360Mbps (MCS7-9;Ntst=1,2;HT40)	14 dBm (25 mW)
	29.3/58.5 Mbps (MCS0-5;Ntst=1,2;HT80)	14 dBm (25 mW)
	263.3/526.5 Mbps (MCS6-8;Ntst=1,2;HT80)	12 dBm (15.8 mW)
	390/780 Mbps (MCS9;Ntst=1,2;HT80)	10 dBm (10 mW)
	Bluetooth	
	1 Mbps (1DH5)	10 dBm (12.5 mW)
	2 Mbps	7 dBm (6.3 mW)
	3 Mbps	7 dBm (6.3 mW)
	BLE (1 Mbps)	7 dBm (6.3 mW)

Feature	Description		
Typical Receiver Sensitivity (PER <= 10%) <i>Note: All values nominal, +/-3 dBm. Sensitivity on CH13 (WLAN)/CH78 (BT) will degrade up to 4-6dB.</i>	802.11a:		
	6 Mbps	-89 dBm	
	54 Mbps	-74 dBm	
	802.11b:		
	1 Mbps	-95 dBm	
	11 Mbps	-90 dBm	(PER<8%)
	802.11g:		
	6 Mbps	-91 dBm	
	54 Mbps	-75 dBm	
	802.11n (2.4 GHz)		
	6.5 Mbps (MCS0;HT20)	-91 dBm	
	65 Mbps (MCS7;HT20)	-73 dBm	
	13.5Mbps(MCS0;HT40)	-85 dBm	
	135Mbps (MCS7;HT40)	-70 dBm	
	802.11n (5 GHz)		
	6.5 Mbps (MCS0;HT20)	-89 dBm	
	65 Mbps (MCS7;HT20)	-70 dBm	
	13.5Mbps(MCS0;HT40)	-86 dBm	
	135Mbps (MCS7;HT40)	-69 dBm	
	802.11ac (5 GHz)		
	6.5 Mbps (MCS0;HT20)	-89 dBm	
	78 Mbps (MCS8;HT20)	-67 dBm	
	13.5 Mbps (MCS0;HT40)	-86 dBm	
	180 Mbps (MCS9;HT40)	-63 dBm	
	29.3 Mbps (MCS0;HT80)	-81 dBm	
	390/780 Mbps (MCS9;HT80)	-60 dBm	
	Bluetooth:		
	1 Mbps (1DH5)	-95 dBm	
	2Mbps (2DH5)	-94 dBm	
	3 Mbps (3DH5)	-88 dBm	
	BLE	-95 dBm	
Operating Systems Supported	Linux Kernel 3x or newer Android 5x and newer		

Feature	Description								
Security	Standards Wireless Equivalent Privacy (WEP) Wi-Fi Protected Access (WPA) IEEE 802.11i (WPA2) Encryption Wireless Equivalent Privacy (WEP, RC4 Algorithm) Temporal Key Integrity Protocol (TKIP, RC4 Algorithm) Advanced Encryption Standard (AES, Rijndael Algorithm) Encryption Key Provisioning Static (40-bit and 128-bit lengths) Pre-Shared (PSK) Dynamic 802.1X Extensible Authentication Protocol Types <table> <tr> <td>EAP-FAST</td><td>PEAP-MSCHAPv2</td></tr> <tr> <td>EAP-TLS</td><td>PEAP-TLS</td></tr> <tr> <td>EAP-TTLS</td><td>LEAP</td></tr> <tr> <td>PEAP-GTC</td><td></td></tr> </table> Note: EAP types are supplicant software dependent.	EAP-FAST	PEAP-MSCHAPv2	EAP-TLS	PEAP-TLS	EAP-TTLS	LEAP	PEAP-GTC	
EAP-FAST	PEAP-MSCHAPv2								
EAP-TLS	PEAP-TLS								
EAP-TTLS	LEAP								
PEAP-GTC									
Compliance	ETSI Regulatory Domain EN 300 328 EN 301 489-1 EN 301 489-17 EN 301 893 EN 60950-1 EU 2002/95/EC (RoHS) FCC Regulatory Domain FCC 15.247 DTS – 802.11b/g (Wi-Fi) – 2.4 GHz FCC 15.407 UNII – 802.11a (Wi-Fi) – 5 GHz FCC 15.247 DSS – BT 2.1 Industry Canada RSS-247 – 802.11a/b/g/n (Wi-Fi) – 2.4 GHz, 5.8 GHz, 5.2 GHz, and 5.4 GHz RSS-247 – BT 2.1								
Warranty	Three Year Warranty								
<i>All specifications are subject to change without notice</i>									

5 WLAN FUNCTIONAL DESCRIPTION

The GEM Hemochron 100A M2 module is designed based on the 60-SIPT SiP. It is optimized for high-speed, reliable, and low-power embedded applications. It is integrated with dual-band WLAN (2.4 GHz/5 GHz) and Bluetooth 4.2. Its functionality includes the following:

- Improved throughput on the link due to frame aggregation, RIFS (reduced inter-frame spacing), and half-guard intervals.
- Support for STBC (Space Time Block Codes) and LDPC (Low Density Parity Check) codes.
- Improved 11n performance due to features such as 11n frame aggregation (A-MPDU and A-MSDU) and low-overhead host-assisted buffering (RX A-MSDU and RX A-MPDU). These techniques can improve performance and efficiency of applications involving large bulk data transfers such as file transfers or high-resolution video streaming.
- IEEE 802.11 ac (Wave 2), 2X2 receive Multi-User MIMO (MU-MIMO) spatial stream multiplexing with data rate up to MCS9 (866.7 Mbps).

Additional functionality is listed in [Table 4](#).

Table 4: WLAN functions

Feature	Description
WLAN MAC	▪ Frame Exchange at the MAC level to deliver data ▪ Received frame filtering and validation (Cyclic Redundancy Check (CRC)) ▪ Generation of MAC header and trailer information (MAC protocol Data Units (MPDUs)) ▪ Fragmentation of data frames (MAC Service Data Units (MSDUs)) ▪ Access Mechanism support for fair access to shared wireless medium through (DCF and EDCA) ▪ A-MPDU Aggregation/Deaggregation (support 802.11ac single –MPDU A-MPDU) ▪ 20/40/80 MHz channel Coexistence ▪ RIFS Burst Receive ▪ Management Information Base ▪ Radio Resource Measurement ▪ Quality of Service ▪ Block Acknowledgement ▪ 802.11ac Downlink MU-MIMO (receive) ▪ Dynamic Frequency Selection ▪ Beamforming ▪ TIM Frame TX and RX ▪ Multi-BSS/Station ▪ Transmit Rate Adaptation. ▪ Transmit Power Control

Feature	Description
WLAN Base Band	▪ 802.11ac 2x2 MU-MIMO (with on-chip Marvell RF radio) ▪ Backward compatibility with legacy 802.11 n/a/b/g technology ▪ WLAN/Bluetooth LNA sharing ▪ PHY rate up to 866.7Mbps ▪ 20MHz bandwidth/channel, 40MHz bandwidth/channel, upper/lower 20MHz packets in 40MHz channel, 20MHz duplicate legacy packets in 40MHz channel operation ▪ 80MHz bandwidth/channel, 4 positions of 20MHz packets in 80MHz channel, upper/lower 40MHz packets in 80MHz channel, 20MHz quadruplicate legacy packets in 80MHz channel mode operation ▪ Modulation and Coding Scheme (MCS): 802.11 ac (MCS0-9. Nsts=1/2); 802.11n (MCS0-15) ▪ Dynamic Frequency Selection (DFS) (Radar detection) – Enhanced radar detection for long and short pulse radar – Enhanced AGC scheme for DFS channel – Japan DFS requirements for W53 and W56 ▪ 802.11 K Radio Resource Measurement ▪ 802.11ac /802.11n optional MIMO features: – 20/40/80 MHz Coexistence with middle-packaged detection (GI detection) for enhanced CCA. – 1 spatial stream STBC reception and transmission – LDPC transmission and reception for 802.11ac and 802.11n – 256 QAM (MCS8-9) modulations supported – Short guard interval – RIFS on receive path for 802.11n packets – 802.11n Greenfield TX/RX ▪ Power Save Feature
WLAN Security	WLAN Encryption features supported include: ▪ Temporal Key Integrity Protocol (TKIP)/Wired Equivalent Privacy (WEP) ▪ Advanced Encryption Standard (AES)/Counter-Mode/CBC-MAC Protocol (CCMP) ▪ Advanced Encryption Standard (AES)/Cipher-Based Message Authentication Code (CMAC) ▪ Advanced Encryption Standard (AES)/Galois/Counter Mode Protocol (GCMP) ▪ WLAN Authentication and Private Infrastructure (WPAI)

Feature	Description							
WLAN Channel	Channel frequency supported.							
	20 MHz		40 MHz		80 MHz			
	Channel	Freq. (MHz)	Channel	Freq. (MHz)	Channel	Freq. (MHz)	Channel	Freq. (MHz)
	1	2412	36	5180	1-5	2422	42	5210
	2	2417	40	5200	2-6	2427	58	5290
	3	2422	44	5220	3-7	2432	74	5370
	4	2427	48	5240	4-8	2437	90	5410
	5	2432	52	5260	5-9	2442	106	5530
	6	2437	56	5280	6-10	2447	122	5610
	7	2442	60	5300	7-11	2452	138	5690
	8	2447	64	5320	36-40	5190	155	5775
	9	2452	100	5500	44-48	5230		
	10	2457	104	5520	52-56	5270		
	11	2462	108	5540	60-64	5310		
	12	2467	112	5560	68-72	5350		
	13	2472	116	5580	76-80	5390		
			120	5600	84-88	5430		
			124	5620	92-96	5470		
			128	5640	100-104	5510		
			132	5660	108-112	5550		
			136	5680	116-120	5590		
			140	5700	124-128	5630		
			144	5720	132-136	5670		
			149	5745	140-144	5710		
			153	5765	149-153	5755		
			157	5785	157-161	5795		
			161	5805				
		165	5825					

6 BLUETOOTH FUNCTIONAL DESCRIPTION

The GEM Hemochron 100A Bluetooth (BT) block is based on the 60-SIPT SiP that already has fully-integrated Bluetooth baseband and radio. Several features and functions are listed in [Table 5](#).

Table 5: Bluetooth functions

Feature	Description
Bluetooth Interface	Voice interface: ▪ Hardware support for continual PCM data transmission/reception without processor overhead. ▪ Standard PCM clock rates from 64 kHz to 2.048 MHz with multi-slot handshake and synchronization. ▪ A-law, U-law, and linear voice PCM encoding/decoding. ▪ SDIO interface ▪ High-Speed UART interface ▪ USB 2.0
Bluetooth Core functionality	▪ Bluetooth 4.2 ▪ Bluetooth Class 2/Bluetooth class 1 ▪ WLAN and Bluetooth share same LNA and antenna ▪ Digital audio interfaces with PCM/TDM interface for voice application ▪ Baseband and radio BDR and EDR package type: 1Mbps, 2Mbps, 3Mbps ▪ Fully functional Bluetooth baseband: AFH, forward error correction, header error control, access code correction, CRC, encryption bit stream generation, and whitening ▪ Adaptive Frequency Hopping (AFH) using Packet Error Rate (PER) ▪ Interlaced scan for faster connection setup ▪ Simultaneous active ACL connection setup ▪ Automatic ACL package type selection ▪ Full master and slave piconet support ▪ Scatter net support ▪ SCO/eSCO links with hardware accelerated audio signal processing and hardware supported PPEC algorithm for speech quality improvement ▪ All standard SCO/eSCO voice coding ▪ All standard pairing, authentication, link key, and encryption operations ▪ Encryption (AES) support
Bluetooth Low Energy (BLE) Core functionality	▪ Advertiser, Scanner, Initiator, Master, and Slave roles support (connects up to 16 links) ▪ WLAN/Bluetooth Coexistence (BCA) protocol support ▪ Shared RF with BDR/EDR ▪ Encryption (AES) support ▪ Intelligent Adaptive Frequency Hopping (AFH) ▪ LE privacy 1.2 ▪ LE Secure Connection ▪ LE Data Length Extension ▪ LE Advertising Length Extension ▪ 2 Mbps LE ▪ Direction Finding – Connectionless Angle of Departure (AoD) ▪ Direction Finding – Connectionless Angle of Arrival (AoA)

7 BLOCK DIAGRAM

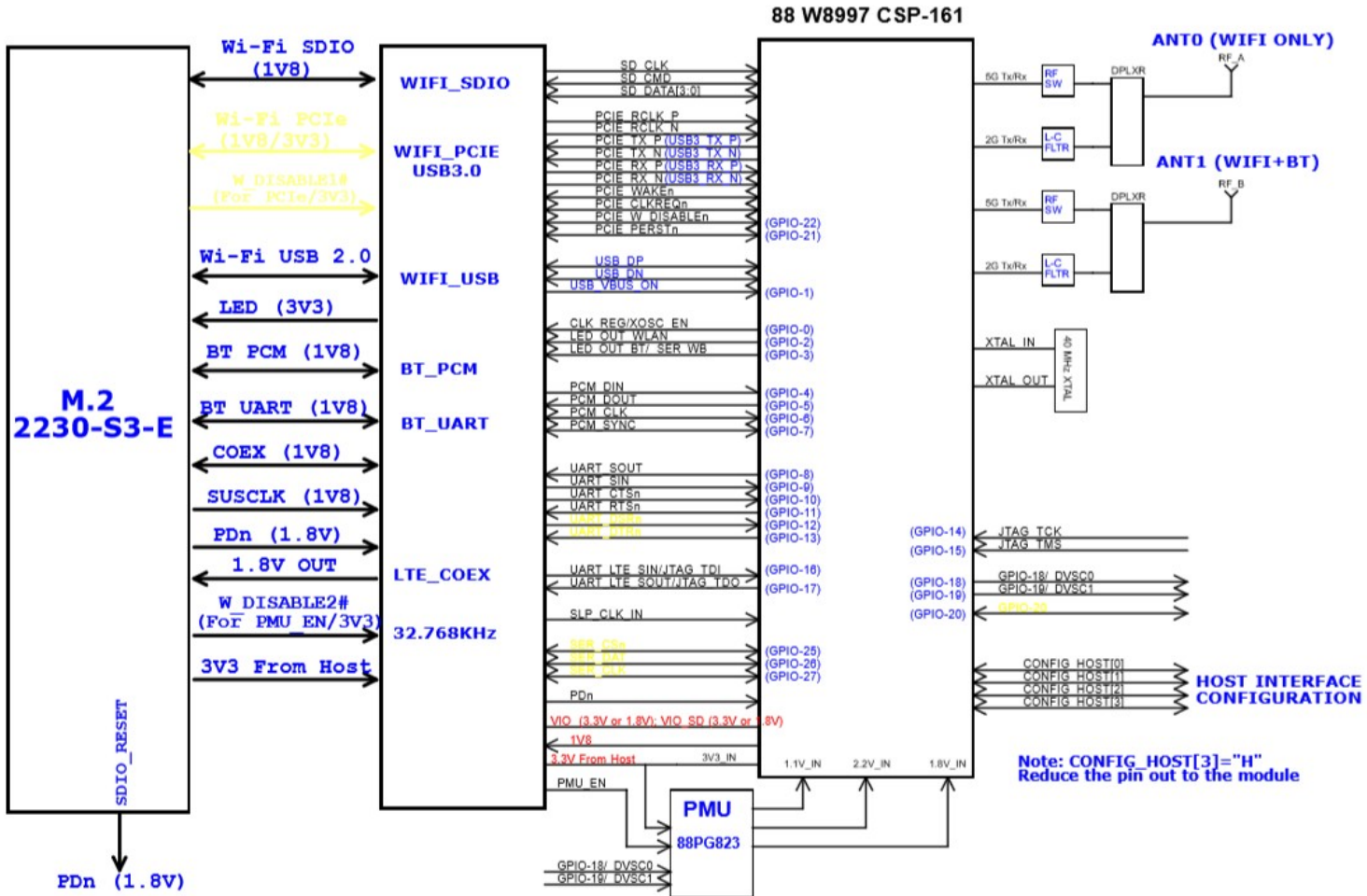


Figure 1: Block Diagram of GEM Hemochron 100A

8 ELECTRICAL CHARACTERISTICS

8.1 Absolute Maximum Ratings

Table 6 summarizes the absolute maximum ratings and Table 7 lists the recommended operating conditions for the GEM Hemochron 100A. Absolute maximum ratings are those values beyond which damage to the device can occur. Functional operation under these conditions, or at any other condition beyond those indicated in the operational sections of this document, is not recommended.

Note: Maximum rating for signals follows the supply domain of the signals.

Table 6: Absolute maximum ratings

Symbol (Domain)	Parameter	Max Rating	Unit
VIO_SD	WLAN host SDIO interface I/O supply (1.8V system)	2.2	V
VIO	I/O configuration power supply (1.8V system)	2.2	V
3V3	External 3.3V power supply	4.0	V
Storage	Storage Temperature	-40 to +85	°C
ANT0; ANT1	Maximum RF input (reference to 50-Ω input)	+10	dBm
ESD	Electrostatic discharge tolerance	2000	V

8.2 Recommended Operating Conditions

Table 7: Recommended Operating Conditions

Symbol (Domain)	Parameter	Min	Typ	Max	Unit
VIO_SD	WLAN host interface I/O supply	1.62	1.8	1.98	V
VIO	WLAN and BT GPIO I/O power supply	1.62	1.8	1.98	V
3V3	External 3.3V power supply	2.97	3.30	3.63	V
T-ambient	Ambient temperature	-30	25	85	°C

8.3 DC Electrical Characteristics

Table 8 list the general DC electrical characteristics over recommended operating conditions (unless otherwise specified).

Table 8: General DC electrical characteristics (For 1.8V operation VIO_SD;VIO)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
VIH	High Level Input Voltage	--	0.7 x 1V8		1V8+0.4	V
VIL	Low Level Input Voltage	--	-0.4		0.3 x 1V8	V
VHYS	Input Hysteresis	--	100			mV
VOH	Output high Voltage	--	1V8-0.4			V
VOL	Output low Voltage	--			0.4	V

8.4 WLAN Radio Receiver Characteristics

Table 9 and Table 10 summarize the WLAN GEM Hemochron 100A receiver characteristics.

Table 9: WLAN receiver characteristics for 2.4 GHz signal chain operation

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Fr _x	Receive input frequency range		2.412		2.484	GHz
Srf	Sensitivity					
	CCK, 1 Mbps	See Note ³		-95		dBm
	CCK, 11 Mbps			-90		
	OFDM, 6 Mbps			-91		
	OFDM, 54 Mbps			-75		
	HT20, MCS0			-91		
	HT20, MCS7			-73		
Rad _j	Adjacent channel rejection					
	OFDM, 6 Mbps	See Note ⁴		TBD		dB
	OFDM, 54 Mbps			TBD		
	HT20, MCS0			TBD		
	HT20, MCS7			TBD		

³Performance data are measured under signal chain operation.

⁴Performance data are measured under signal chain operation.

Table 10: WLAN receiver characteristics for 5 GHz dual chain operation

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Fr _x	Receive input frequency range		5.15		5.825	GHz
Srf	Sensitivity					
	OFDM, 6 Mbps	See Note ⁵		-89		dBm
	OFDM, 54 Mbps			-74		
	HT20, MCS0			-89		
	HT20, MCS7			-70		
	HT40, MCS0			-86		
	HT40, MCS7			-69		
Rad _j	Adjacent channel rejection					
	OFDM, 6 Mbps	See Note ⁶		TBD		dB
	OFDM, 54 Mbps			TBD		
	HT20, MCS0			TBD		
	HT20, MCS7			TBD		

⁵Performance data are measured under signal chain operation

⁶Performance data are measured under signal chain operation.

8.5 WLAN Transmitter Characteristics

Table 11: WLAN transmitter characteristics for 2.4 GHz per chain operation

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Ftx	Transmit output frequency range		2.412		2.484	GHz
Pout	Output power	See Note ⁷				dBm
	11b mask compliant	1-11Mbps		18		
	11g mask compliant	6-36Mbps		18		
	11g EVM compliant	48-54Mbps		16		
	11n HT20 mask compliant	MCS0-5/MCS8-13		18		
	11n HT20 EVM compliant	MCS6-7/MCS14-15		16		
	11n HT40 mask compliant	MCS0-5/MCS8-13		16		
	11n HT40 EVM compliant	MCS6-7/MCS14-15		14		
ATx	Transmit power accuracy at 25 °C	-	-	+ 2.0		dB

Freq.	Mode/Rate (Mbps)	Output Power Per Chain (dBm)	Max. Current Consumption	
			Single Chain (mA) ⁸	Dual Chains (mA) ⁸
2412 MHz	1 Mbps	18dBm	340	620
	54 Mbps	16dBm	280	500
	HT20 MCS7	16dBm	280	510
2442 MHz	1 Mbps	18dBm	340	620
	54 Mbps	16dBm	280	500
	HT20 MCS7	16dBm	280	510
2472 MHz	1 Mbps	18dBm	340	620
	54 Mbps	16dBm	280	500
	HT20 MCS7	16dBm	280	510

Table 11: WLAN transmitter characteristics for 5 GHz per chain operation

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Ftx	Transmit output frequency range		5.15		5.925	GHz
Pout	Output power	See Note ³				dBm
	11a mask compliant	6-36Mbps		18		
	11a EVM compliant	48-54Mbps		16		
	11n HT20 mask compliant	MCS0-5/MCS8-13		18		
	11n HT20 EVM compliant	MCS6-7/MCS14-15		16		
	11n HT40 mask compliant	MCS0-5/MCS8-13		16		
	11n HT40 EVM compliant	MCS6-7/MCS14-15		16		
	11ac HT20 mask compliant	MCS0-6 (Ntst=1,2)		18		
	11ac HT20 EVM compliant	MCS7-8(Ntst=1,2)		16		
	11ac HT40 mask compliant	MCS0-5 (Ntst=1,2)		16		
	11ac HT40 EVM compliant	MCS6-9(Ntst=1,2)		14		
	11ac HT80 mask compliant	MCS0-5 (Ntst=1,2)		14		

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
	11ac HT80 EVM compliant	MCS6-8(Ntst=1,2)		12		
	11ac HT80 EVM compliant	MCS9(Ntst=1,2)		10		
ATx	Transmit power accuracy at 25 °C	-	-	+ 2.0		dB

Freq.	Mode/Rate [Mbps]	Output Power Per Chain [dBm]	Max. Current Consumption	
			Single Chain (mA) ⁸	Dual Chains (mA) ⁸
5180 MHz	6 Mbps	18 dBm	400	710
	54 Mbps	16 dBm	330	610
	HT20 MCS0	18 dBm	400	720
	HT20 MCS7	16 dBm	360	620
5190 MHz	HT40 MCS7	14 dBm	320	550
5500 MHz	6 Mbps	18 dBm	380	680
	54 Mbps	16 dBm	330	600
	HT20 MCS0	18 dBm	370	690
	HT20 MCS7	16 dBm	320	600
5510 MHz	HT40 MCS7	14 dBm	300	530
5825 MHz	6 Mbps	18 dBm	380	690
	54 Mbps	16 dBm	310	600
	HT20 MCS0	18 dBm	360	710
	HT20 MCS7	16 dBm	340	550
5795 MHz	HT40 MCS7	14 dBm	300	530

⁷Performance data are measured under single chain operation.

Note: Final TX power values on each channel are limited by the regulatory certification test limit.

9 BLUETOOTH RADIO CHARACTERISTICS

Table 12 through Table 15 describe the basic rate transmitter performance, enhanced data transmitter performance, basic rate receiver performance, enhanced rate receiver performance, and current consumption conditions at 25°C.

Table 12: Basic rate transmitter performance temperature at 25°C (1.8V)

Test Parameter		Min	Typ	Max	BT Spec.	Unit
Maximum RF Output Power		8	10	11	0 ~ +20	dBm
Frequency Range		2.4	—	2.4835	$2.4 \leq f \leq 2.4835$	GHz
20 dB Bandwidth		—	919.5	—	≤ 1000	KHz
Δf_{1avg} Maximum Modulation		140	165	175	$140 < \Delta f_{1avg} < 175$	KHz
Δf_{2max} Minimum Modulation		—	135	—	≥ 115	KHz
$\Delta f_{2avg}/\Delta f_{1avg}$		—	0.9	—	≥ 0.80	—
Initial Carrier Frequency		—	+/-5	—	$\leq \pm 75$	KHz
Drift Rate (DH1 package)		—	4	—	≤ 20	KHz/50 μ s
Drift (DH3 packet)		—	8	—	≤ 25	KHz
Drift (DH5 packet)		—	7	—	≤ 40	KHz
Adjacent Channel Power	$F \geq \pm 3\text{MHz}$	—	-50	—	< -40	dBm
	$F = \pm 2\text{MHz}$	—	-46	—	≤ -20	dBm
	$F = \pm 1\text{MHz}$	—	-15	—	N/A	dBm

Table 13: Enhanced data rate transmitter performance 25°C (1.8V)

Test Parameter		Min	Typ	Max	BT Spec.	Unit
Relative Transmit Power		5	7	9		dBm
Max Carrier Frequency Stability wo	2-DH5	—	1	—	≤ ±10	KHz
	3-DH5	—	1	—		
Max Carrier Frequency Stability wi	2-DH5	—	4	—	≤ ±75	KHz
	3-DH5	—	4	—		
Max Carrier Frequency Stability w0+wi	2-DH5	—	5	—	≤ ±75	KHz
	3-DH5	—	5	—		
RMS DEVM	2-DH5	—	4	—	≤ 20	%
	3-DH5	—	4	—	≤13	%
Peak DEVM	2-DH5	—	9	—	≤ 35	%
	3-DH5	—	9	—	≤ 25	%
99% DEVM	2-DH5	—	12	—	≤ 30	%
	3-DH5	—	12	—	≤ 20	%
EDR Differential Phase Encoding		—	99	—	≥ 99	%
Adjacent Channel Power	F ≥ ± 3MHz	—	TBD	—	< −40	dBm
	F = ± 2MHz	—	TBD	—	≤ −20	dBm

Table 14: Basic rate receiver performance at 1.8V

Test Parameter		Min	Typ	Max	BT Spec.	Unit
Sensitivity (1DH5)	BER ≤ 0.1%	—	-95	-92	≤ -70	dBm
Maximum Input	BER ≤ 0.1%	-20	-10	—	≥ -20	dBm
Carrier-to-Interferer Ratio (C/I)	Co-Channel	—	10	11	11	
	C/I (± 1 MHz)	—	-4	0	0	dB
	C/I (± 2 MHz)	—	-45	—	-30	dB
	C/I (± 3 MHz)	—	-49	—	-40	dB
Maximum Level of Intermodulation Interferers		-39	-30	-	≥ -39	dBm

Table 15: Enhanced data rate receiver performance 1.8V

Test Parameter		Min	Typ	Max	Bluetooth Specification	Unit
Sensitivity (BER ≤ 0.01%)	π/4 DQPSK	—	-94	-91	≤ -70	dBm
	8 DPSK	—	-88	-85	≤ -70	dBm
Maximum Input (BER ≤ 0.1%)	π/4 DQPSK	-20	—	—	≥ -20	dBm
	8 DPSK	-20	—	—	≥ -20	dBm
Co-Channel C/I (BER ≤ 0.1%)	π/4 DQPSK	—	10	13	≤ ±13	dB
	8 DPSK	—	16	20	≤ ±20	dB
Adjacent Channel C/I (1 MHz)	π/4 DQPSK	—	-9	0	≤ 0	dB
	8 DPSK	—	-6	5	≤ 5	dB
Second Adjacent Channel C/I (2 MHz)	π/4 DQPSK	—	-47	-30	≤ -30	dB
	8 DPSK	—	-42	-25	≤ -25	dB
Third Adjacent Channel C/I (3 MHz)	π/4 DQPSK	—	-51	-40	≤ -40	dB
	8 DPSK	—	-48	-33	≤ -33	dB
Out-of-band blocking	30-2000MHz	—	-12.5	—	—	dBm
	2-2.399GHz	—	-12.4	—	—	dBm
	2.484-3GHz	—	-18	—	—	dBm
	3-12.75GHz	—	-2.6	—	—	dBm

10 SDIO TIMING REQUIREMENTS

The SDIO host interface pins are powered from the VIO_SD voltage supply. The SDIO electrical specifications are identical for the 1-bit SDIO and 4-bit SDIO modes.

10.1 SDR12, SDR25, SDR50 Mode (up to 100MHz) (1.8V)

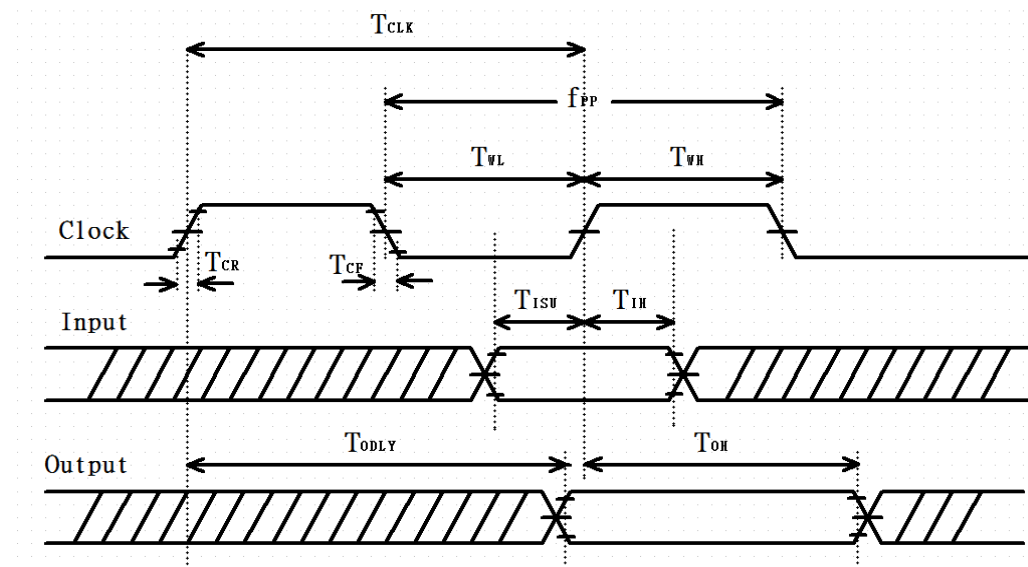


Figure 2:SDIO protocol timing Diagram--- SDR12, SDR25, SDR50 modes (up to 100 MHz) (1.8V)

Table 16: SDIO timing requirements--- SDR12, SDR25, SDR50 modes (up to 100 MHz) (1.8V)

Note: Over full range of values specified in the Recommended Operating Conditions unless otherwise specified.

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
f _{PP}	Clock Frequency	SDR12/25/50	25	-	100	MHz
T _{ISU}	Input setup time	SDR12/25/50	3	--	-	ns
T _{IH}	Input Hold time	SDR12/25/50	0.8	-	-	ns
T _{CLK}	Clock Time	SDR12/25/50	10	-	40	ns
T _{CR} , T _{CF}	Raise time, Fall time T _{CR} , T _{CF} < 2ns (max) at 100 MHz C _{CARD} = 10pF	SDR12/25/50	-	-	0.2 * T _{CLK}	ns
T _{ODLY}	Output delay time C _L ≤ 30pF	SDR12/25/50	-	-	7.5	ns
T _{OH}	Output hold time C _L = 15pF	SDR12/25/50	1.5	-	-	ns

10.2 SDR104 Mode (208MHz) (1.8V)

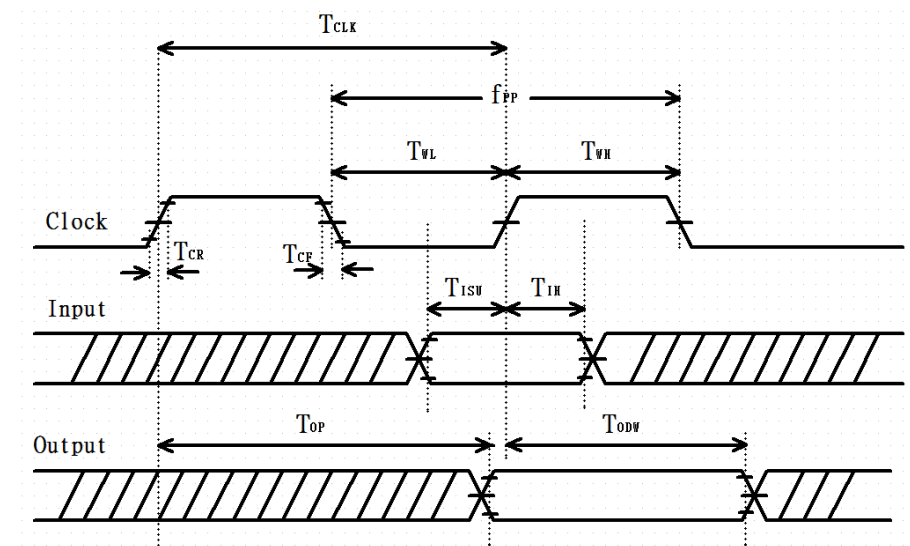


Figure 3: SDIO protocol timing Diagram--- SDR104 modes (up to 208 MHz) (1.8V)

Table 17: SDIO timing requirements--- SDR104 modes (up to 208MHz) (1.8V)

Note: Over full range of values specified in the Recommended Operating Conditions unless otherwise specified.

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
f _{PP}	Clock Frequency	SDR104	0	-	208	MHz
T _{ISU}	Input setup time	SDR104	1.4	--	-	ns
T _{IH}	Input Hold time	SDR104	0.8	-	-	ns
T _{CLK}	Clock Time	SDR104	4.8	-	-	ns
T _{CR} , T _{CF}	Raise time, Fall time T _{CR} , T _{CF} < 0.96ns (max) at 208 MHz C _{CARD} = 10 pF	SDR104	-	-	0.2 * T _{CLK}	ns
T _{OP}	Card Output phase	SDR104	0	-	10	ns
T _{ODW}	Output timing pf variable data window	SDR12/25/50	2.88	-	-	ns

10.3 DDR50 Mode (50MHz) (1.8V)

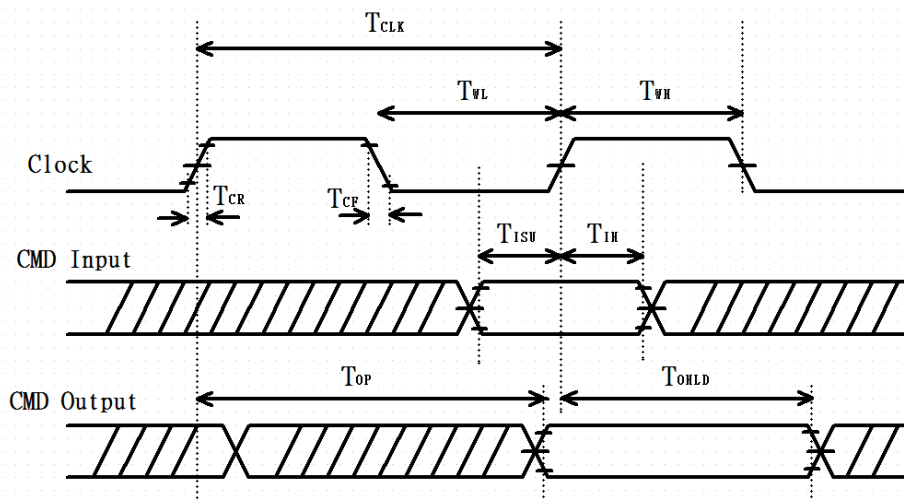


Figure 4: SDIO CMD timing Diagram--- DDR50 modes (50 MHz) (1.8V)

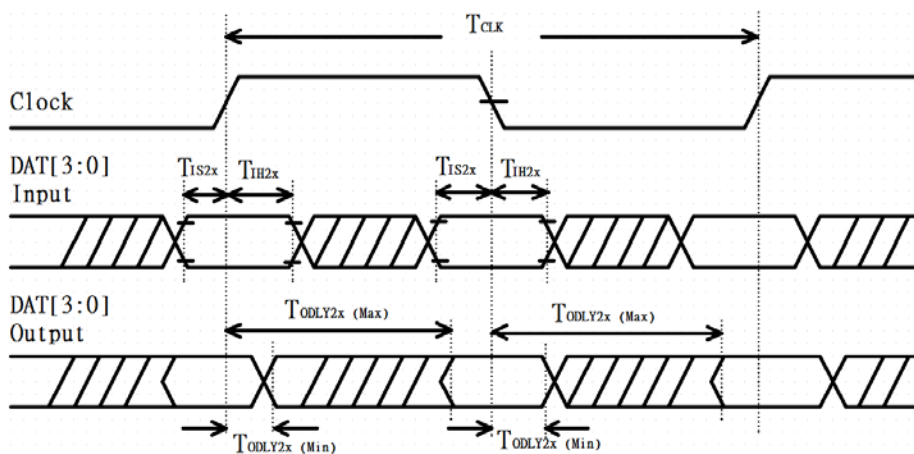


Figure 5: SDIO DAT[3:0] timing Diagram--- DDR50 modes (50 MHz) (1.8V)

Note: In DDR50 mode, DAT[3:0] lines are samples on both edges pF the clock (not applicable for CMD line)

Table 18: SDIO timing requirements--- DDR50 modes (50MHz)

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Clock						
TCLK	Clock time 50MHz (max) between rising edge	DDR50	20	--	--	ns
TCR, TCF	Rise time, fall time TCR, TCF <4.00ns (max) at 50MHz. CCARD=10pF	DDR50	--	--	0.2*TCLK	ns
Clock Duty	--	DDR50	45	--	55	%
CMD Input (referenced to clock rising edge)						

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
TIS	Input setup time CCARD $\leq$ 10pF (1 card)	DDR50	6	--	--	ns
IIH	Input hold time CCARD $\leq$ 10pF (1 card)	DDR50	0.8	--	--	ns
CMD Output (referenced to clock rising and falling edge)						
TODLY	Output delay time during data transfer mode CL $\leq$ 30pF (1card)	DDR50	--	--	13.7	ns
TOHLD	Output hold time CL $\geq$ 15pF (1 card)	DDR50	1.5	--	--	ns
DAT[3:0] Input (referenced to clock rising and falling edges)						
TIS2X	Input setup time CCARD $\leq$ 10pF (1 card)	DDR50	3	--	--	ns
TIH2X	Input hold time CCARD $\leq$ 10pF (1 card)	DDR50	0.8	--	--	ns
DAT[3:0] Output (referenced to clock rising and falling edges)						
TODLY2X (max)	Output delay time during data transfer mode CL $\leq$ 25pF (1card)	DDR50	--	--	7.0	ns
TODLY2X (min)	Output hold time CL $\geq$ 15pF (1 card))	DDR50	1.5	--	--	ns

11 USB SPECIFICATIONS

11.1 USB LS Driver and Receiver Parameters

Notes: Over full range of values specified in the Recommended Operating Conditions unless otherwise specified.

The load is 100 Ω differential for these parameters, unless other specified.

Table 16: USB LS Driver and Receiver Specifications

Symbol	Parameter	Min.	Typ.	Max.	Unit
BR	Baud rate	-	1.5	-	Mbps
BRPPM	Baud rate tolerance	-15000	-	15000	ppm
Driver Specifications					
VOH	Output signal ended high Defined with 1.425K Ω pull-up resistor to 3.6V	2.8	-	3.6	V
VOL	Output signal ended low Defined with 1.425K Ω pull-up resistor to ground	0.0	-	0.3	V
VCRS	Output signal crossover voltage	1.3		2.0	V
TLR	Data fall time	75.0	-	300.0	ns

Symbol	Parameter	Min.	Typ.	Max.	Unit
	Defined from 10% to 90% for raise time and 90% to 10% for fall time				
TLF	Data risel time Defined from 10% to 90% for raise time and 90% to 10% for fall time	75.0	-	300.0	ns
TLRFM	Rise and fall time matching	80.0	-	125.0	%
TUDJ1	Source jitter total: to next transition *Including frequency tolerance. Timing difference between the differential data signals. *Defined at crossover point of differential signals	-95	-	95	ns
TUDJ2	Source jitter total: for paired transitions *Including frequency tolerance. Timing difference between the differential data signals. *Defined at crossover point of differential signals	-150	-	150	ns
Receiver Specifications					
VIH	Input signal ended high	2.0	-	-	V
VIL	Input signal ended low	-	-	0.8	V
VDI	Differential input sensitivity	0.2	-	-	V

11.2 USB FS Driver and Receiver Parameters

Notes: Over full range of values specified in the Recommended Operating Conditions unless otherwise specified.

The load is 100Ω differential for these parameters, unless other specified.

Table 17: USB FS Driver and Receiver Specifications

Symbol	Parameter	Min.	Typ.	Max.	Unit
BR	Baud rate	-	12.0	-	Mbps
BRPPM	Baud rate tolerance	-2500	-	2500	ppm
Driver Specifications					
VOH	Output signal ended high Defined with 1.425KΩ pull-up resistor to 3.6V	2.8	-	3.6	V
VOL	Output signal ended low Defined with 1.425KΩ pull-up resistor to ground	0.0	-	0.3	V
VCRS	Output signal crossover voltage	1.3		2.0	V
TFR	Output raise time Defined from 10% to 90% for raise time and 90% to 10% for fall time	-4.0	-	20.0	ns
TFL	Output fall time Defined from 10% to 90% for raise time and 90% to 10% for fall time	-4.0	-	20.0	ns
TDJ1	Source jitter total: to next transition *Including frequency tolerance. Timing difference between the differential data signals. *Defined at crossover point of differential signals	-3.5	-	3.5	ns
TDJ2	Source jitter total: for paired transitions *Including frequency tolerance. Timing difference between the differential data signals. *Defined at crossover point of differential signals	-4.0	-	4.0	ns
TFDEOP	Source jitter for differential transition to SEO transition. Defined at crossover point of differential signals	-2.0	-	5.0	ns
Receiver Specifications					
VIH	Input signal ended high	2.0	-	-	V
VIL	Input signal ended low	-	-	0.8	V
VDI	Differential input sensitivity	0.2	-	-	V
TJR1	Receiver jitter: to next transition Defined at crossover point of differential data signals	-18.5	-	18.5	ns
TJR2	Receiver jitter: for paired transitions Defined at crossover point of differential data signals	-9.0	-	9.0	ns

11.2 USB FS Driver and Receiver Parameters

Notes: Over full range of values specified in the Recommended Operating Conditions unless otherwise specified.

The load is 100Ω differential for these parameters, unless other specified.

Table 18: USB HS Driver and Receiver Specifications

Symbol	Parameter	Min.	Typ.	Max.	Unit
BR	Baud rate	-	480	-	Mbps
BRPPM	Baud rate tolerance	-500	-	500	ppm
Driver Specifications					
VHSOH	Data signal high	360	-	440	mV
VHSOL	Data signal low	-10	-	10	mV
THSR	Data rise time Defined from 10% to 90% for raise time and 90% to 10% for fall time	500	-	-	ns
THSF	Data fall time Defined from 10% to 90% for raise time and 90% to 10% for fall time	-500	-	-	ns
Receiver Specifications					
VHSCM	Input signal ended low	-50	-	500	mV

12 PCM INTERFACE SPECIFICATIONS

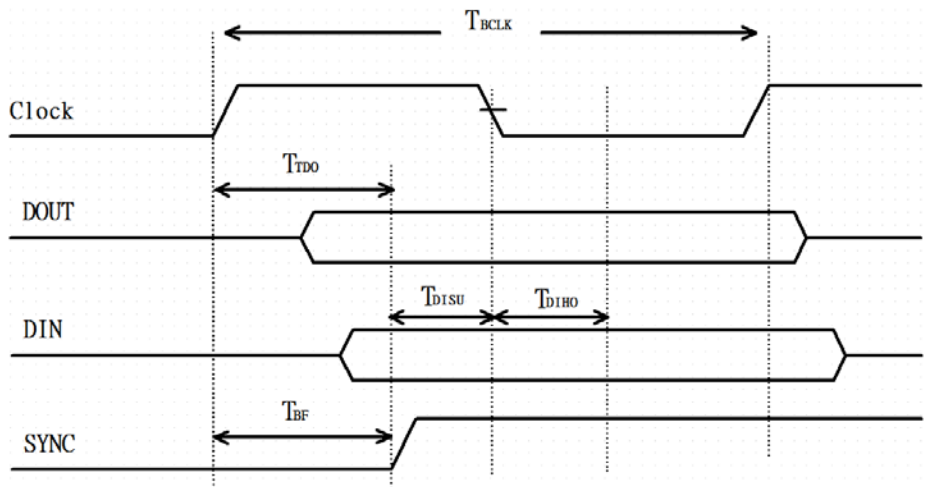


Figure 6: PCM Timing Specification – Master Mode

Table 19: PCM Timing Specification – Master Mode

Symbol	Parameter	Min.	Typ.	Max.	Unit
F _{BCLK}	-	-	2/2.048	-	MHz
Duty Cycle _{BCLK}	-	0.4	0.5	0.6	-
T _{BCLK} rise/fall	-	-	3	-	ns
T _{D0}	-	-	-	15	ns
T _{DISU}	-	20	-	-	ns
T _{DIHO}	-	15	-	-	ns
T _{BF}	-	-	-	15	ns

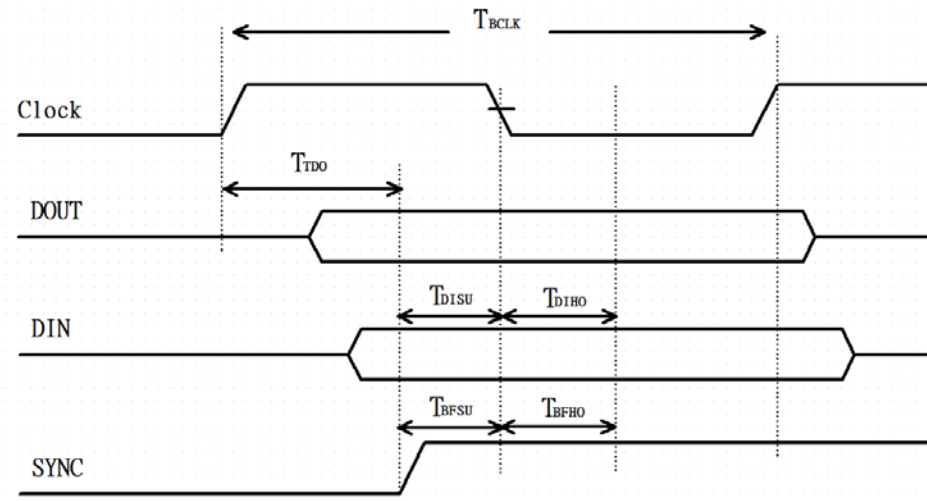


Figure 7: PCM Timing Specification – Slave Mode

Table 20: PCM Timing Specification – Slave Mode

Symbol	Parameter	Min.	Typ.	Max.	Unit
FBCLK	-	-	2/2.048	-	MHz
Duty Cycle _{BCLK}	-	0.4	0.5	0.6	-
T _{BCLK rise/fall}	-	-	3	-	ns
TDO	-	-	-	30	ns
TDISU	-	15	-	-	ns
TDIHO	-	10	-	-	ns
TBFSU	-	15	-	-	ns
TBFHO	-	10	-	-	ns

2. PIN DEFINITIONS

Table 21: Pin definitions

Pin #	Name	Type	Voltage Ref.	Description	If Not Used
1	GND	-	-	Ground	GND
2	3.3V	Power	-	3.3V module power supply	-
3	USB_D+	I/O	3.3V	USB Differential Data-Positive	N/C
4	3.3V	Power	-	3.3V module power supply	-
5	USB_D-	I/O	3.3V	USB Differential Data-Negative	N/C
6	LED1#	O,PU	3.3V	LED indicator for WLAN with 10mA drive capability	N/C
7	GND	-	-	Ground	GND
8	PCM_CLK	I/O	1.8V	PCM Clock Signal (Optional) Optimal clock used for some codecs. Output if Master mode; Input if Slave mode.	N/C
9	SDIO CLK	I,PU	1.8V	SDIO 4-bit Mode Clock Input	N/C
10	PCM_SYNC	I/O	1.8V	PCM Sync Pulse Signal Output if Master mode; Input if Slave mode.	N/C
11	SDIO CMD	I/O	1.8V	SDIO 4-bit Mode Command/Response	N/C
12	PCM_IN	I	1.8V	PCM Data	N/C
13	SDIO DATA0	I/O,PU	1.8V	SDIO 4-bit Mode DATA line Bit[0]	N/C
14	PCM_OUT	O	1.8V	PCM Data	N/C
15	SDIO DATA1	I/O,PU	1.8V	SDIO 4-bit Mode DATA line Bit[1]	N/C
16	LED2#	N/C	N/C	N/C	N/C
17	SDIO DATA2	I/O,PU	1.8V	SDIO 4-bit Mode DATA line Bit[2]	N/C
18	GND	-	-	Ground	GND
19	SDIO DATA3	I/O,PU	1.8V	SDIO 4-bit Mode DATA line Bit[3]	N/C
20	UART WAKE#	O	3.3V	Reserved for BT to wakeup Host	N/C
21	SDIO WAKE#	O	1.8V	Reserved for Wake-On-WLAN	N/C
22	UART TXD	O	1.8V	UART Serial Data Output	N/C

Pin #	Name	Type	Voltage Ref.	Description	If Not Used
23	SDIO RESET#	N/C	N/C	N/C	N/C
32	UART RXD	I	1.8V	UART Serial Data Input	N/C
33	GND	-	-	Ground	GND
34	UART RTS	O,WPU	1.8V	UART Request To Send (Active low)	N/C
35	PERp0	N/C	1.8V	PCIe Receive Data-Positive	N/C
36	UART CTS	I, PU	1.8V	UART Clear To Send (Active low)	N/C
37	PERn0	N/C	1.8V	PCIe Receive Data-Negative	N/C
38	VENDOR DEFINED38	N/C	N/C	N/C	N/C
39	GND	-	-	Ground	GND
40	VENDOR DEFINED40	N/C	N/C	N/C	N/C
41	PETp0	N/C	1.8V	PCIe Transmit Data-Positive	N/C
42	VENDOR DEFINED42	N/C	N/C	N/C	N/C
43	PETn0	N/C	1.8V	PCIe Transmit Data-Negative	N/C
44	COEX3	I/O	1.8V	General purpose I/O pin.	N/C
45	GND	-	-	Ground	GND
46	COEX2	O,PD	1.8V	Serial data to external LTE device/	N/C
47	REFCLKp0	N/C	1.8V	PCIe Differential Clock input-Positive	N/C
48	COEX1	I,PD	1.8V	Serial data from external LTE device/	N/C
49	REFCLKn0	N/C	1.8V	PCIe Differential Clock input-Negative	N/C
50	SUSCLK(32KHz)	I,PU	3.3V	Sleep Clock Input An external sleep clock of 32.768KHz with minimum +/- 250ppm is required for power saving mode	-
51	GND	-	-	Ground	GND
52	PERST0#	N/C	3.3V	PCIe host indication to reset the device (input) (active low)	N/C
53	CLKREQ0#	N/C	3.3V	PCIe clock request (input/output) (active low)	GND
54	W_DISABLE2#	N/C	3.3V	N/C	N/C
55	PEWAKE0#	N/C	3.3V	PCIe wake signal (input/output) (active low)	N/C
56	W_DISABLE1# (O)(0/3.3V)	I,PU	3.3V	Enable input for all Regulators inside the 60-SIPT. Note: DO NOT float this pin. Pull-up to 3.3V with 100K for normal operation.	N/C
57	GND	-	-	Ground	GND
58	I2C DATA (I/O)(0/3.3V)	N/C	N/C	N/C	N/C
59	RESERVED/PETp1	N/C	N/C	N/C	N/C
60	I2C CLK (O)(0/3.3V)	N/C	N/C	N/C	N/C
61	RESERVED/PETn1	N/C	N/C	N/C	N/C
62	ALERT# (I)(0/3.3V)	N/C	N/C	N/C	N/C

Pin #	Name	Type	Voltage Ref.	Description	If Not Used
63	GND	-	-	Ground	GND
64	RESERVED	N/C	N/C	N/C	N/C
65	RESERVED/PERp1	N/C	N/C	N/C	N/C
66	UIM_SWP/PERST1#	N/C	N/C	N/C	N/C
67	RESERVED/PERn1	N/C	N/C	N/C	N/C
68	UIM_POWER_SNK/ CLKREQ1#	N/C	N/C	N/C	N/C
69	GND	-	-	Ground	GND
70	UIM_POWER_SRC/ GPIO1/PEWAKE1#	N/C	N/C	N/C	N/C
71	RESERVED/REFCLKp 1	N/C	N/C	N/C	N/C
72	3.3V	Power	-	3.3V module power supply	-
73	RESERVED/REFCLKn 1	N/C	N/C	N/C	N/C
74	3.3V	Power	-	3.3V module power supply	-
75	GND	-	-	Ground	GND
76	GND	-	-	Ground	GND
77	GND	-	-	Ground	GND

13 MECHANICAL SPECIFICATIONS

Module dimensions of GEM Hemochron 100A are 22 mm x 30 mm x 3.3 mm. Detail drawings are shown in [Figure 8](#).

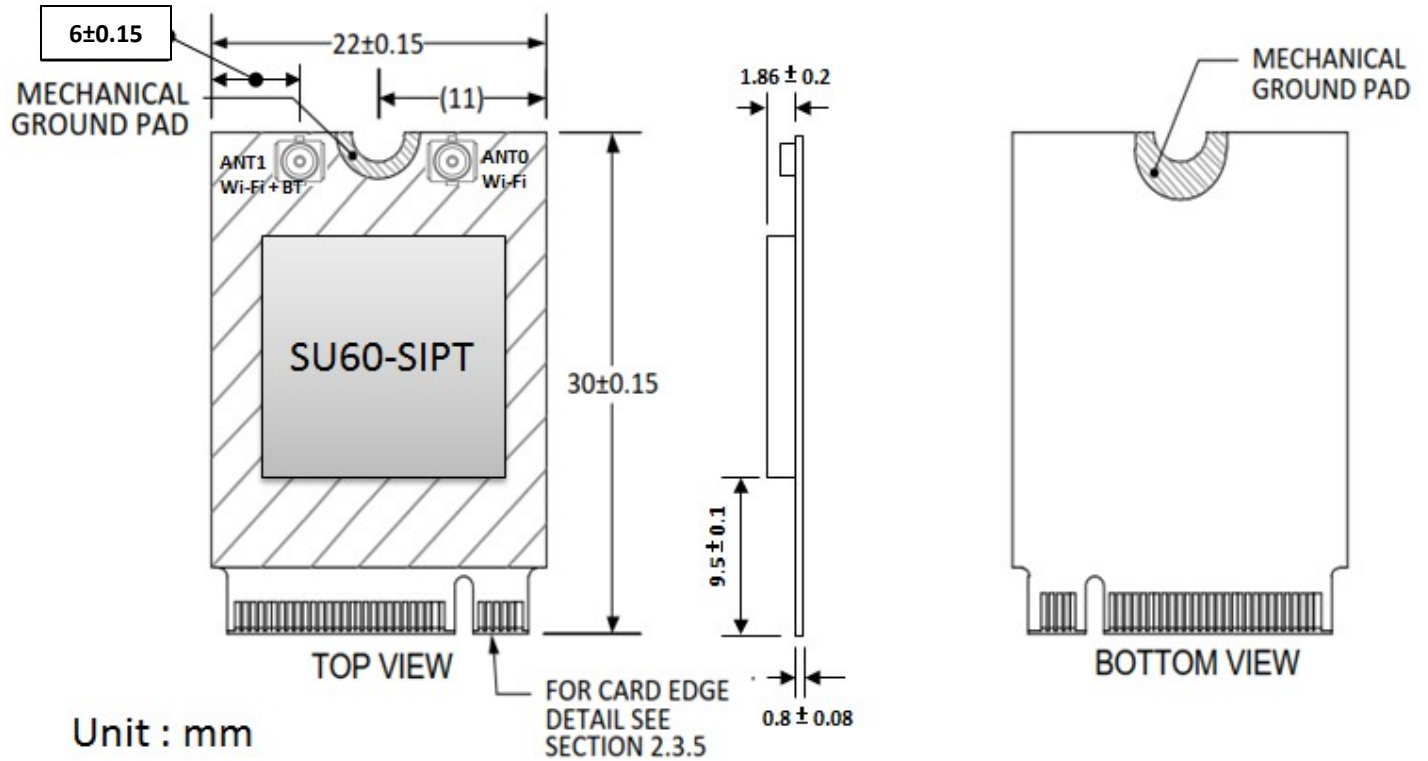


Figure 8: Module dimension of GEM Hemochron 100A

Note: The Wi-Fi MAC address is located on the product label.
The last digit of Wi-Fi MAC address is assigned to either *0*, *4*, *8* or *C*.
The BT MAC address is the Wi-Fi MAC address plus 3.

14 MOUNTING

The GEM Hemochron 100A connects to the host via a standard PCI EXPRESS M2 connector. The Kyocera's (www.Kyocera-connector.com) 6411 series provide 1.8 mm, 2.3 mm and 3.2 mm connector heights.

Because the GEM Hemochron 100A is a single-side component module, we recommend the following part number (which has a 2.3 mm connector height): **24-6411-067-101-894E**

The stand-off mating to the recommend 2.3 mm connector from EMI STOP (www.EMISTOP.com) is part number **F50M16-041525P1D4M**. Detail layout and stencil opening are show in [Figure 9](#).

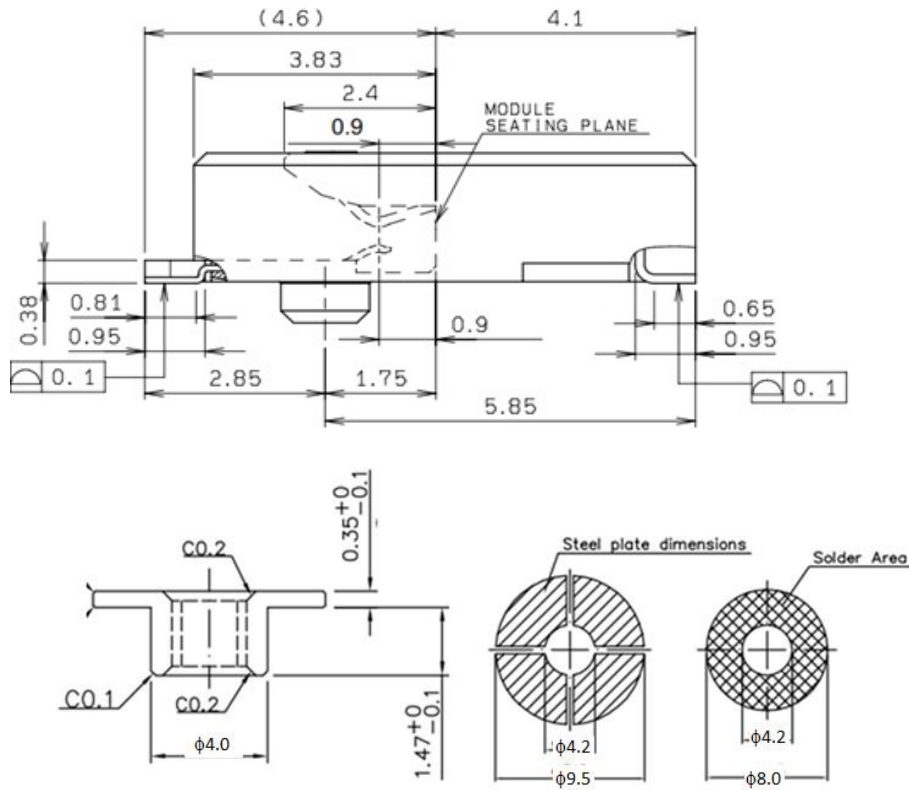


Figure 9: Mounting information of the GEM Hemochron 100A and recommended layout pattern for the stand-off

15 RF LAYOUT DESIGN GUIDELINES / PRECAUTIONS

The following is a list of RF layout design guidelines and recommendation when installing a Accriva Diagnostics radio into your device.

- Do not run antenna cables directly above or directly below the radio.
- Do not place any parts or run any high speed digital lines below the radio.
- If there are other radios or transmitters located on the device (such as a *Bluetooth* radio), place the devices as far apart from each other as possible. Also, make sure there is at least 25 dB isolation between the Bluetooth antenna and the Wi-Fi antenna.
- Ensure that there is the maximum allowable spacing separating the antenna connectors on the Accriva Diagnostics radio from the antenna. In addition, do not place antennas directly above or directly below the radio.
- Accriva Diagnostics recommends the use of a double-shielded cable for the connection between the radio and the antenna elements.
- Be sure to put the capacitor on the power pin as close as possible to reduce the radiation issue.
- Use proper electro-static-discharge (ESD) procedures when installing the Accriva Diagnostics radio module.
- To get maximum throughput when operate at MIMO 2x2, two antennas with at least 25 dB isolation are recommended.
- To avoid negatively impacting Tx power and receiver sensitivity, do not cover the antennas with metallic objects or components.
- Opening/handling/removing must be done on an anti-ESD treated workbench. All workers must be also have undergone anti-ESD treatment.
- The devices should be mounted within one year of the date of delivery.

16 REGULATORY

16.1 Regulatory IDs Summary

Model	US/FCC	Canada/IC
GEM Hemochron 100A	2AQV3-GEM100A	24216-GEM100A

16.2 Certified Antennas

Model	Type	Connector	2400~2483.5MHz
			5150~5250MHz 5250~5350MHz 5470~5725MHz 5725~5850MHz
Laird/NanoBlade-IP04	PCB Dipole	IPEX U.FL	2 dBi (2.4-2.5 GHz), 3.9 dBi (5.15-5.35 GHz), 4 dBi (5.6 GHz)
Laird/MAF95310 Mini NanoBlade Flex	PCB Dipole	IPEX U.FL	2.79 dBi @ 2.4 GHz, 3.38 dBi @ 5 GHz
Ethertronics/WLAN_1000146	Magnetic Dipole	IPEX U.FL	2.5 dBi (2.390-2.490), 3.5 dBi (4.900-5.100), 3.5 dBi (5.150-5.350), 3.5 dBi (5.70-5.900)
LSR/FlexPIFA 001-0016	PIFA	IPEX U.FL	2.5 dBi@2.4 GHz, 3 dBi@5 GHz
mFlexPIFA 001-0030	PIFA	IPEX U.FL	2 dBi (2.4-2.5 GHz)
LSR/001-0009	Dipole	IPEX U.FL	2 dBi@2.4 GHz, 2 dBi@5 GHz

17 FCC AND IC REGULATORY

Model	US/FCC	Canada/IC
GEM Hemochron 100A	2AQV3-GEM100A	24216-GEM100A

The GEM Hemochron 100A has been designed to pass certification with the antenna listed below. The required antenna impedance is 50 ohms.

Table 22: FCC antenna information

Model	Type	Connector	Peak gain (dBi)				
			2400~2483.5 MHz	5150~5250 MHz	5250~5350 MHz	5470~5725 MHz	5725~5850 MHz
Laird/NanoBlade-IP04	PCB Dipole	IPEX U.FL	2.0 dBi	3.9 dBi	3.9 dBi	4.0dBi	
Laird/MAF95310 Mini NanoBlade Flex	PCB Dipole	IPEX U.FL	2.79 dB	3.38 dBi			
Ethertronics/ WLAN_1000146	Magnetic Dipole	IPEX U.FL	2.5 dBi	3.5 dBi			
LSR/FlexPIFA 001-0016	PIFA	IPEX U.FL	2.5dBi	3.0 dBi			
LSR/001-0009	Dipole	IPEX U.FL	2.0dBi	2.0 dBi			

17.1 FCC

Federal Communication Commission Interference Statement

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

FCC Caution

Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate this equipment.

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Important Note

Radiation Exposure Statement

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator and your body.

This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

Country Code selection feature to be disabled for products marketed to the US/Canada.

This device is intended only for OEM integrators under the following conditions:

1. The antenna must be installed such that 20 cm is maintained between the antenna and users, and
2. The transmitter module may not be co-located with any other transmitter or antenna,
3. For all products market in US, OEM has to limit the operation channels in CH1 to CH11 for 2.4G band by supplied firmware programming tool. OEM shall not supply any tool or info to the end-user regarding to Regulatory Domain change.

As long as the three conditions above are met, further **transmitter** testing will not be required. However, the OEM integrator is still responsible for testing their end-product for any additional compliance requirements required with this module installed.

Important Note

In the event that these conditions **cannot be met** (for example certain laptop configurations or co-location with another transmitter), then the FCC authorization is no longer considered valid and the FCC ID **cannot** be used on the final product. In these circumstances, the OEM integrator will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate FCC authorization.

End Product Labeling

This transmitter module is authorized only for use in device where the antenna may be installed such that 20 cm may be maintained between the antenna and users. The final end product must be labeled in a visible area with the following:

Contains FCC ID: 2AQV3-GEM100A .

Manual Information to the End User

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module.

The end user manual shall include all required regulatory information/warning as show in this manual.

17.2 Industry Canada

Industry Canada Statement

This device complies with Industry Canada's license-exempt RSSs. Operation is subject to the following two conditions:

- This device may not cause interference; and
- This device must accept any interference, including interference that may cause undesired operation of the device.

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence.

L'exploitation est autorisée aux deux conditions suivantes:

- l'appareil ne doit pas produire de brouillage;
- l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

This radio transmitter (IC: 24216-GEM100A) has been approved by Industry Canada to operate with the antenna types listed below with the maximum permissible gain indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Le présent émetteur radio (IC: 24216-GEM100A) a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés ci-dessous et ayant un gain admissible maximal. Les types d'antenne non inclus dans cette liste, et dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

Antenna Information

Table 23: Antenna information

Model	Type	Connector	Peak gain (dBi)				
			2400~2483.5 MHz	5150~5250 MHz	5250~5350 MHz	5470~5725 MHz	5725~5850 MHz
Laird/NanoBlade-IP04	PCB Dipole	IPEX U.FL	2.0 dBi	3.9 dBi	3.9 dBi	4.0 dBi	
Laird/MAF95310 Mini NanoBlade Flex	PCB Dipole	IPEX U.FL	2.79 dB	3.38 dBi			
Ethertronics/ WLAN_1000146	Magnetic Dipole	IPEX U.FL	2.5 dBi	3.5 dBi			
LSR/ FlexPIFA 001-0016	PIFA	IPEX U.FL	2.5 dBi	3.0 dBi			
LSR/001-0009	Dipole	IPEX U.FL	2.0 dBi	2.0 dBi			

Caution:

(i) The device for operation in the band 5150–5250 MHz is only for indoor use to reduce the potential for harmful interference to co-channel mobile satellite systems;

(ii) For devices with detachable antenna(s), the maximum antenna gain permitted for devices in the bands 5250-5350 MHz and 5470-5725 MHz shall be such that the equipment still complies with EIRP limit;

(iii) For devices with detachable antenna(s), the maximum antenna gain permitted for devices in the band 5725-5850 MHz shall be such that the equipment still complies with the EIRP limits specified for point-to-point and non-point-to-point operation as appropriate; and

Operations in the 5.25-5.35GHz band are restricted to indoor usage only.

Avertissement:

(i) les dispositifs fonctionnant dans la bande de 5150 à 5250MHz sont réservés uniquement pour une utilisation à l'intérieur afin de réduire les risques de brouillage préjudiciable aux systèmes de satellites mobiles utilisant les mêmes canaux;

(ii) pour les dispositifs munis d'antennes amovibles, le gain maximal d'antenne permis pour les dispositifs utilisant les bandes de 5250 à 5350MHz et de 5470 à 5725 MHz doit être conforme à la limite de la p.i.r.e.;

(iii) pour les dispositifs munis d'antennes amovibles, le gain maximal d'antenne permis (pour les dispositifs utilisant la bande de 5725 à 5850 MHz) doit être conforme à la limite de la p.i.r.e. spécifiée pour l'exploitation point à point et l'exploitation non point à point, selon le cas;

Les opérations dans la bande de 5.25-5.35GHz sont limitées à un usage intérieur seulement.

Radiation Exposure Statement

This equipment complies with Canada radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator and your body.

Déclaration d'exposition aux radiations

Cet équipement est conforme Canada limites d'exposition aux radiations dans un environnement non contrôlé. Cet équipement doit être installé et utilisé à distance minimum de 20cm entre le radiateur et votre corps.

This device is intended only for OEM integrators under the following condition:

- The transmitter module may not be co-located with any other transmitter or antenna.

As long as the condition above is met, further transmitter test will not be required. However, the OEM integrator is still responsible for testing their end-product for any additional compliance requirements required with this module installed.

Cet appareil est conçu uniquement pour les intégrateurs OEM dans les conditions suivantes:

- Le module émetteur peut ne pas être coïmplanté avec un autre émetteur ou antenne.

Tant que les 1 condition ci-dessus sont remplies, des essais supplémentaires sur l'émetteur ne seront pas nécessaires. Toutefois, l'intégrateur OEM est toujours responsable des essais sur son produit final pour toutes exigences de conformité supplémentaires requis pour ce module installé.

Important Note

In the event that these conditions cannot be met (for example certain laptop configurations or co-location with another transmitter), then the Canada authorization is no longer considered valid and the IC ID cannot be used on the final product. In these circumstances, the OEM integrator will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate Canada authorization.

Note Important

Dans le cas où ces conditions ne peuvent être satisfaites (par exemple pour certaines configurations d'ordinateur portable ou de certaines co-localisation avec un autre émetteur), l'autorisation du Canada n'est plus considéré comme valide et l'ID IC ne peut pas être utilisé sur le produit final. Dans ces circonstances, l'intégrateur OEM sera chargé de réévaluer le produit final (y compris l'émetteur) et l'obtention d'une autorisation distincte au Canada.

End Product Labeling

The final end product must be labeled in a visible area with the following: **Contains IC: 24216-GEM100A.**

laque signalétique du produit final

Le produit final doit être étiqueté dans un endroit visible avec l'inscription suivante: **Contient des IC: 24216-GEM100A.**

Manual Information to the End User

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module.

The end user manual shall include all required regulatory information/warning as show in this manual.

Manuel d'information à l'utilisateur final

L'intégrateur OEM doit être conscient de ne pas fournir des informations à l'utilisateur final quant à la façon d'installer ou de supprimer ce module RF dans le manuel de l'utilisateur du produit final qui intègre ce module.

Le manuel de l'utilisateur final doit inclure toutes les informations réglementaires requises et avertissements comme indiqué dans ce manuel.