Appendix for 2A_n41A

Product Name: 5G CPE
Model No: S3

Appendix A: Effective (Isotropic) Radiated Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	25.34	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Inner_Full	25.36	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	24.88	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	24.65	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	25.41	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Inner_Full	25.36	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	25.33	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	24.64	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	24.47	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Inner_Full	24.22	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	24.09	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	23.43	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	24.25	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Inner_Full	24.45	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	24.29	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	23.73	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	23.38	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Inner_Full	23.41	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	22.90	PC3	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	22.87	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	23.37	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Inner_Full	23.34	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	22.99	PC3	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	23.24	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	25.36	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Inner_Full	25.44	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	24.98	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	24.80	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	25.36	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Inner_Full	25.36	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	24.55	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	25.06	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	24.66	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Inner_Full	24.56	PC3	PASS

DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	24.54	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	24.00	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	24.87	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Inner_Full	24.66	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	24.80	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	23.61	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	24.00	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Inner_Full	23.86	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	23.39	PC3	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	23.49	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	23.70	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Inner_Full	23.81	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	22.99	PC3	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	23.41	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	25.17	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Inner_Full	24.94	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	24.67	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	24.76	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	25.02	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Inner_Full	24.99	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	24.44	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	24.85	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	24.37	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Inner_Full	24.23	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	24.87	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	22.98	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	24.36	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Inner_Full	24.26	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	24.62	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	22.97	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	23.30	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Inner_Full	23.54	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	23.16	PC3	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	22.90	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	23.67	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Inner_Full	23.22	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	22.76	PC3	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	23.06	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	25.23	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Inner_Full	25.10	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	24.50	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	23.78	PC3	PASS

DC_2A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	25.04	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Inner_Full	25.13	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	24.13	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	23.41	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	24.34	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Inner_Full	24.54	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	24.41	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	22.87	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Outer_Full	24.61	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Inner_Full	24.48	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	24.18	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	22.88	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	23.55	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Inner_Full	23.35	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	23.91	PC3	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	23.01	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Outer_Full	23.57	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Inner_Full	23.30	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Edge_1RB_Left	23.51	PC3	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Edge_1RB_Right	23.21	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

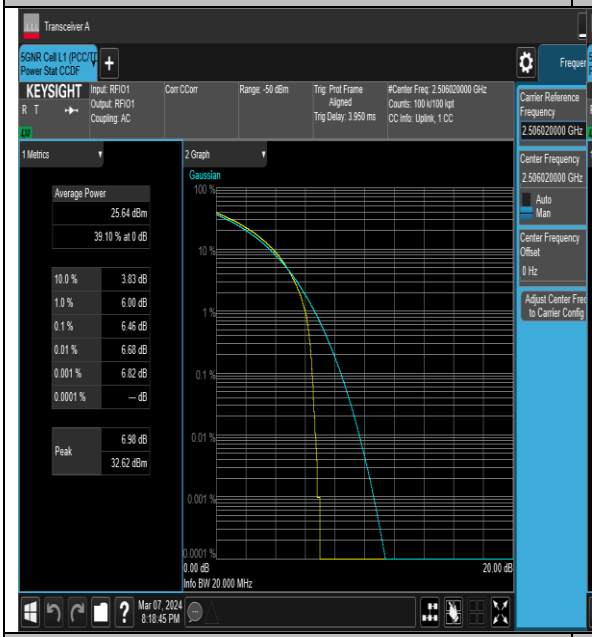
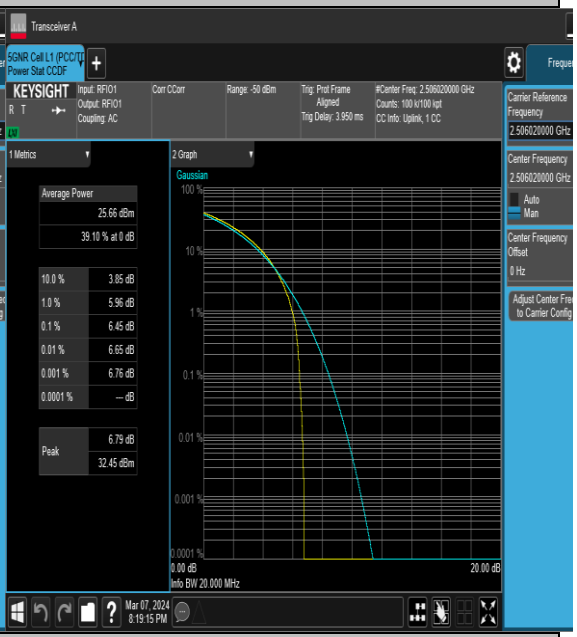
Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	6.46	≤13	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	6.45	≤13	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	7.11	≤13	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	7.03	≤13	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	6.96	≤13	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	6.99	≤13	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	6.61	≤13	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	6.59	≤13	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	7.01	≤13	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	7.03	≤13	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	6.93	≤13	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	6.80	≤13	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	6.83	≤13	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	6.98	≤13	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	7.03	≤13	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	7.07	≤13	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	6.97	≤13	PASS

DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	7.03	≤13	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	6.95	≤13	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	6.92	≤13	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	7.44	≤13	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Outer_Full	7.17	≤13	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	7.49	≤13	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Outer_Full	7.48	≤13	PASS

Test Graphs

NTNV_DC_2A_n41A_PC3_30_5+20_M+L_TID6_#1	NTNV_DC_2A_n41A_PC3_30_5+20_M+L_TID7_#1
	
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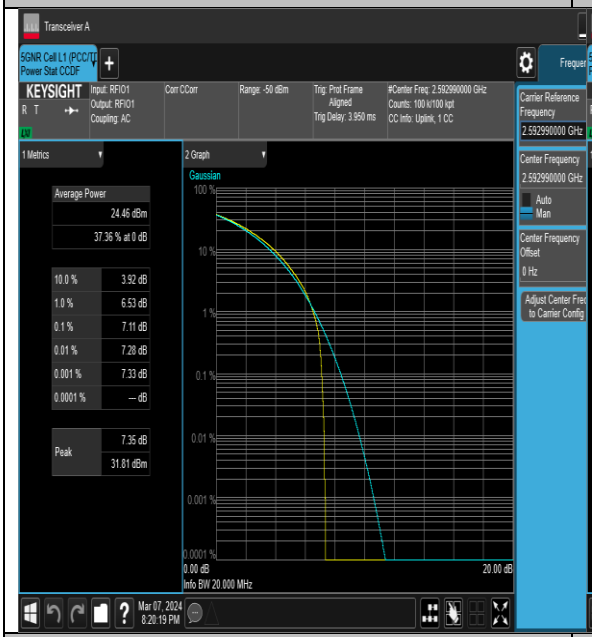	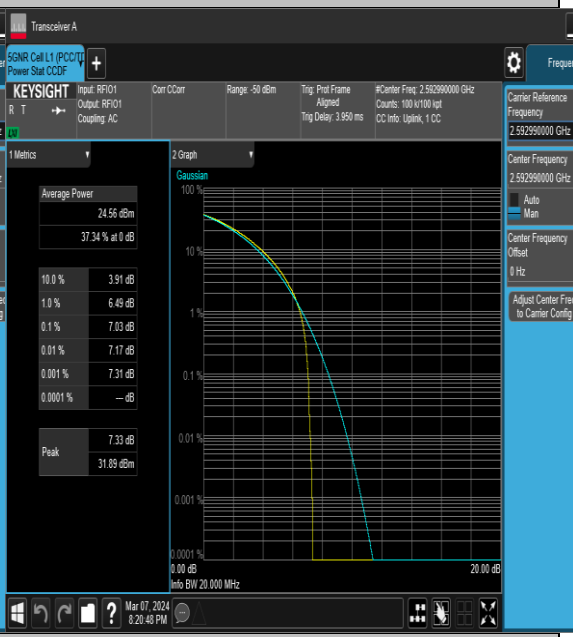
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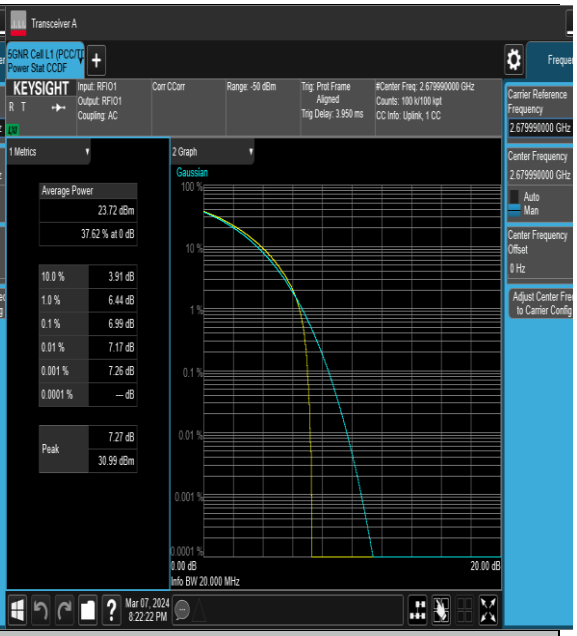
NTNV_DC_2A_n41A_PC3_30_5+40_M+L_TID6_#1



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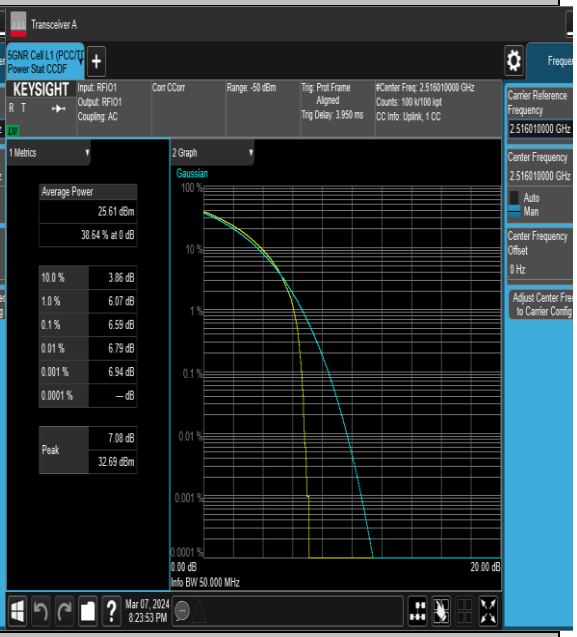

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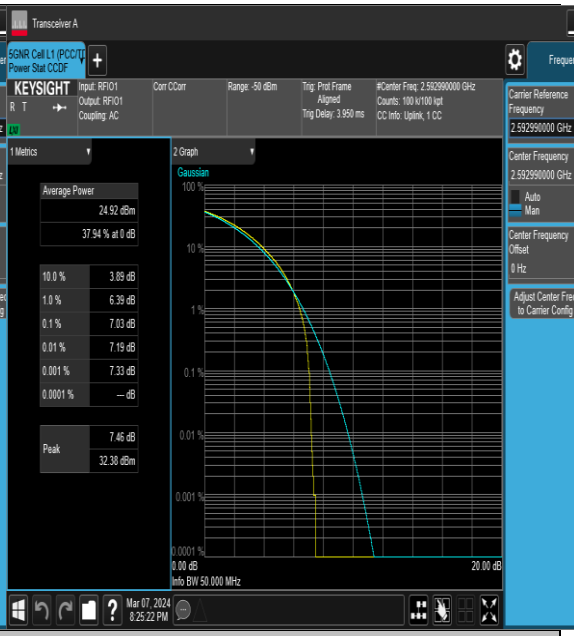

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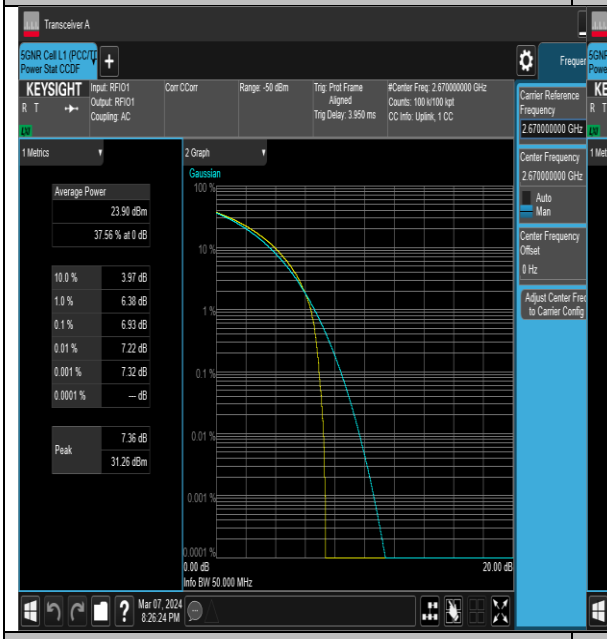

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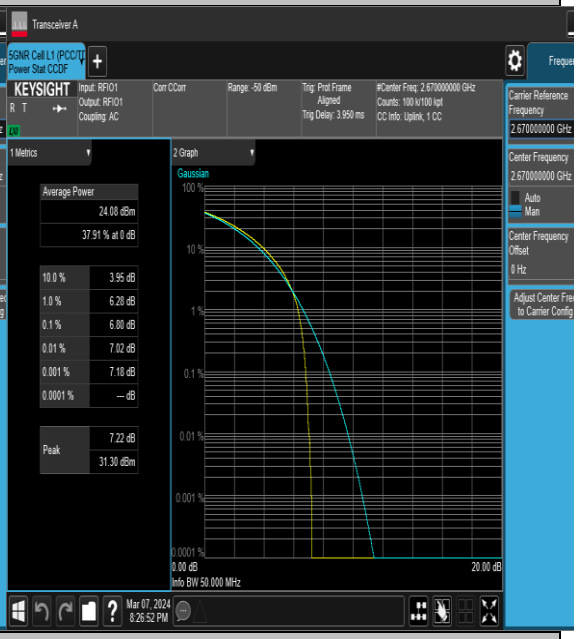

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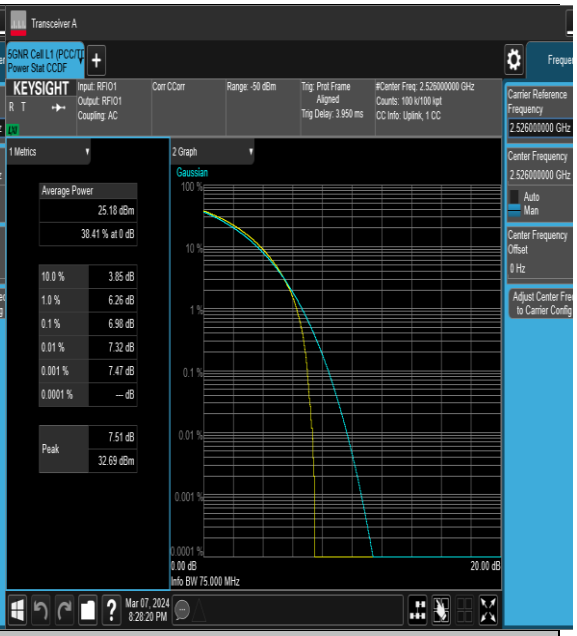

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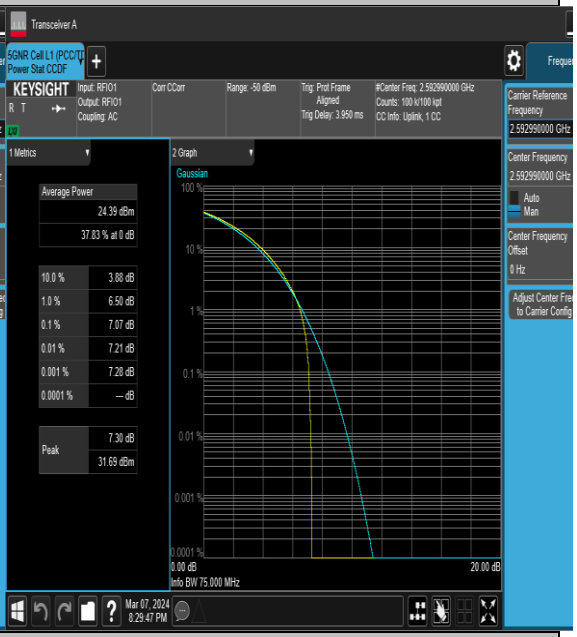

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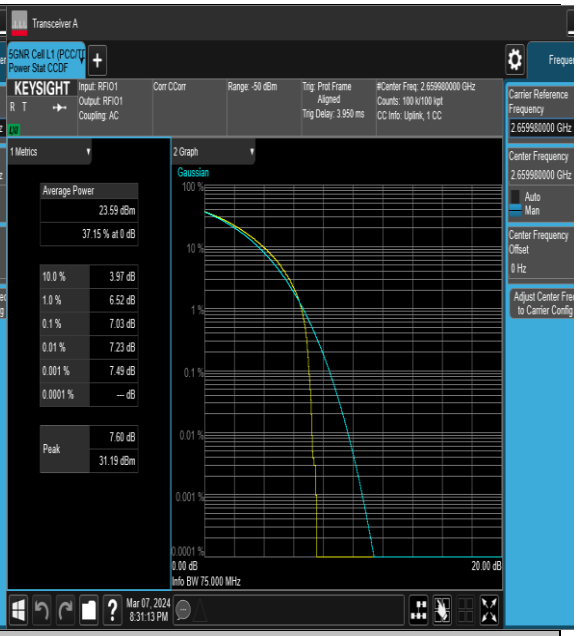
NTNV_DC_2A_n41A_PC3_30_5+100_M+L_TID6_#1



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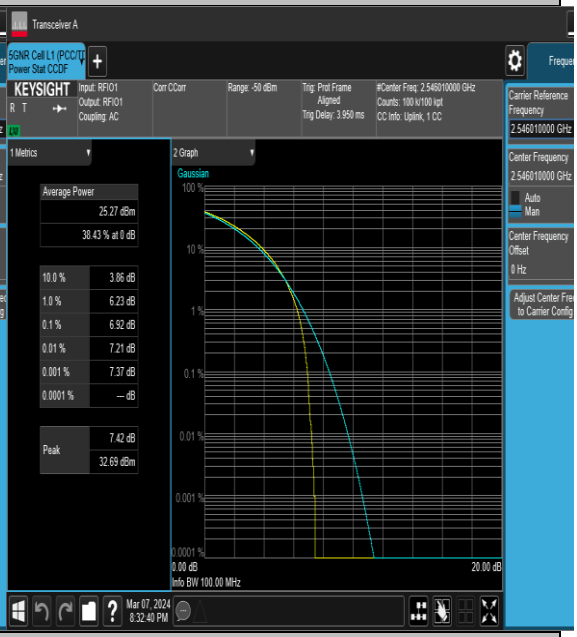

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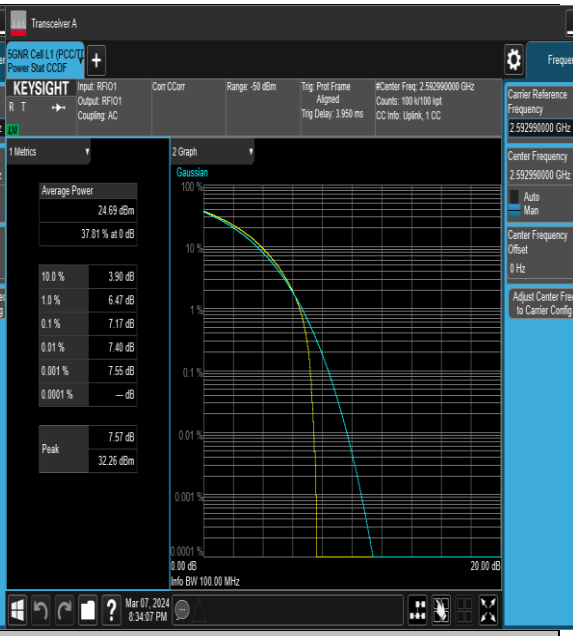

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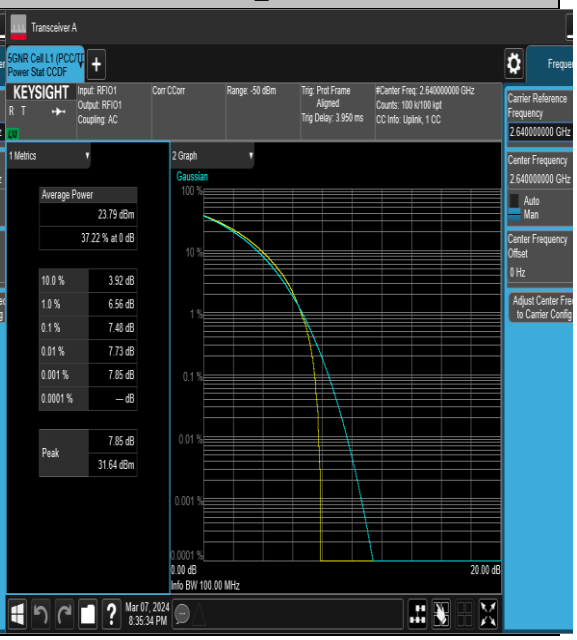
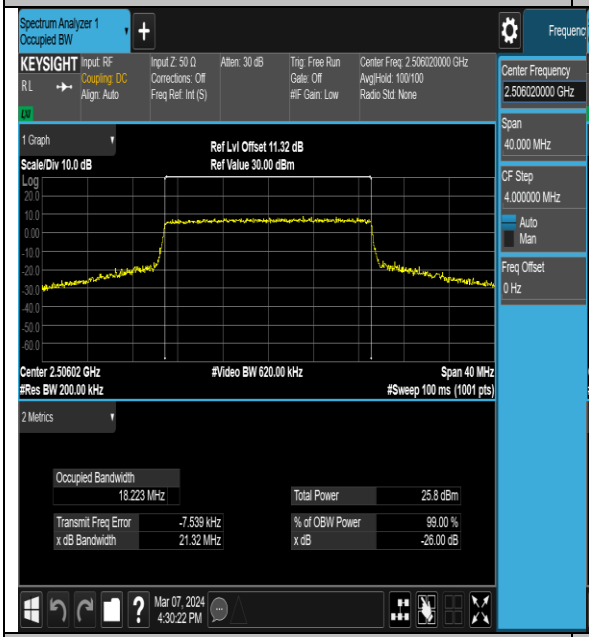
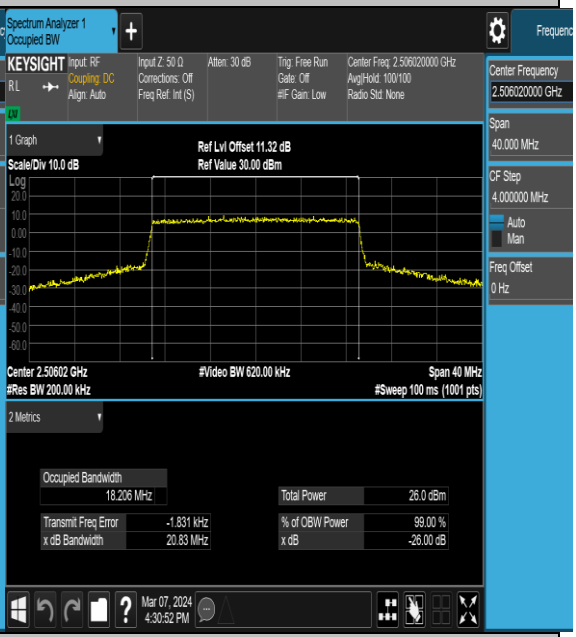
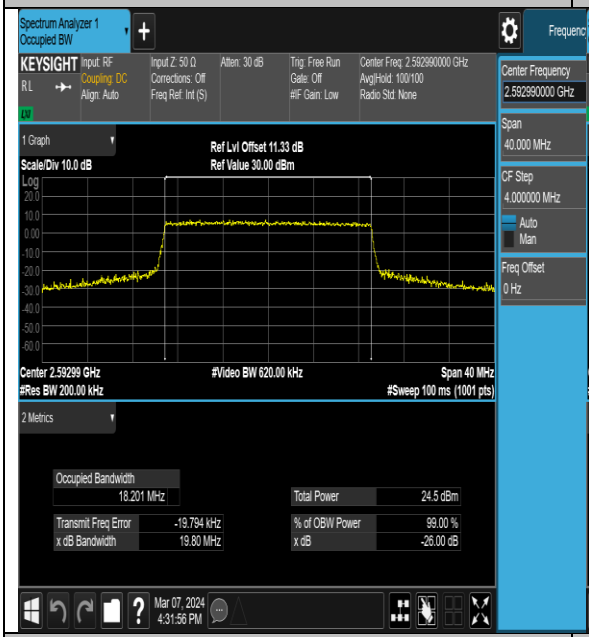
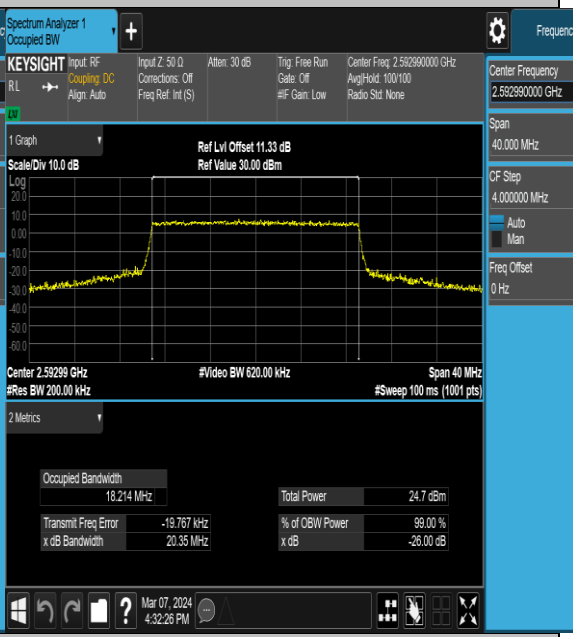


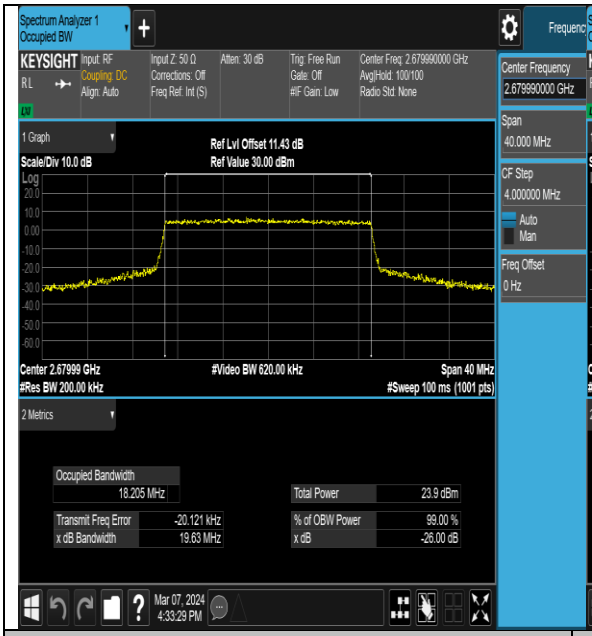
Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	18.223	21.32	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	18.206	20.83	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	18.201	19.80	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	18.214	20.35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	18.205	19.63	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	18.217	19.99	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	37.766	39.72	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	37.763	39.62	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	37.753	39.81	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	37.796	39.62	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	37.770	39.51	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	37.725	39.66	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	57.797	60.18	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	57.822	60.14	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	57.798	60.11	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	57.722	60.16	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	57.788	67.91	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	57.822	71.43	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	97.095	121.5	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	96.961	100.8	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	97.219	100.6	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Outer_Full	97.196	100.6	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	97.275	100.7	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Outer_Full	97.258	104.3	PASS

Test Graphs

NTNV_DC_2A_n41A_PC3_30_5+20_M+L_TID5_Ant1#1	NTNV_DC_2A_n41A_PC3_30_5+20_M+L_TID6_Ant1#1
	
NTNV_DC_2A_n41A_PC3_30_5+20_M+M_TID5_Ant1#1	NTNV_DC_2A_n41A_PC3_30_5+20_M+M_TID6_Ant1#1
	
NTNV_DC_2A_n41A_PC3_30_5+20_M+H_TID5_Ant1#1	NTNV_DC_2A_n41A_PC3_30_5+20_M+H_TID6_Ant1#1



NTNV_DC_2A_n41A_PC3_30_5+40_M+L_TID5_Ant1#1

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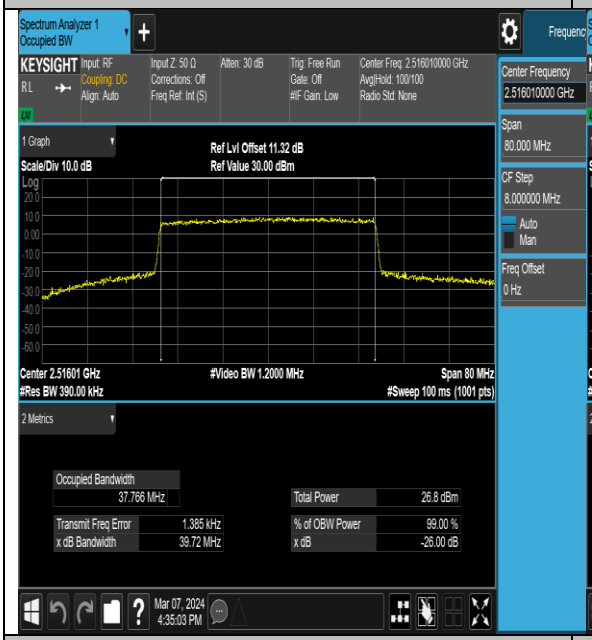

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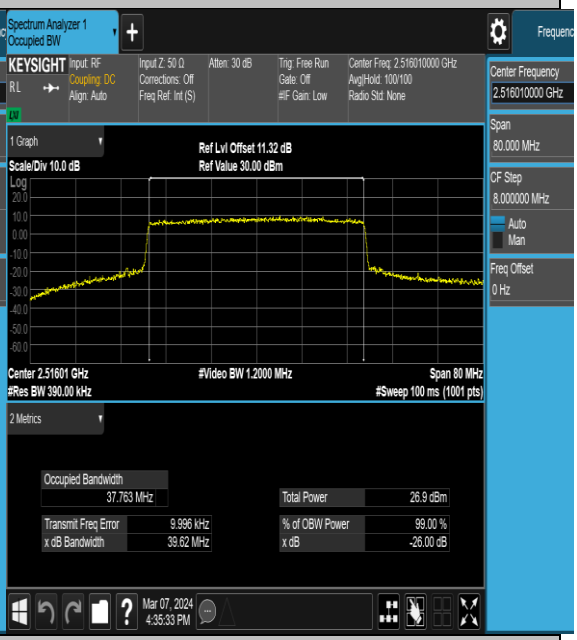

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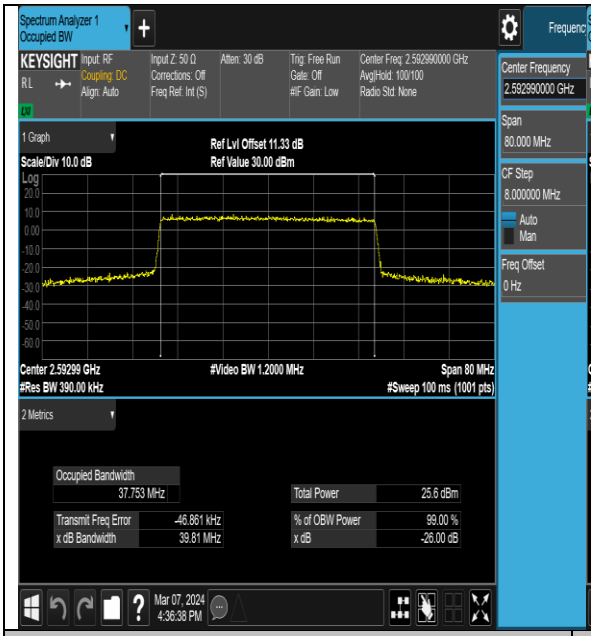
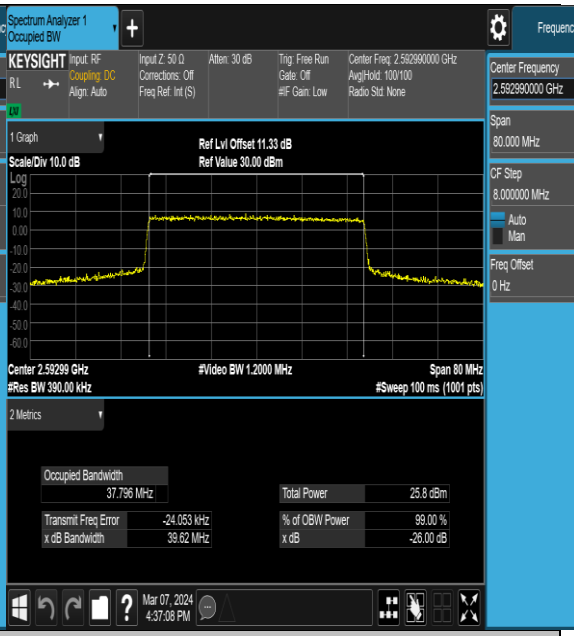


NTNV_DC_2A_n41A_PC3_30_5+40_M+H_TID5_Ant1#1



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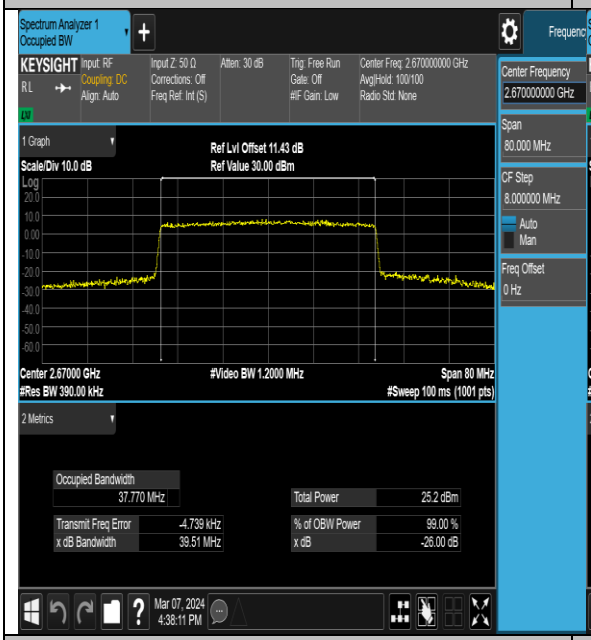

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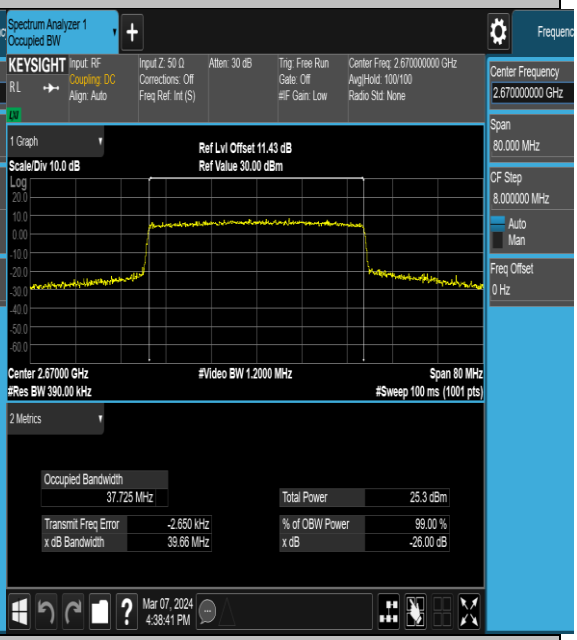

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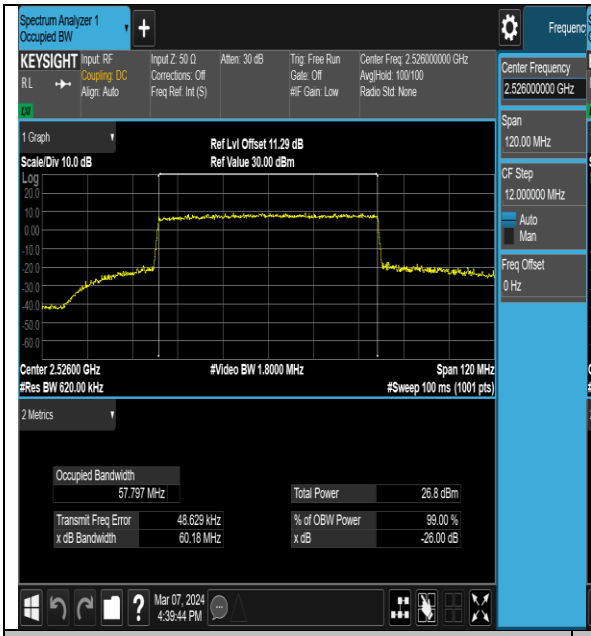
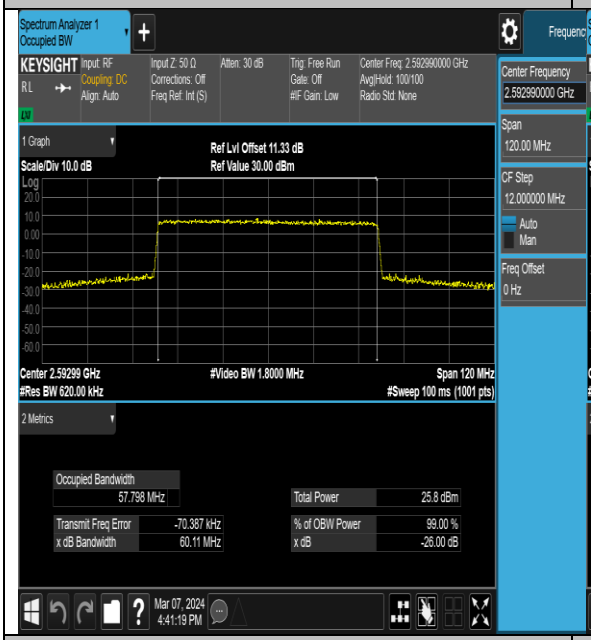


NTNV_DC_2A_n41A_PC3_30_5+60_M+M_TID5_Ant1#1

NTNV_DC_2A_n41A_PC3_30_5+60_M+M_TID6_Ant1#1



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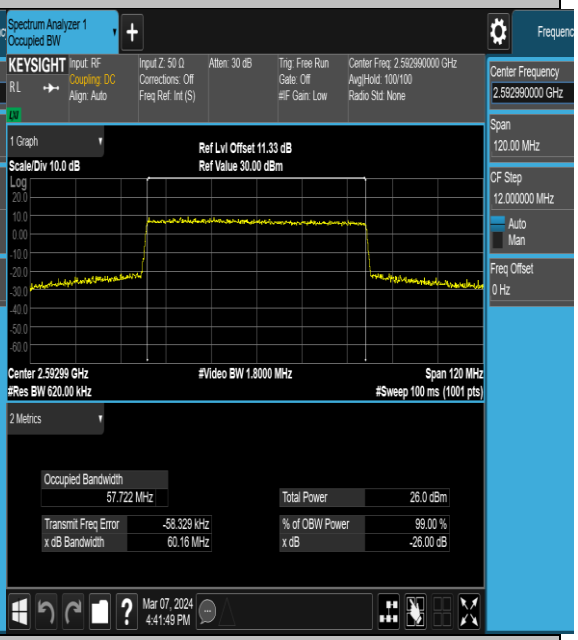

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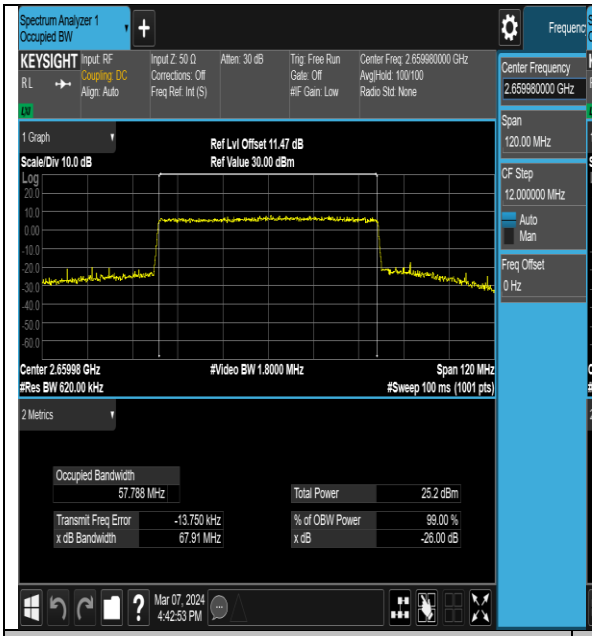

NTNV_DC_2A_n41A_PC3_30_5+100_M+L_TID5_Ant1#1

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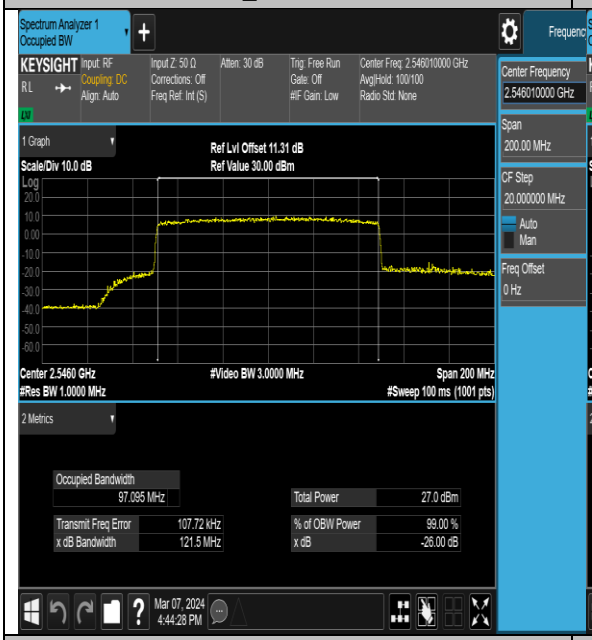

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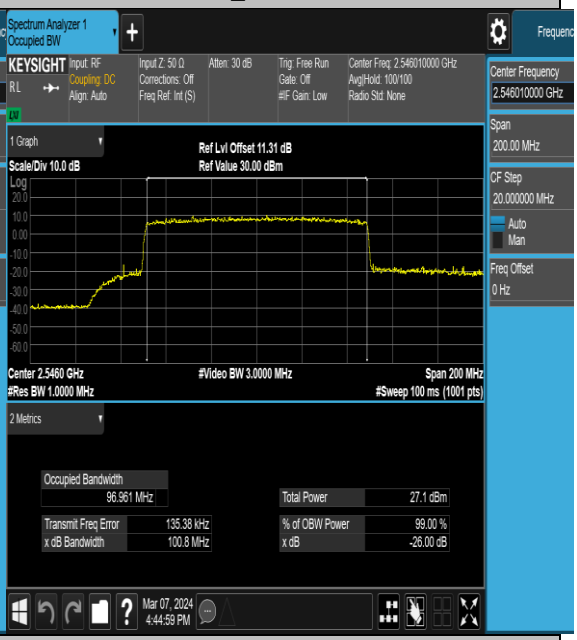
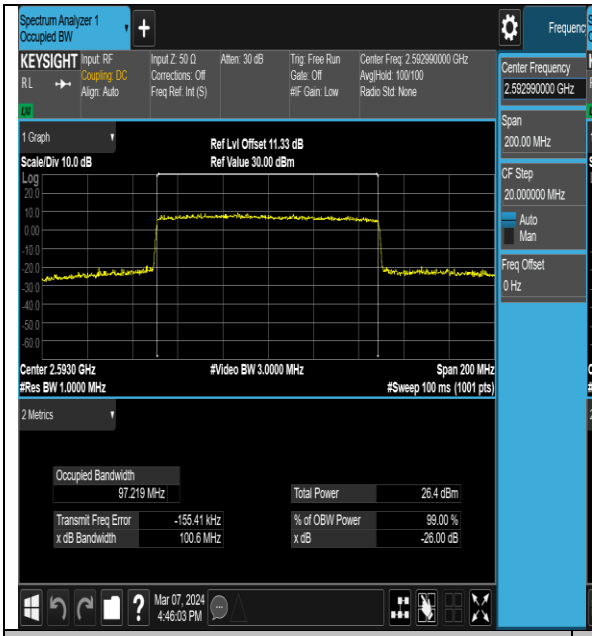
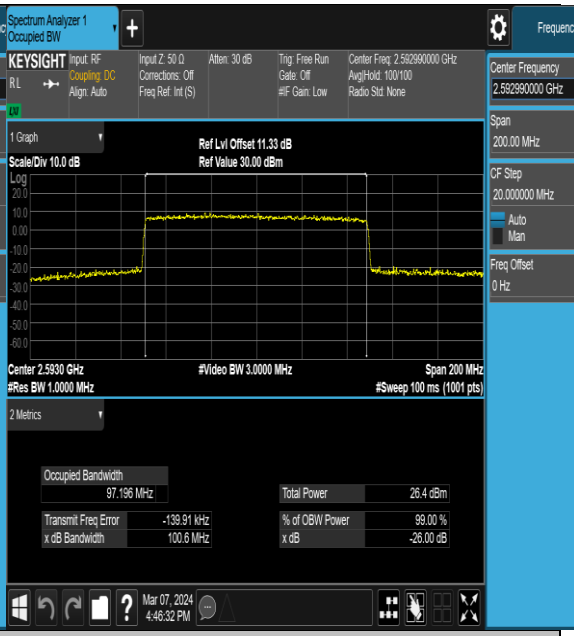


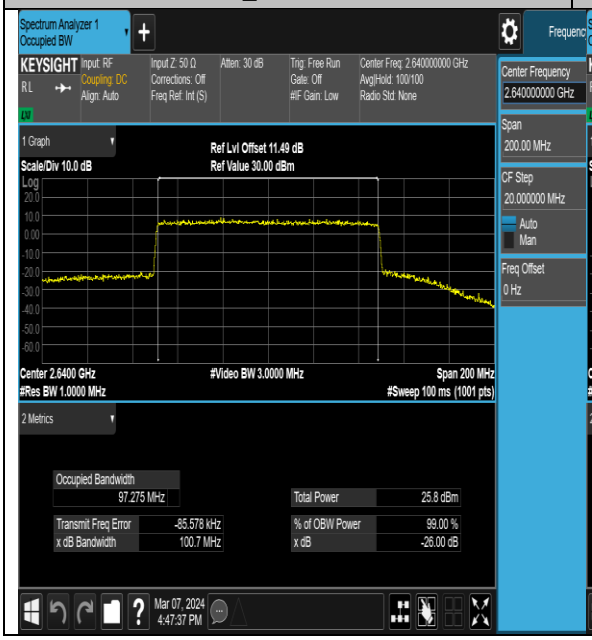
NTNV_DC_2A_n41A_PC3_30_5+100_M+M_TID6_Ant1#1

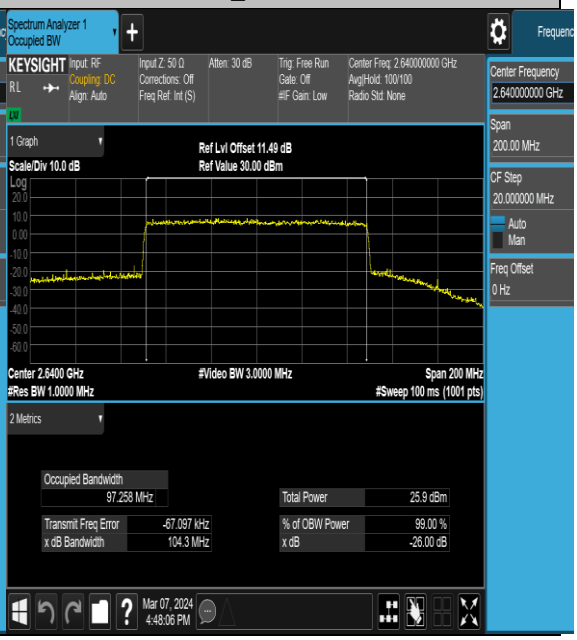


NTNV_DC_2A_n41A_PC3_30_5+100_M+H_TID5_Ant1#1



NTNV_DC_2A_n41A_PC3_30_5+100_M+H_TID6_Ant1#1



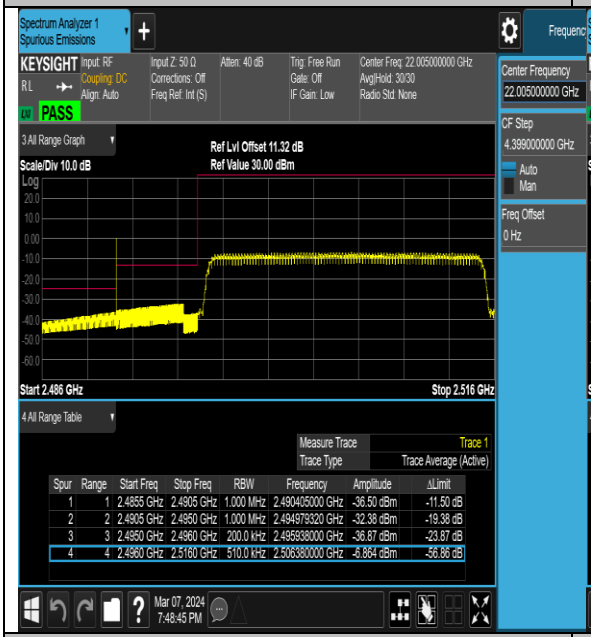
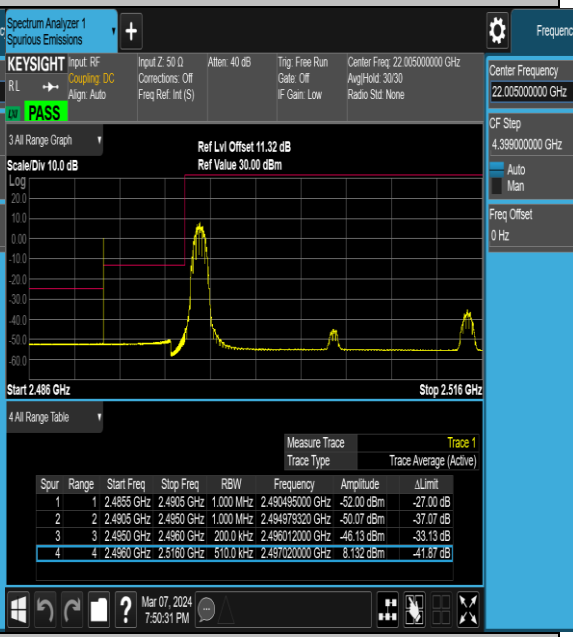
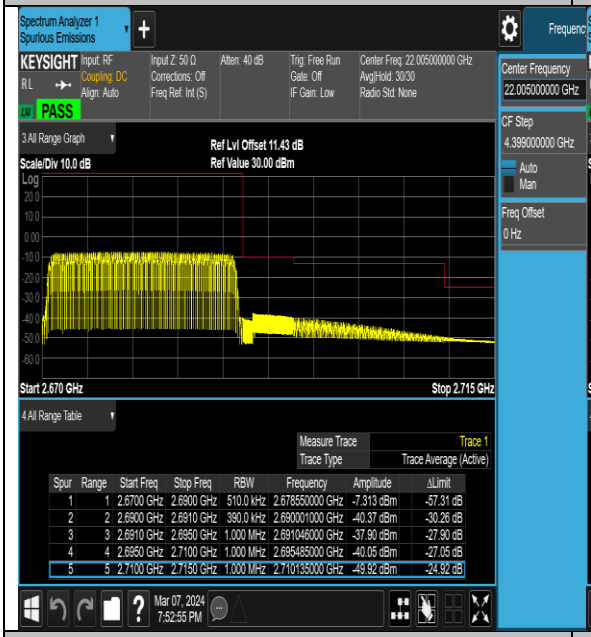
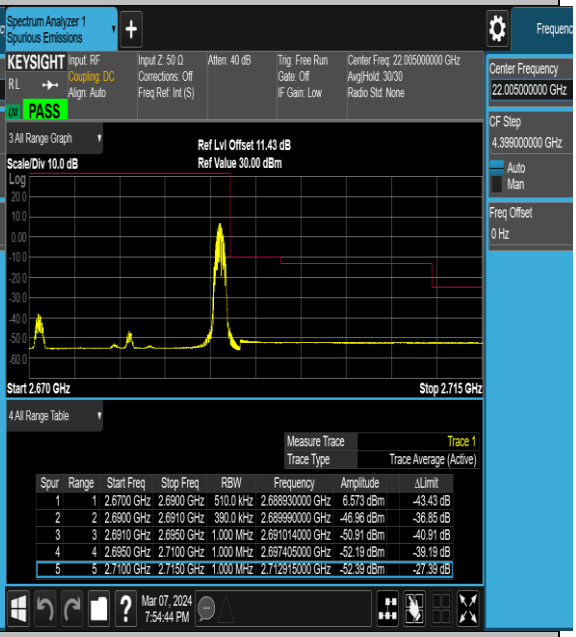


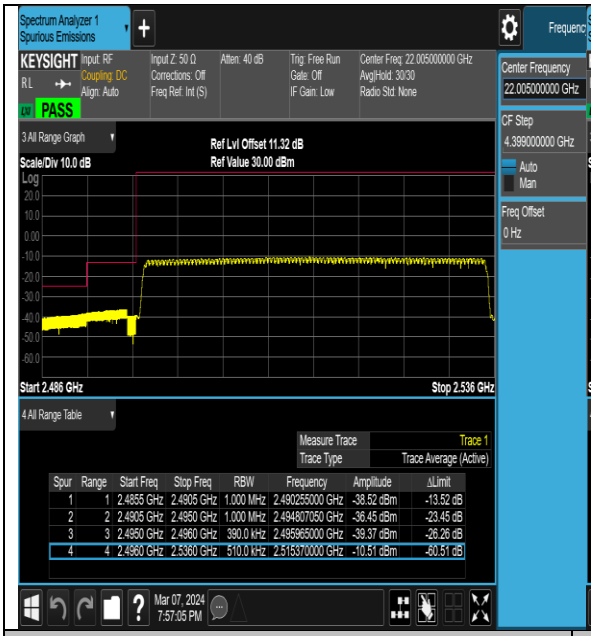
Appendix D: Band Edge for NSA

Test Result

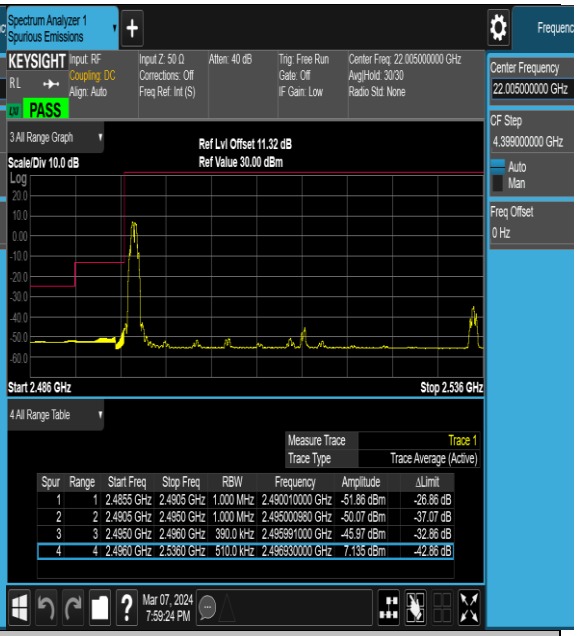
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs

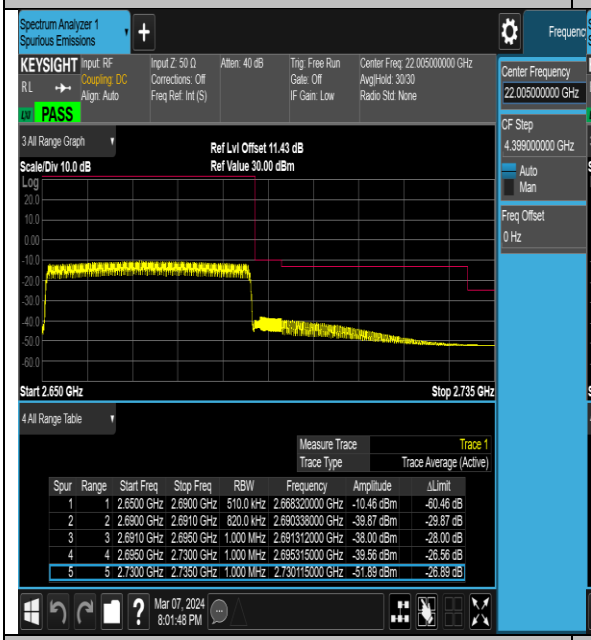
NTNV_DC_2A_n41A_PC3_30_5+20_M+L_TID6_#1	NTNV_DC_2A_n41A_PC3_30_5+20_M+L_TID4_#1
	
NTNV_DC_2A_n41A_PC3_30_5+20_M+H_TID6_#1	NTNV_DC_2A_n41A_PC3_30_5+20_M+H_TID5_#1
	
NTNV_DC_2A_n41A_PC3_30_5+40_M+L_TID6_#1	NTNV_DC_2A_n41A_PC3_30_5+40_M+L_TID4_#1



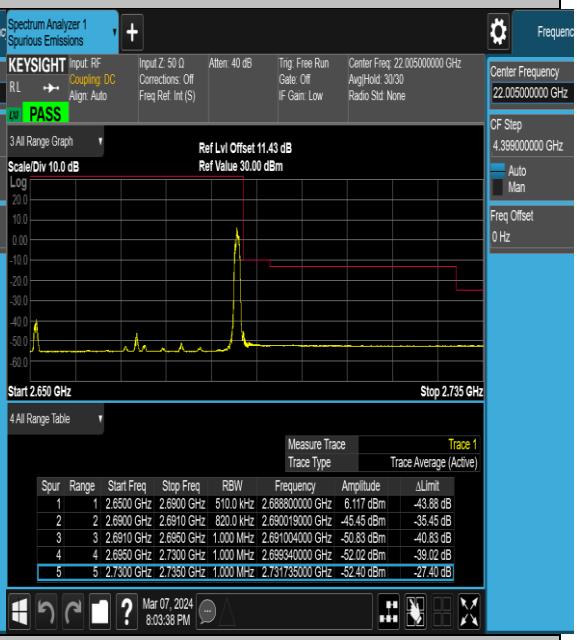
NTNV_DC_2A_n41A_PC3_30_5+40_M+H_TID6_#1



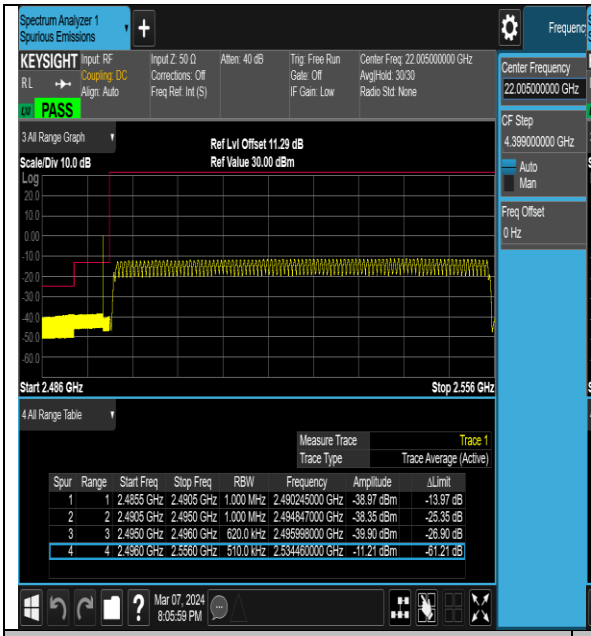
NTNV_DC_2A_n41A_PC3_30_5+40_M+H_TID5_#1



NTNV_DC_2A_n41A_PC3_30_5+60_M+L_TID6_#1



NTNV_DC_2A_n41A_PC3_30_5+60_M+L_TID4_#1



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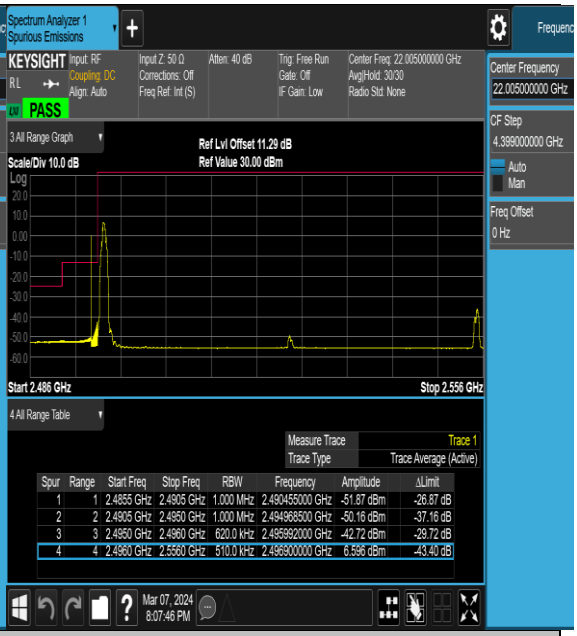

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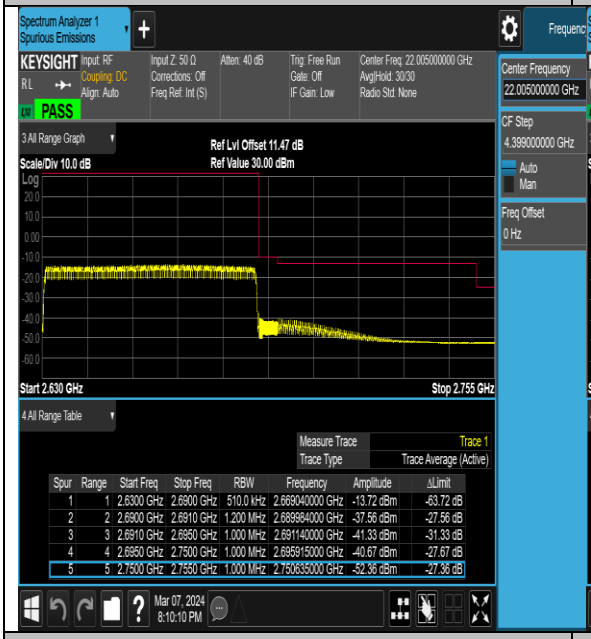
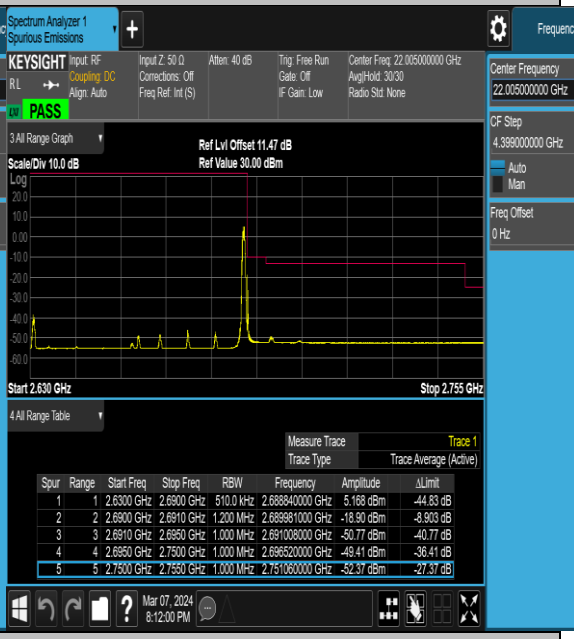


NTNV_DC_2A_n41A_PC3_30_5+100_M+L_TID6_#1



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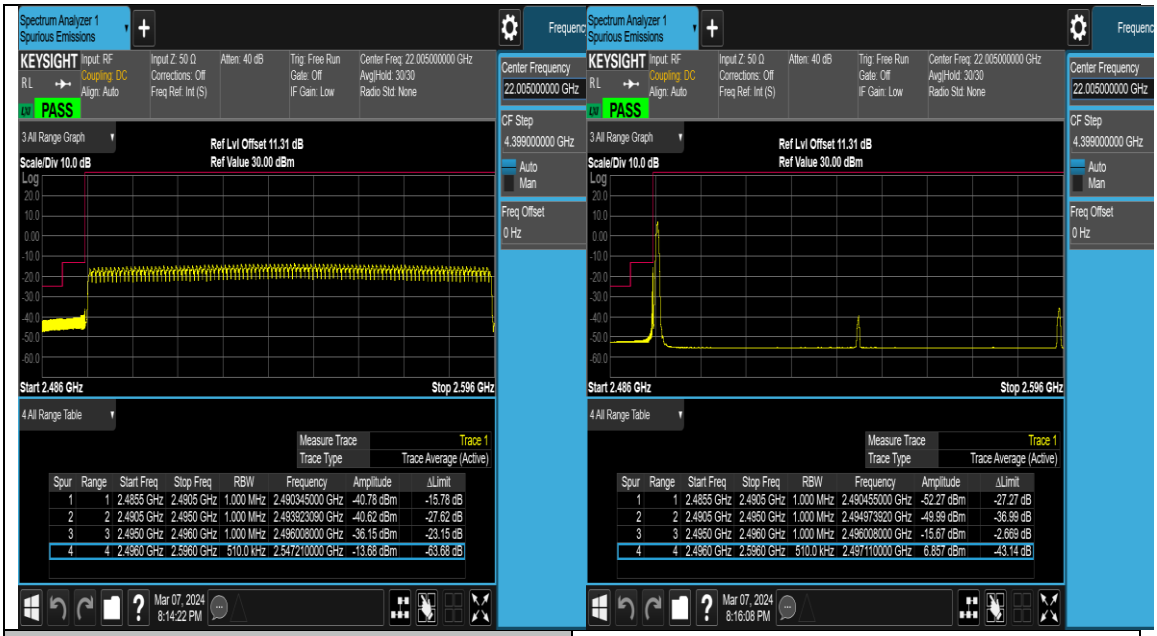
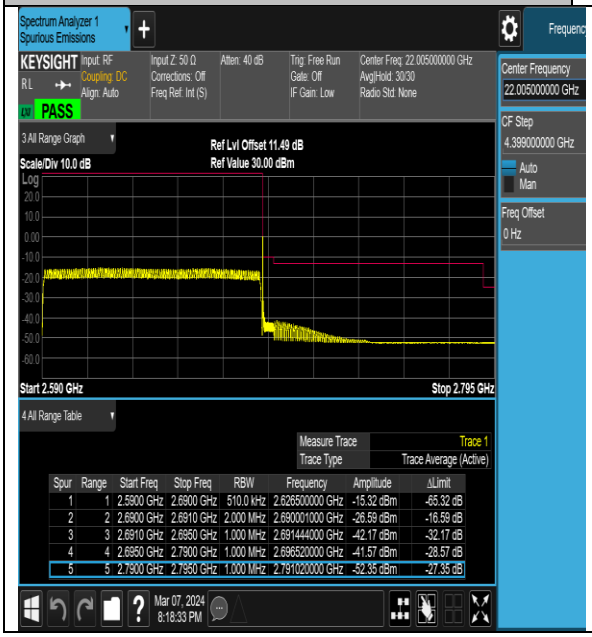


NTNV_DC_2A_n41A_PC3_30_5+100_M+H_TID6_#1



Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-50.25	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-34.71	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	3000	18000	-36.76	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	18000	26500	-38.11	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-49.93	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.70	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	18000	-36.46	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	18000	26500	-38.03	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-50.09	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.61	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	18000	-36.61	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	18000	26500	-37.96	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-50.32	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-35.78	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	3000	18000	-36.70	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	18000	26500	-37.86	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-50.19	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.57	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	18000	-36.54	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	18000	26500	-38.00	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-50.35	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.63	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	18000	-36.54	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	18000	26500	-38.04	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-50.37	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-37.67	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	3000	18000	-36.22	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	18000	26500	-38.01	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-50.18	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.68	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	18000	-36.65	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	18000	26500	-38.19	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-50.12	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.60	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	18000	-36.76	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	18000	26500	-37.97	-25	PASS

DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-50.26	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-37.73	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	3000	18000	-36.54	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	18000	26500	-37.96	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-50.26	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.62	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	18000	-36.68	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	18000	26500	-38.09	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-50.38	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.68	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	18000	-36.52	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	18000	26500	-37.96	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-49.96	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-37.41	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	3000	18000	-36.74	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	18000	26500	-38.21	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-50.30	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.76	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	18000	-36.72	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	18000	26500	-38.03	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.35	-35	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.45	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	18000	-36.64	-25	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	18000	26500	-38.22	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-50.31	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-37.54	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	3000	18000	-36.60	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	18000	26500	-38.07	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.20	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.68	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	18000	-36.73	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	18000	26500	-38.06	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-49.90	-35	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.60	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	18000	-36.70	-25	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	18000	26500	-37.97	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	30	1000	-50.16	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	1000	3000	-34.76	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	3000	18000	-36.43	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	18000	26500	-38.01	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-50.01	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.68	-25	PASS

DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	3000	18000	-36.63	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	18000	26500	-38.04	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-50.32	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.68	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	3000	18000	-36.60	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	18000	26500	-38.08	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	30	1000	-50.41	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	1000	3000	-35.30	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	3000	18000	-36.71	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Outer_Full	18000	26500	-38.15	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-50.11	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.61	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	3000	18000	-36.73	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	18000	26500	-37.90	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-50.10	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.67	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	3000	18000	-36.74	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	18000	26500	-38.00	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	30	1000	-50.24	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	1000	3000	-37.59	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	3000	18000	-36.54	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	18000	26500	-38.19	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-50.30	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.66	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	3000	18000	-36.64	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	18000	26500	-38.06	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-50.09	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.64	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	3000	18000	-36.69	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	18000	26500	-38.12	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	30	1000	-50.26	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	1000	3000	-37.76	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	3000	18000	-36.69	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Outer_Full	18000	26500	-38.25	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-50.17	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.63	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	3000	18000	-36.66	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	18000	26500	-38.14	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-50.28	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.57	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	3000	18000	-36.61	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	18000	26500	-37.95	-25	PASS

DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	30	1000	-50.32	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	1000	3000	-37.71	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	3000	18000	-36.66	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	18000	26500	-38.22	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-50.20	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.61	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	3000	18000	-36.69	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	18000	26500	-37.95	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.31	-35	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.60	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	3000	18000	-36.69	-25	PASS
DC_2A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	18000	26500	-38.17	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	30	1000	-50.18	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	1000	3000	-37.74	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	3000	18000	-36.27	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Outer_Full	18000	26500	-37.96	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-49.94	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.61	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	3000	18000	-36.67	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	18000	26500	-38.06	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-49.89	-35	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.50	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	3000	18000	-36.62	-25	PASS
DC_2A_n41A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	18000	26500	-38.04	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	30	1000	-50.44	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	1000	3000	-34.73	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	3000	18000	-36.67	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	18000	26500	-38.19	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-50.29	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.71	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	3000	18000	-36.58	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	18000	26500	-38.07	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-50.31	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.62	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	3000	18000	-36.43	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	18000	26500	-38.00	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	30	1000	-50.32	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	1000	3000	-33.44	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	3000	18000	-36.57	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Outer_Full	18000	26500	-38.17	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-50.24	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.71	-25	PASS

DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	3000	18000	-36.56	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	18000	26500	-38.10	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-50.31	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.74	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	3000	18000	-36.47	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	18000	26500	-38.06	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	30	1000	-50.21	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	1000	3000	-37.78	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	3000	18000	-36.60	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	18000	26500	-38.05	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-50.28	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.59	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	3000	18000	-36.55	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	18000	26500	-37.94	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-50.46	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.36	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	3000	18000	-36.31	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	18000	26500	-38.01	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	30	1000	-50.29	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	1000	3000	-37.36	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	3000	18000	-36.51	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	18000	26500	-37.87	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-50.17	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.47	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	3000	18000	-36.47	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	18000	26500	-38.13	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-50.23	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.31	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	3000	18000	-36.57	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	18000	26500	-38.08	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	30	1000	-49.83	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	1000	3000	-37.36	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	3000	18000	-36.37	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	18000	26500	-38.01	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-50.00	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.22	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	3000	18000	-36.64	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	18000	26500	-37.89	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.00	-35	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.37	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	3000	18000	-36.71	-25	PASS
DC_2A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	18000	26500	-38.18	-25	PASS

DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	30	1000	-50.53	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	1000	3000	-37.33	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	3000	18000	-36.52	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	18000	26500	-38.00	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.26	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.36	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	3000	18000	-36.71	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	18000	26500	-38.11	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-50.48	-35	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.34	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	3000	18000	-36.52	-25	PASS
DC_2A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	18000	26500	-38.01	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	30	1000	-50.30	-35	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	1000	3000	-34.71	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	3000	18000	-36.62	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	18000	26500	-38.08	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-50.29	-35	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.47	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	3000	18000	-36.59	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	18000	26500	-37.93	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-50.36	-35	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.53	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	3000	18000	-36.69	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	18000	26500	-38.14	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	30	1000	-50.27	-35	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	1000	3000	-35.54	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	3000	18000	-36.53	-25	PASS
DC_2A_n41A	30	5+10	CP-16QAM	M+L	Outer_Full	18000	26500	-38.06	-25	PASS

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DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-50.28	-35	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.46	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	3000	18000	-36.59	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	18000	26500	-38.00	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-49.96	-35	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.53	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	3000	18000	-36.47	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	18000	26500	-38.08	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	30	1000	-50.45	-35	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	1000	3000	-36.52	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	3000	18000	-36.42	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	18000	26500	-38.08	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-50.35	-35	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.31	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	3000	18000	-36.59	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	18000	26500	-38.06	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-50.07	-35	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.40	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	3000	18000	-36.67	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	18000	26500	-38.08	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Outer_Full	30	1000	-50.38	-35	PASS

DC_2A_n41A	30	5+100	CP-16QAM	M+M	Outer_Full	1000	3000	-37.05	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Outer_Full	3000	18000	-36.66	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Outer_Full	18000	26500	-38.15	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-50.58	-35	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.42	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	3000	18000	-36.57	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	18000	26500	-37.93	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-50.22	-35	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.88	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	3000	18000	-36.60	-25	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	18000	26500	-38.17	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	30	1000	-50.41	-35	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	1000	3000	-37.80	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	3000	18000	-36.46	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	18000	26500	-38.16	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-50.50	-35	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.94	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	3000	18000	-36.61	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	18000	26500	-38.19	-25	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.33	-35	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.83	-25	PASS
DC_2A_n41A	30	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	18000	-36.62	-25	PASS

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DC_2A_n41A	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	18000	26500	-37.94	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Outer_Full	30	1000	-50.13	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Outer_Full	1000	3000	-37.61	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Outer_Full	3000	18000	-36.63	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Outer_Full	18000	26500	-38.19	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.28	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.80	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	3000	18000	-36.75	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	18000	26500	-38.23	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-50.31	-35	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.86	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	3000	18000	-36.34	-25	PASS
DC_2A_n41A	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	18000	26500	-38.14	-25	PASS