

Appendix for 2A_n5A

Product Name: 5G CPE

Model No: S3

Appendix A: Average Power Output for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	21.79	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Inner_Full	23.36	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	21.94	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	21.85	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	21.79	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Inner_Full	22.85	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	21.88	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	21.70	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	21.79	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Inner_Full	23.20	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	21.78	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	21.62	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	21.63	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Inner_Full	22.51	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	22.24	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	21.60	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	21.50	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Inner_Full	23.15	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	21.63	PC3	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	21.47	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	21.60	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Inner_Full	22.43	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	21.28	PC3	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	21.56	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	21.86	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Inner_Full	23.36	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	21.95	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	21.72	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	21.80	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Inner_Full	22.80	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	22.04	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	21.92	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	21.85	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Inner_Full	23.13	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	21.80	PC3	PASS

DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	21.73	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	21.75	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Inner_Full	22.59	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	22.06	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	21.72	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	21.59	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Inner_Full	23.07	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	21.74	PC3	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	21.46	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	21.56	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Inner_Full	22.71	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	21.63	PC3	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	21.73	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	21.83	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Inner_Full	23.37	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	21.98	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	21.97	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	21.82	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Inner_Full	22.75	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	22.12	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	21.45	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	21.87	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Inner_Full	23.22	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	21.99	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	21.55	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	21.70	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Inner_Full	22.73	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	21.99	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	21.63	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	21.68	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Inner_Full	23.34	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	21.93	PC3	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	21.61	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	21.66	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Inner_Full	22.64	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	21.79	PC3	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	21.51	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	21.81	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Inner_Full	23.12	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	21.85	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	21.52	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	21.76	PC3	PASS

DC_2A_n5A	15	5+20	CP-16QAM	M+L	Inner_Full	22.84	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	22.10	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	21.50	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	21.83	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Inner_Full	23.17	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	21.95	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	21.53	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	21.68	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Inner_Full	22.63	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	21.88	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	22.07	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	21.68	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Inner_Full	23.12	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	21.78	PC3	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	21.60	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	21.69	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Inner_Full	22.65	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	21.78	PC3	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	21.80	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

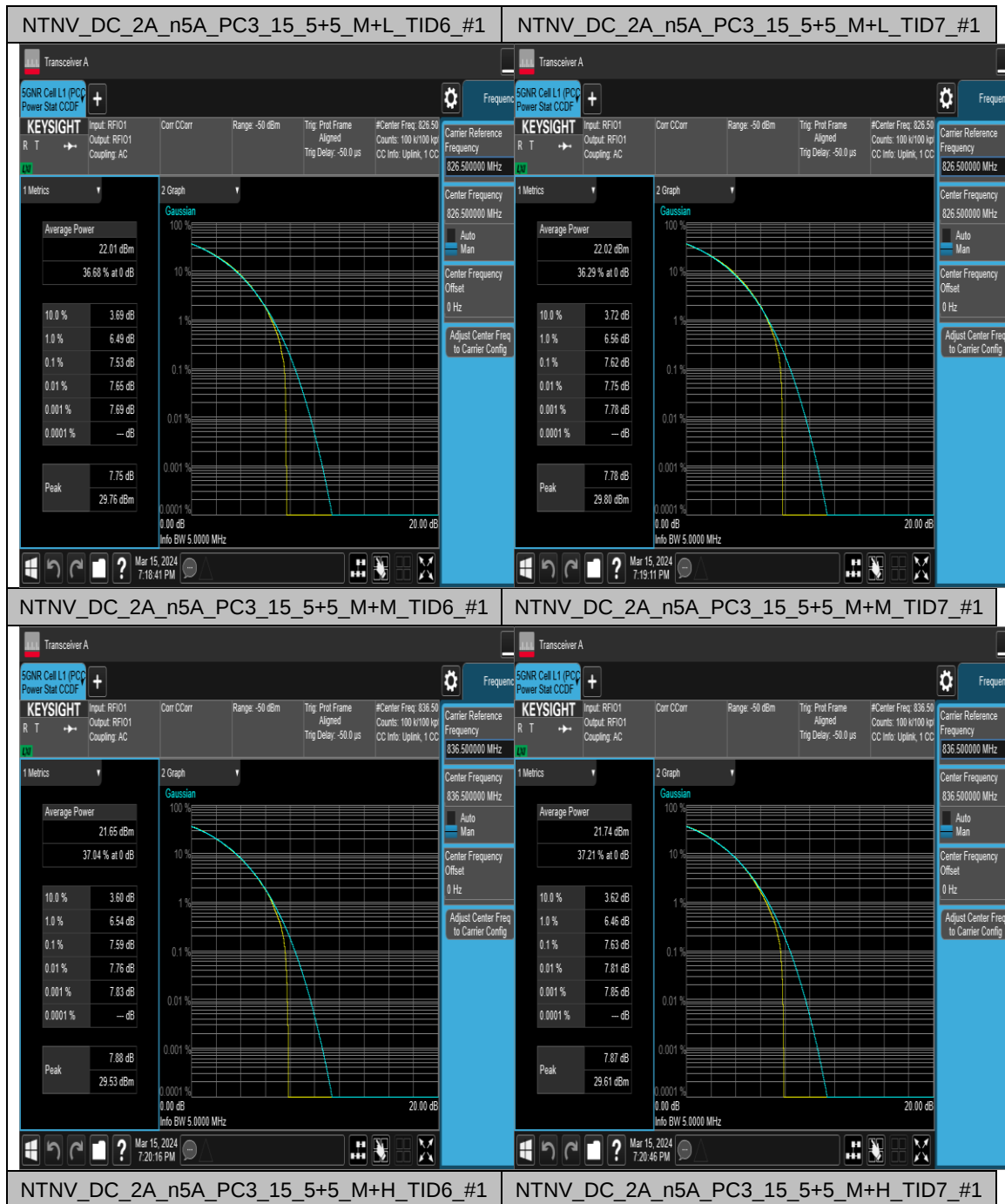
Peak-to-Average Ratio(CCDF)

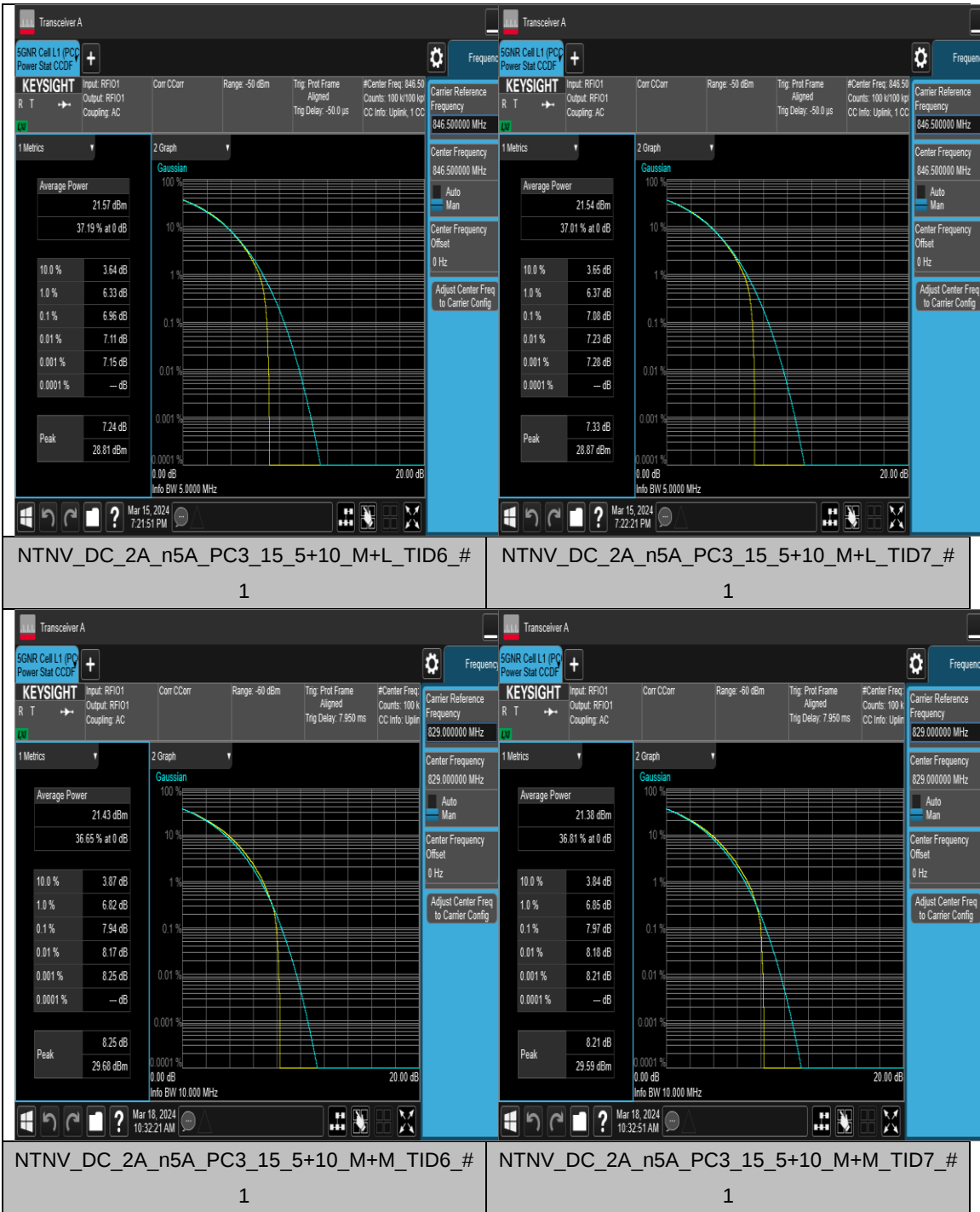
Test Result

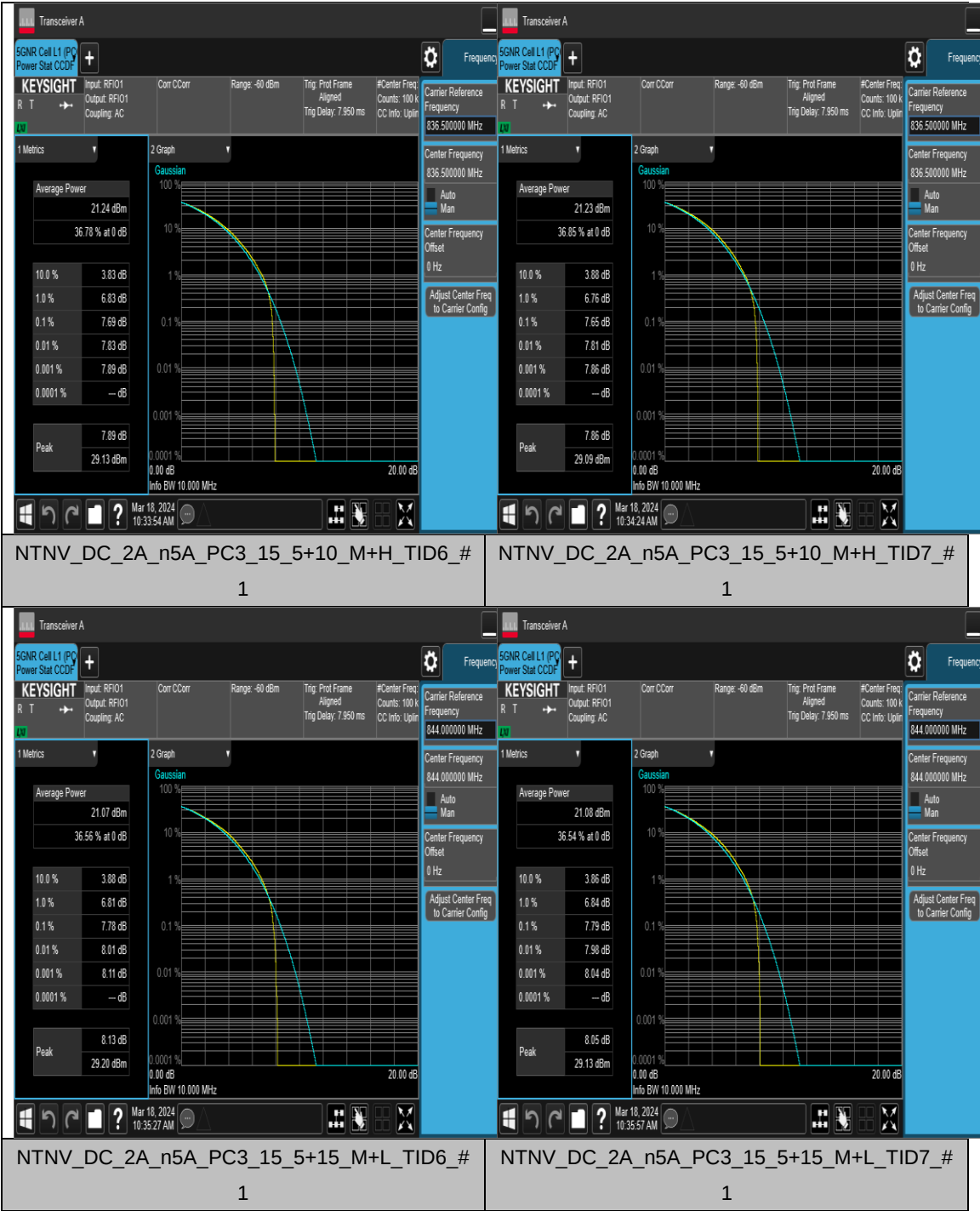
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n5 A	15	5+5	CP-QPSK	M+L	Outer_Full	7.53	≤13	PASS
DC_2A_n5 A	15	5+5	CP-16QAM	M+L	Outer_Full	7.62	≤13	PASS
DC_2A_n5 A	15	5+5	CP-QPSK	M+M	Outer_Full	7.59	≤13	PASS
DC_2A_n5 A	15	5+5	CP-16QAM	M+M	Outer_Full	7.63	≤13	PASS
DC_2A_n5 A	15	5+5	CP-QPSK	M+H	Outer_Full	6.96	≤13	PASS
DC_2A_n5 A	15	5+5	CP-16QAM	M+H	Outer_Full	7.08	≤13	PASS
DC_2A_n5 A	15	5+10	CP-QPSK	M+L	Outer_Full	7.94	≤13	PASS
DC_2A_n5 A	15	5+10	CP-16QAM	M+L	Outer_Full	7.97	≤13	PASS
DC_2A_n5 A	15	5+10	CP-QPSK	M+M	Outer_Full	7.69	≤13	PASS
DC_2A_n5 A	15	5+10	CP-16QAM	M+M	Outer_Full	7.65	≤13	PASS
DC_2A_n5 A	15	5+10	CP-QPSK	M+H	Outer_Full	7.78	≤13	PASS
DC_2A_n5 A	15	5+10	CP-16QAM	M+H	Outer_Full	7.79	≤13	PASS
DC_2A_n5 A	15	5+15	CP-QPSK	M+L	Outer_Full	7.90	≤13	PASS
DC_2A_n5 A	15	5+15	CP-16QAM	M+L	Outer_Full	7.90	≤13	PASS
DC_2A_n5 A	15	5+15	CP-QPSK	M+M	Outer_Full	7.29	≤13	PASS
DC_2A_n5 A	15	5+15	CP-16QAM	M+M	Outer_Full	8.08	≤13	PASS
DC_2A_n5 A	15	5+15	CP-QPSK	M+H	Outer_Full	7.54	≤13	PASS

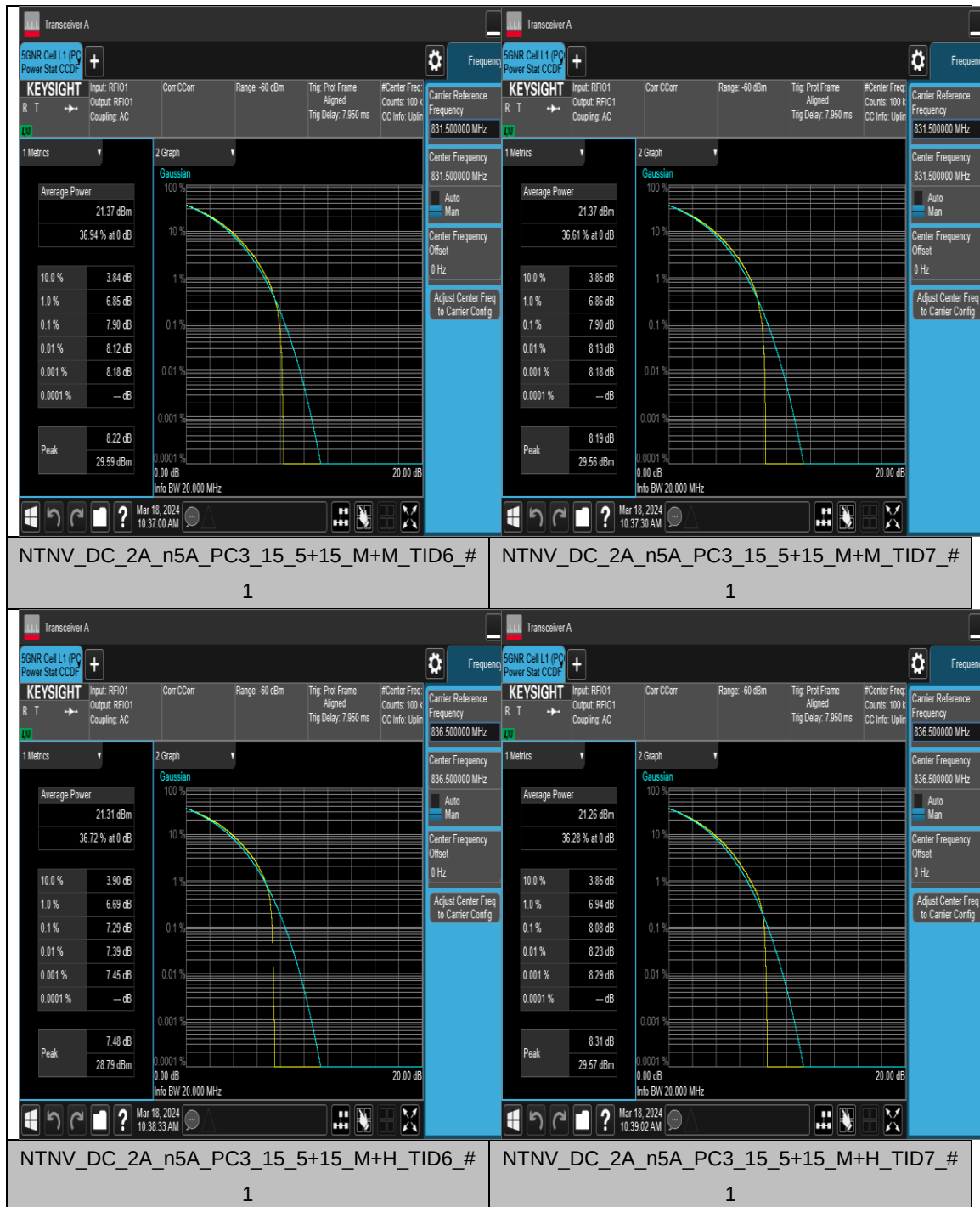
DC_2A_n5 A	15	5+15	CP-16QAM	M+H	Outer_Full	7.49	≤13	PASS
DC_2A_n5 A	15	5+20	CP-QPSK	M+L	Outer_Full	7.68	≤13	PASS
DC_2A_n5 A	15	5+20	CP-16QAM	M+L	Outer_Full	7.68	≤13	PASS
DC_2A_n5 A	15	5+20	CP-QPSK	M+M	Outer_Full	7.71	≤13	PASS
DC_2A_n5 A	15	5+20	CP-16QAM	M+M	Outer_Full	7.73	≤13	PASS
DC_2A_n5 A	15	5+20	CP-QPSK	M+H	Outer_Full	7.83	≤13	PASS
DC_2A_n5 A	15	5+20	CP-16QAM	M+H	Outer_Full	7.84	≤13	PASS

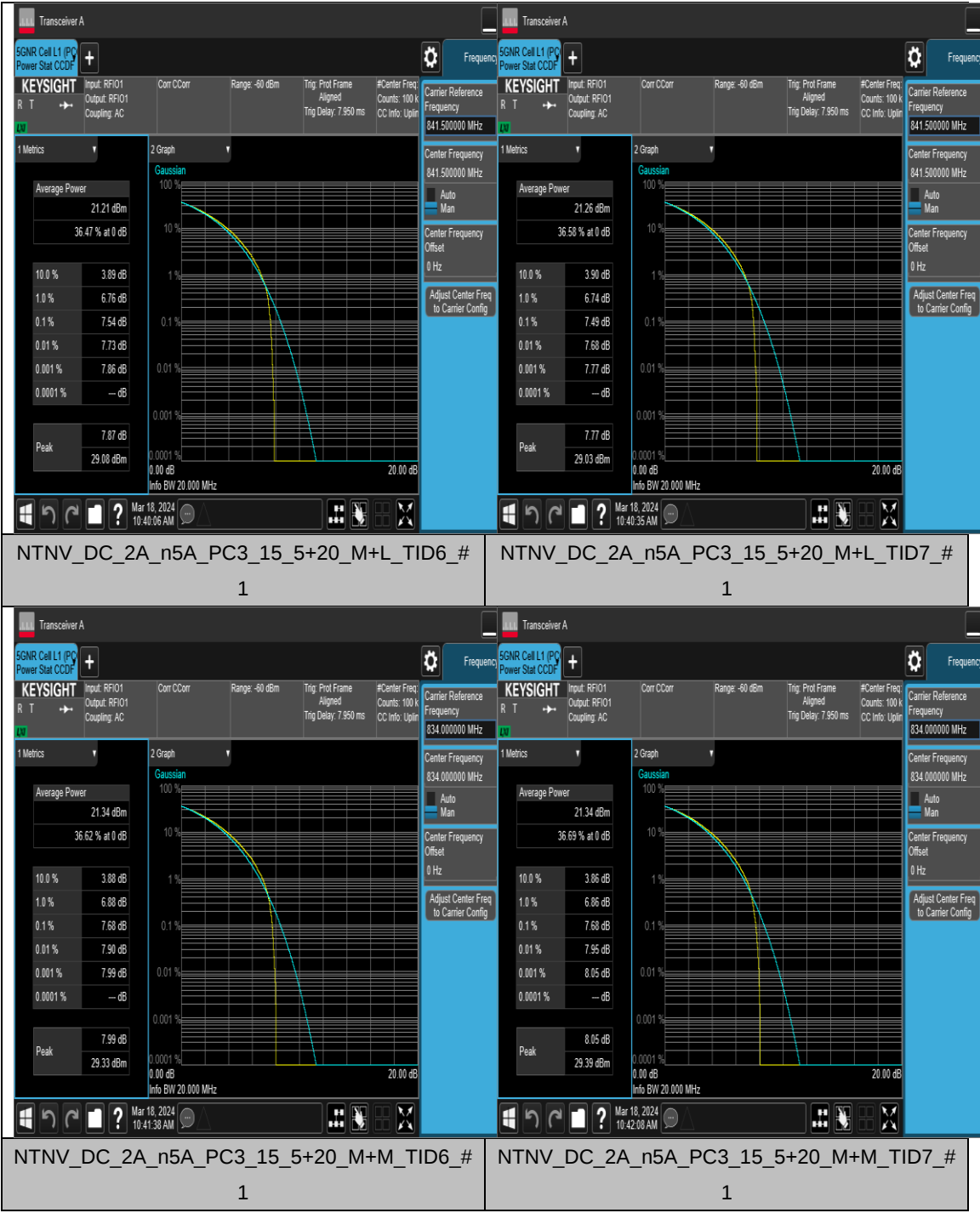
Test Graphs

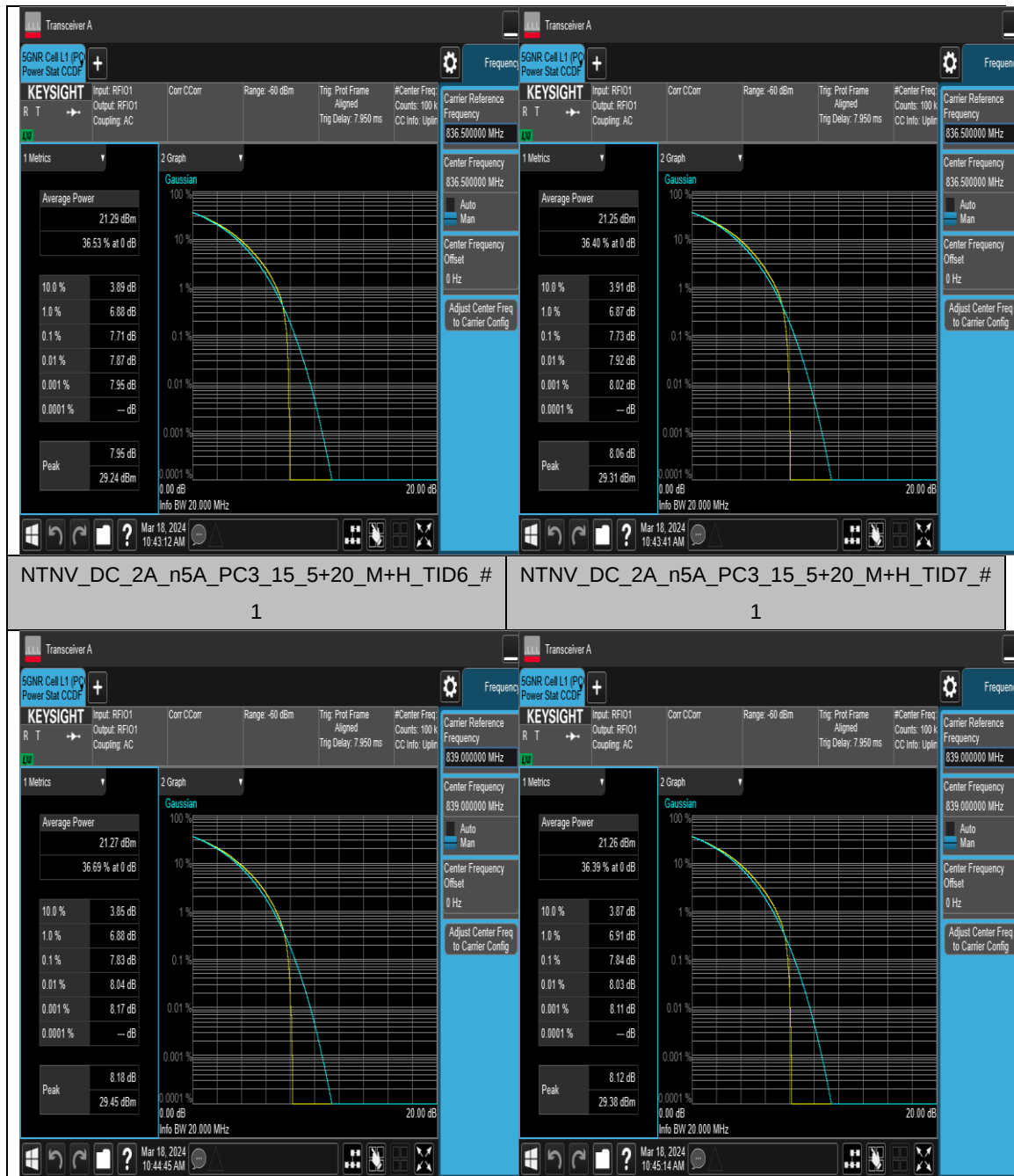












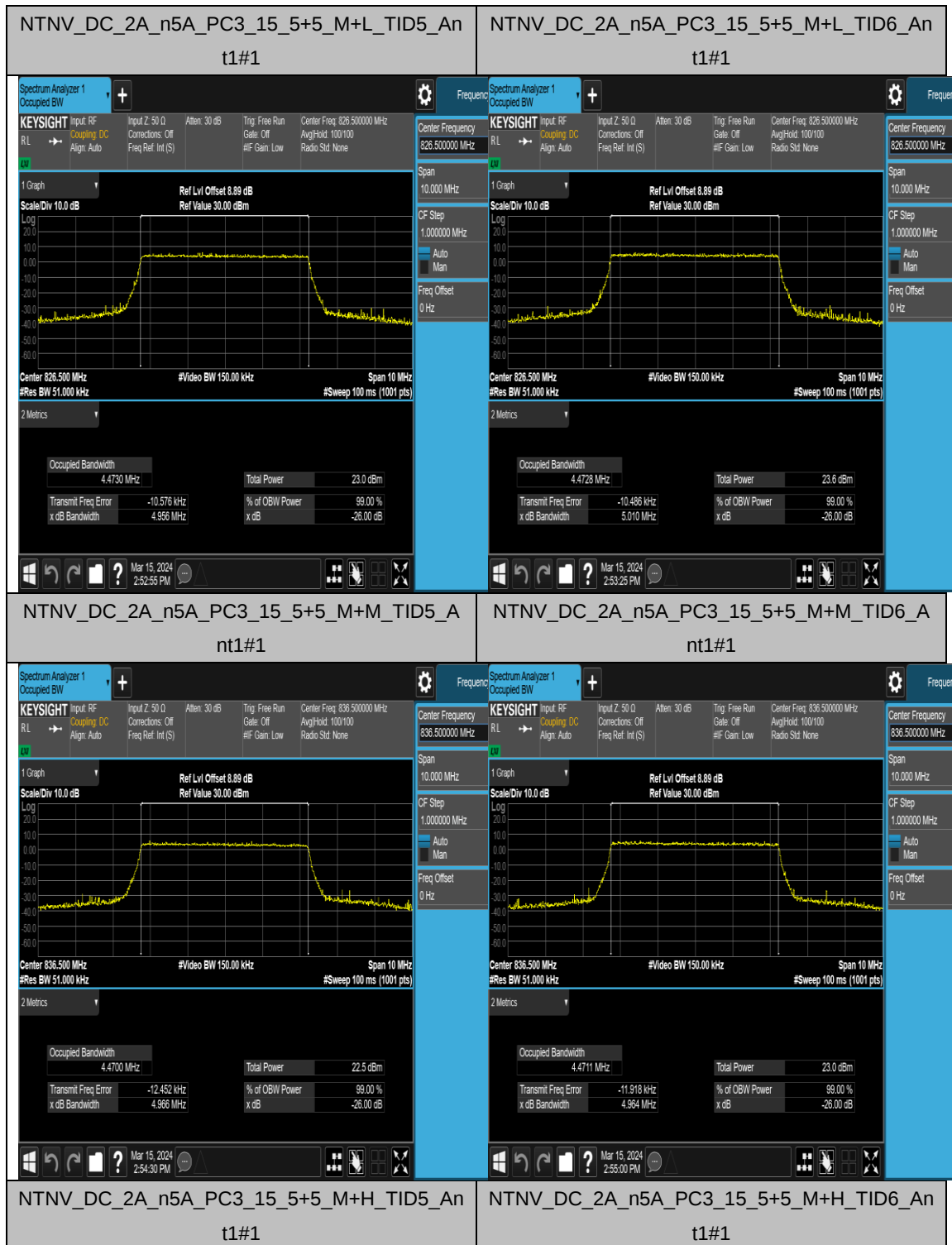
Appendix C: 26dB Bandwidth and Occupied Bandwidth for

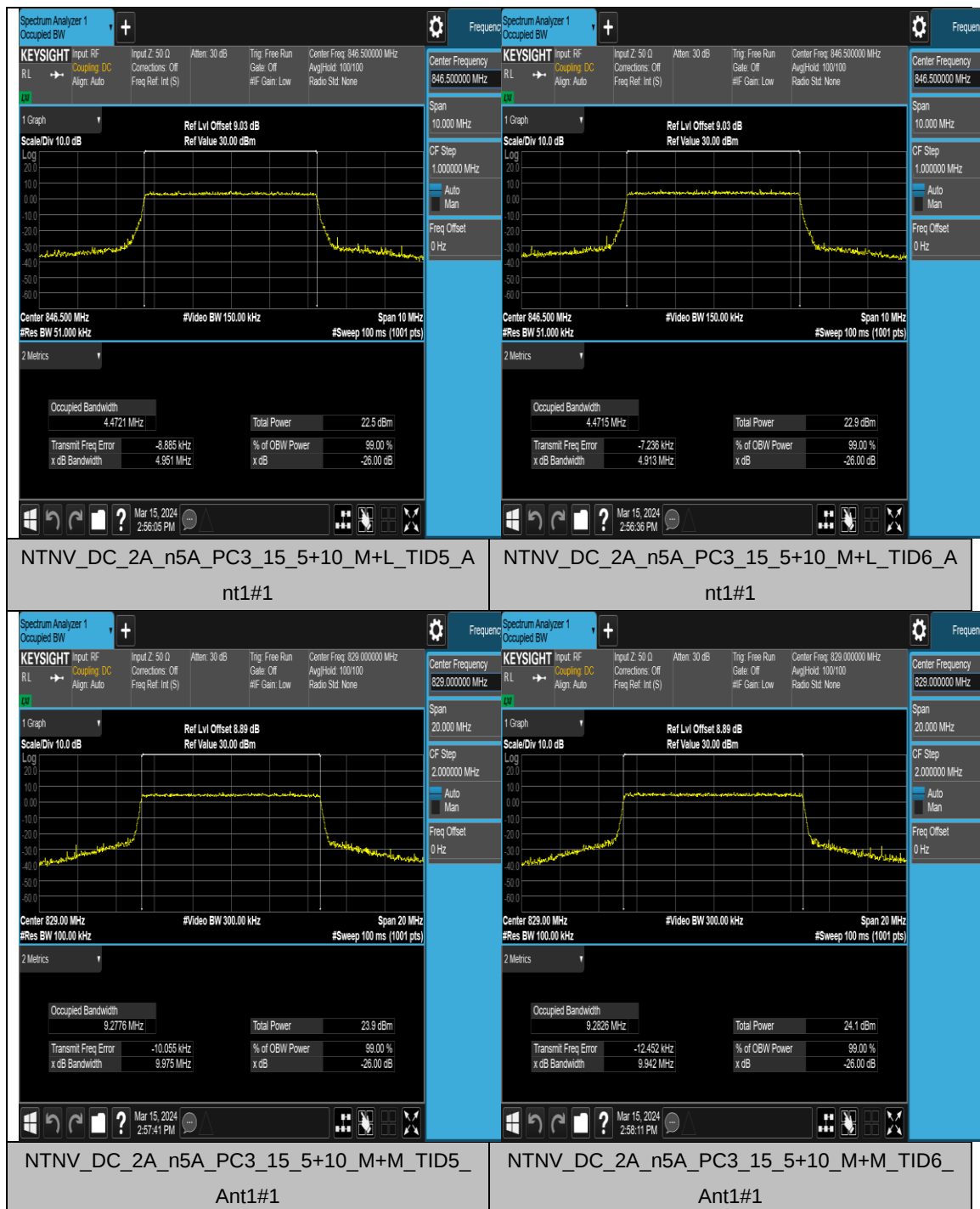
NSA

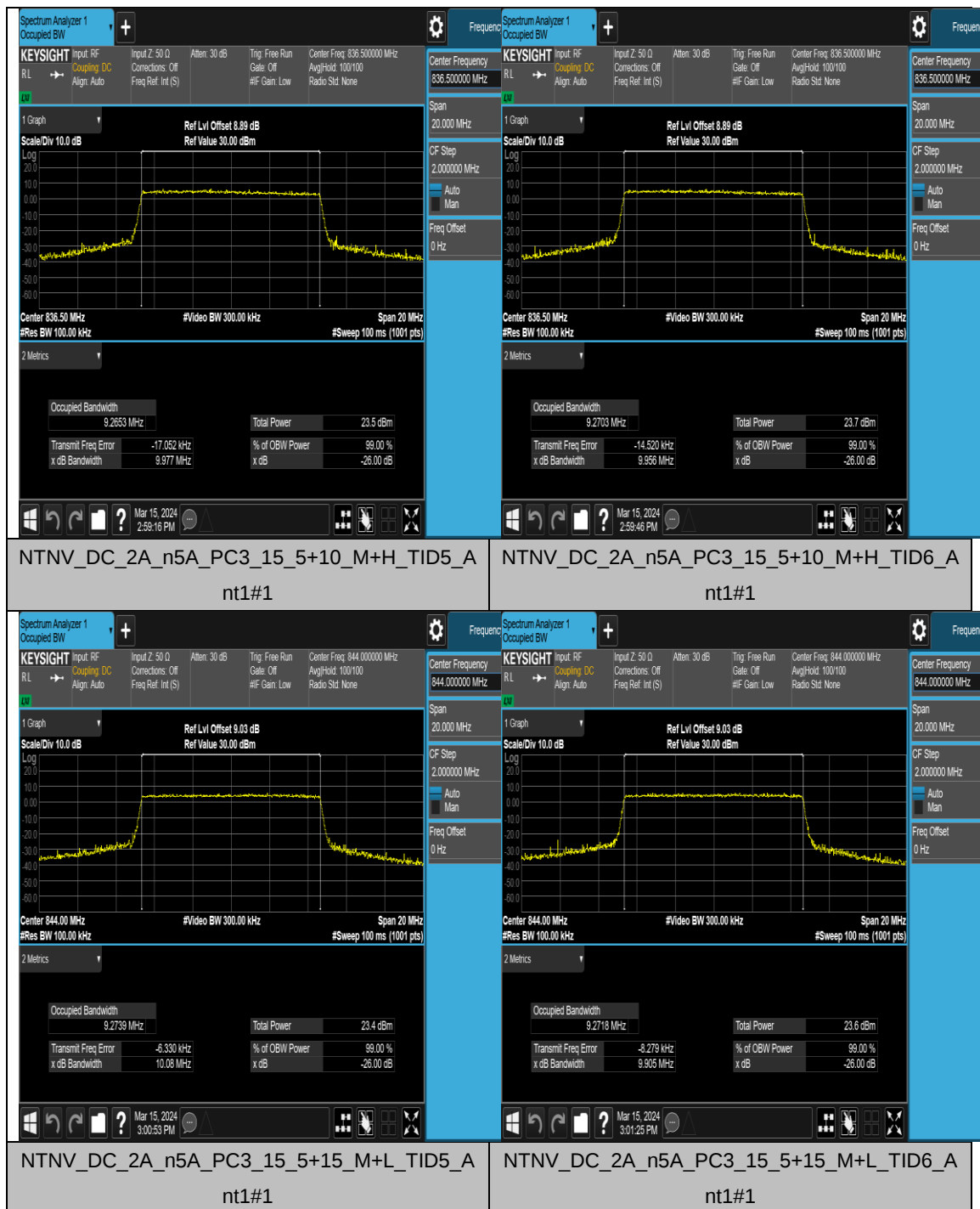
Test Result

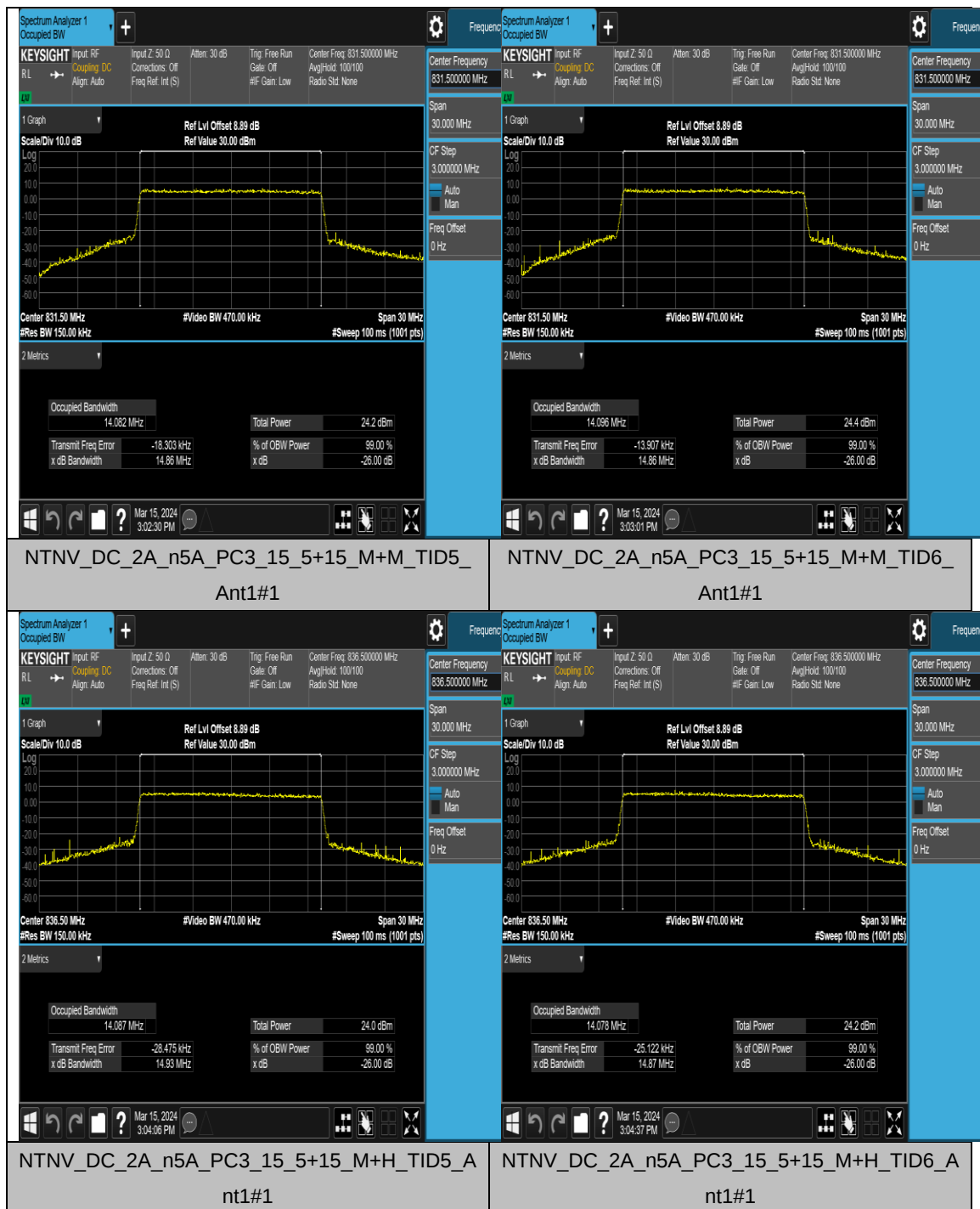
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4730	4.956	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4728	5.010	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4700	4.966	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4711	4.964	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4721	4.951	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4715	4.913	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2776	9.975	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2826	9.942	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2653	9.977	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2703	9.956	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2739	10.08	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2718	9.905	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	14.082	14.86	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	14.096	14.86	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	14.087	14.93	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	14.078	14.87	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	14.109	15.96	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	14.110	14.84	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	18.881	19.84	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	18.879	19.81	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	18.885	19.79	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	18.887	19.77	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	18.880	19.84	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	18.873	19.79	PASS

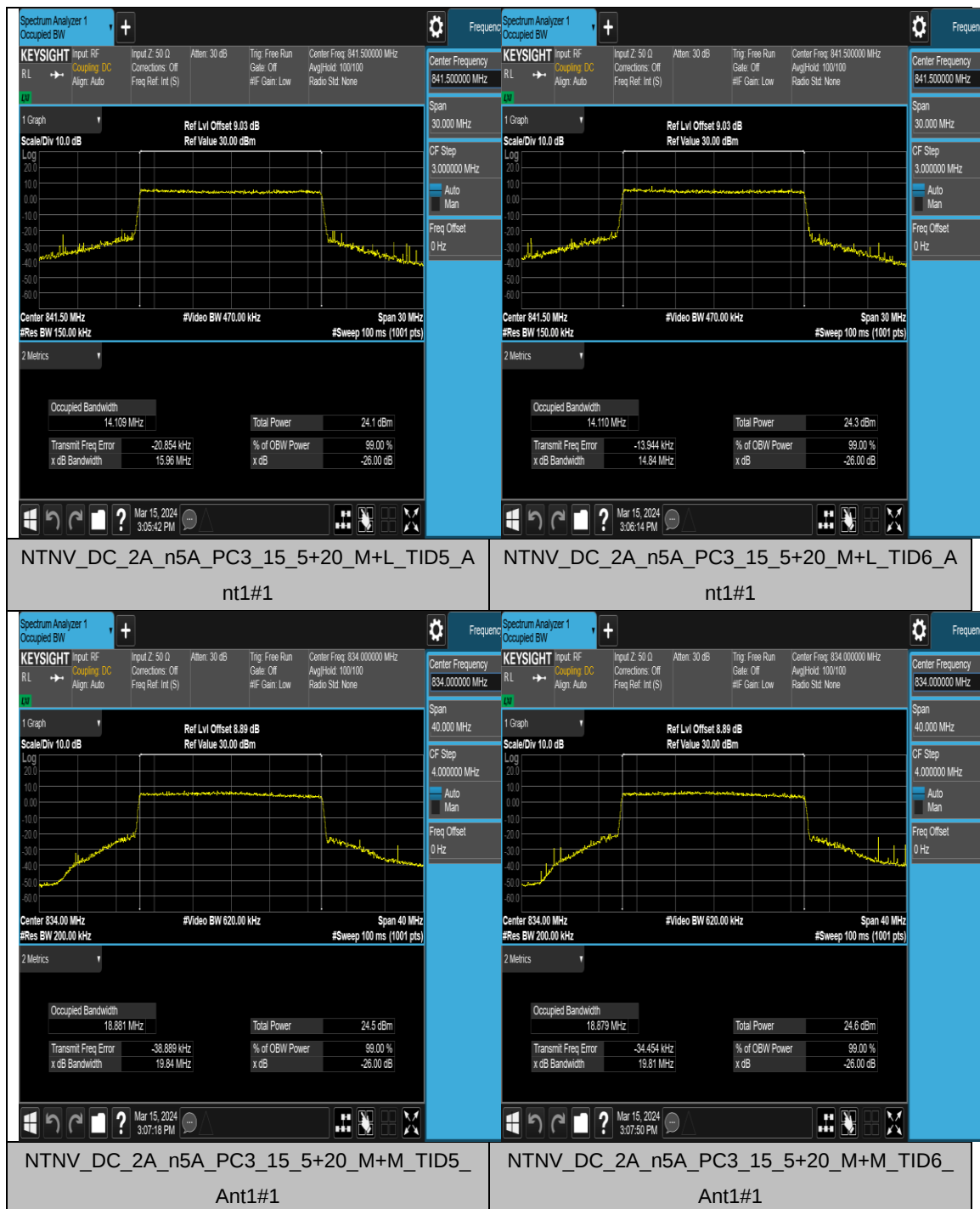
Test Graphs

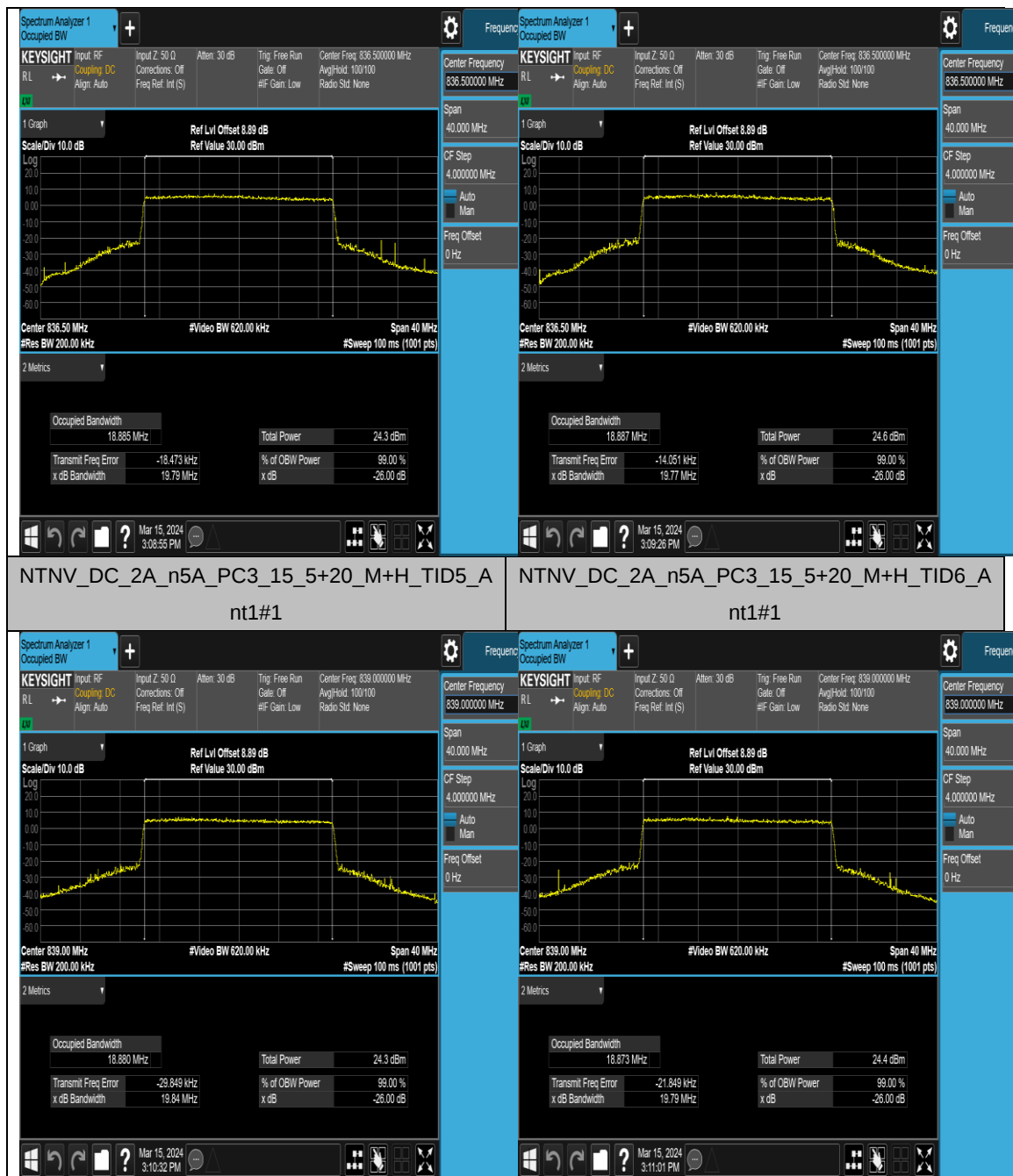










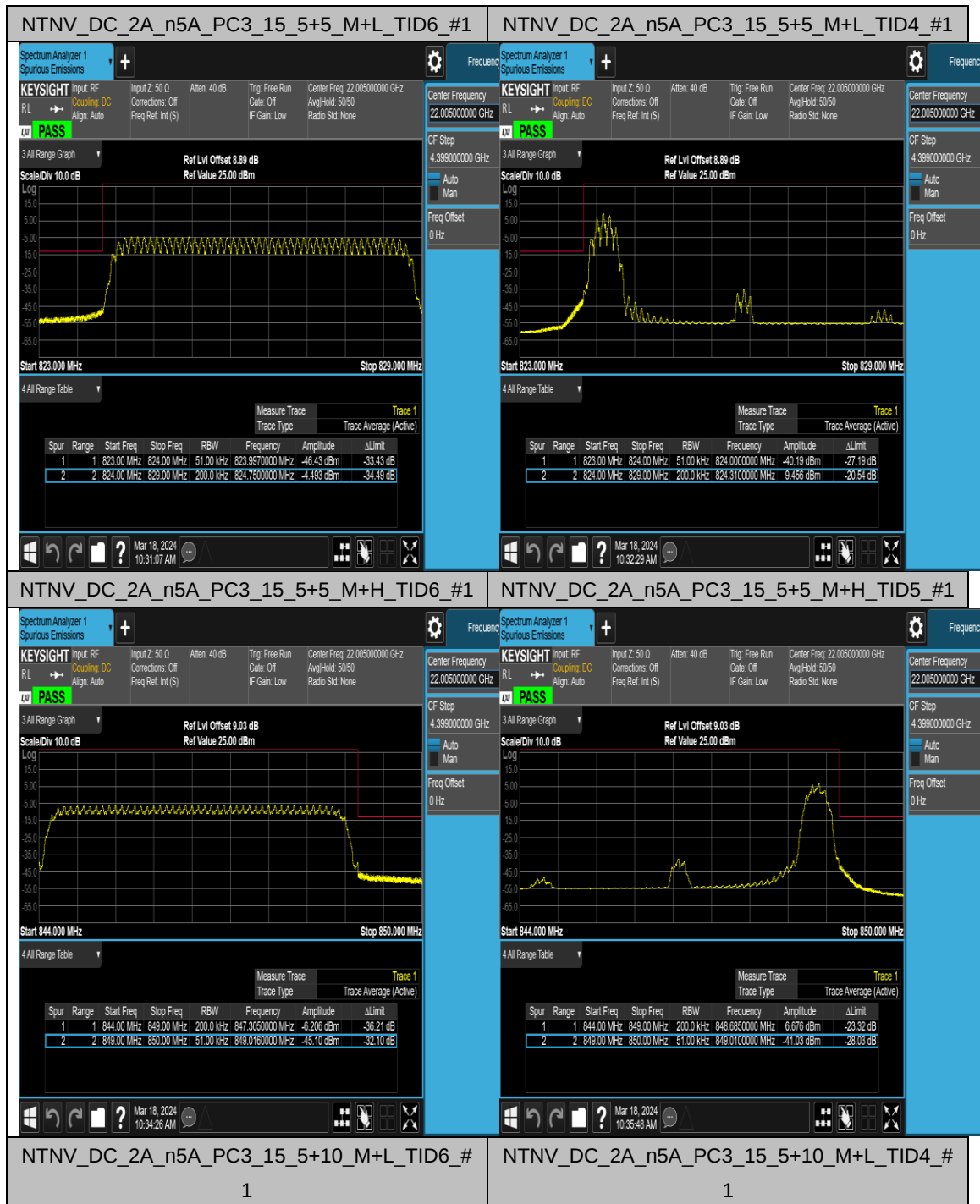


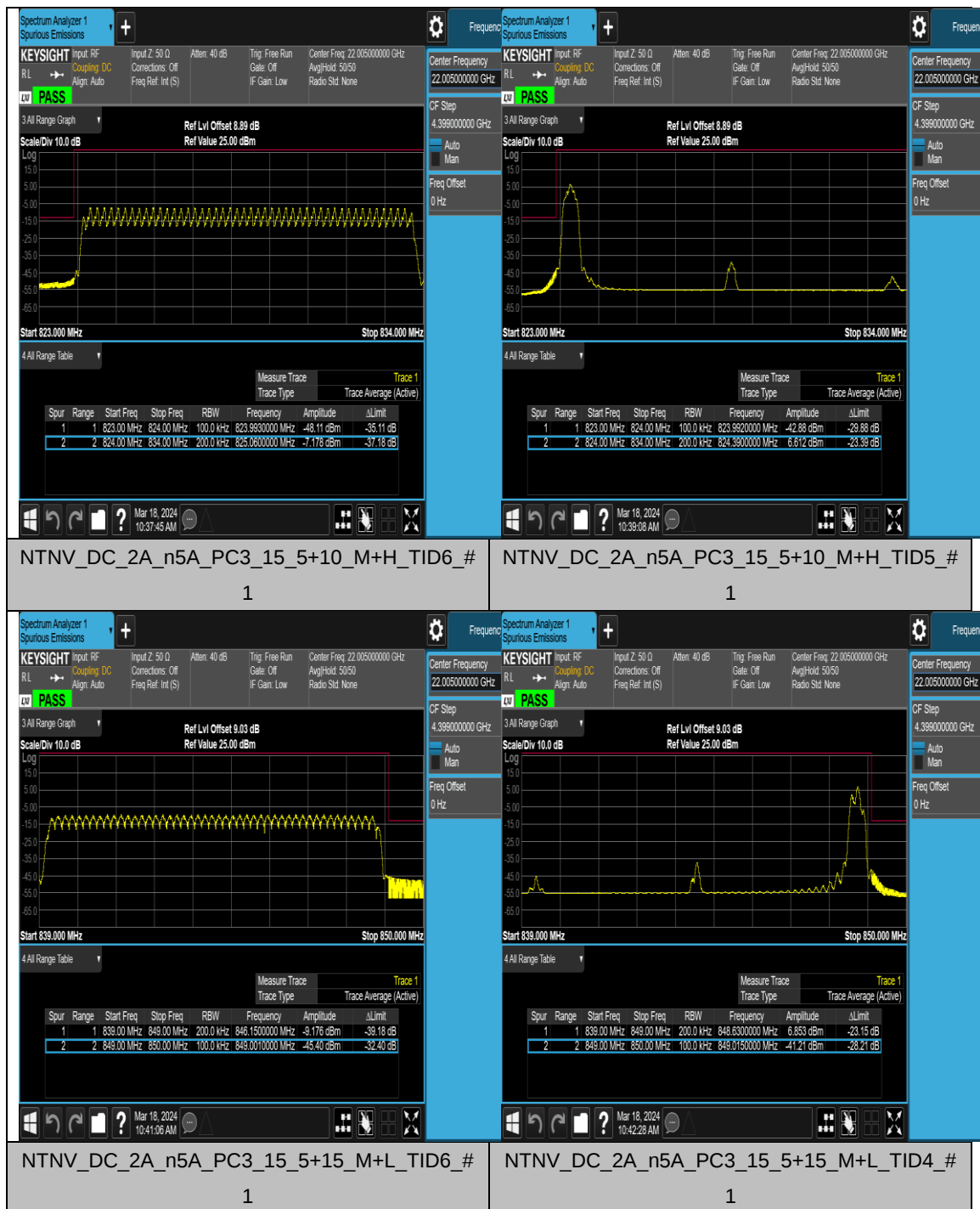
Appendix D: Band Edge for NSA

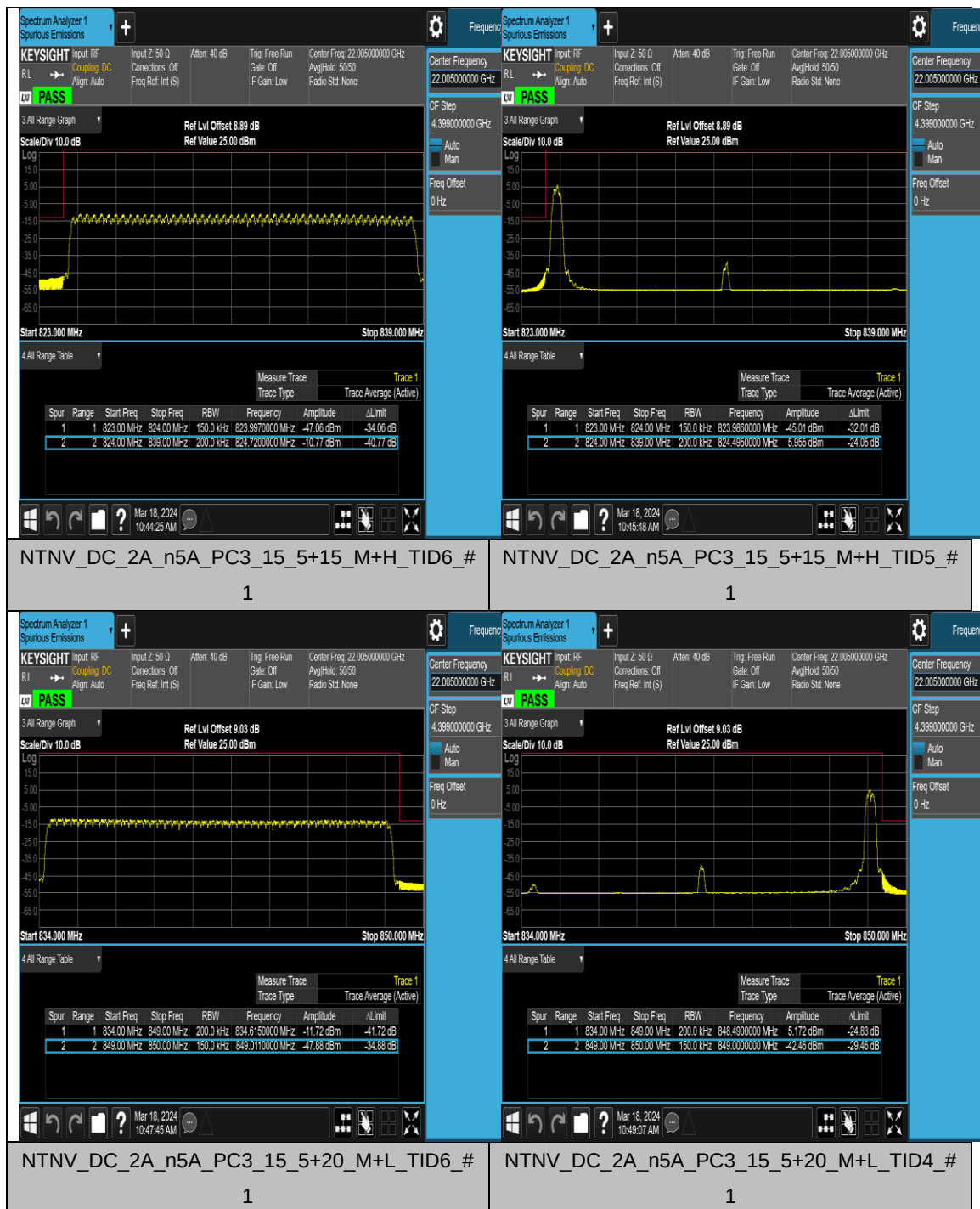
Test Result

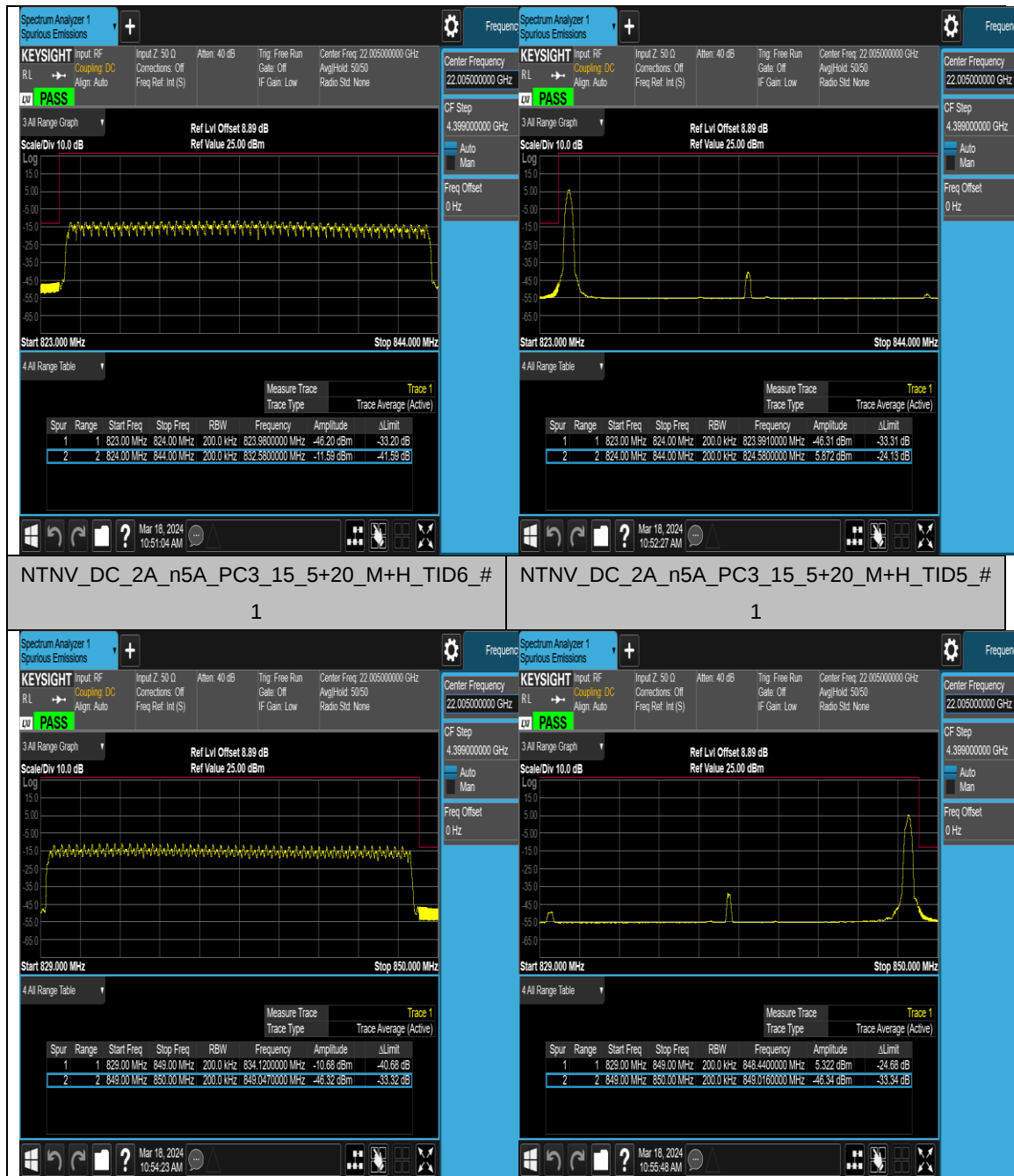
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.38	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-72.29	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-41.35	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-36.72	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-43.89	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-45.12	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-60.85	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.95	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-51.58	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-36.64	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.92	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.18	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.14	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.33	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.42	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-36.65	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-44.00	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.39	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-63.33	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-73.47	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-41.59	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-36.63	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-43.99	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-45.22	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.08	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.70	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-51.26	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-36.68	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-44.12	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.31	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-61.49	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.28	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.38	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-36.73	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.80	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.33	-13	PASS

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DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-61.73	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-72.34	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-52.47	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-36.72	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-44.07	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-45.12	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-61.40	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.09	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.44	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-36.62	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-44.15	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.42	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.18	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.53	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-51.80	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-36.68	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-44.04	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.32	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.20	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-73.02	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-52.59	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-36.66	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-44.07	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-45.27	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.17	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.32	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.53	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-36.69	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-44.06	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.50	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.81	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.14	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-52.21	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-36.69	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-44.10	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.43	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.66	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-72.88	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-40.39	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-36.63	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-44.08	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-45.36	-13	PASS

DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.35	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.19	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-51.92	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-36.75	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-44.04	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.22	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.84	-33	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.13	-23	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.67	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-36.68	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-44.14	-13	PASS
DC_2A_n5A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.22	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-63.33	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-71.04	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-40.03	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-36.67	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-44.12	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-45.08	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.97	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.23	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-52.26	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-36.68	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.93	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.37	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.67	-33	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-71.53	-23	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-50.36	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-36.70	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-44.15	-13	PASS
DC_2A_n5A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.41	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-61.36	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-72.66	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-40.35	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-36.74	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-44.21	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-45.44	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.19	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.49	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-51.53	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-36.69	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-44.04	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.26	-13	PASS

DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.24	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.58	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.23	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-36.65	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-44.05	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.38	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.79	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-71.78	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-41.57	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-36.64	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-43.99	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-45.37	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.38	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-71.81	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-51.95	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-36.72	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-44.07	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.27	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-61.89	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-71.88	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.25	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-36.64	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-44.00	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.34	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.26	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-71.33	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-49.50	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-36.67	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-44.02	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-45.33	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.73	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.70	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-51.88	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-36.64	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.88	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.45	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.71	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-71.31	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-51.03	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-36.65	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-44.08	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.23	-13	PASS

DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-61.73	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-73.32	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-49.39	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-36.65	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-43.84	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-45.37	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.97	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.57	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.38	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-36.65	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.85	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.25	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.63	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-71.27	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-51.44	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-36.62	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.86	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.43	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.53	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-72.70	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-42.50	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-36.56	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-44.07	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-45.40	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.22	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.89	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-51.13	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-36.65	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-44.11	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.33	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.63	-33	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.45	-23	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.55	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-36.68	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.97	-13	PASS
DC_2A_n5A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.10	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-61.54	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-71.92	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-43.71	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-36.63	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-44.08	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-45.36	-13	PASS

DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-61.95	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.23	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-51.94	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-36.68	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-44.01	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.34	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.63	-33	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.18	-23	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-50.56	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-36.64	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-44.11	-13	PASS
DC_2A_n5A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.40	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.06	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-71.52	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-41.14	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-36.66	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-44.06	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-45.28	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.63	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.19	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-50.88	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-36.61	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-44.06	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.24	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.04	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.45	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.07	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-36.64	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.99	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.33	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-61.91	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-71.98	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-40.36	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-36.63	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-43.98	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-45.35	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-63.47	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.44	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-49.95	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-36.62	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-44.16	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.31	-13	PASS

DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-60.93	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.32	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.23	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-36.66	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-44.05	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.43	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.19	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-73.49	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-47.29	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-36.65	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-44.06	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-45.42	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.52	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.74	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-51.48	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-36.57	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-44.05	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.32	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.73	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.38	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-51.53	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-36.61	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.94	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.29	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.32	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-72.98	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-47.30	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-36.64	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-44.11	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-45.40	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.73	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.55	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-51.89	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-36.59	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-44.01	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.43	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.80	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-71.96	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-50.66	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-36.54	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.92	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.32	-13	PASS

DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.69	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-73.01	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-42.74	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-36.62	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-44.12	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-45.42	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-61.92	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.34	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.37	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-36.68	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-44.06	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.29	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-61.54	-33	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-71.68	-23	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-46.79	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-36.53	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.98	-13	PASS
DC_2A_n5A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.38	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-61.51	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-71.40	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-41.77	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-36.63	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-44.02	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-45.26	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-61.19	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-71.55	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-52.61	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-36.63	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.90	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.26	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.56	-33	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-71.76	-23	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-49.99	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-36.60	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-44.20	-13	PASS
DC_2A_n5A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.43	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.04	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-73.62	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-39.55	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-36.63	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-44.09	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-45.35	-13	PASS

DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-61.82	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.74	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-52.39	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-36.66	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.90	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.36	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.37	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-70.73	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.03	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-36.69	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.95	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.45	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-61.86	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-73.38	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-38.62	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-36.67	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-43.99	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-45.35	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.08	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-71.21	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-52.32	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-36.67	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-44.05	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.39	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.85	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.29	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-51.47	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-36.62	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.87	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.38	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-61.99	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-73.97	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-43.23	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-36.46	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-43.82	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-45.30	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.49	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.61	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.55	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-36.63	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-44.04	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.22	-13	PASS

DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.64	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.02	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-52.19	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-36.72	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-44.16	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.41	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.85	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-72.31	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-44.49	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-36.50	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-44.01	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-45.37	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-61.80	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.43	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.32	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-36.62	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-44.05	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.32	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.98	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.18	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-52.28	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-36.51	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.98	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.41	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.22	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-72.06	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-42.20	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-36.67	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-44.01	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-45.22	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-60.55	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-70.51	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.43	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-36.61	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.97	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.31	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.50	-33	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-71.35	-23	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.33	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-36.49	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.78	-13	PASS
DC_2A_n5A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.22	-13	PASS

DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-61.39	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-72.34	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-42.10	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-36.55	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-44.03	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-45.38	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.31	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.00	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-52.39	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-36.70	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.98	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.38	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-61.72	-33	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.91	-23	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-50.87	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-36.54	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.93	-13	PASS
DC_2A_n5A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.40	-13	PASS