

Appendix D.4: Maximum conducted output power

Test Result Channel Power

Test Mode	Antenna	Frequency[M Hz]	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	14.75	98.12	0.08	14.83	≤23.98	PASS
		5200	13.94	98.12	0.08	14.02	≤23.98	PASS
		5240	13.88	97.65	0.10	13.98	≤23.98	PASS
11N20SISO	Ant1	5180	13.54	99.01	0.04	13.58	≤23.98	PASS
		5200	13.51	99.01	0.04	13.55	≤23.98	PASS
		5240	14.14	98.77	0.05	14.19	≤23.98	PASS
11N40SISO	Ant1	5190	13.87	99.01	0.04	13.91	≤23.98	PASS
		5230	14.27	98.76	0.05	14.32	≤23.98	PASS
11AC20SISO	Ant1	5180	11.13	98.77	0.05	11.18	≤23.98	PASS
		5200	14.16	98.77	0.05	14.21	≤23.98	PASS
		5240	14.17	98.77	0.05	14.22	≤23.98	PASS
11AC40SISO	Ant1	5190	13.93	98.76	0.05	13.98	≤23.98	PASS
		5230	14.35	98.76	0.05	14.40	≤23.98	PASS
11AC80SISO	Ant1	5210	14.34	99.00	0.04	14.38	≤23.98	PASS
11AX20SISO	Ant1	5180	13.44	98.77	0.05	13.49	≤23.98	PASS
		5200	13.91	98.77	0.05	13.96	≤23.98	PASS
		5240	13.93	98.77	0.05	13.98	≤23.98	PASS
11AX40SISO	Ant1	5190	13.56	98.76	0.05	13.61	≤23.98	PASS
		5230	14.04	98.76	0.05	14.09	≤23.98	PASS
11AX80SISO	Ant1	5210	13.97	98.76	0.05	14.02	≤23.98	PASS

Note: The Duty Cycle Factor is compensated in the graph.

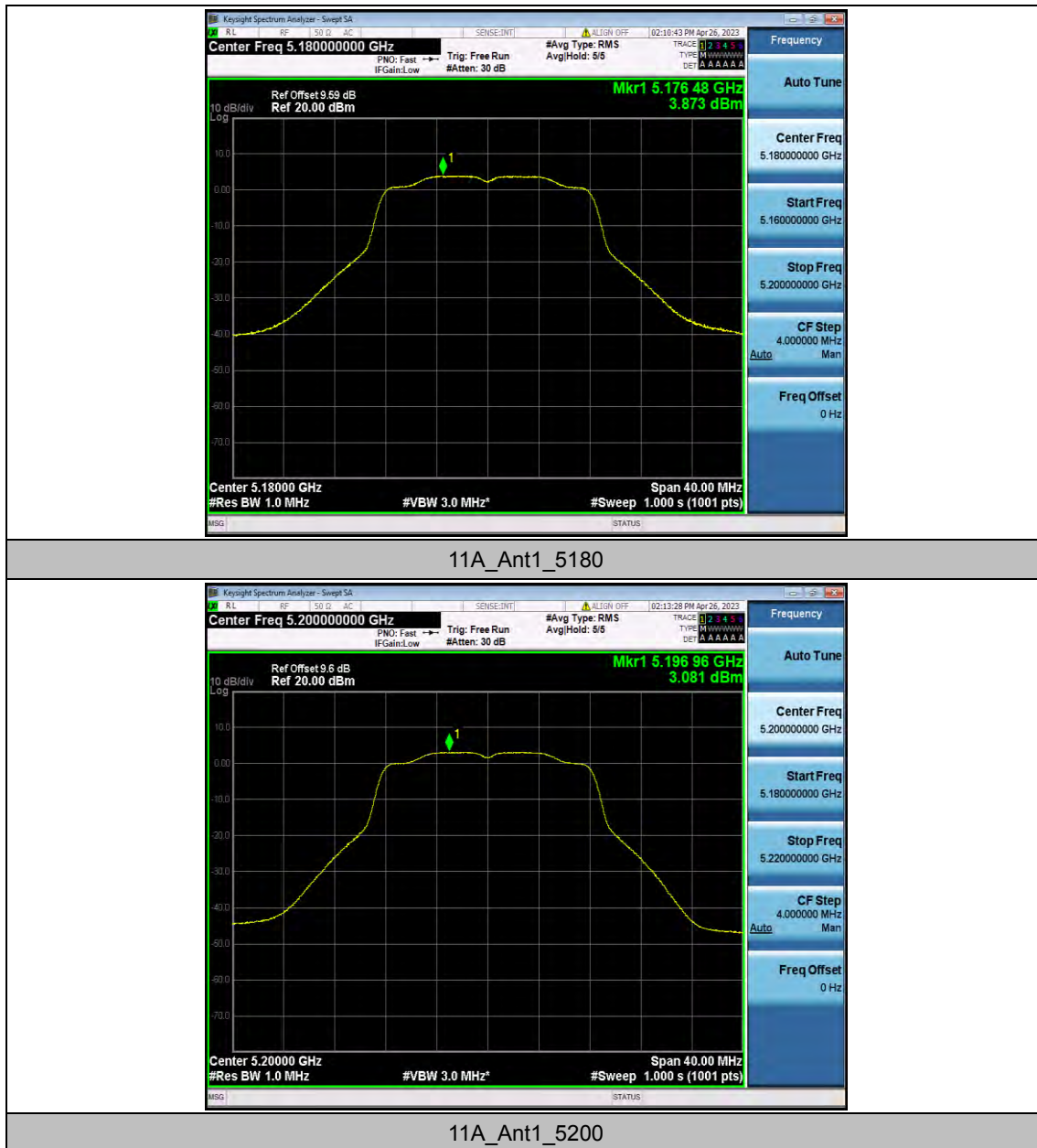
Appendix D.5: Maximum power spectral density

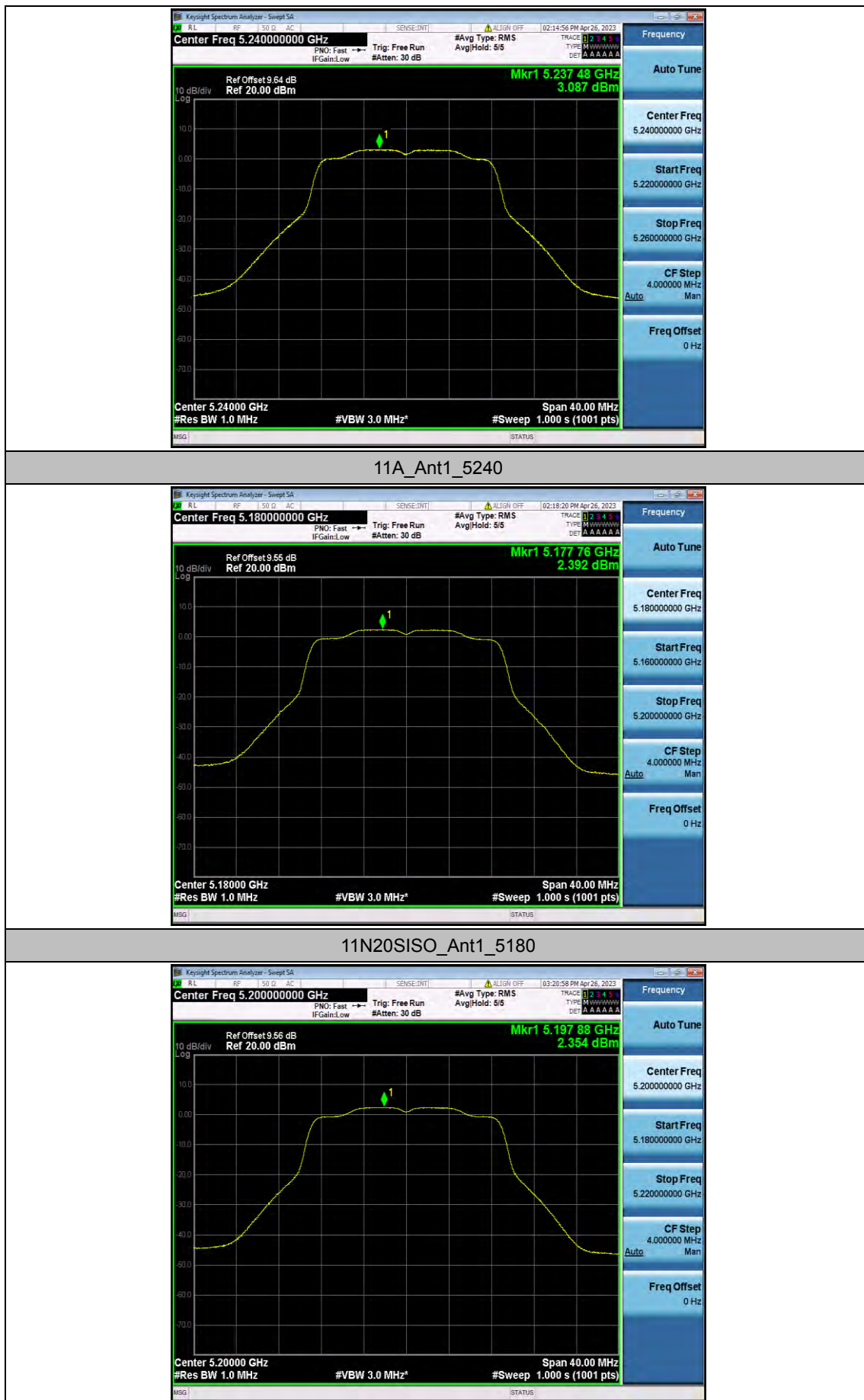
Test Result

TestMode	Antenna	Frequency[MHz]	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant1	5180	3.87	≤11.00	PASS
		5200	3.08	≤11.00	PASS
		5240	3.09	≤11.00	PASS
11N20SISO	Ant1	5180	2.39	≤11.00	PASS
		5200	2.35	≤11.00	PASS
		5240	3.09	≤11.00	PASS
11N40SISO	Ant1	5190	-0.28	≤11.00	PASS
		5230	0.06	≤11.00	PASS
11AC20SISO	Ant1	5180	-0.03	≤11.00	PASS
		5200	3.02	≤11.00	PASS
		5240	3.21	≤11.00	PASS
11AC40SISO	Ant1	5190	-0.31	≤11.00	PASS
		5230	0.11	≤11.00	PASS
11AC80SISO	Ant1	5210	-2.89	≤11.00	PASS
11AX20SISO	Ant1	5180	2.06	≤11.00	PASS
		5200	2.58	≤11.00	PASS
		5240	2.68	≤11.00	PASS
11AX40SISO	Ant1	5190	-0.85	≤11.00	PASS
		5230	-0.38	≤11.00	PASS
11AX80SISO	Ant1	5210	-3.35	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.
 2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs





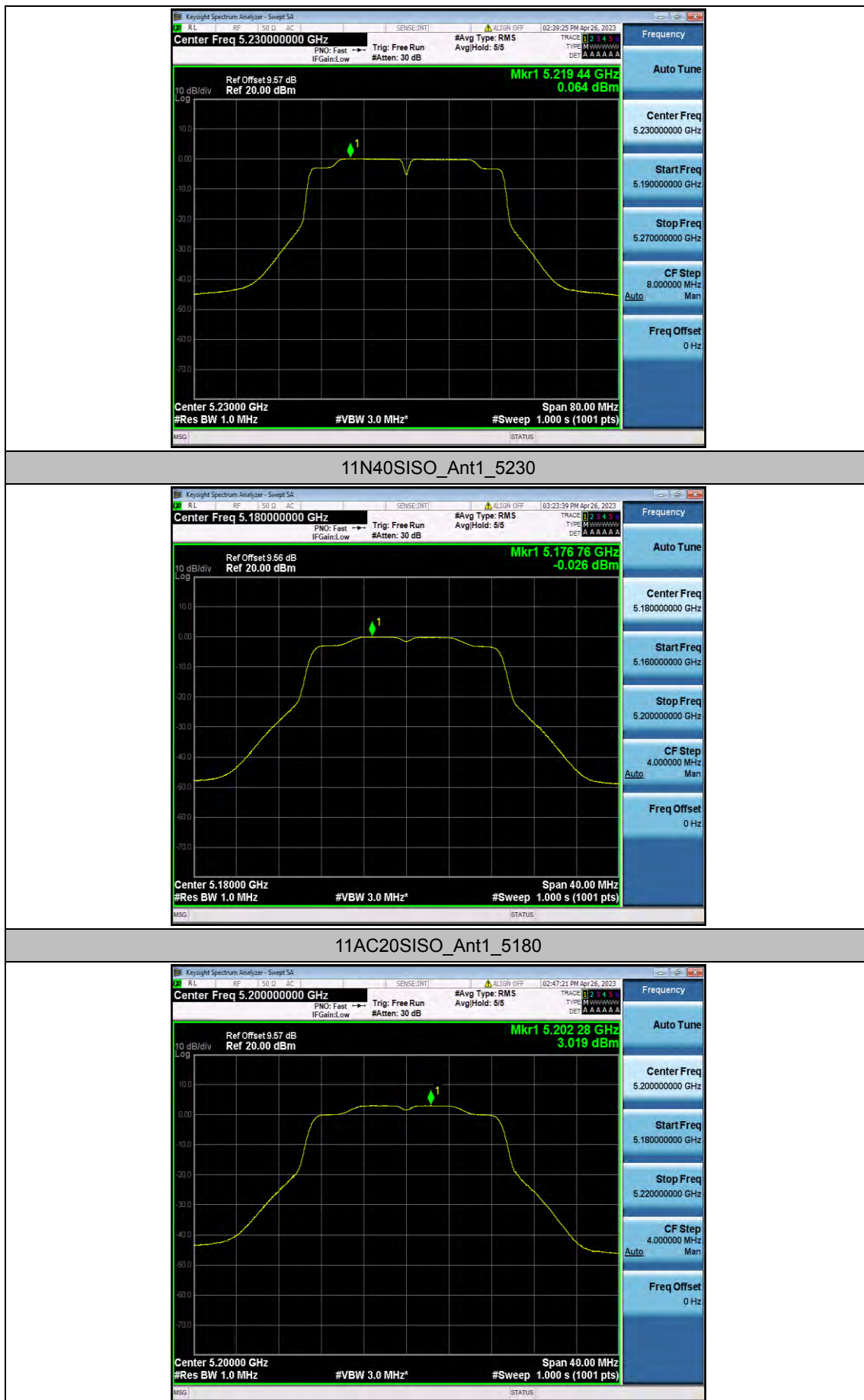
11N20SISO_Ant1_5200



11N20SISO_Ant1_5240



11N40SISO_Ant1_5190



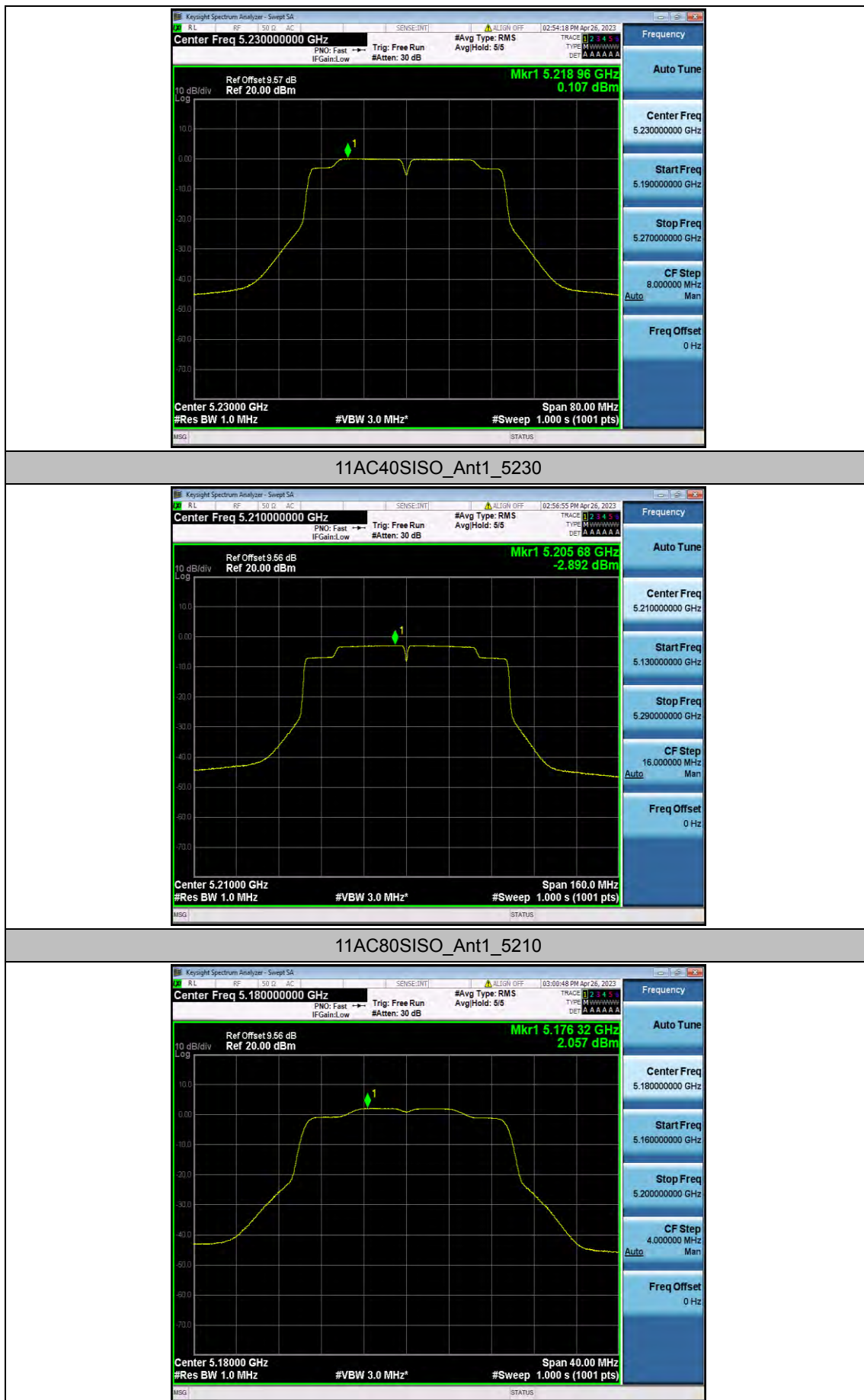
11AC20SISO_Ant1_5200



11AC20SISO_Ant1_5240



11AC40SISO_Ant1_5190



11AX20SISO_Ant1_5180



11AX20SISO_Ant1_5200



11AX20SISO_Ant1_5240



11AX40SISO_Ant1_5190



11AX40SISO_Ant1_5230



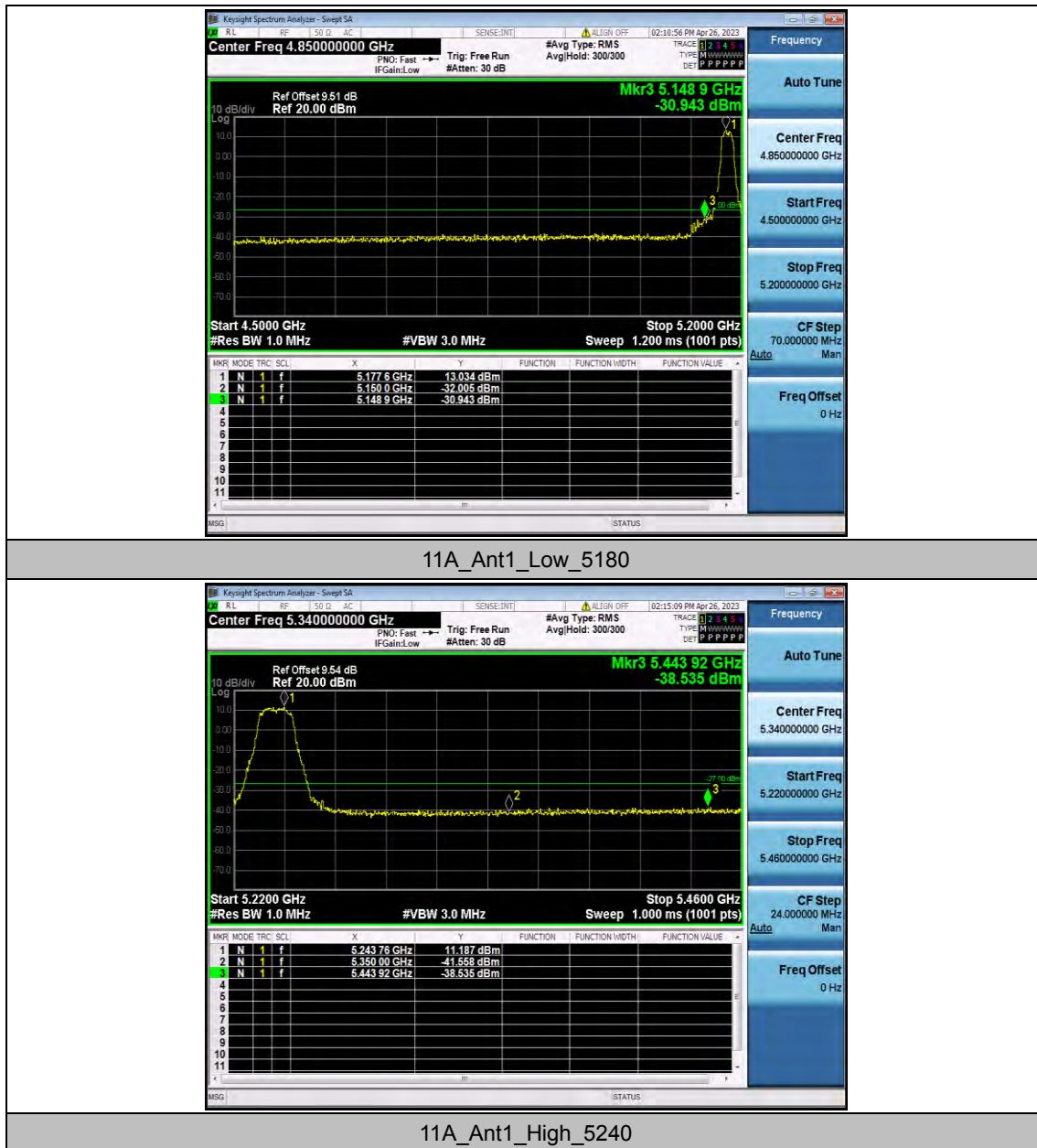
11AX80SISO_Ant1_5210

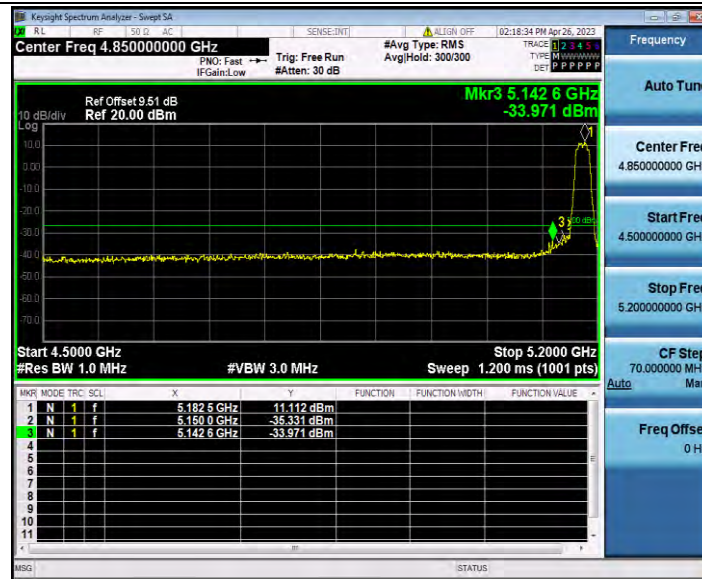
Appendix D.6: Band edge measurements

Test Result B1

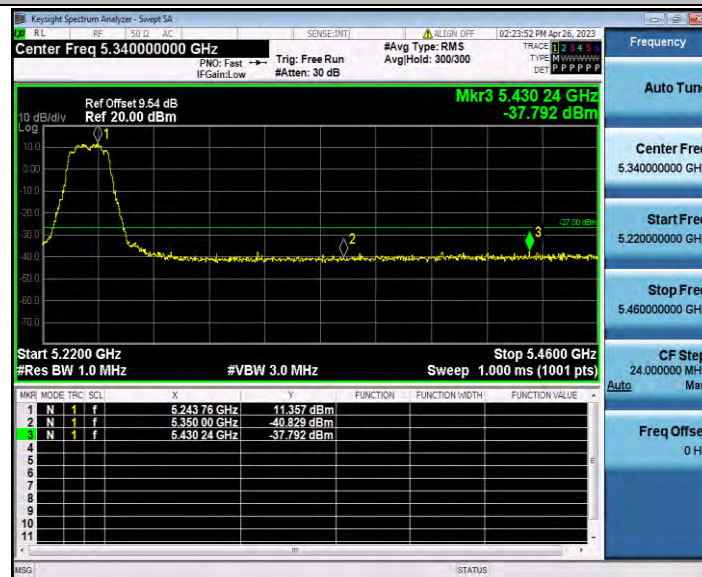
TestMode	Antenna	ChName	Frequency[MHz]	Result[dBm]	Limit[dBm]	Verdict
11A	Ant1	Low	5180	-30.94	≤ -27	PASS
		High	5240	-38.54	≤ -27	PASS
11N20SISO	Ant1	Low	5180	-33.97	≤ -27	PASS
		High	5240	-37.79	≤ -27	PASS
11N40SISO	Ant1	Low	5190	-32.84	≤ -27	PASS
		High	5230	-37.63	≤ -27	PASS
11AC20SISO	Ant1	Low	5180	-37.78	≤ -27	PASS
		High	5240	-38.33	≤ -27	PASS
11AC40SISO	Ant1	Low	5190	-31.62	≤ -27	PASS
		High	5230	-37.59	≤ -27	PASS
11AC80SISO	Ant1	Low	5210	-31.87	≤ -27	PASS
		High	5210	-36.68	≤ -27	PASS
11AX20SISO	Ant1	Low	5180	-34.54	≤ -27	PASS
		High	5240	-36.4	≤ -27	PASS
11AX40SISO	Ant1	Low	5190	-31.31	≤ -27	PASS
		High	5230	-37.75	≤ -27	PASS
11AX80SISO	Ant1	Low	5210	-31.28	≤ -27	PASS
		High	5210	-37.84	≤ -27	PASS

Test Graphs B1

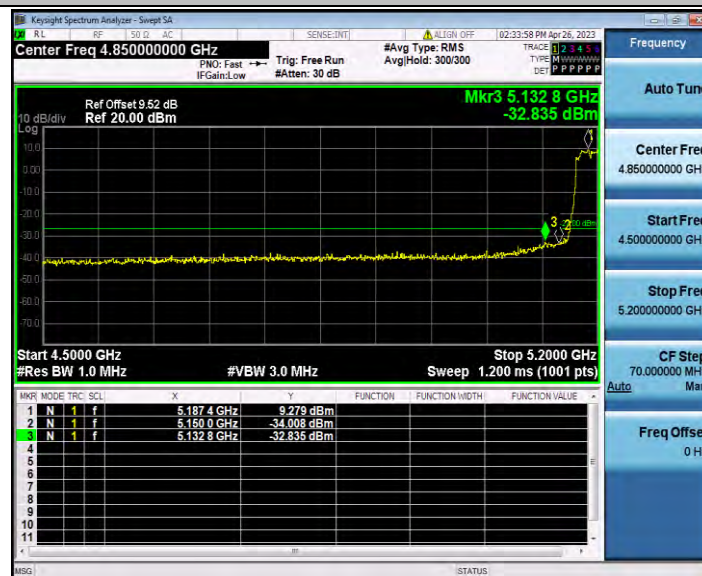




11N20SISO_Ant1_Low_5180



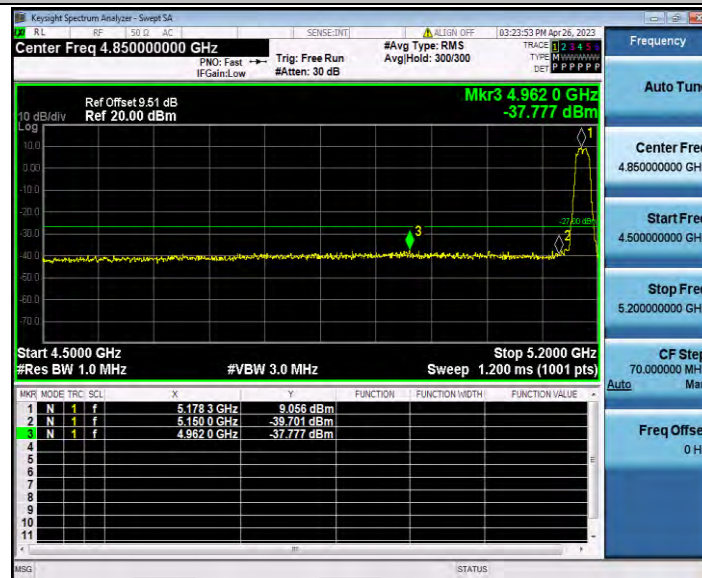
11N20SISO_Ant1_High_5240



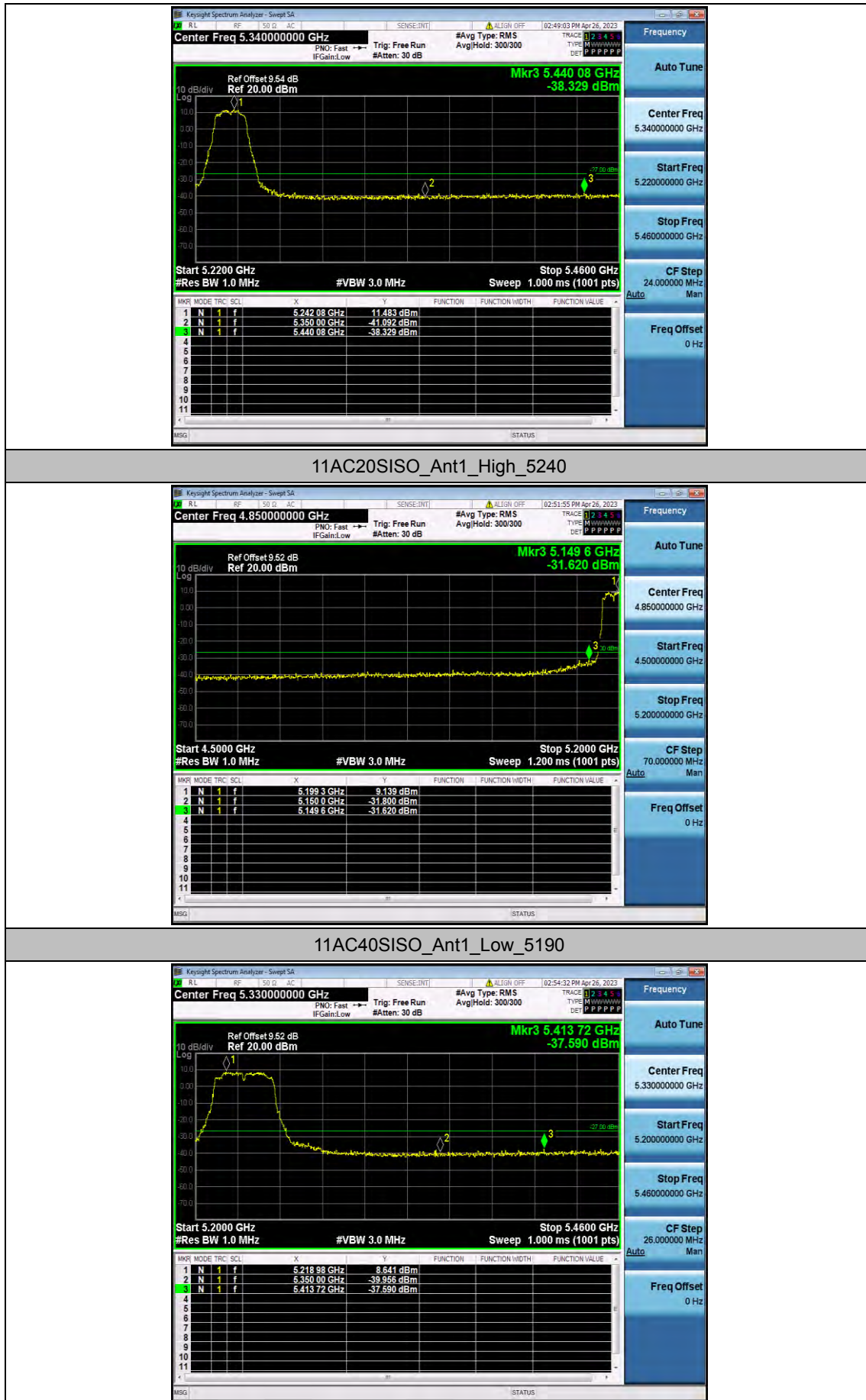
11N40SISO_Ant1_Low_5190



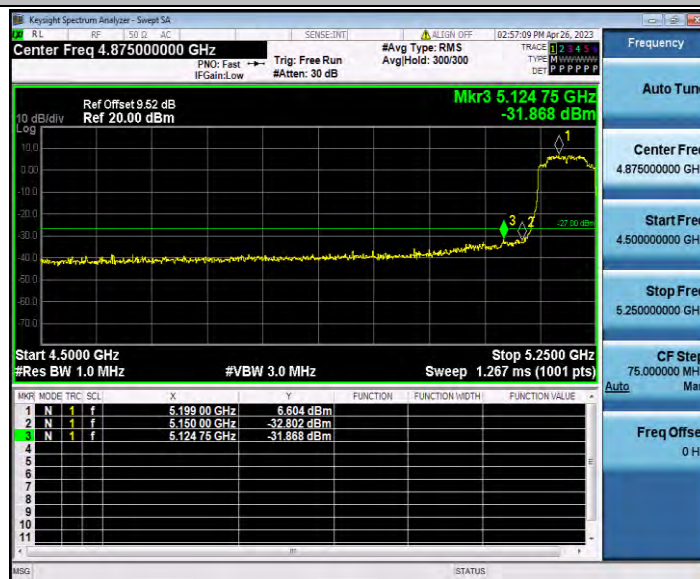
11N40SISO_Ant1_High_5230



11AC20SISO_Ant1_Low_5180



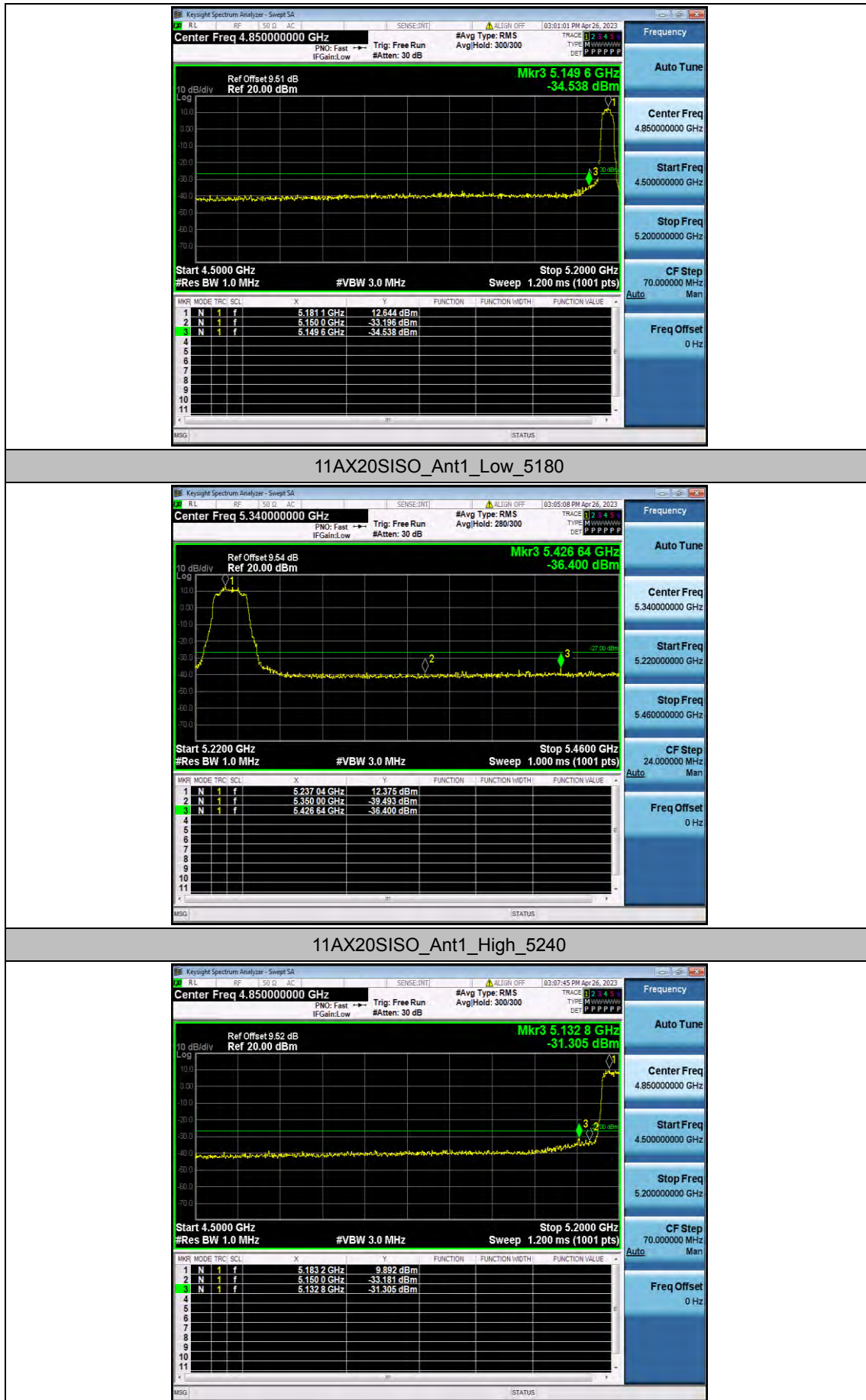
11AC40SISO_Ant1_High_5230



11AC80SISO_Ant1_Low_5210



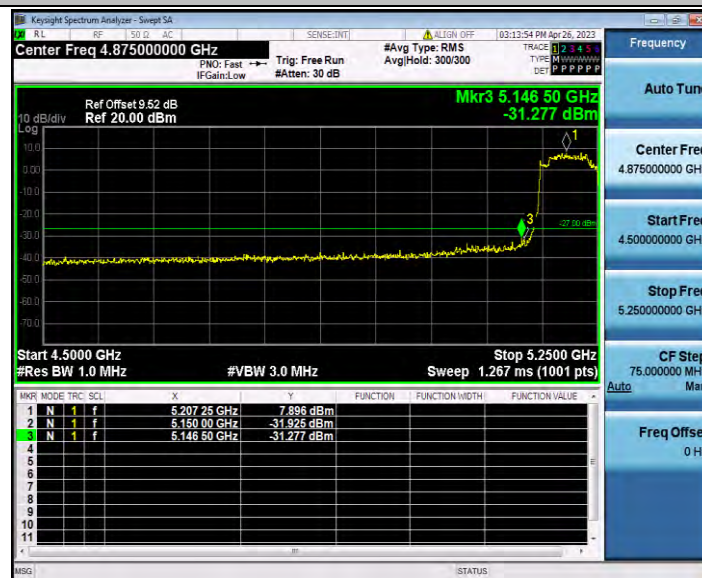
11AC80SISO_Ant1_High_5210



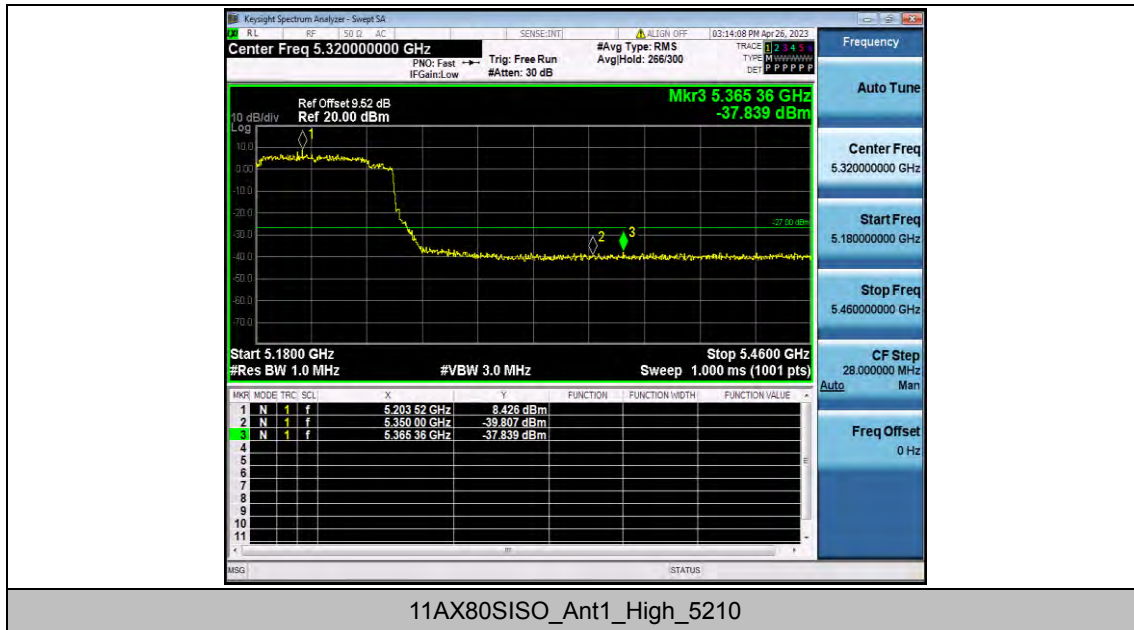
11AX40SISO_Ant1_Low_5190



11AX40SISO_Ant1_High_5230



11AX80SISO_Ant1_Low_5210



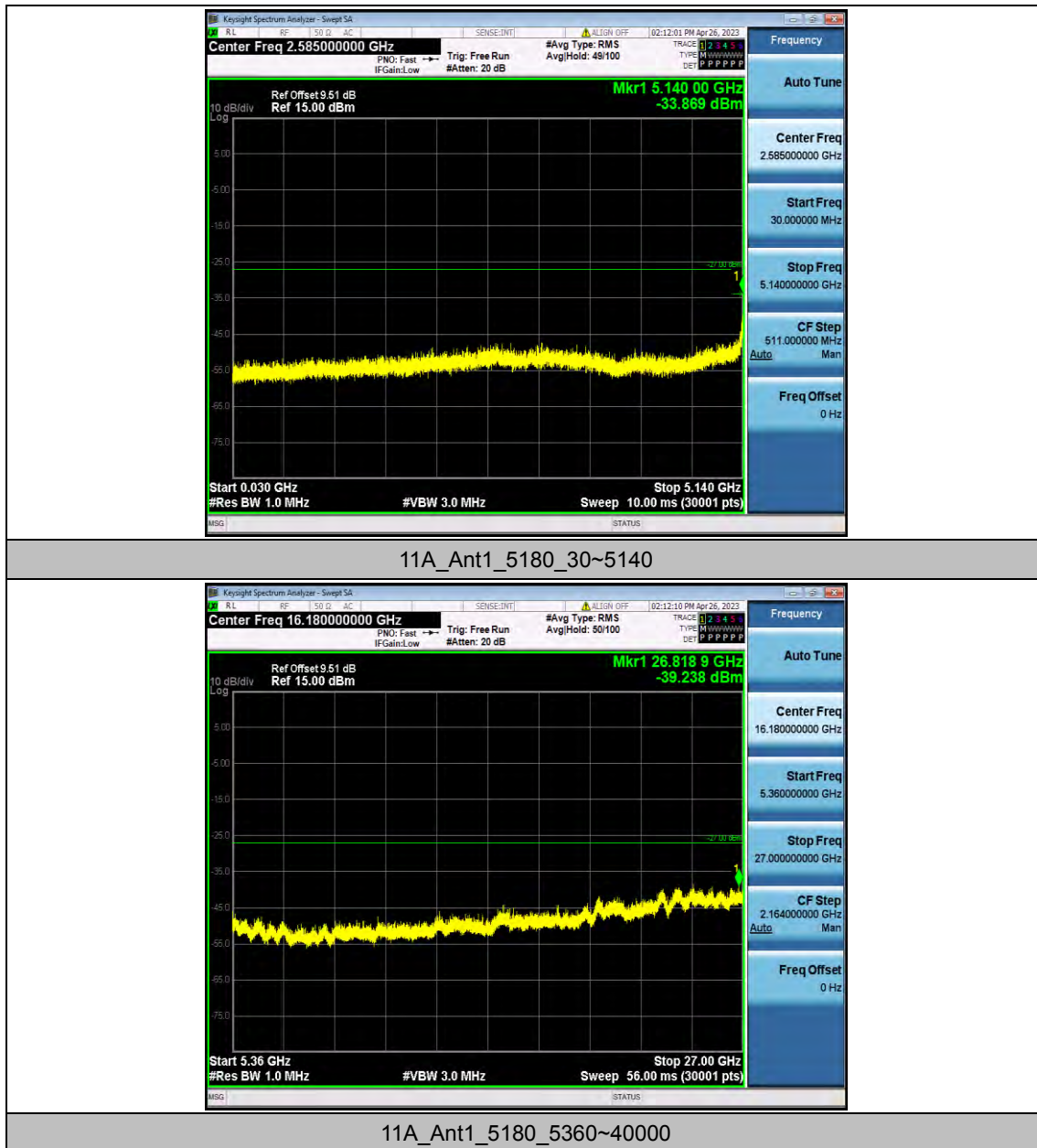
Appendix D.7: Conducted Spurious Emission

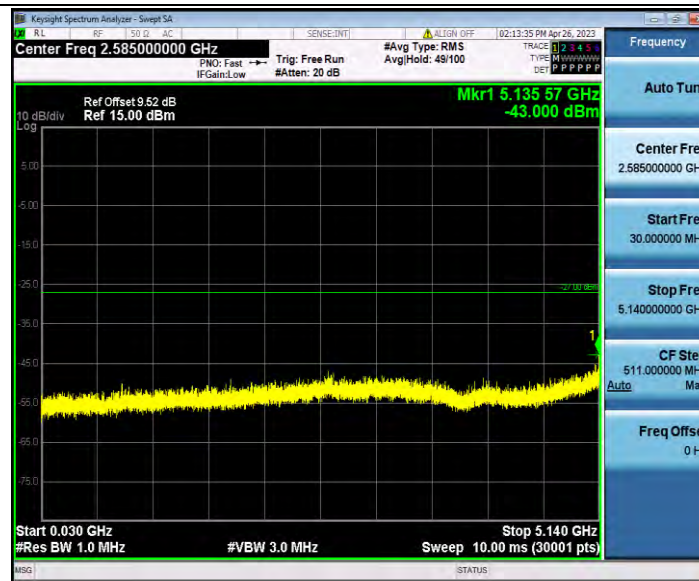
Test Result

TestMode	Antenna	Frequency[MHz]	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	30~5140	5140	-33.87	≤-27	PASS
			5360~40000	26818.95	-39.24	≤-27	PASS
		5200	30~5140	5135.57	-43	≤-27	PASS
			5360~40000	24305.1	-39.06	≤-27	PASS
		5240	30~5140	5100.82	-46.06	≤-27	PASS
			5360~40000	25639.57	-39.09	≤-27	PASS
11N20SISO	Ant1	5180	30~5140	5139.83	-39.13	≤-27	PASS
			5360~40000	26552.77	-38.42	≤-27	PASS
		5200	30~5140	5104.4	-45.7	≤-27	PASS
			5360~40000	24252.44	-38.36	≤-27	PASS
		5240	30~5140	5088.22	-45.73	≤-27	PASS
			5360~40000	26716.52	-37.61	≤-27	PASS
11N40SISO	Ant1	5190	30~5140	5138.3	-36.04	≤-27	PASS
			5360~40000	24242.34	-37.77	≤-27	PASS
		5230	30~5140	5137.62	-42.27	≤-27	PASS
			5360~40000	26726.61	-37.82	≤-27	PASS
11AC20SISO	Ant1	5180	30~5140	3028.55	-46.15	≤-27	PASS
			5360~40000	25133.91	-37.71	≤-27	PASS
		5200	30~5140	5121.09	-45.09	≤-27	PASS
			5360~40000	25628.02	-38.53	≤-27	PASS
		5240	30~5140	5138.47	-45.75	≤-27	PASS
			5360~40000	26052.89	-38.38	≤-27	PASS
11AC40SISO	Ant1	5190	30~5140	5137.96	-34.5	≤-27	PASS
			5360~40000	26759.8	-37.94	≤-27	PASS
		5230	30~5140	5137.27	-42.47	≤-27	PASS
			5360~40000	26512.38	-37.61	≤-27	PASS
11AC80SISO	Ant1	5210	30~5140	5137.45	-35.22	≤-27	PASS
			5360~40000	23525.34	-38.09	≤-27	PASS
11AX20SISO	Ant1	5180	30~5140	5136.42	-41.51	≤-27	PASS
			5360~40000	25098.57	-38.6	≤-27	PASS
		5200	30~5140	5127.74	-46.22	≤-27	PASS
			5360~40000	23643.64	-38.33	≤-27	PASS
		5240	30~5140	5107.3	-45.61	≤-27	PASS
			5360~40000	26052.89	-37.12	≤-27	PASS
11AX40SISO	Ant1	5190	30~5140	5132.51	-34.33	≤-27	PASS
			5360~40000	24333.23	-37.43	≤-27	PASS

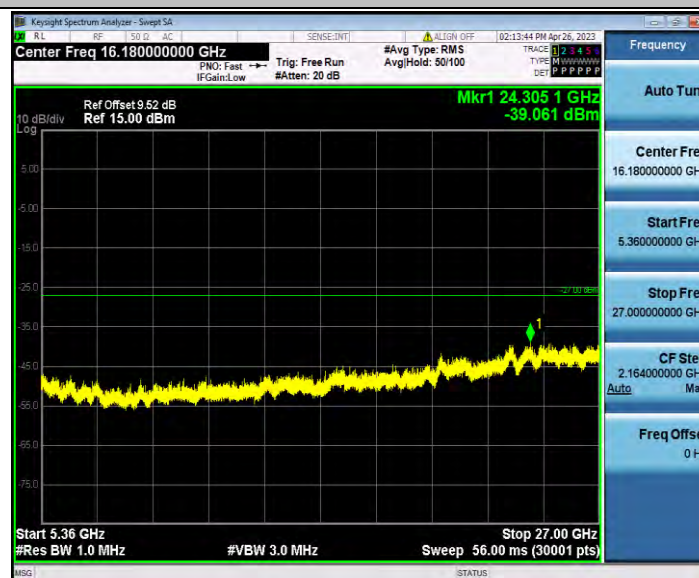
		5230	30~5140	5140	-42.52	≤ -27	PASS
			5360~40000	24299.33	-37.56	≤ -27	PASS
11AX80SISO	Ant1	5210	30~5140	5130.8	-36.63	≤ -27	PASS
			5360~40000	26764.12	-37.41	≤ -27	PASS

Test Graphs

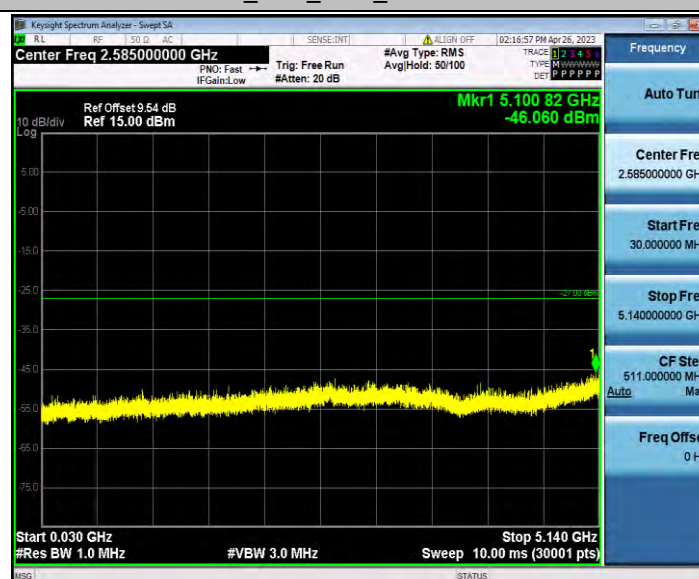




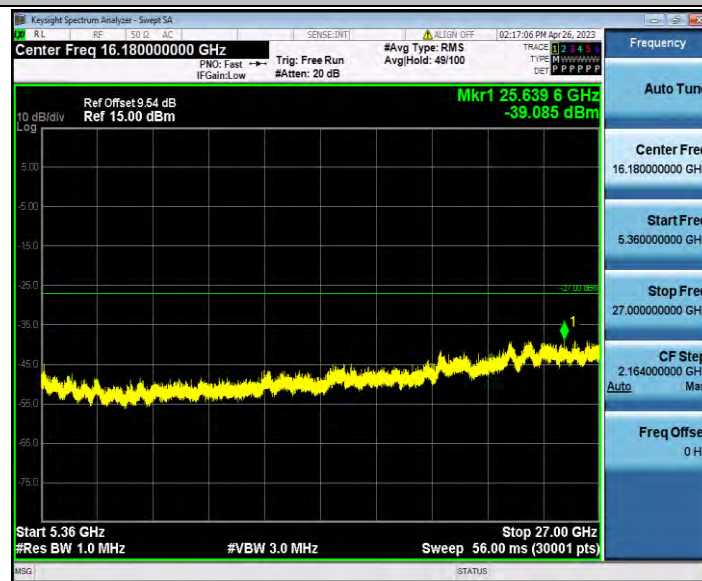
11A_Ant1_5200_30~5140



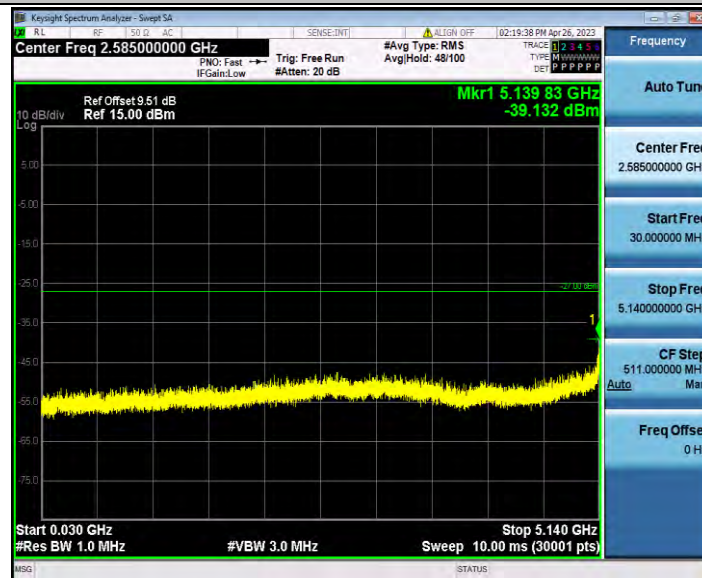
11A_Ant1_5200_5360~40000



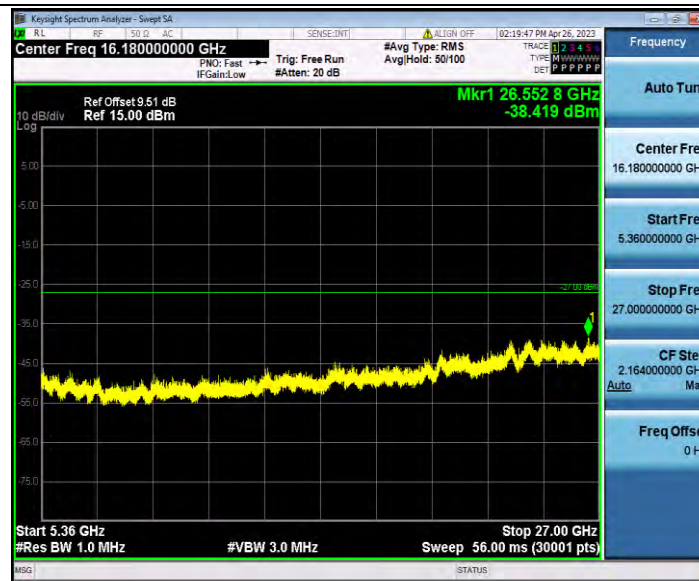
11A_Ant1_5240_30~5140



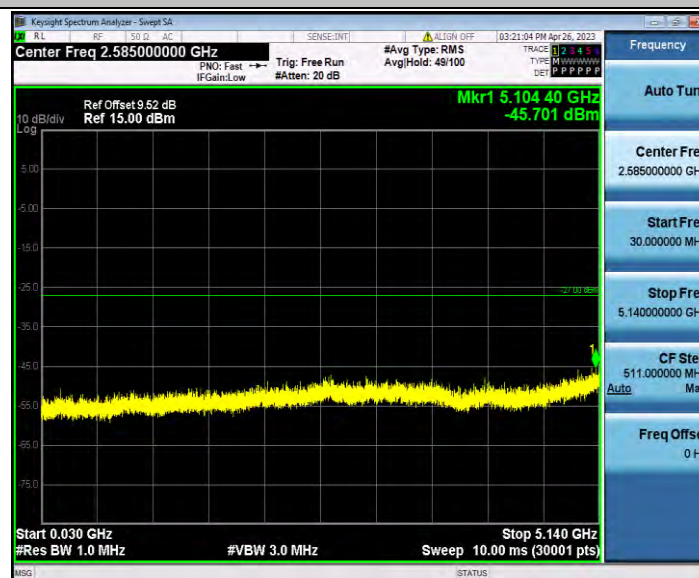
11A_Ant1_5240_5360~40000



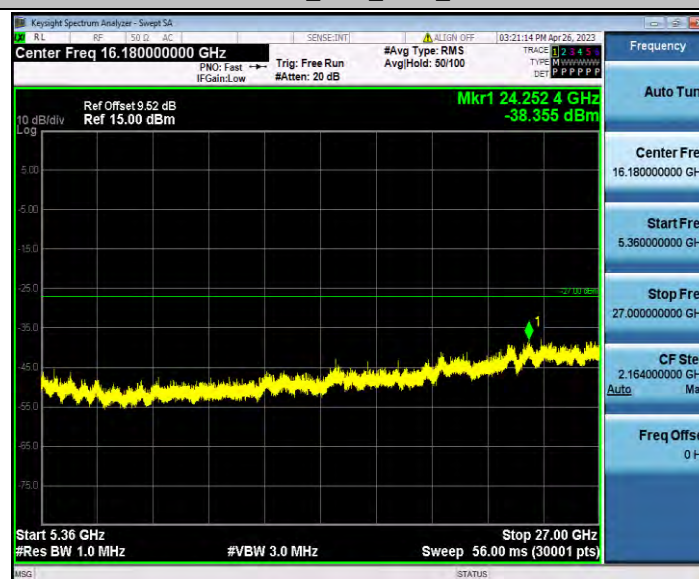
11N20SISO_Ant1_5180_30~5140



11N20SISO_Ant1_5180_5360~40000



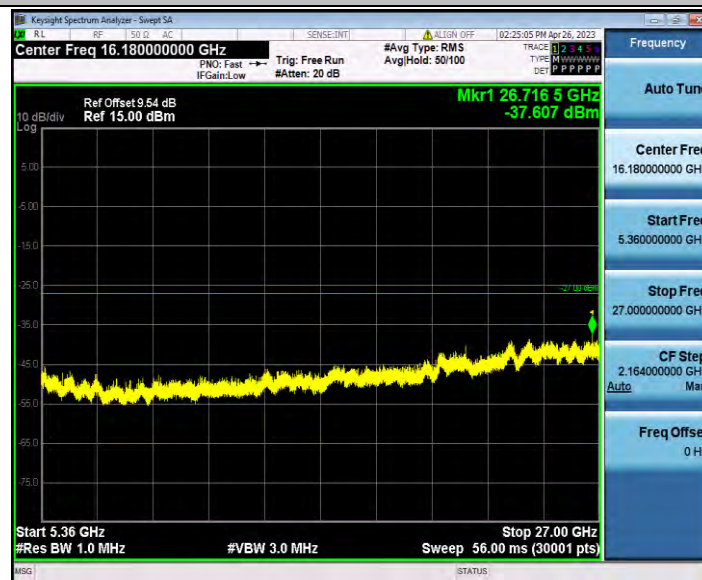
11N20SISO_Ant1_5200_30~5140



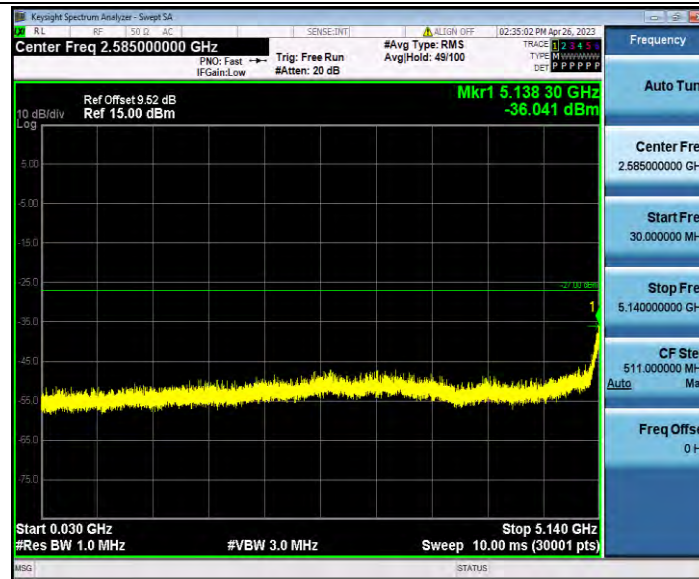
11N20SISO_Ant1_5200_5360~40000



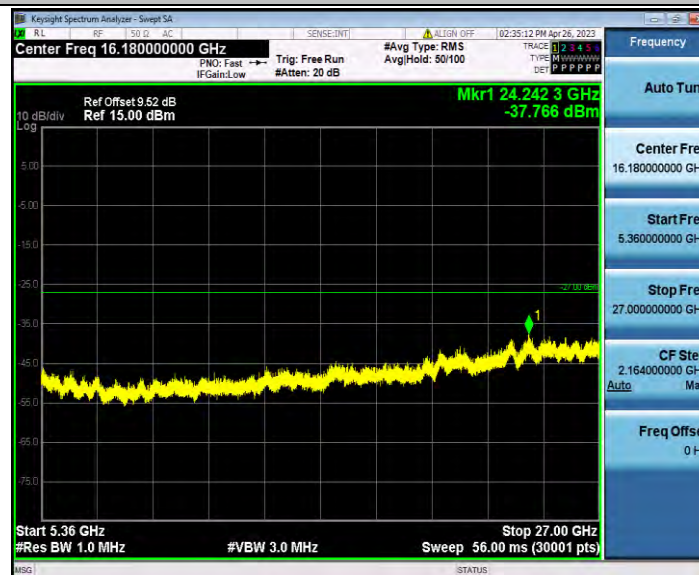
11N20SISO_Ant1_5240_30~5140



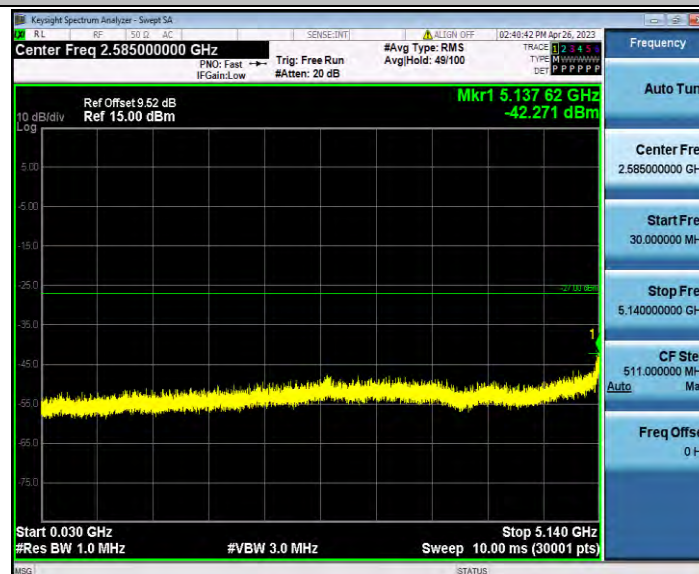
11N20SISO_Ant1_5240_5360~40000



11N40SISO_Ant1_5190_30~5140



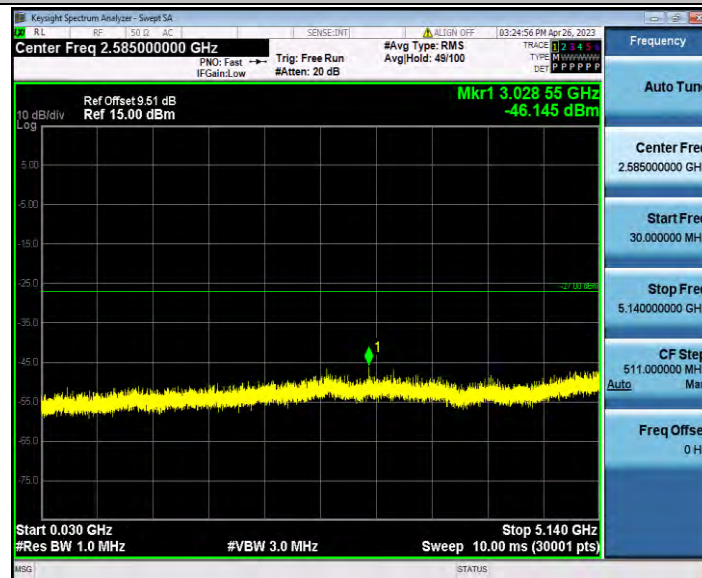
11N40SISO_Ant1_5190_5360~40000



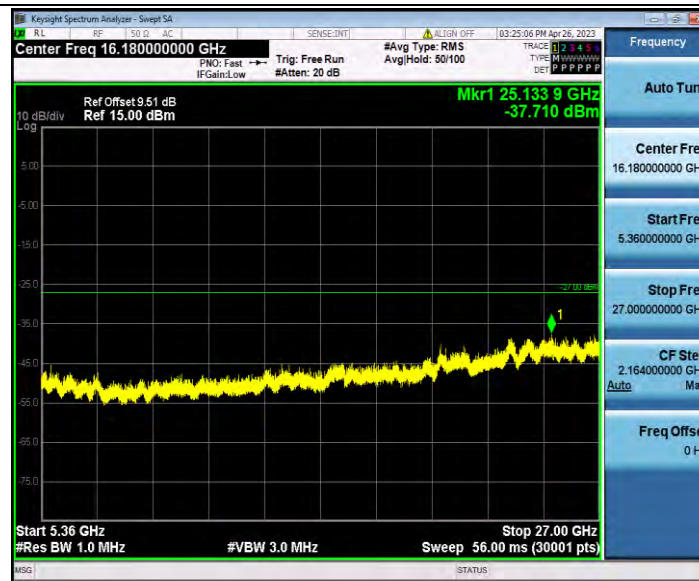
11N40SISO_Ant1_5230_30~5140



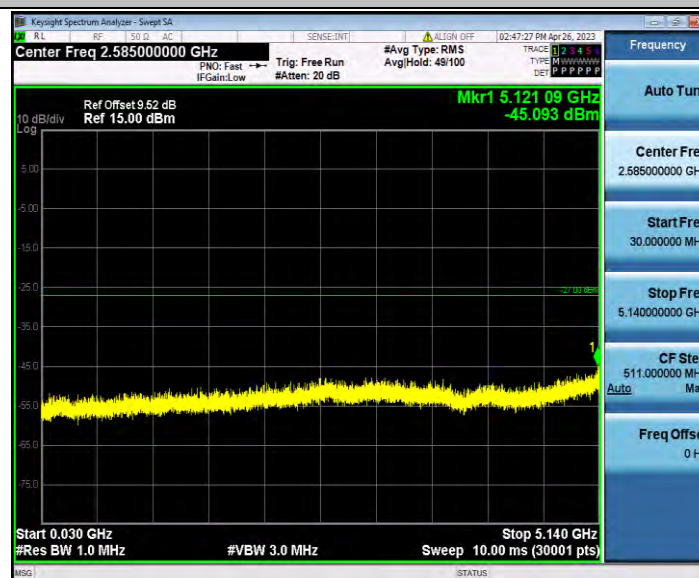
11N40SISO_Ant1_5230_5360~40000



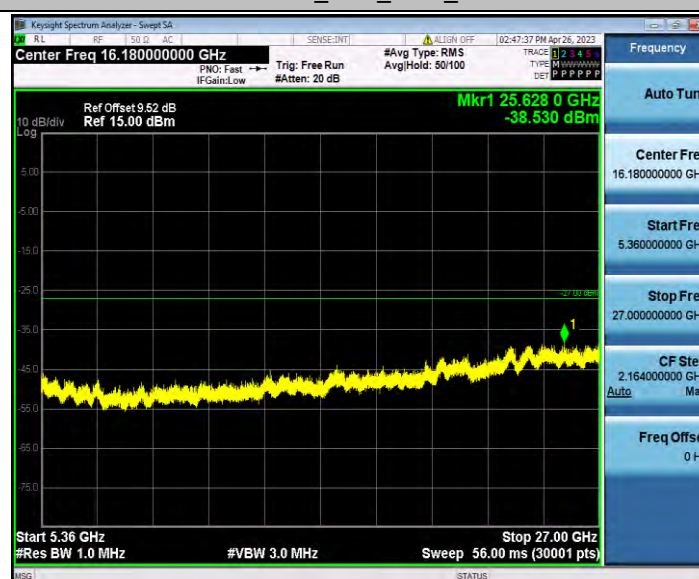
11AC20SISO_Ant1_5180_30~5140



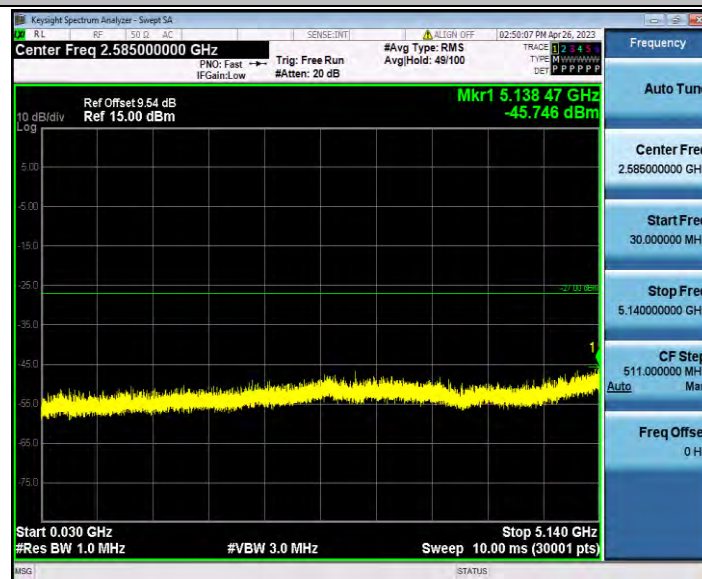
11AC20SISO_Ant1_5180_5360~40000



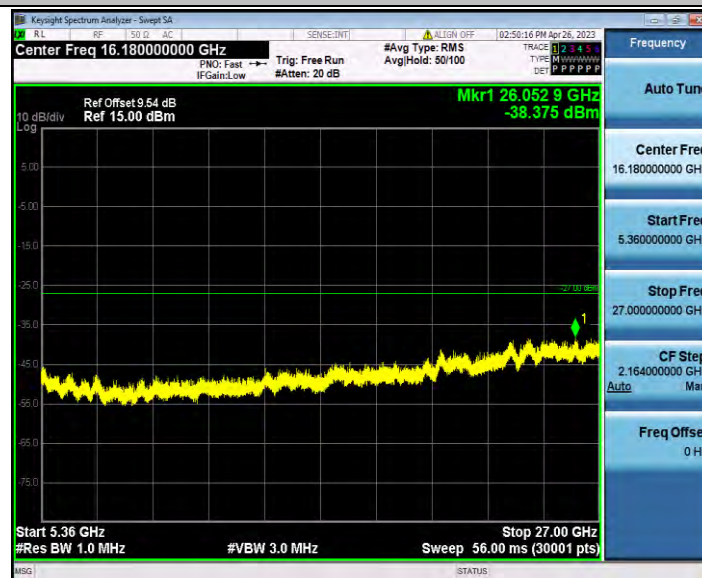
11AC20SISO_Ant1_5200_30~5140



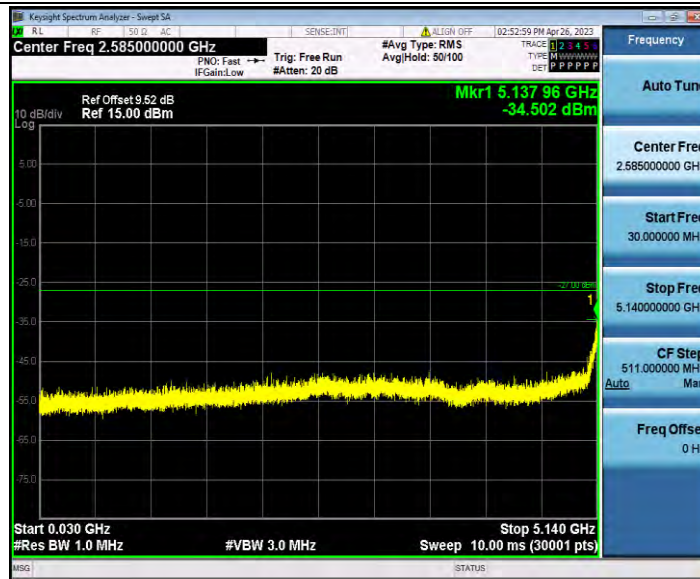
11AC20SISO_Ant1_5200_5360~40000



11AC20SISO_Ant1_5240_30~5140



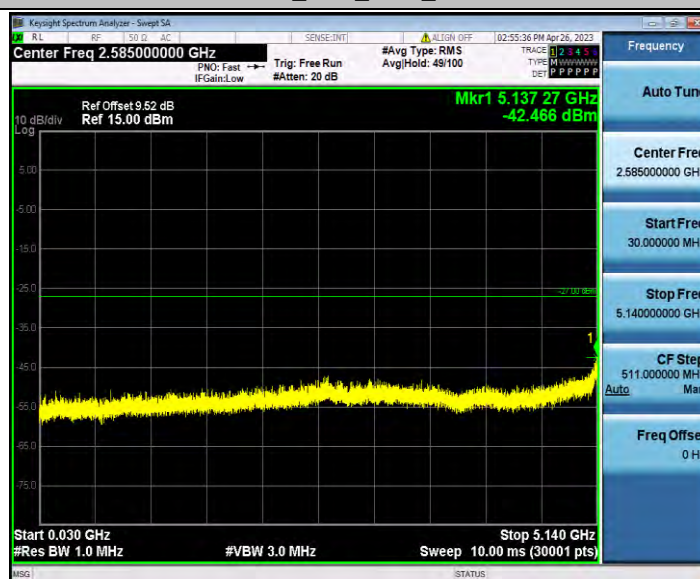
11AC20SISO_Ant1_5240_5360~40000



11AC40SISO_Ant1_5190_30~5140



11AC40SISO_Ant1_5190_5360~40000



11AC40SISO_Ant1_5230_30~5140



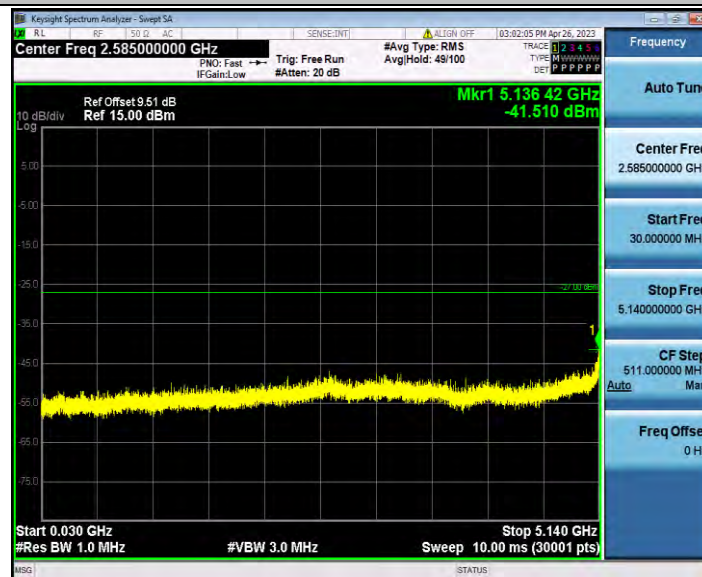
11AC40SISO_Ant1_5230_5360~40000



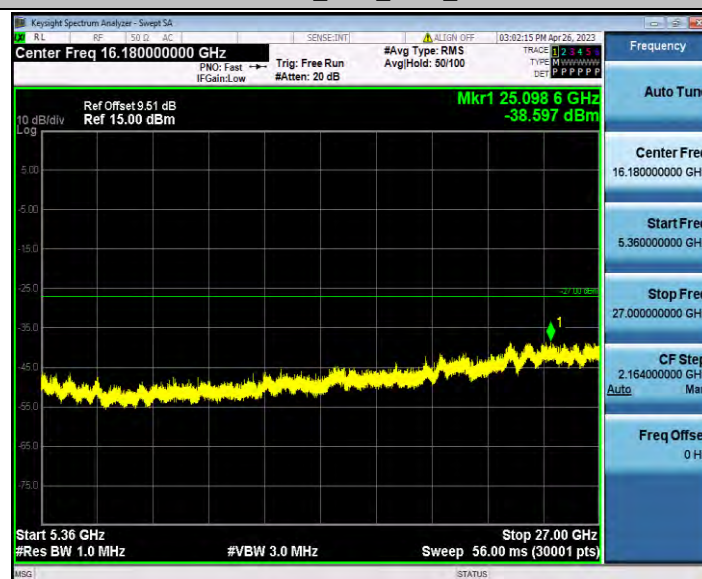
11AC80SISO_Ant1_5210_30~5140



11AC80SISO_Ant1_5210_5360~40000



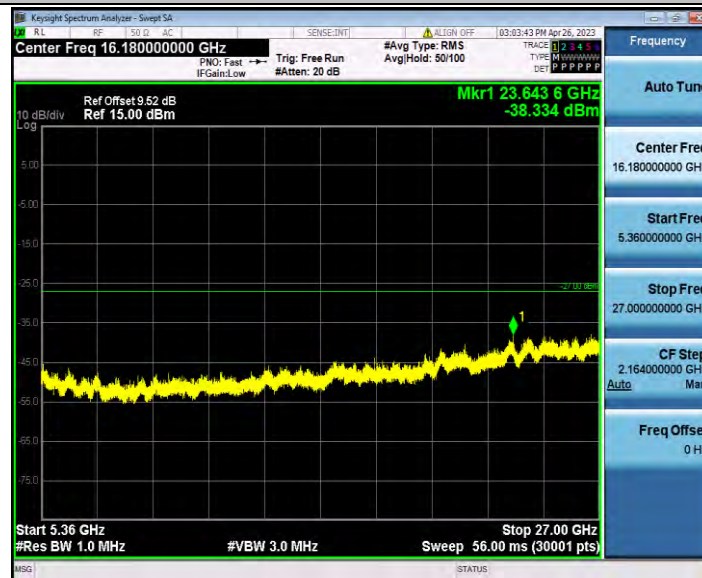
11AX20SISO_Ant1_5180_30~5140



11AX20SISO_Ant1_5180_5360~40000



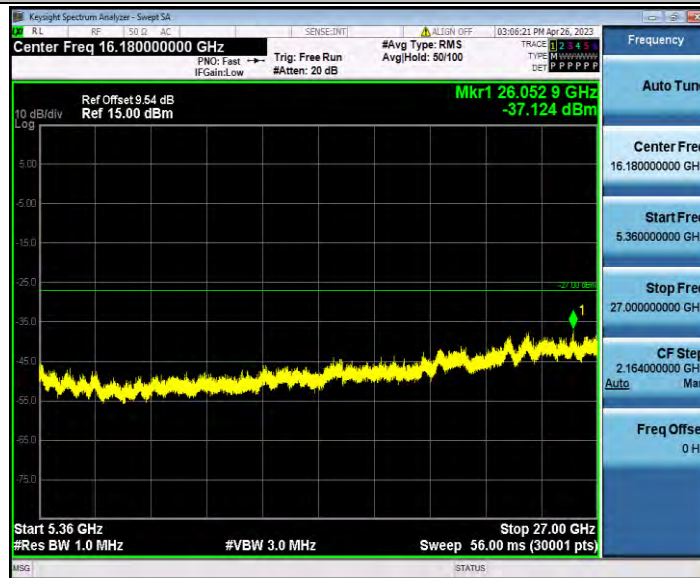
11AX20SISO_Ant1_5200_30~5140



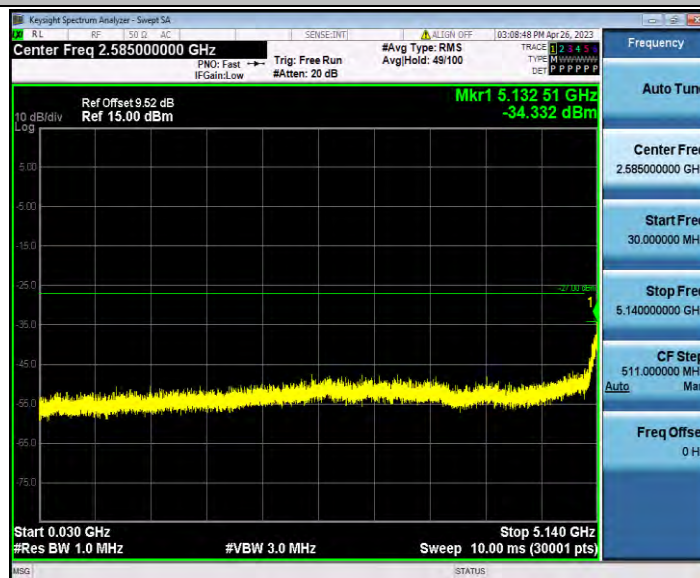
11AX20SISO_Ant1_5200_5360~40000



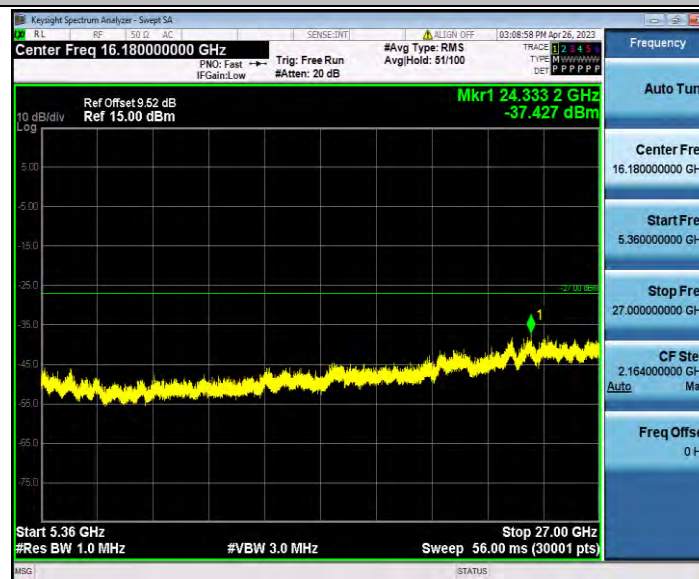
11AX20SISO_Ant1_5240_30~5140



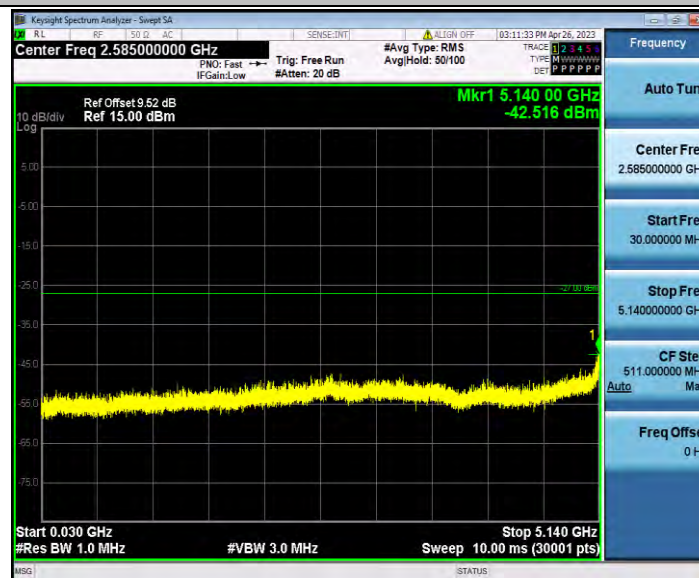
11AX20SISO_Ant1_5240_5360~40000



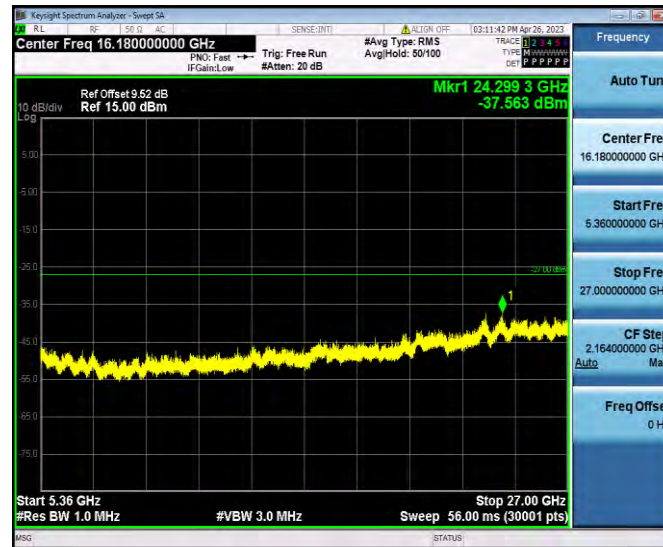
11AX40SISO_Ant1_5190_30~5140



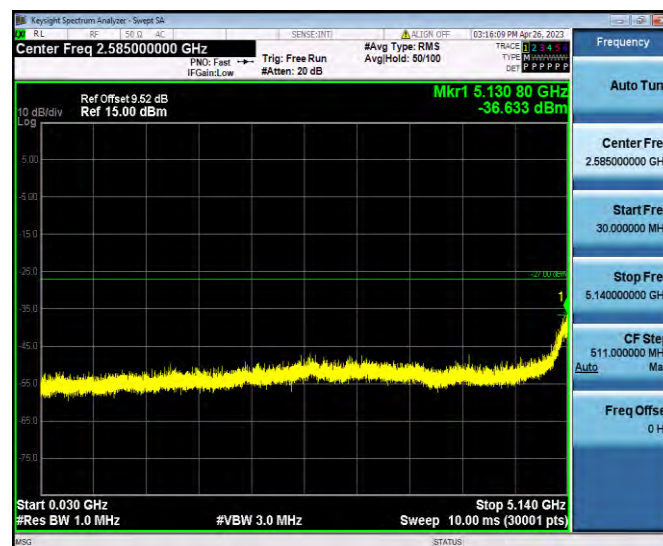
11AX40SISO_Ant1_5190_5360~40000



11AX40SISO_Ant1_5230_30~5140



11AX40SISO_Ant1_5230_5360~40000



11AX80SISO_Ant1_5210_5360~40000



11AX80SISO_Ant1_5210_5360~40000