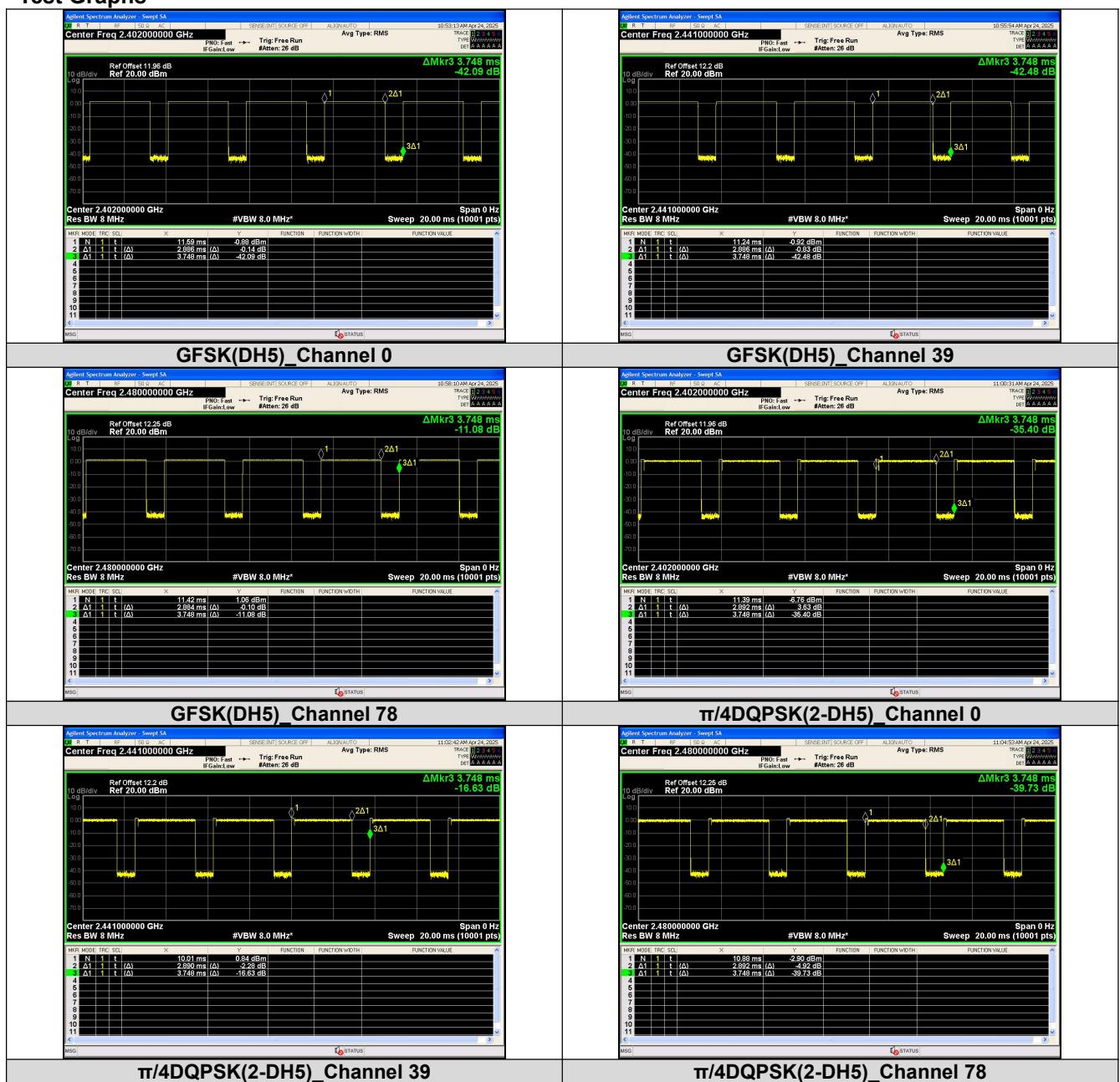


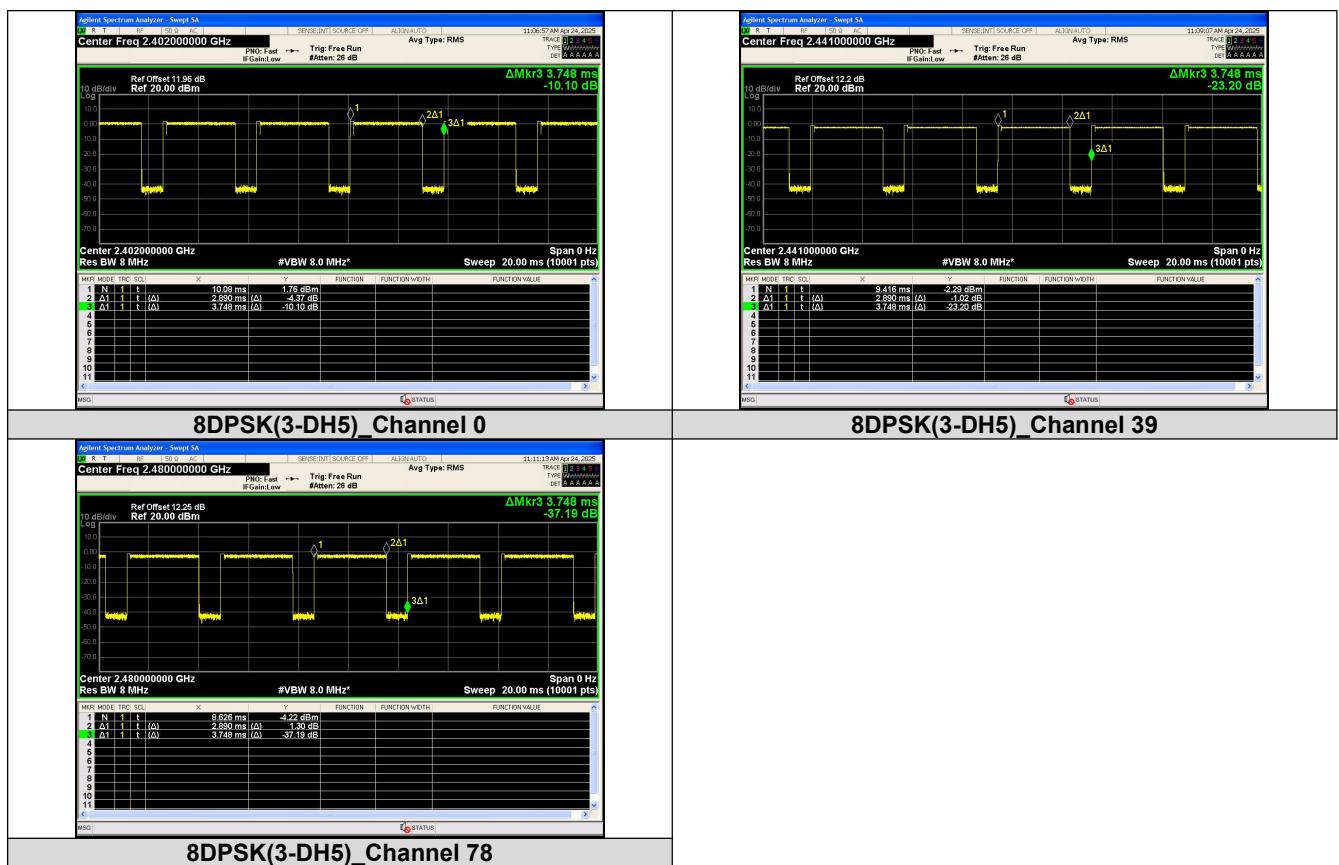
## APPENDIX VI. Duty Cycle

### Test Result

Modulation	packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)	1/T
GFSK	DH5	0	2.886	3.748	77.00	0.7700	1.1351	0.3465
		39	2.886	3.748	77.00	0.7700	1.1351	0.3465
		78	2.884	3.748	76.95	0.7695	1.1379	0.3467
$\pi/4$ DQPSK	2-DH5	0	2.892	3.748	77.16	0.7716	1.1261	0.3458
		39	2.890	3.748	77.11	0.7711	1.1289	0.3460
		78	2.892	3.748	77.16	0.7716	1.1261	0.3458
8DPSK	3-DH5	0	2.890	3.748	77.11	0.7711	1.1289	0.3460
		39	2.890	3.748	77.11	0.7711	1.1289	0.3460
		78	2.890	3.748	77.11	0.7711	1.1289	0.3460

### Test Graphs





## APPENDIX VII. Dwell Time Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	CH39 (2441MHz)	0.3804	319	121.35	< 400	PASS
	DH3		1.632	154	251.33		PASS
	DH5		2.880	107	308.16		PASS
π/4DQPSK	2-DH1	CH39 (2441MHz)	0.3900	319	124.41	< 400	PASS
	2-DH3		1.632	156	254.59		PASS
	2-DH5		2.896	116	335.94		PASS
8DPSK	3-DH1	CH39 (2441MHz)	0.9048	67	60.62	< 400	PASS
	3-DH3		0.6480	154	99.79		PASS
	3-DH5		0.8800	104	91.52		PASS

### Test Graphs

