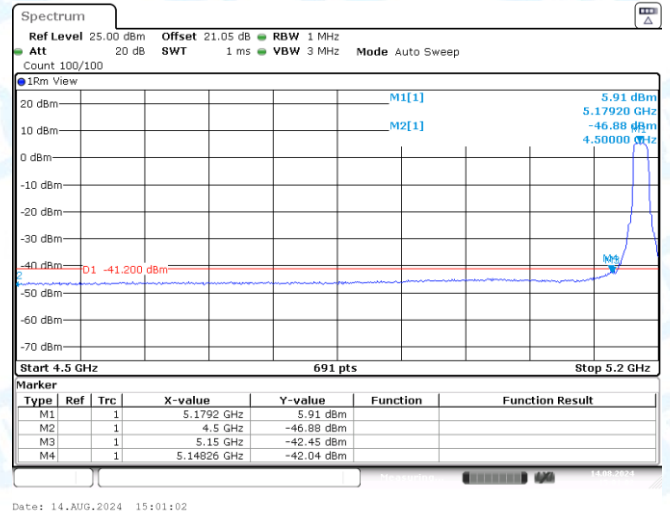
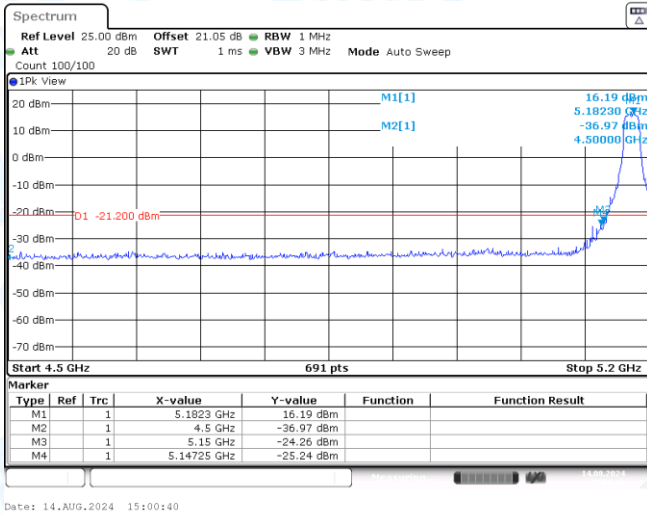
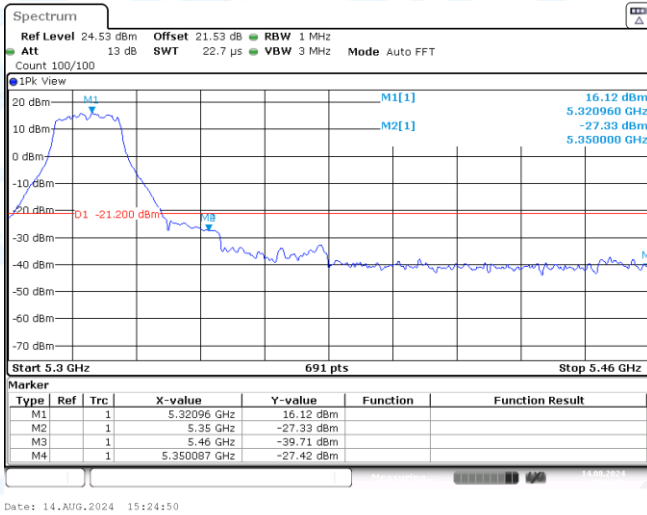


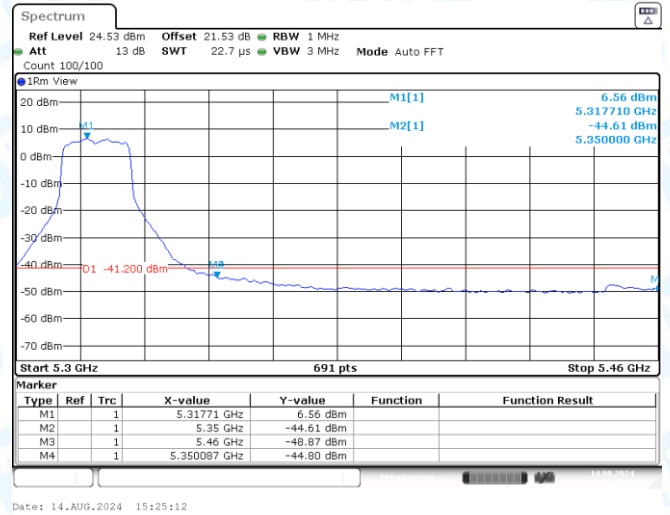
10.2 Test Graphs



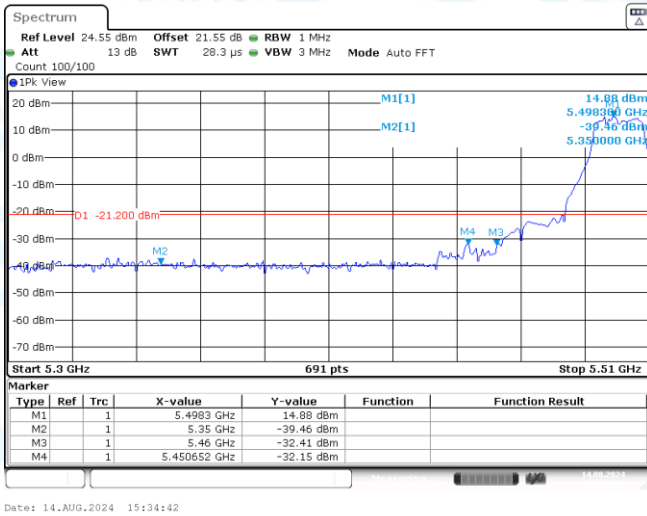
11A-Ant2-5180-PASS



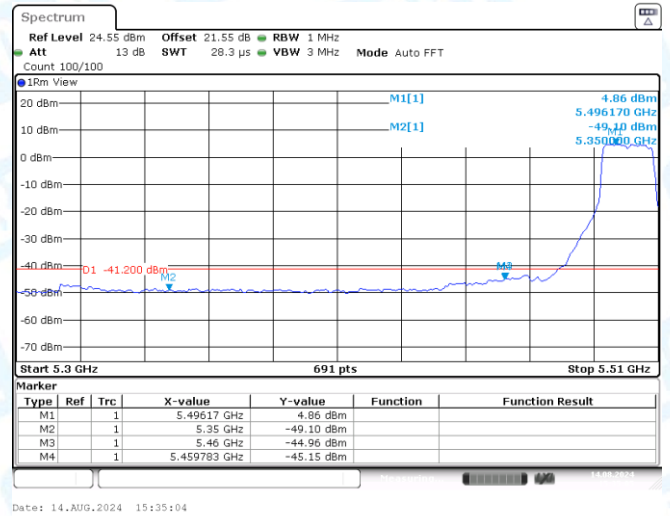
11A-Ant2-5180-PASS



11A-Ant2-5320-PASS

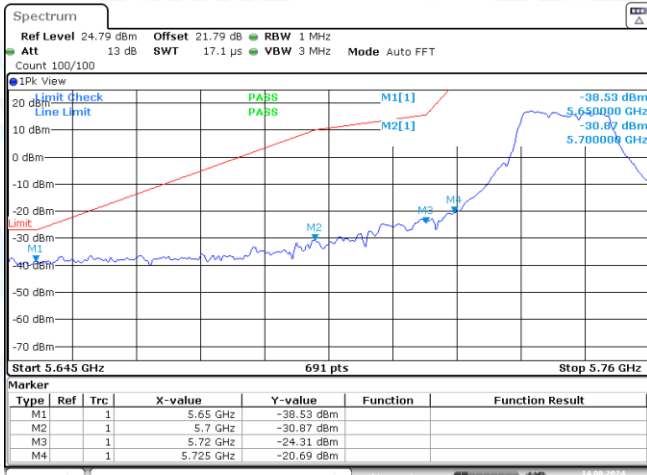


11A-Ant2-5320-PASS

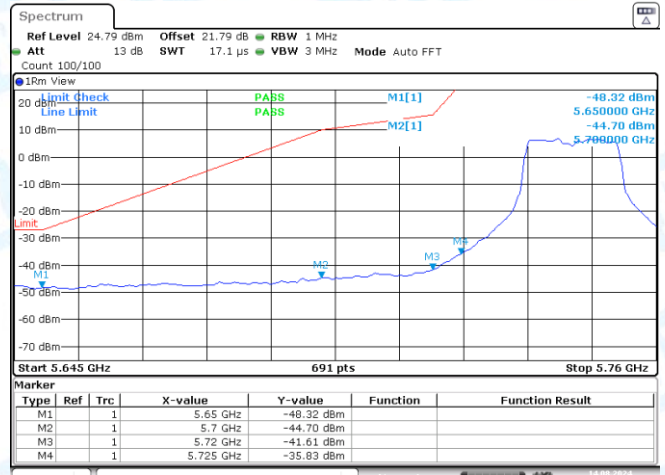


11A-Ant2-5500-PASS

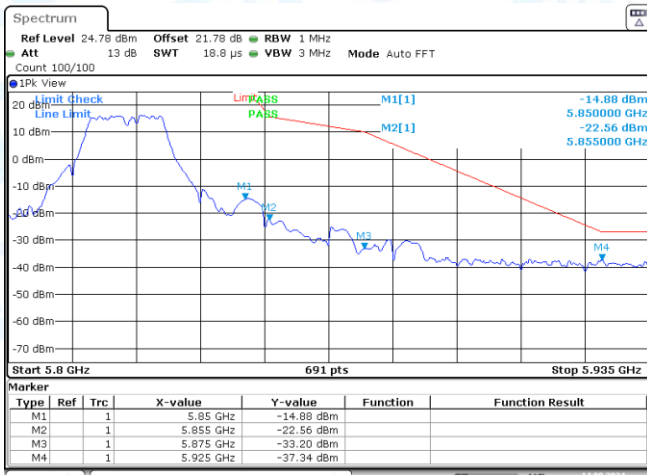
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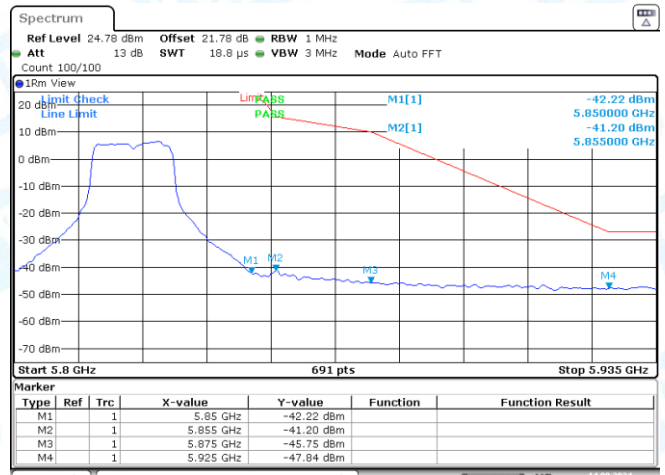
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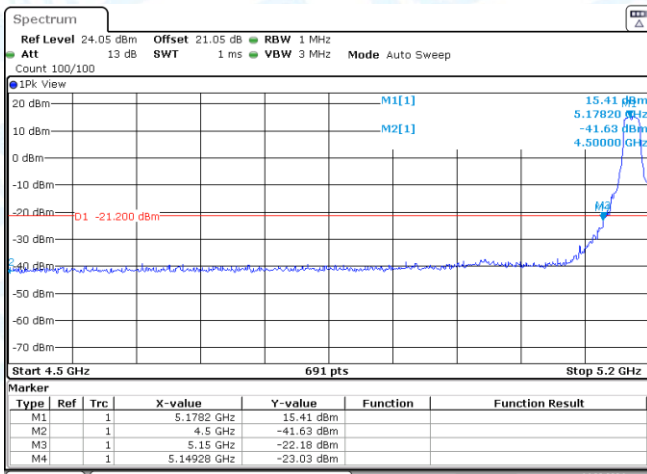
11A-Ant2-5745-PASS



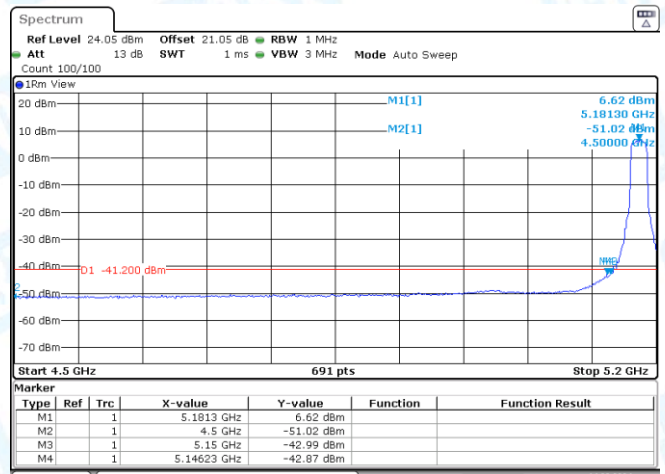
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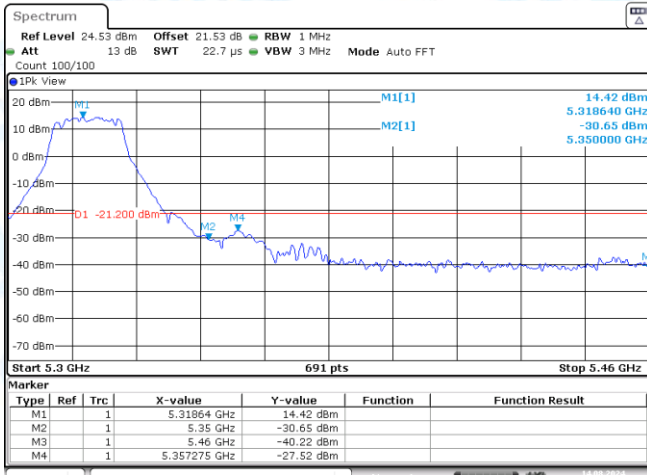
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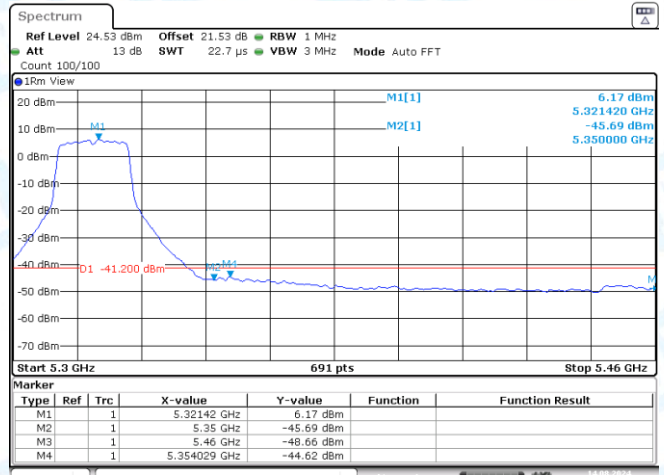
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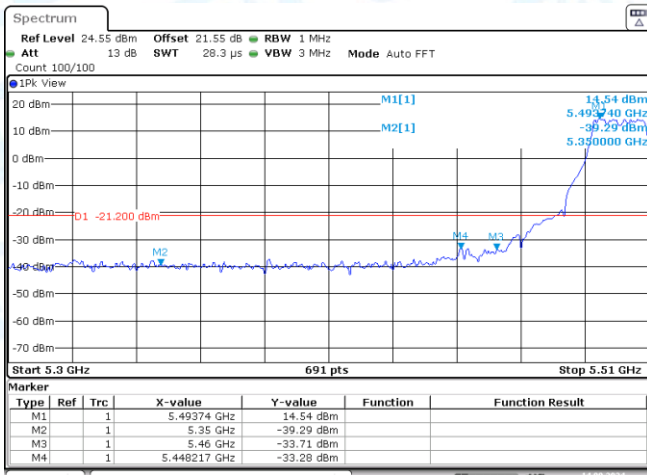
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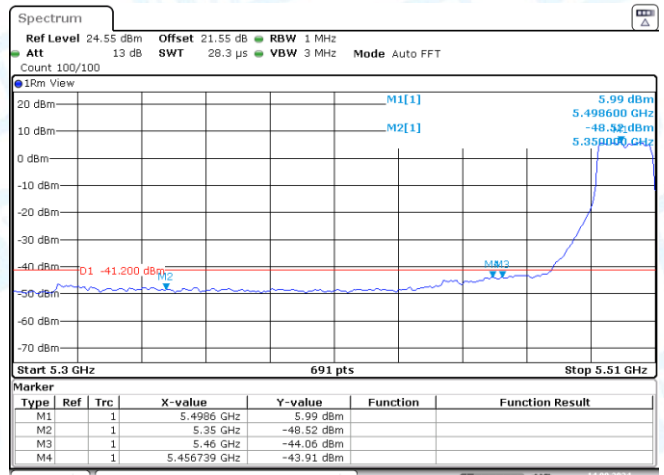
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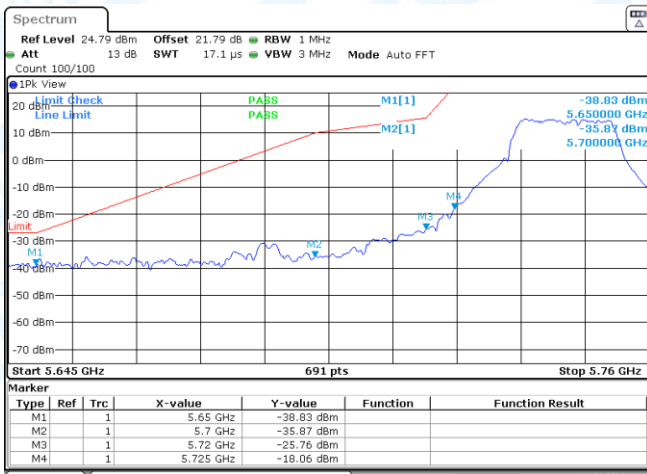
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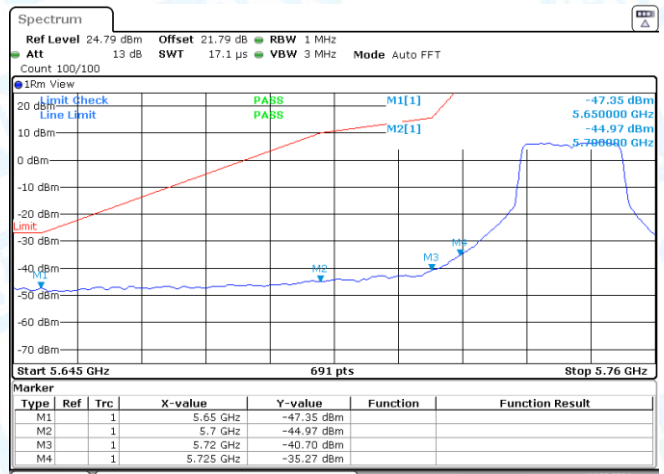
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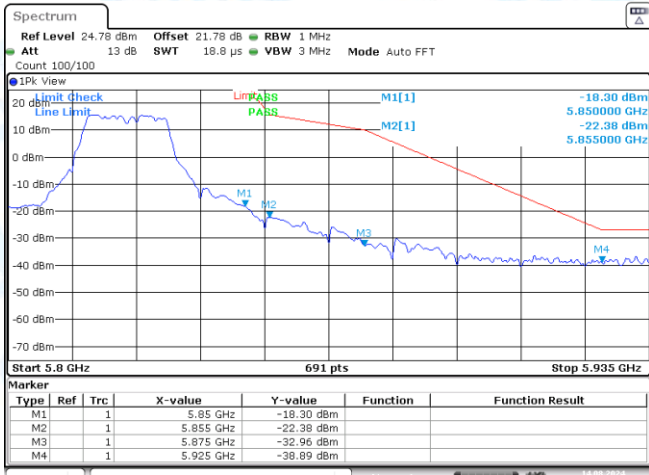
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11N20SISO-Ant2-5745-PASS

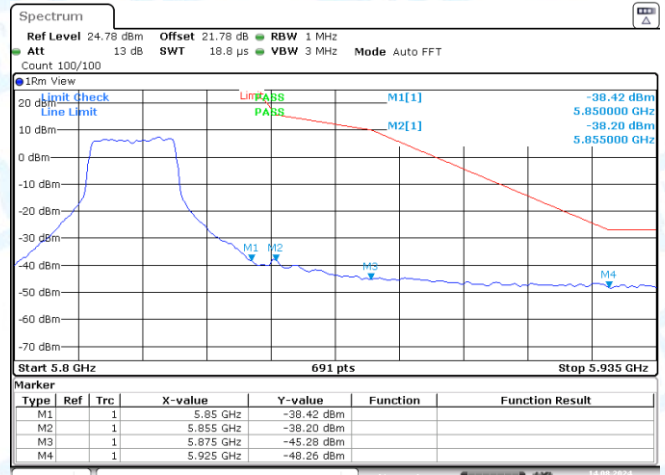


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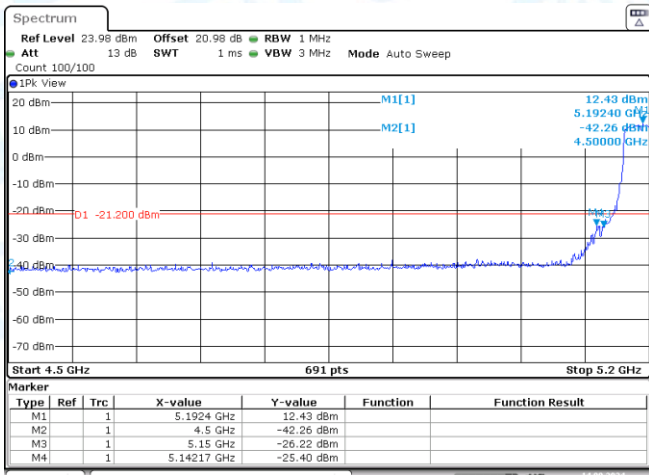
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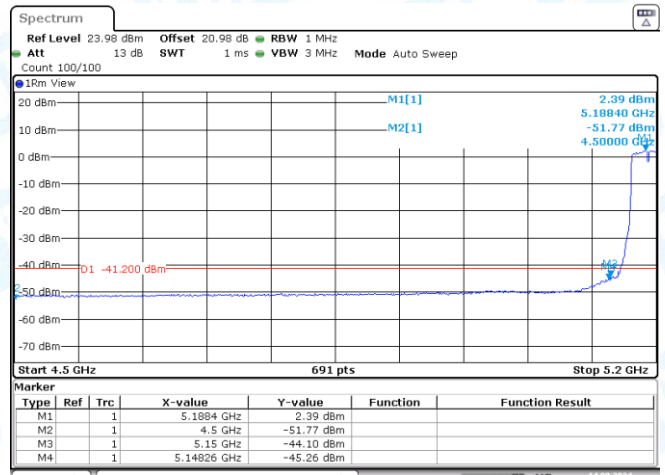
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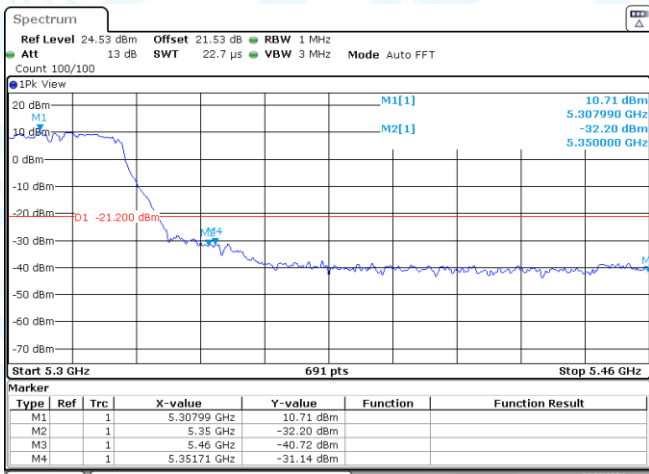
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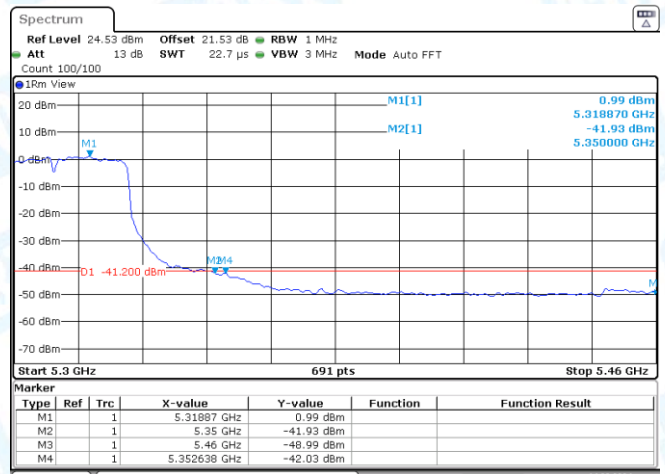
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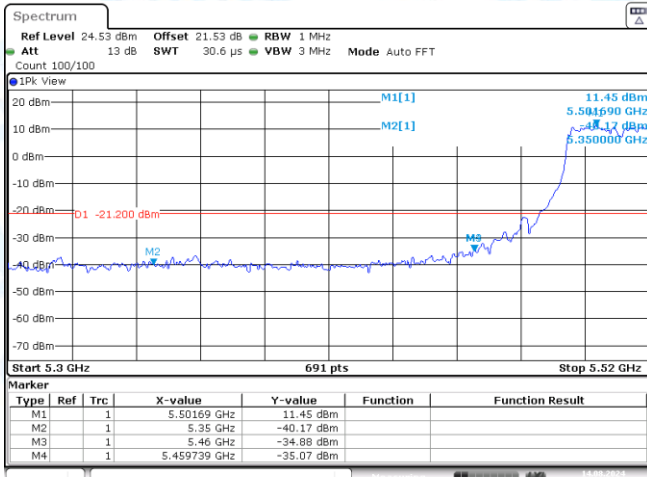
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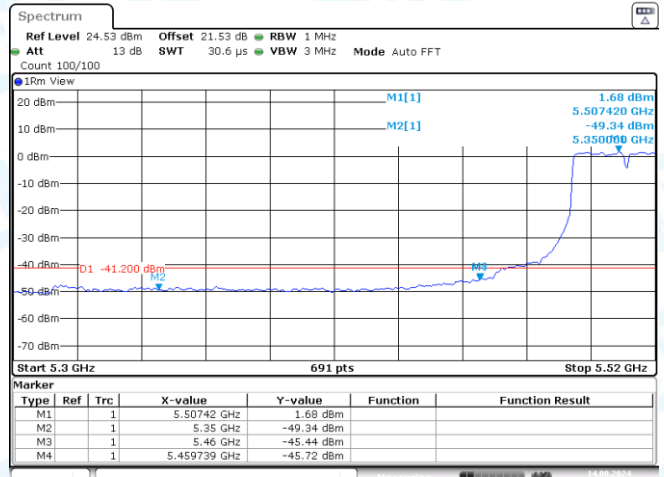
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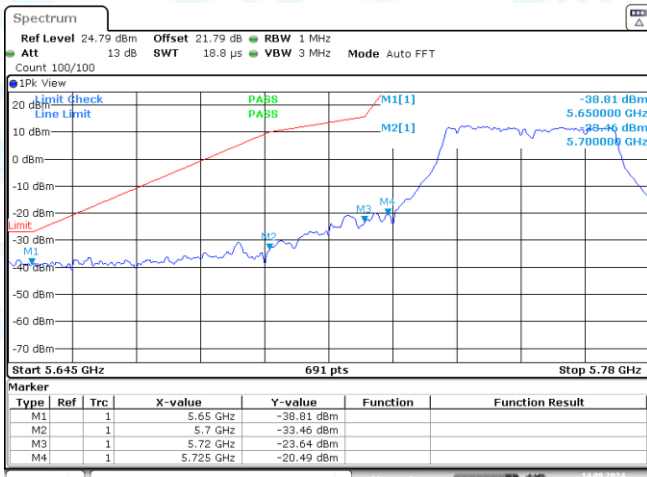
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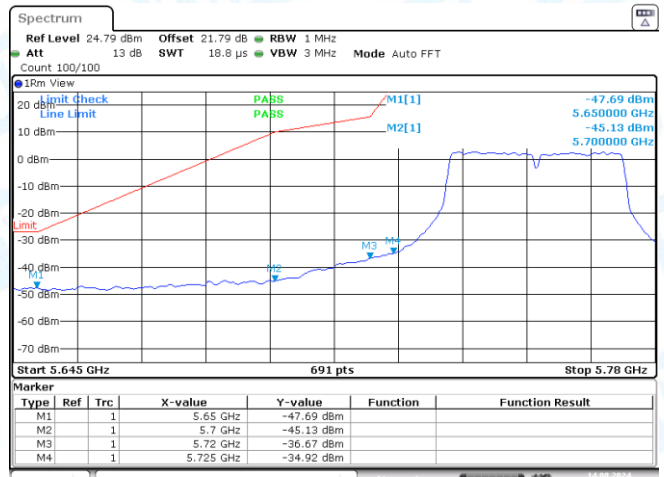
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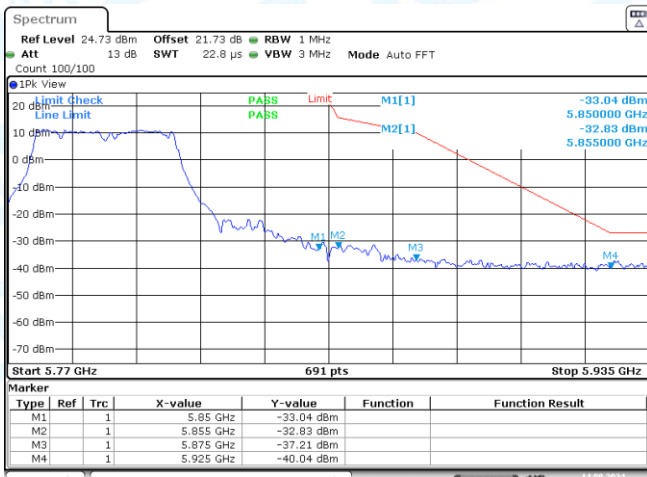
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11N40SISO-Ant2-5755-PASS



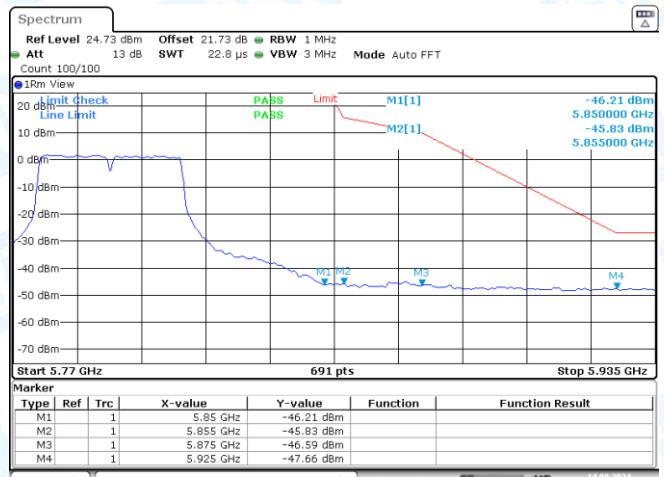
Date: 14.AUG.2024 19:01:59

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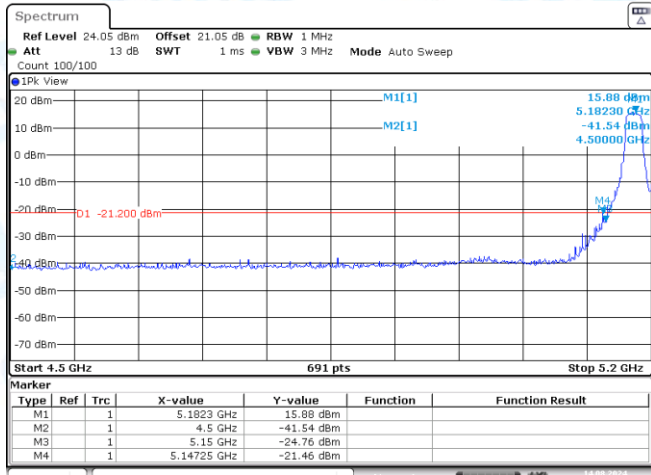
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11N40SISO-Ant2-5795-PASS

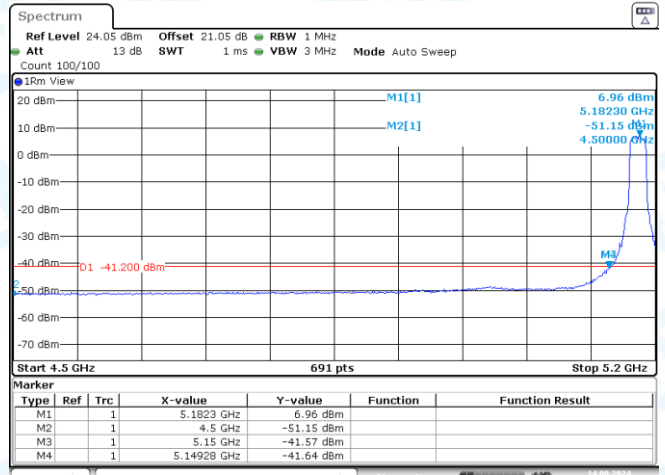


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11N40SISO-Ant2-5795-PASS



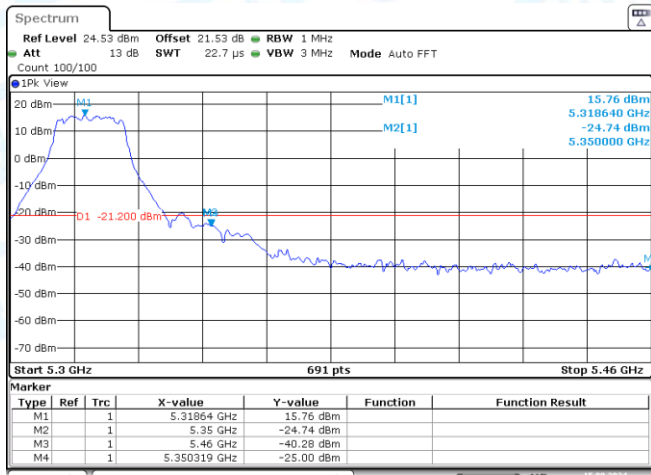
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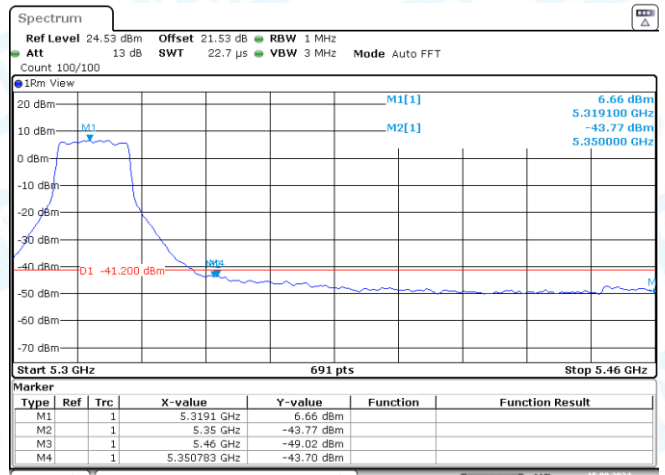
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11AC20SISO-Ant2-5180-PASS

11AC20SISO-Ant2-5180-PASS



Date: 15.AUG.2024 13:39:48



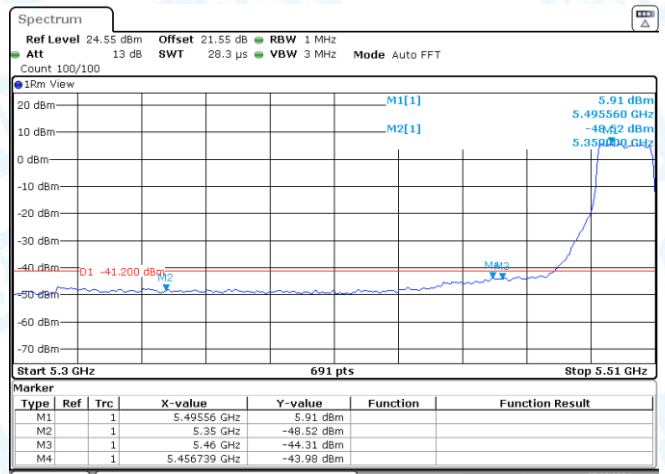
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11AC20SISO-Ant2-5320-PASS

11AC20SISO-Ant2-5320-PASS



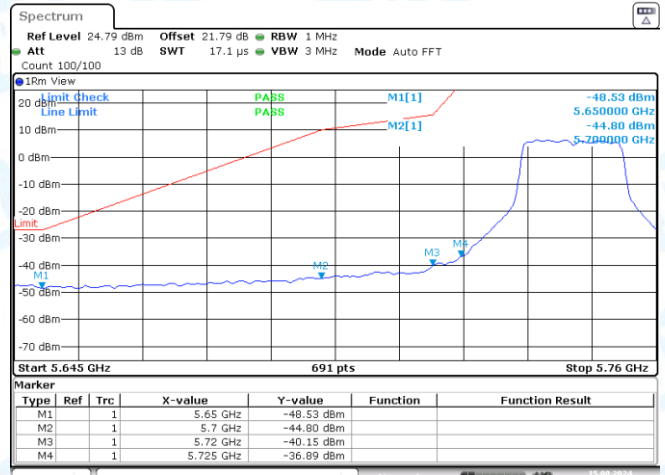
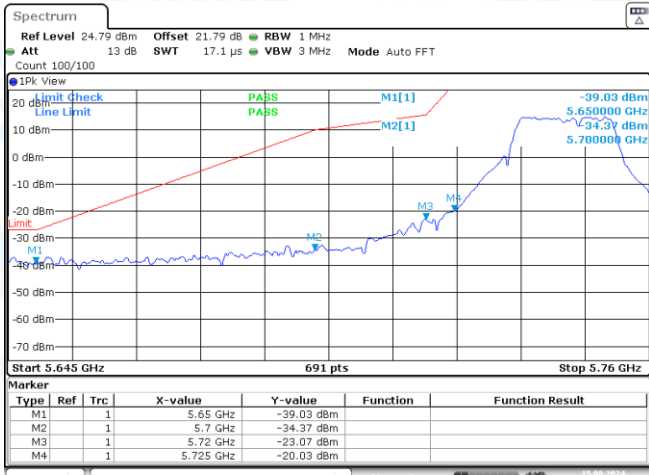
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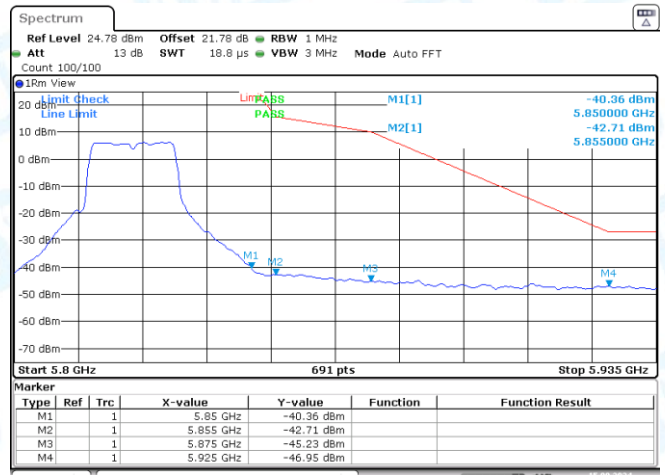
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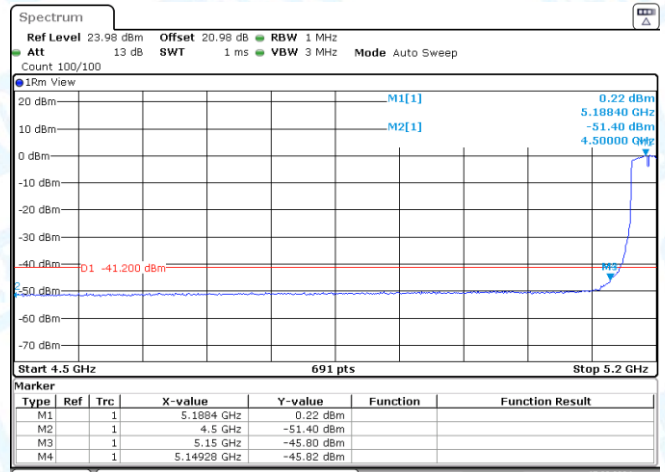
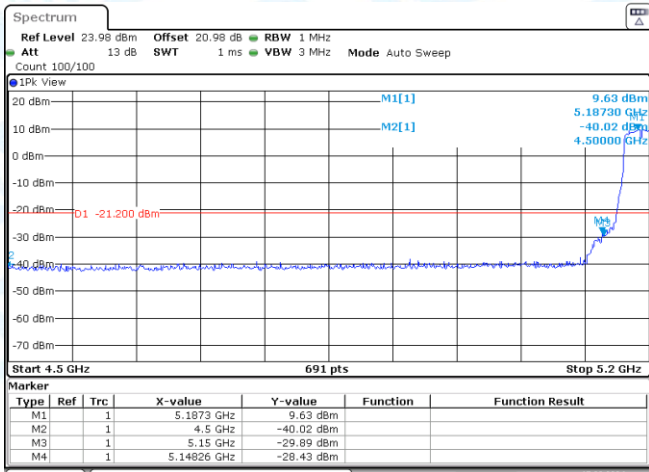
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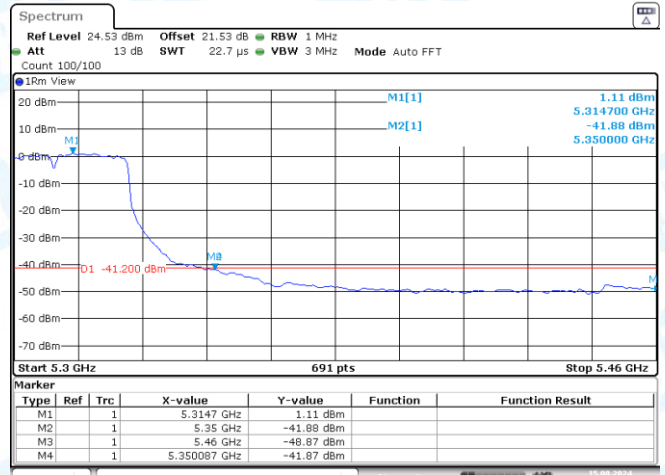
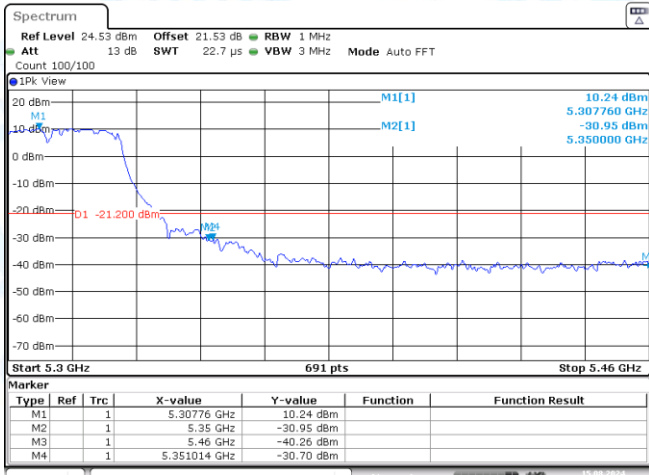
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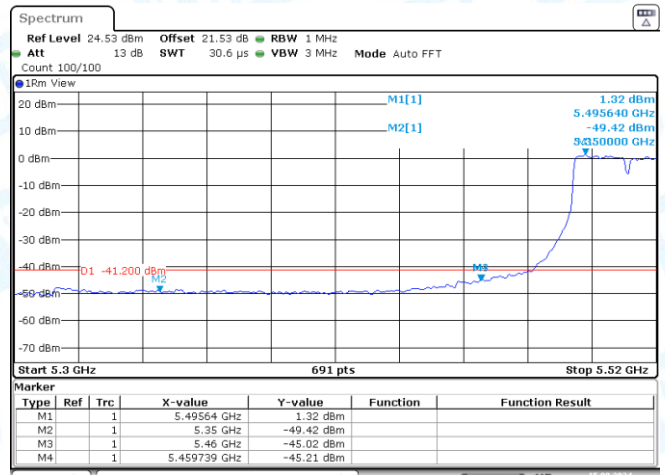
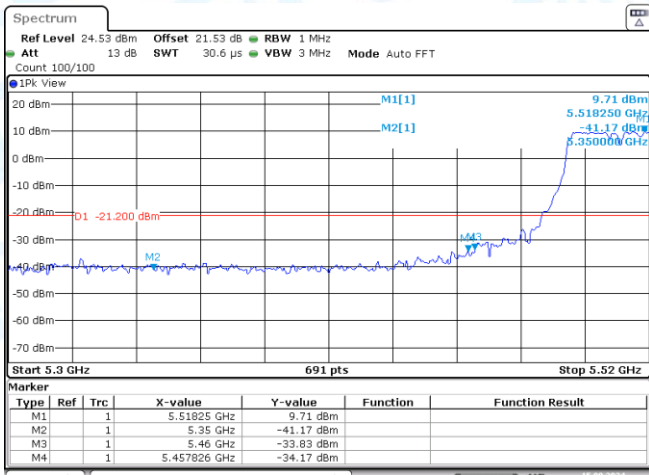
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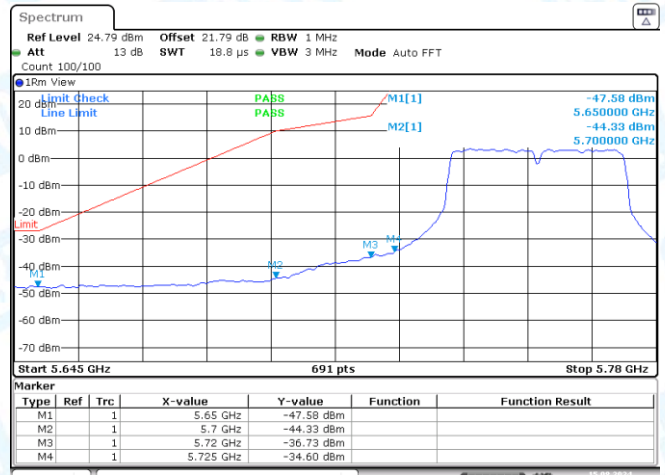
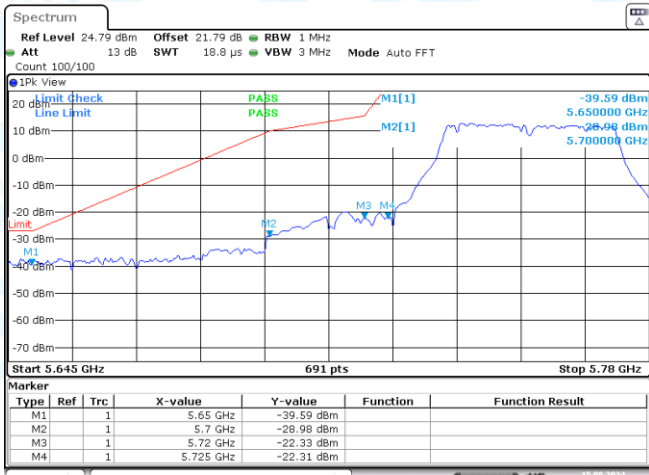
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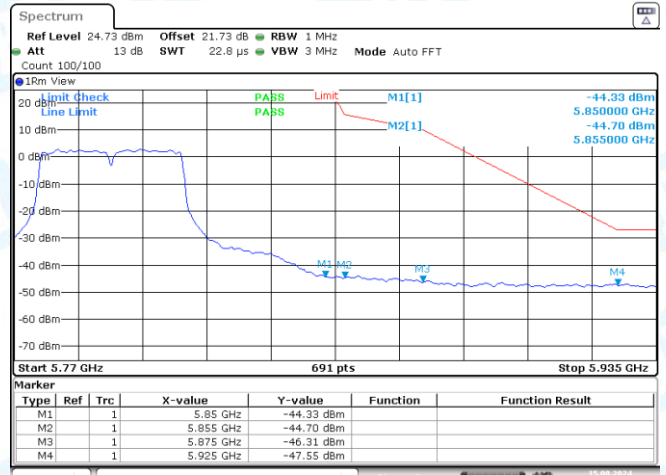
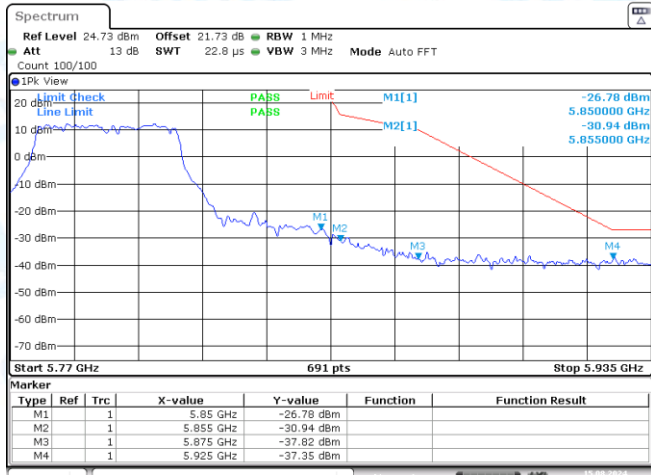
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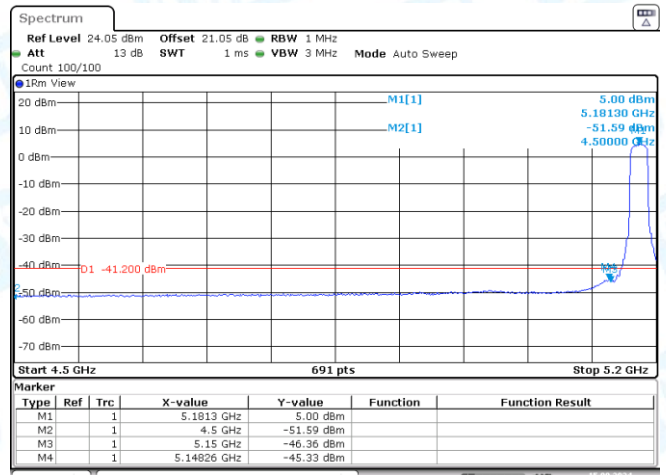
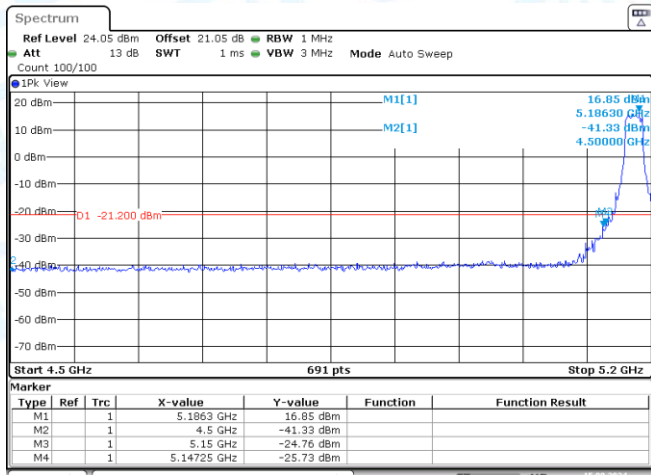
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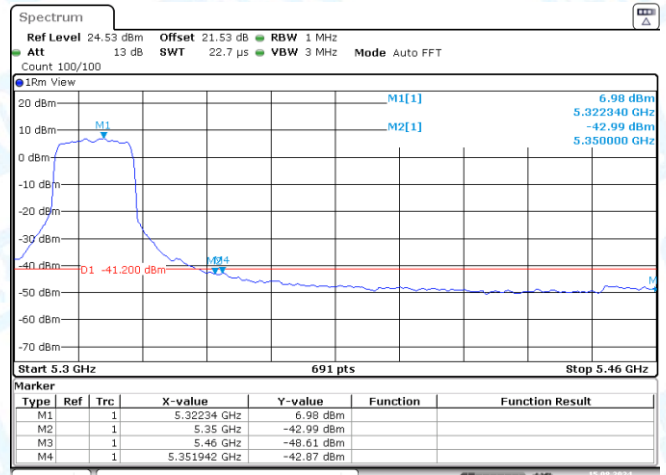
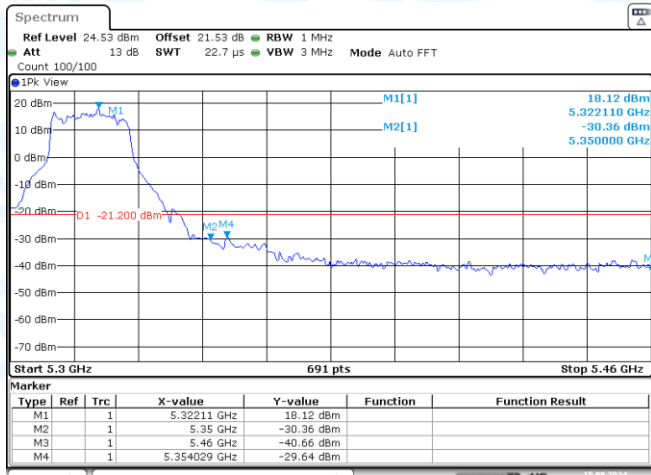
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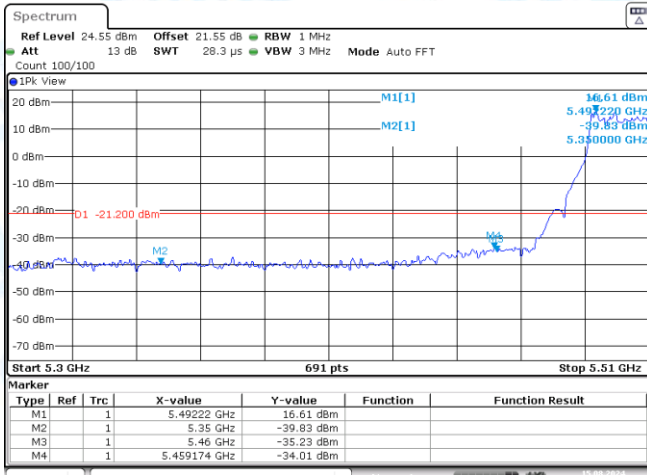
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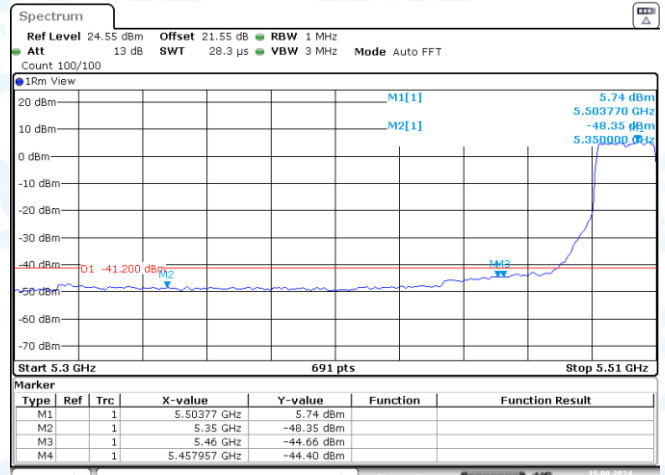


11AX20SISO-Ant2-5320-PASS

11AX20SISO-Ant2-5320-PASS



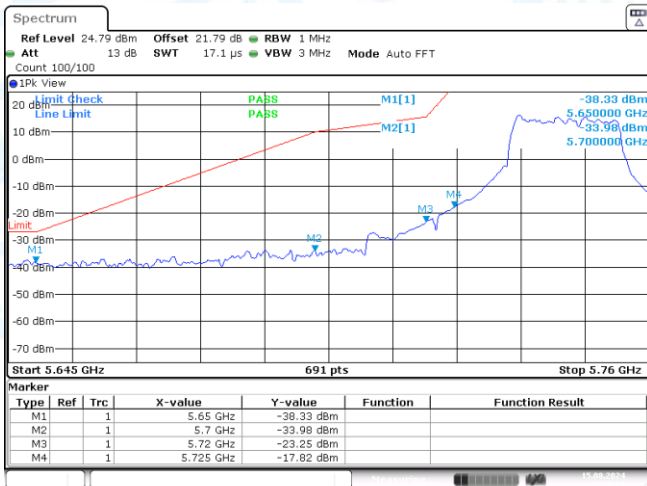
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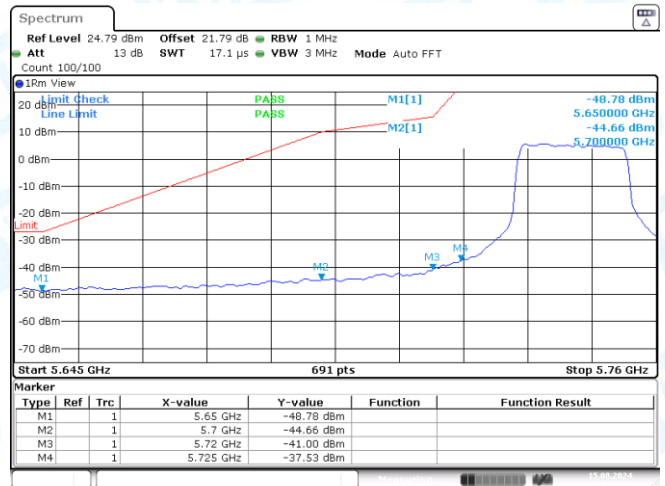
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11AX20SISO-Ant2-5500-PASS

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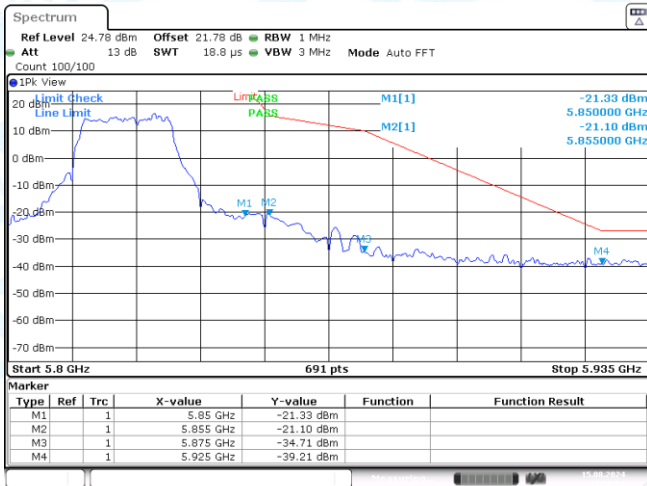
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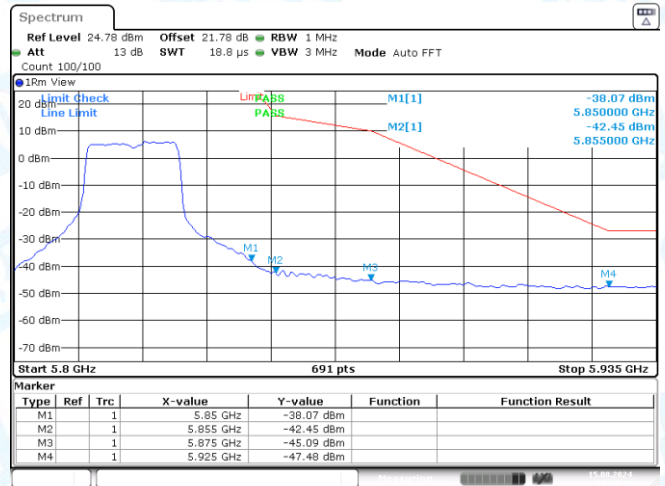
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11AX20SISO-Ant2-5745-PASS

11AX20SISO-Ant2-5745-PASS



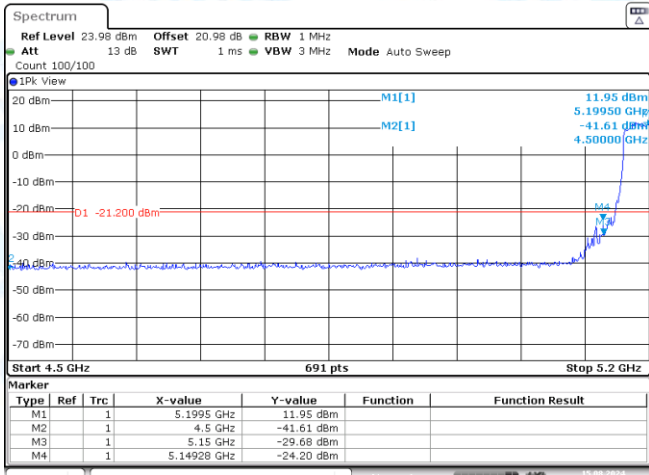
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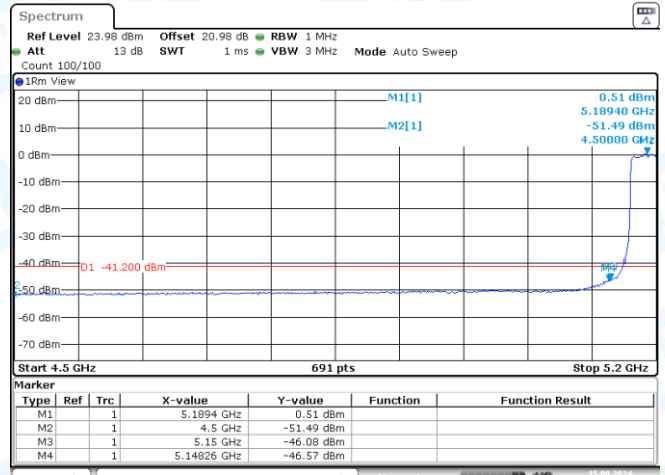
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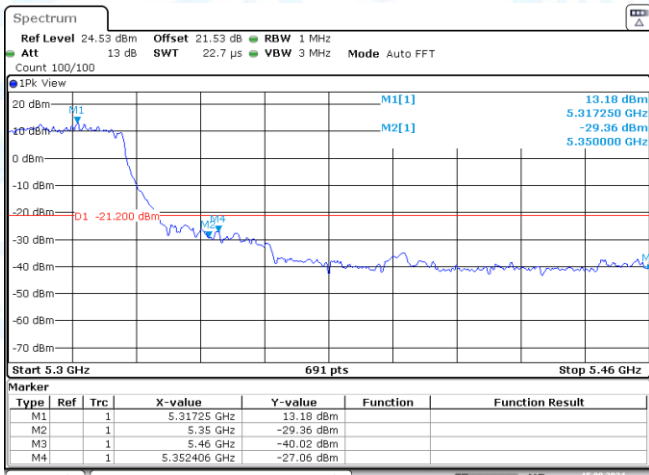
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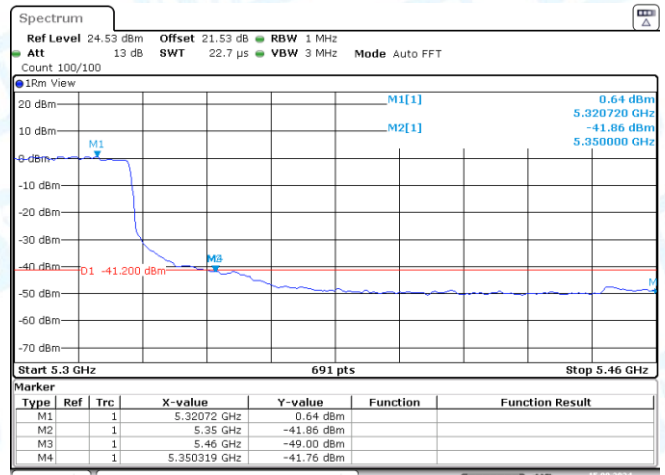
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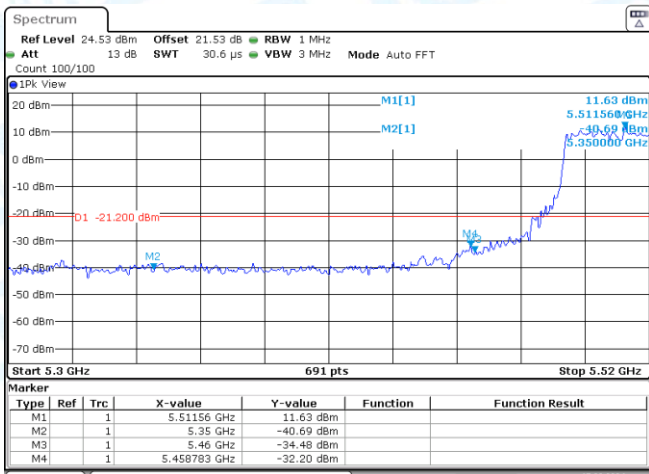
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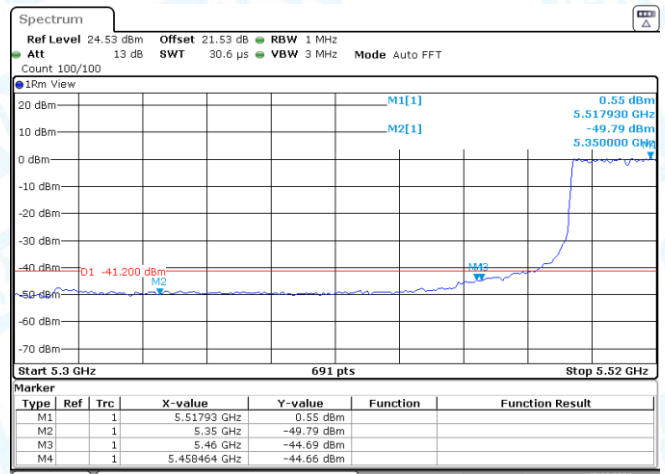
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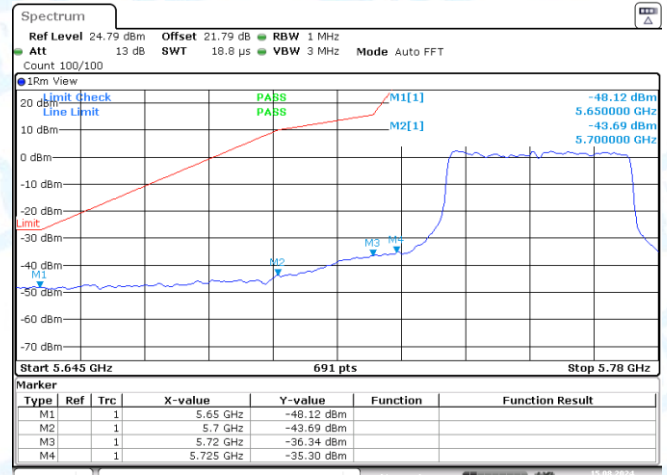
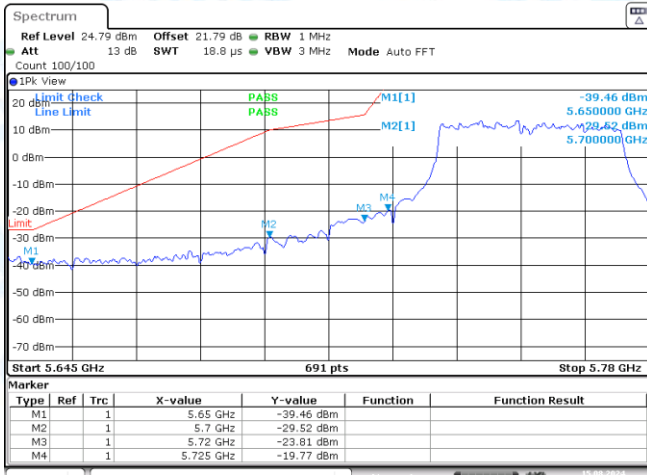
11AX40SISO-Ant2-5310-PASS



11AX40SISO-Ant2-5510-PASS

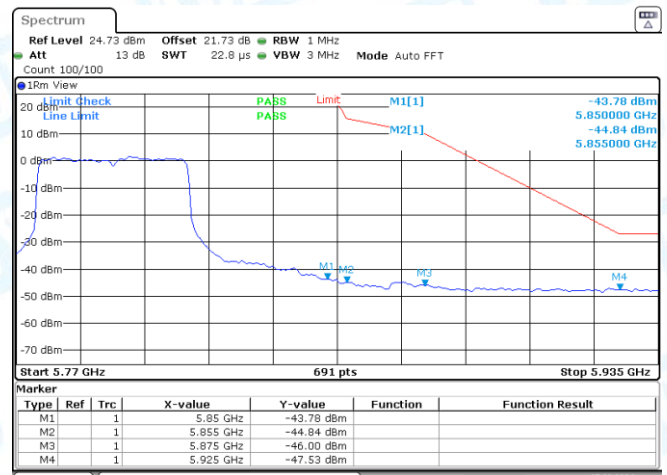
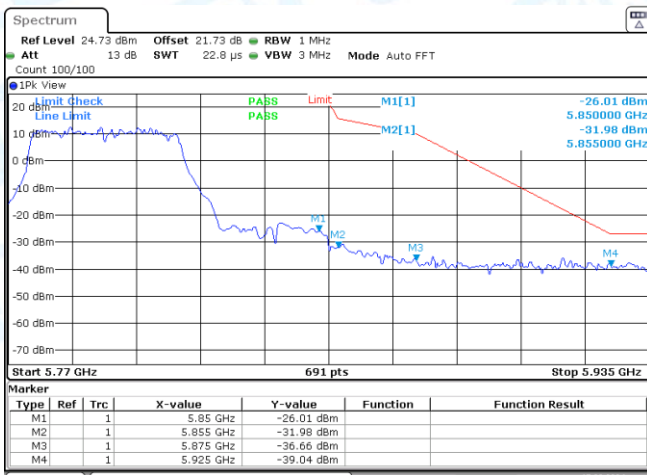


11AX40SISO-Ant2-5510-PASS



11AX40SISO-Ant2-5755-PASS

11AX40SISO-Ant2-5755-PASS



11AX40SISO-Ant2-5795-PASS

11AX40SISO-Ant2-5795-PASS

-----END OF THE REPORT-----