

4. Maximum Power Spectral Density

Test Mode	Test Channel	Ant	Level [dBm/MHz]	10log(1/x) Factor [dB]	PSD [dBm/MHz]	Limit [dBm/MHz]	Verdict
11A	5180	Ant1	1.41	0	1.41	<11.00	PASS
11A	5180	Ant2	0.59	0	0.59	<11.00	PASS
11A	5220	Ant1	1.98	0	1.98	<11.00	PASS
11A	5220	Ant2	0.4	0	0.4	<11.00	PASS
11A	5240	Ant1	1.95	0	1.95	<11.00	PASS
11A	5240	Ant2	0.63	0	0.63	<11.00	PASS
11A	5260	Ant1	1.94	0	1.94	<11.00	PASS
11A	5260	Ant2	0.7	0	0.7	<11.00	PASS
11A	5300	Ant1	1.84	0	1.84	<11.00	PASS
11A	5300	Ant2	0.65	0	0.65	<11.00	PASS
11A	5320	Ant1	1.82	0	1.82	<11.00	PASS
11A	5320	Ant2	0.69	0	0.69	<11.00	PASS
11A	5500	Ant1	0.17	0	0.17	<11.00	PASS
11A	5500	Ant2	-0.18	0	-0.18	<11.00	PASS
11A	5600	Ant1	1.25	0	1.25	<11.00	PASS
11A	5600	Ant2	1.31	0	1.31	<11.00	PASS
11A	5700	Ant1	1.22	0	1.22	<11.00	PASS
11A	5700	Ant2	1.03	0	1.03	<11.00	PASS
11N20	5180	Ant1	-0.61	0	-0.61	<11.00	PASS
11N20	5180	Ant2	0.08	0	0.08	<11.00	PASS
11N20	5220	Ant1	1.56	0	1.56	<11.00	PASS
11N20	5220	Ant2	0.69	0	0.69	<11.00	PASS
11N20	5240	Ant1	1.97	0	1.97	<11.00	PASS
11N20	5240	Ant2	1.16	0	1.16	<11.00	PASS
11N20	5260	Ant1	1.31	0	1.31	<11.00	PASS
11N20	5260	Ant2	1.25	0	1.25	<11.00	PASS
11N20	5300	Ant1	1.72	0	1.72	<11.00	PASS
11N20	5300	Ant2	1.21	0	1.21	<11.00	PASS
11N20	5320	Ant1	1.88	0	1.88	<11.00	PASS
11N20	5320	Ant2	0.72	0	0.72	<11.00	PASS

11N20	5500	Ant1	-0.24	0	-0.24	<11.00	PASS
11N20	5500	Ant2	-0.86	0	-0.86	<11.00	PASS
11N20	5600	Ant1	0.79	0	0.79	<11.00	PASS
11N20	5600	Ant2	1.98	0	1.98	<11.00	PASS
11N20	5700	Ant1	1.9	0	1.9	<11.00	PASS
11N20	5700	Ant2	1.55	0	1.55	<11.00	PASS
11N40	5190	Ant1	-2.33	0	-2.33	<11.00	PASS
11N40	5190	Ant2	-3.77	0	-3.77	<11.00	PASS
11N40	5230	Ant1	-1.77	0	-1.77	<11.00	PASS
11N40	5230	Ant2	-2.96	0	-2.96	<11.00	PASS
11N40	5270	Ant1	-1.06	0	-1.06	<11.00	PASS
11N40	5270	Ant2	-2.72	0	-2.72	<11.00	PASS
11N40	5310	Ant1	-1.07	0	-1.07	<11.00	PASS
11N40	5310	Ant2	-2.95	0	-2.95	<11.00	PASS
11N40	5510	Ant1	-3.92	0	-3.92	<11.00	PASS
11N40	5510	Ant2	-4.4	0	-4.4	<11.00	PASS
11N40	5590	Ant1	-2.84	0	-2.84	<11.00	PASS
11N40	5590	Ant2	-1.62	0	-1.62	<11.00	PASS
11N40	5670	Ant1	-1.9	0	-1.9	<11.00	PASS
11N40	5670	Ant2	1.41	0	1.41	<11.00	PASS
11AC20	5180	Ant1	1.17	0	1.17	<11.00	PASS
11AC20	5180	Ant2	0.97	0	0.97	<11.00	PASS
11AC20	5220	Ant1	1.06	0	1.06	<11.00	PASS
11AC20	5220	Ant2	0.94	0	0.94	<11.00	PASS
11AC20	5240	Ant1	1.8	0	1.8	<11.00	PASS
11AC20	5240	Ant2	0.98	0	0.98	<11.00	PASS
11AC20	5260	Ant1	1.94	0	1.94	<11.00	PASS
11AC20	5260	Ant2	1.47	0	1.47	<11.00	PASS
11AC20	5300	Ant1	2.13	0	2.13	<11.00	PASS
11AC20	5300	Ant2	1.47	0	1.47	<11.00	PASS
11AC20	5320	Ant1	1.86	0	1.86	<11.00	PASS
11AC20	5320	Ant2	1.58	0	1.58	<11.00	PASS
11AC20	5500	Ant1	-0.67	0	-0.67	<11.00	PASS
11AC20	5500	Ant2	-1.53	0	-1.53	<11.00	PASS



SGS-CSTC Standards Technical Services Co., Ltd. Shenzhen Branch

Report No.: SZEM180300232405
Page: 508 of 636

11AC20	5600	Ant1	0.72	0	0.72	<11.00	PASS
11AC20	5600	Ant2	1.26	0	1.26	<11.00	PASS
11AC20	5700	Ant1	1.11	0	1.11	<11.00	PASS
11AC20	5700	Ant2	1.94	0	1.94	<11.00	PASS
11AC80	5210	Ant1	-5.71	0	-5.71	<11.00	PASS
11AC80	5210	Ant2	-4.87	0	-4.87	<11.00	PASS
11AC80	5290	Ant1	-5.02	0	-5.02	<11.00	PASS
11AC80	5290	Ant2	-5.16	0	-5.16	<11.00	PASS
11AC80	5530	Ant1	-6.2	0	-6.2	<11.00	PASS
11AC80	5530	Ant2	-5.53	0	-5.53	<11.00	PASS
11AC80	5610	Ant1	-5.33	0	-5.33	<11.00	PASS
11AC80	5610	Ant2	-2.9	0	-2.9	<11.00	PASS
11AC40	5190	Ant1	-3.87	0	-3.87	<11.00	PASS
11AC40	5190	Ant2	-2.43	0	-2.43	<11.00	PASS
11AC40	5230	Ant1	-3.31	0	-3.31	<11.00	PASS
11AC40	5230	Ant2	-2.31	0	-2.31	<11.00	PASS
11AC40	5270	Ant1	-2.46	0	-2.46	<11.00	PASS
11AC40	5270	Ant2	-1.94	0	-1.94	<11.00	PASS
11AC40	5310	Ant1	-2.45	0	-2.45	<11.00	PASS
11AC40	5310	Ant2	-2.43	0	-2.43	<11.00	PASS
11AC40	5510	Ant1	-3.95	0	-3.95	<11.00	PASS
11AC40	5510	Ant2	-4.37	0	-4.37	<11.00	PASS
11AC40	5590	Ant1	-3.09	0	-3.09	<11.00	PASS
11AC40	5590	Ant2	-1.77	0	-1.77	<11.00	PASS
11AC40	5670	Ant1	-2.45	0	-2.45	<11.00	PASS
11AC40	5670	Ant2	1.54	0	1.54	<11.00	PASS
11N20	5180	Ant1+ Ant2	2.76	0	2.76	<7.99	PASS
11N20	5220	Ant1+ Ant2	4.16	0	4.16	<7.99	PASS
11N20	5240	Ant1+ Ant2	4.59	0	4.59	<7.99	PASS
11N20	5260	Ant1+ Ant2	4.29	0	4.29	<7.99	PASS
11N20	5300	Ant1+ Ant2	4.48	0	4.48	<7.99	PASS
11N20	5320	Ant1+ Ant2	4.35	0	4.35	<7.99	PASS
11N20	5500	Ant1+ Ant2	2.47	0	2.47	<7.99	PASS



SGS-CSTC Standards Technical Services Co., Ltd. Shenzhen Branch

Report No.: SZEM180300232405
Page: 509 of 636

11N20	5600	Ant1+ Ant2	4.44	0	4.44	<7.99	PASS
11N20	5700	Ant1+ Ant2	4.74	0	4.74	<7.99	PASS
11N40	5190	Ant1+ Ant2	0.02	0	0.02	<7.99	PASS
11N40	5230	Ant1+ Ant2	0.69	0	0.69	<7.99	PASS
11N40	5270	Ant1+ Ant2	1.20	0	1.20	<7.99	PASS
11N40	5310	Ant1+ Ant2	1.10	0	1.10	<7.99	PASS
11N40	5510	Ant1+ Ant2	-1.14	0	-1.14	<7.99	PASS
11N40	5590	Ant1+ Ant2	0.82	0	0.82	<7.99	PASS
11N40	5670	Ant1+ Ant2	3.07	0	3.07	<7.99	PASS
11AC20	5180	Ant1+ Ant2	4.08	0	4.08	<7.99	PASS
11AC20	5220	Ant1+ Ant2	4.01	0	4.01	<7.99	PASS
11AC20	5240	Ant1+ Ant2	4.42	0	4.42	<7.99	PASS
11AC20	5260	Ant1+ Ant2	4.72	0	4.72	<7.99	PASS
11AC20	5300	Ant1+ Ant2	4.82	0	4.82	<7.99	PASS
11AC20	5320	Ant1+ Ant2	4.73	0	4.73	<7.99	PASS
11AC20	5500	Ant1+ Ant2	1.93	0	1.93	<7.99	PASS
11AC20	5600	Ant1+ Ant2	4.01	0	4.01	<7.99	PASS
11AC20	5700	Ant1+ Ant2	4.56	0	4.56	<7.99	PASS
11AC80	5210	Ant1+ Ant2	-2.26	0	-2.26	<7.99	PASS
11AC80	5290	Ant1+ Ant2	-2.08	0	-2.08	<7.99	PASS
11AC80	5530	Ant1+ Ant2	-2.84	0	-2.84	<7.99	PASS
11AC80	5610	Ant1+ Ant2	-0.94	0	-0.94	<7.99	PASS
11AC40	5190	Ant1+ Ant2	-0.08	0	-0.08	<7.99	PASS
11AC40	5230	Ant1+ Ant2	0.23	0	0.23	<7.99	PASS
11AC40	5270	Ant1+ Ant2	0.82	0	0.82	<7.99	PASS
11AC40	5310	Ant1+ Ant2	0.57	0	0.57	<7.99	PASS
11AC40	5510	Ant1+ Ant2	-1.14	0	-1.14	<7.99	PASS
11AC40	5590	Ant1+ Ant2	0.63	0	0.63	<7.99	PASS
11AC40	5670	Ant1+ Ant2	3.00	0	3.00	<7.99	PASS

Remark: directional gain=6dB_i+6dB_o=9.01dB_i, So the limit=11-(9.01-6)=7.99 dB_m/MHz

Test Mode	Test Channel	Ant	Level [dBm/500kHz]	10log(1/x) Factor[dB]	10log(500kHz/RBW) Factor [dB]	PSD [dBm/500kHz]	Limit [dBm/500kHz]	Verdict
11A	5745	Ant 1	-0.81	0	0	-0.81	<17.00	PASS
11A	5745	Ant 2	2.55	0	0	2.55	<17.00	PASS
11A	5785	Ant 1	-0.94	0	0	-0.94	<17.00	PASS
11A	5785	Ant 2	2.27	0	0	2.27	<17.00	PASS
11A	5825	Ant 1	-0.71	0	0	-0.71	<17.00	PASS
11A	5825	Ant 2	0.99	0	0	0.99	<17.00	PASS
11N20	5745	Ant 1	-0.72	0	0	-0.72	<17.00	PASS
11N20	5745	Ant 2	2.01	0	0	2.01	<17.00	PASS
11N20	5785	Ant 1	-0.19	0	0	-0.19	<17.00	PASS
11N20	5785	Ant 2	2.19	0	0	2.19	<17.00	PASS
11N20	5825	Ant 1	-1.3	0	0	-1.3	<17.00	PASS
11N20	5825	Ant 2	1.35	0	0	1.35	<17.00	PASS
11N40	5755	Ant 1	-4.24	0	0	-4.24	<17.00	PASS
11N40	5755	Ant 2	-0.51	0	0	-0.51	<17.00	PASS
11N40	5795	Ant 1	-4.46	0	0	-4.46	<17.00	PASS
11N40	5795	Ant 2	-0.77	0	0	-0.77	<17.00	PASS
11AC20	5745	Ant 1	-1.72	0	0	-1.72	<17.00	PASS
11AC20	5745	Ant 2	2.73	0	0	2.73	<17.00	PASS
11AC2	5785	Ant	-1.67	0	0	-1.67	<17.00	PASS



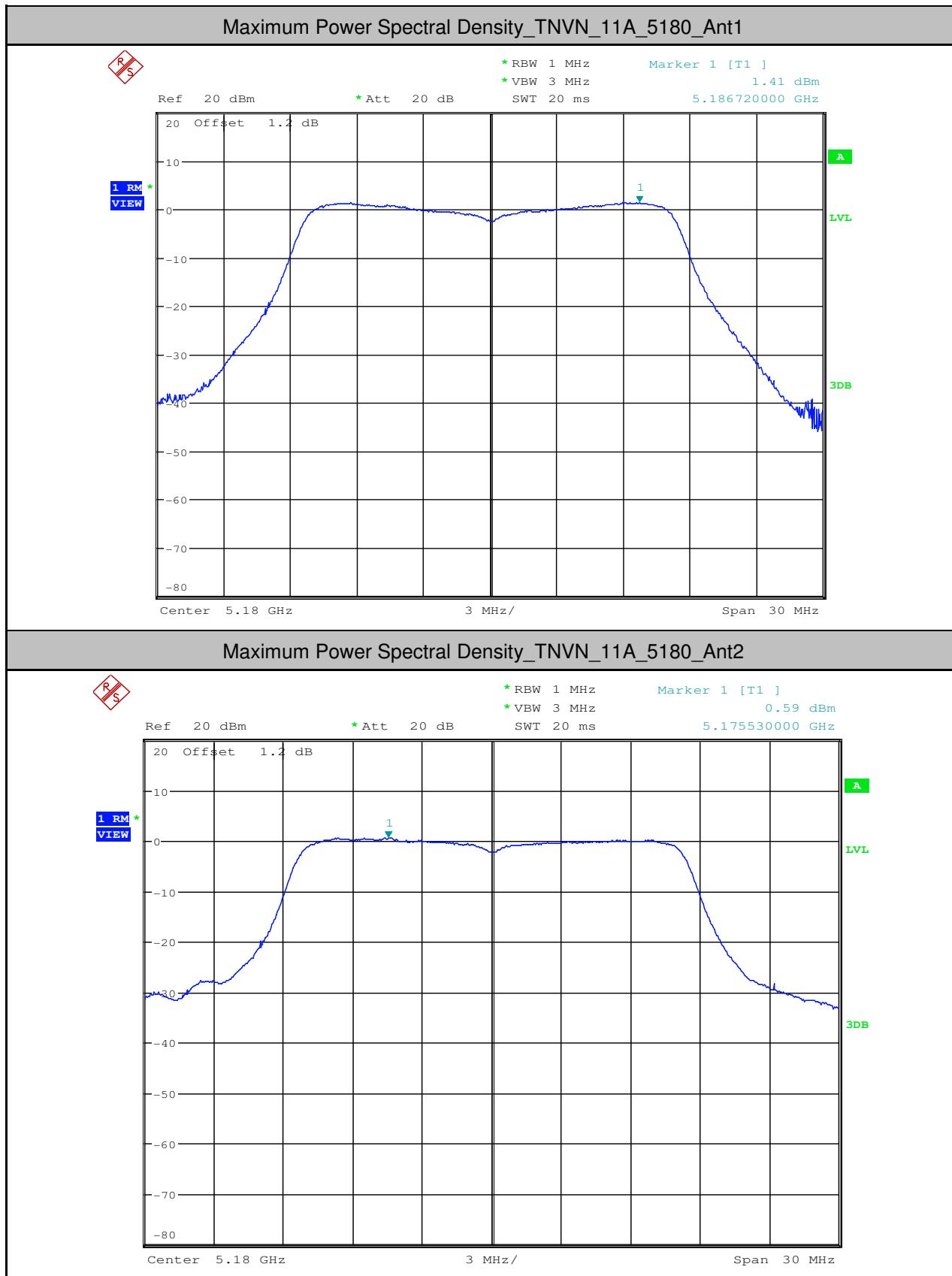
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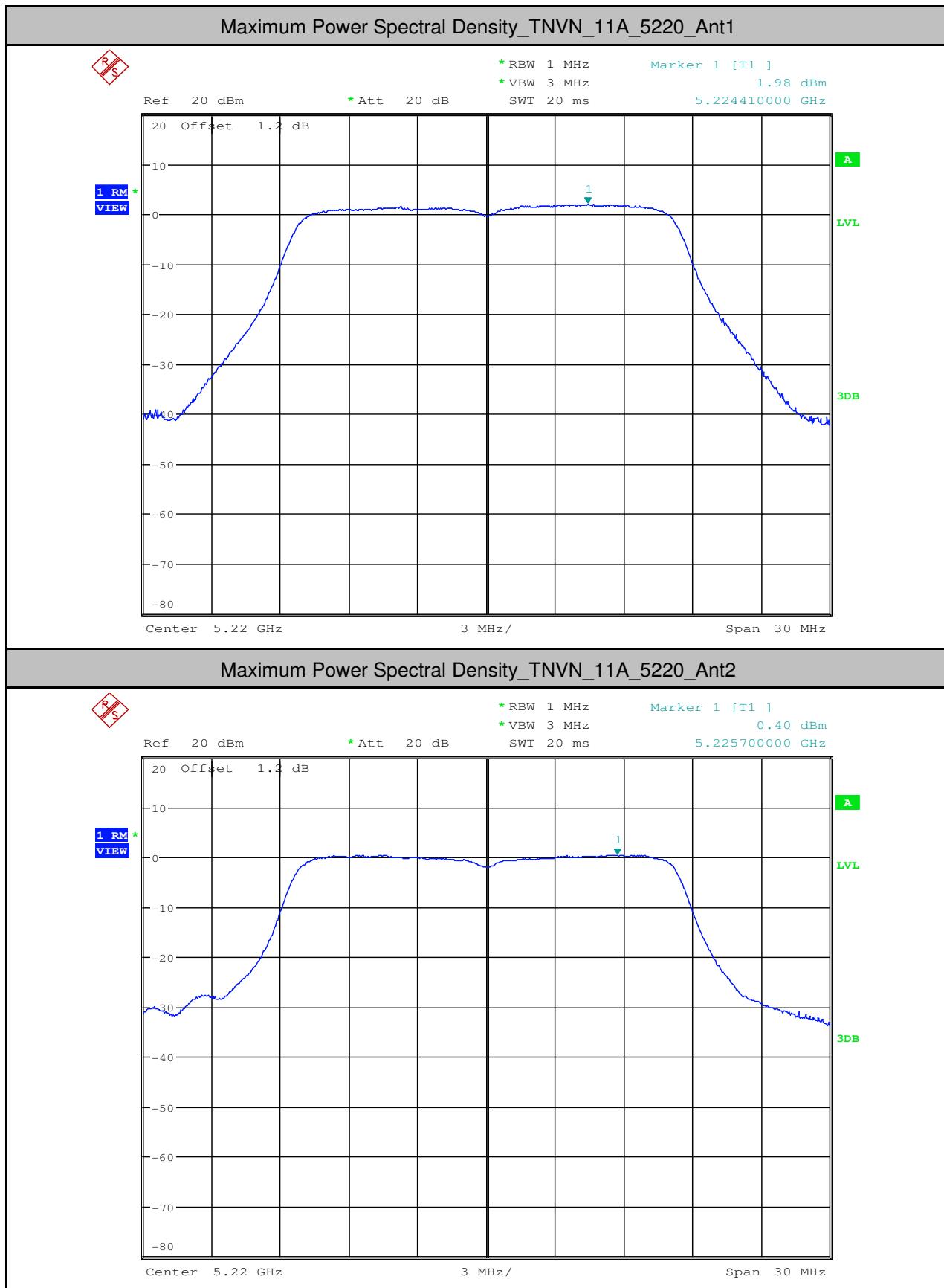
Report No.: SZEM180300232405
Page: 511 of 636

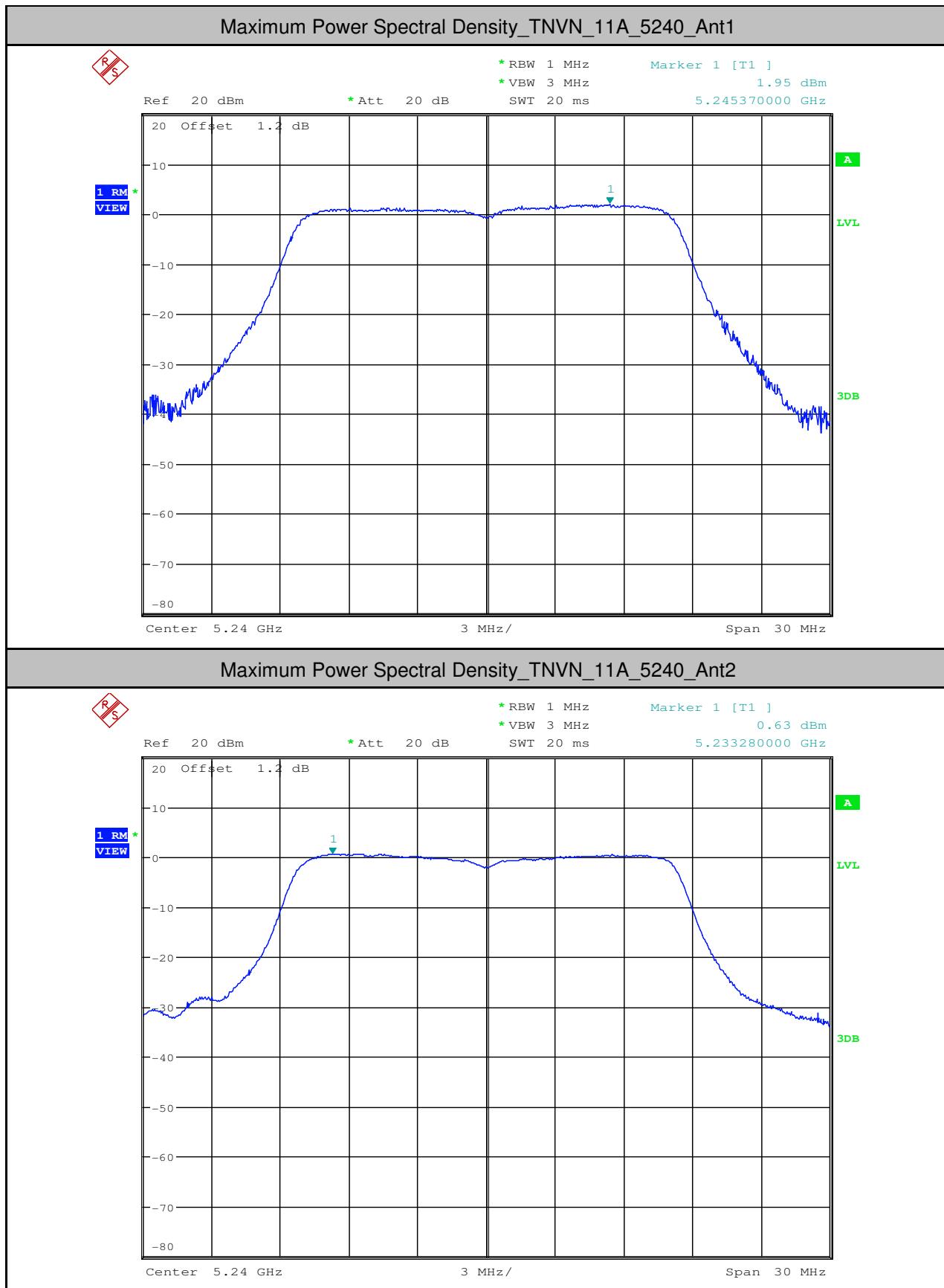
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11AC20	5785	Ant 2	2.4	0	0	2.4	<17.00	PASS	
11AC20	5825	Ant 1	-1.31	0	0	-1.31	<17.00	PASS	
11AC20	5825	Ant 2	1.53	0	0	1.53	<17.00	PASS	
11AC80	5775	Ant 1	-7.87	0	0	-7.87	<17.00	PASS	
11AC80	5775	Ant 2	-2.7	0	0	-2.7	<17.00	PASS	
11AC40	5755	Ant 1	-4.68	0	0	-4.68	<17.00	PASS	
11AC40	5755	Ant 2	0.31	0	0	0.31	<17.00	PASS	
11AC40	5795	Ant 1	-4.42	0	0	-4.42	<17.00	PASS	
11AC40	5795	Ant 2	-0.02	0	0	-0.02	<17.00	PASS	

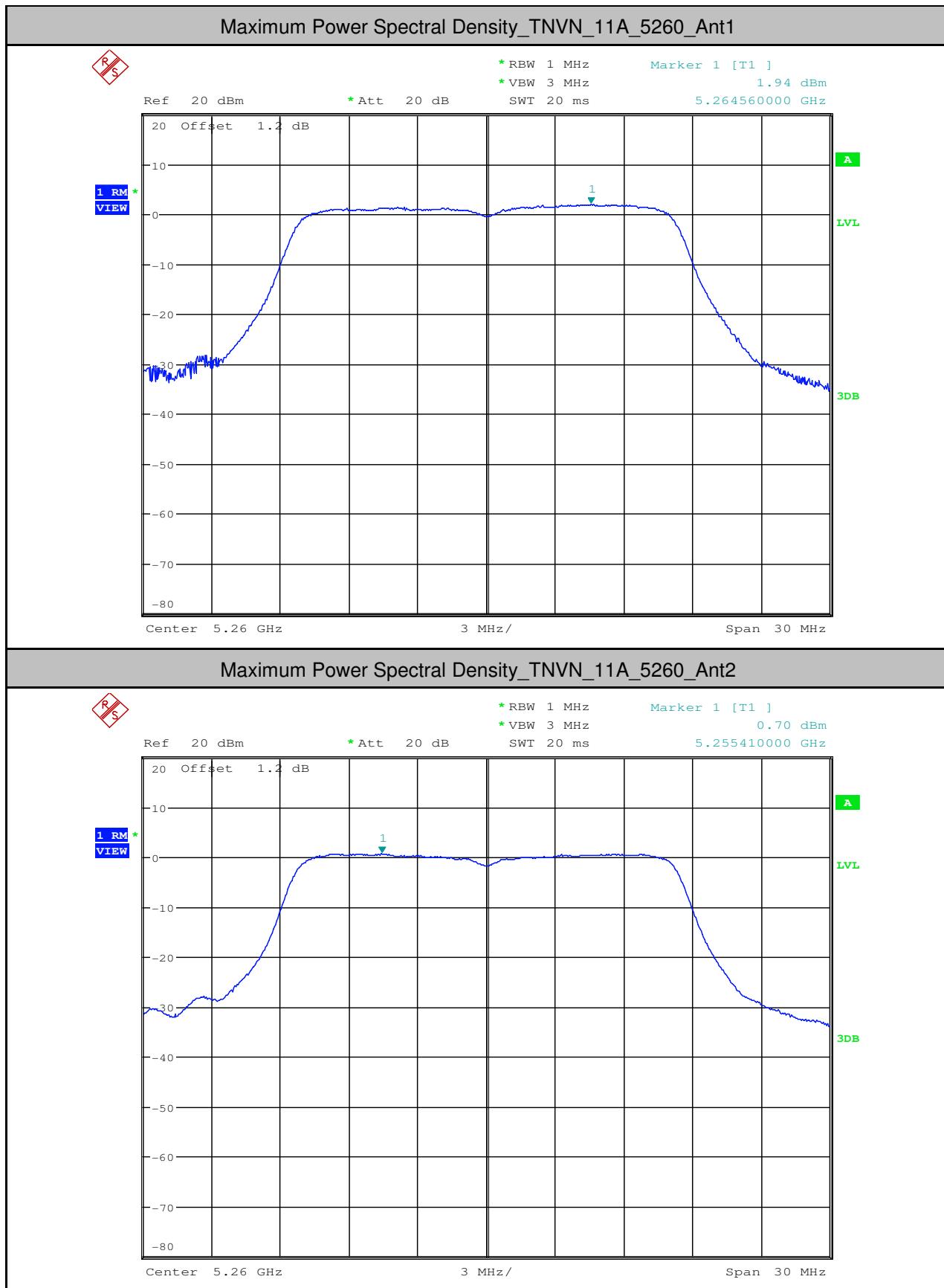
Test Mode	Test Channel	Ant	Level	10log(1/x) Factor[dB]	10log(500kHz/RBW)	PSD	Limit	Verdict
			[dBm/500kHz]		Factor [dB]			
11N20	5745	Ant1+ANT2	3.87	0	0	3.87	<13.99	PASS
11N20	5785	Ant1+ANT2	4.17	0	0	4.17	<13.99	PASS
11N20	5825	Ant1+ANT2	3.23	0	0	3.23	<13.99	PASS
11N40	5755	Ant1+ANT2	1.02	0	0	1.02	<13.99	PASS
11N40	5795	Ant1+ANT2	0.78	0	0	0.78	<13.99	PASS
11AC20	5745	Ant1+ANT2	4.06	0	0	4.06	<13.99	PASS
11AC20	5785	Ant1+ANT2	3.84	0	0	3.84	<13.99	PASS
11AC20	5825	Ant1+ANT2	3.35	0	0	3.35	<13.99	PASS
11AC80	5775	Ant1+ANT2	-1.55	0	0	-1.55	<13.99	PASS
11AC40	5755	Ant1+ANT2	1.51	0	0	1.51	<13.99	PASS
11AC40	5795	Ant1+ANT2	1.33	0	0	1.33	<13.99	PASS

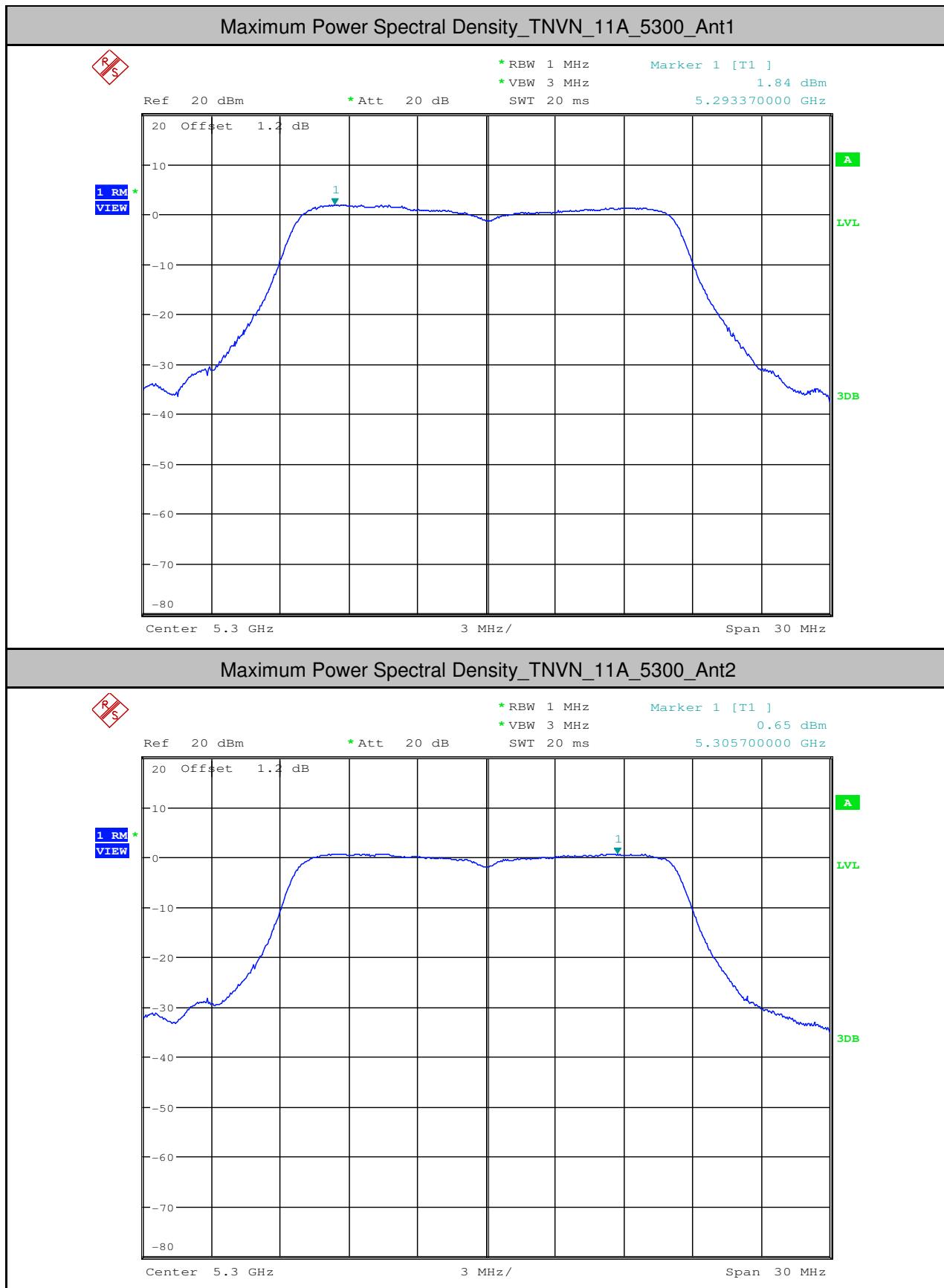
Remark: directional gain=6dBi+6dBi=9.01dBi, So the limit=17-(9.01-6)=13.99 dBm/500kHz

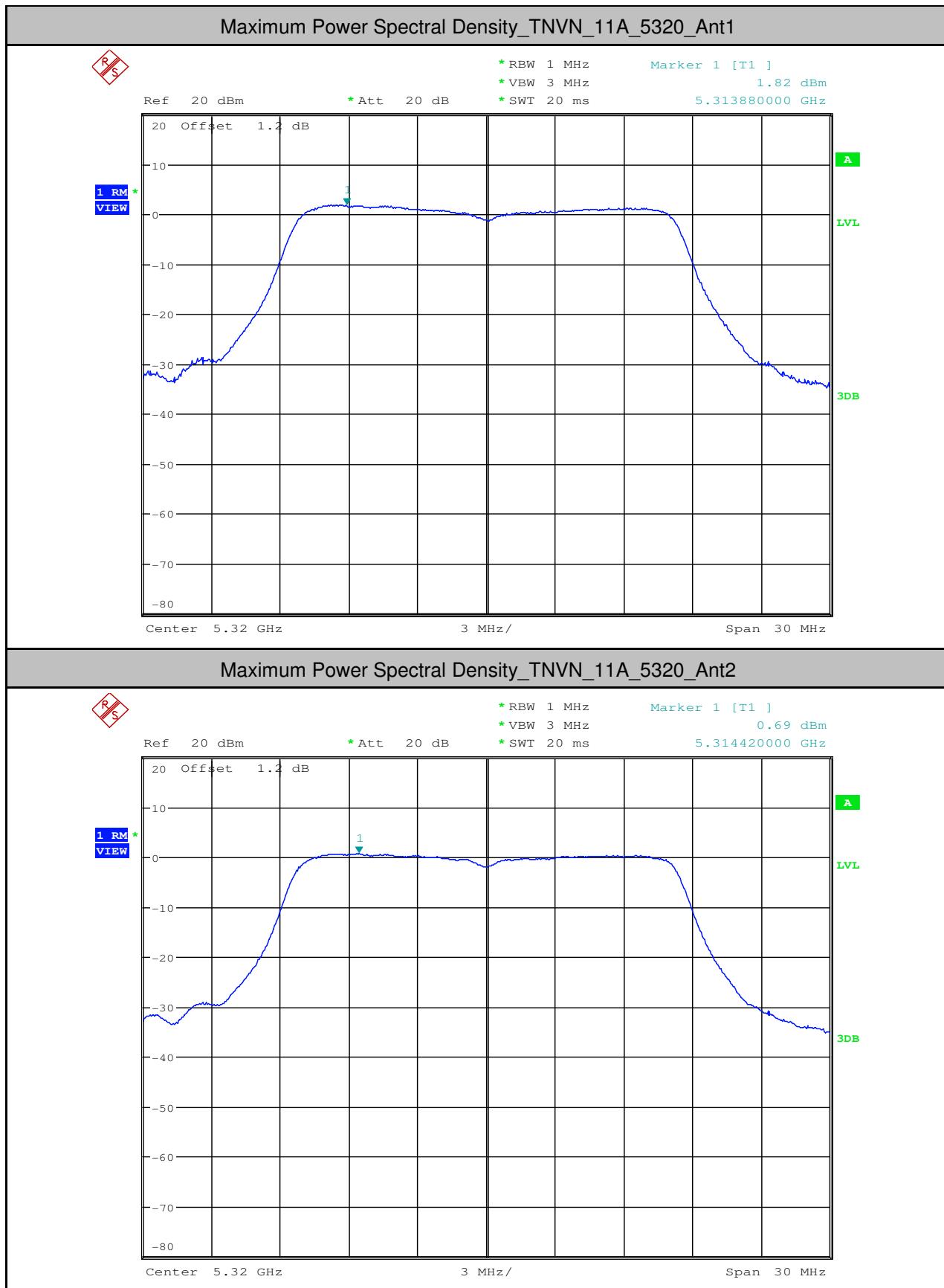


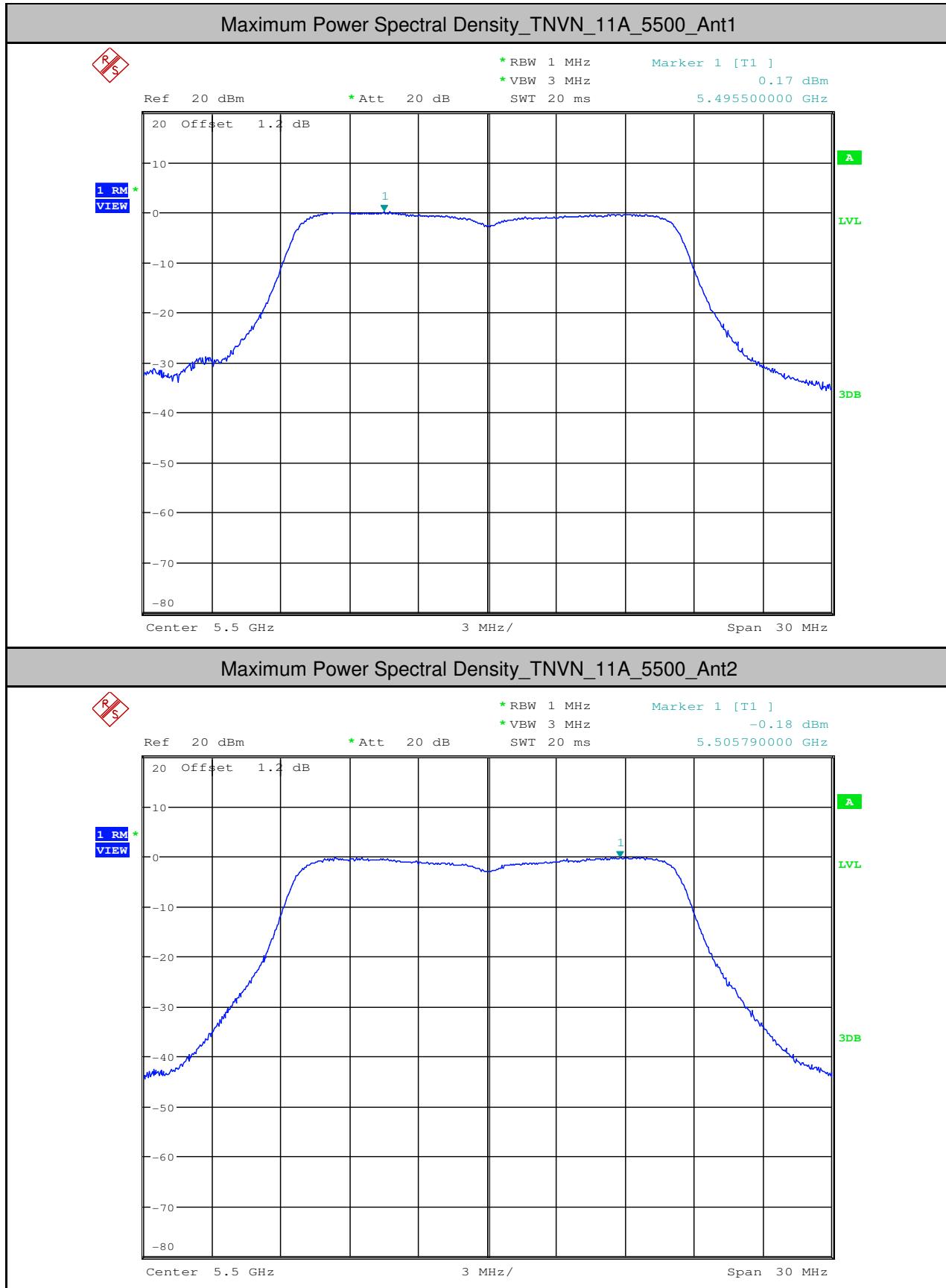


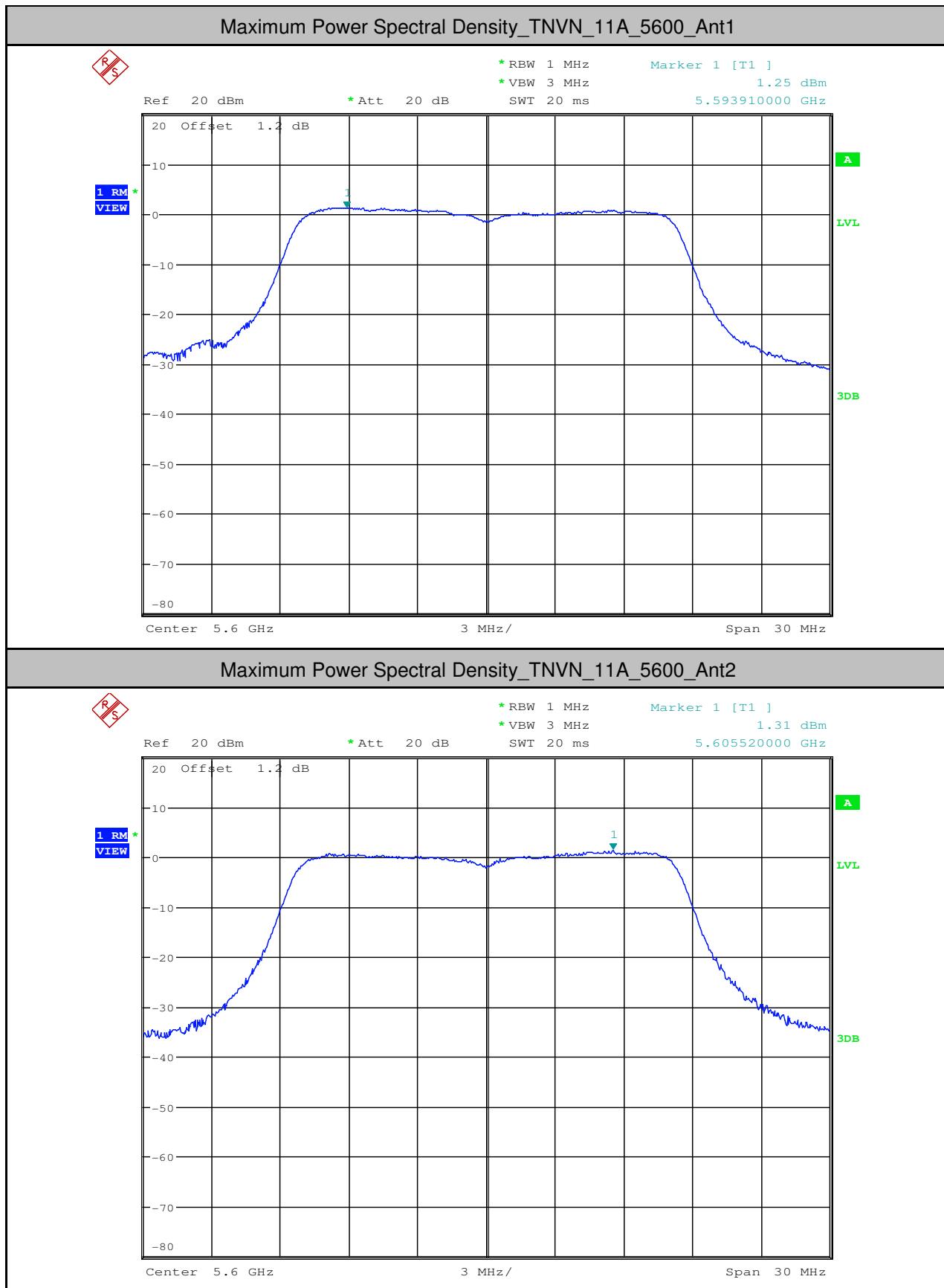


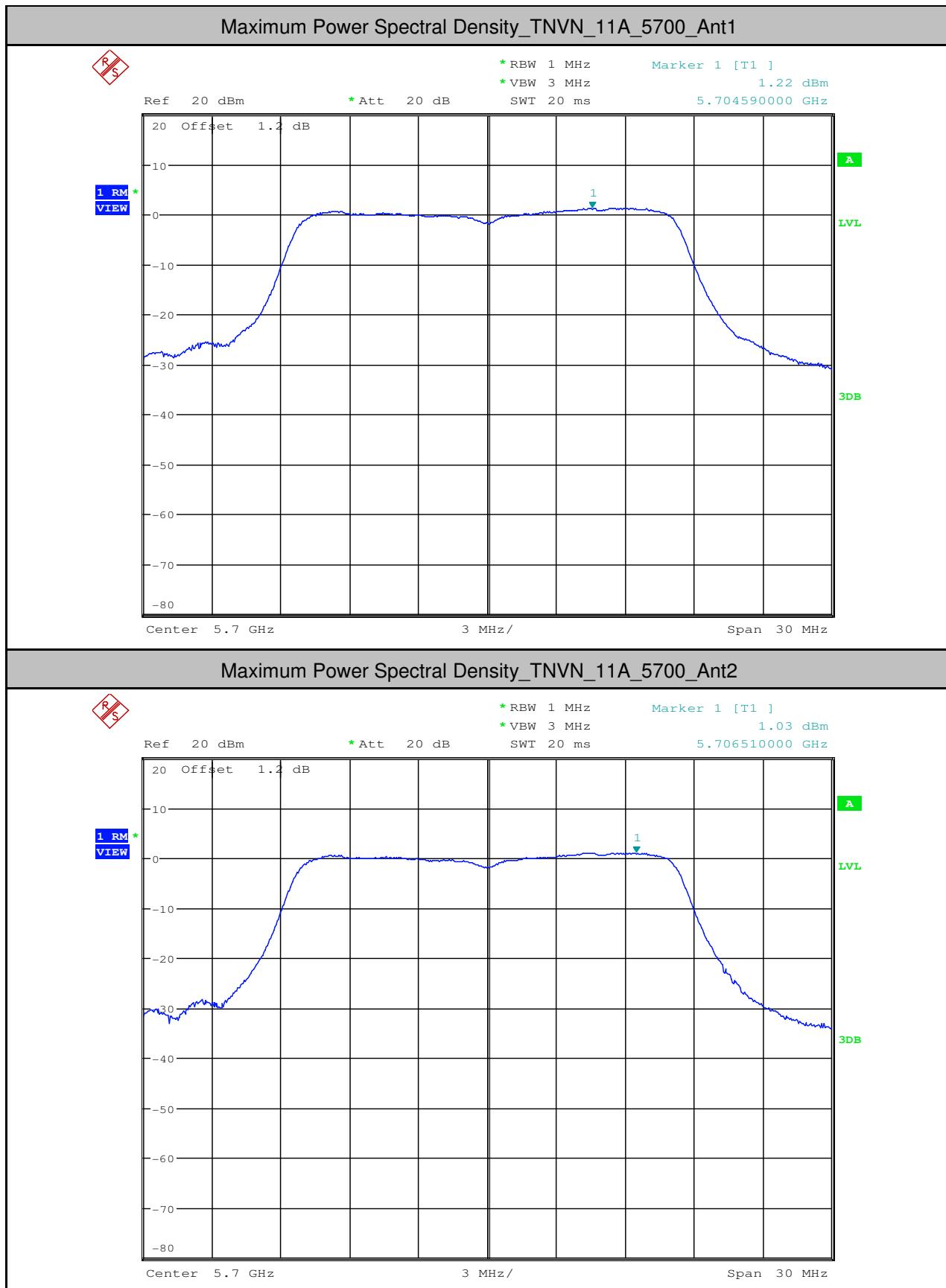


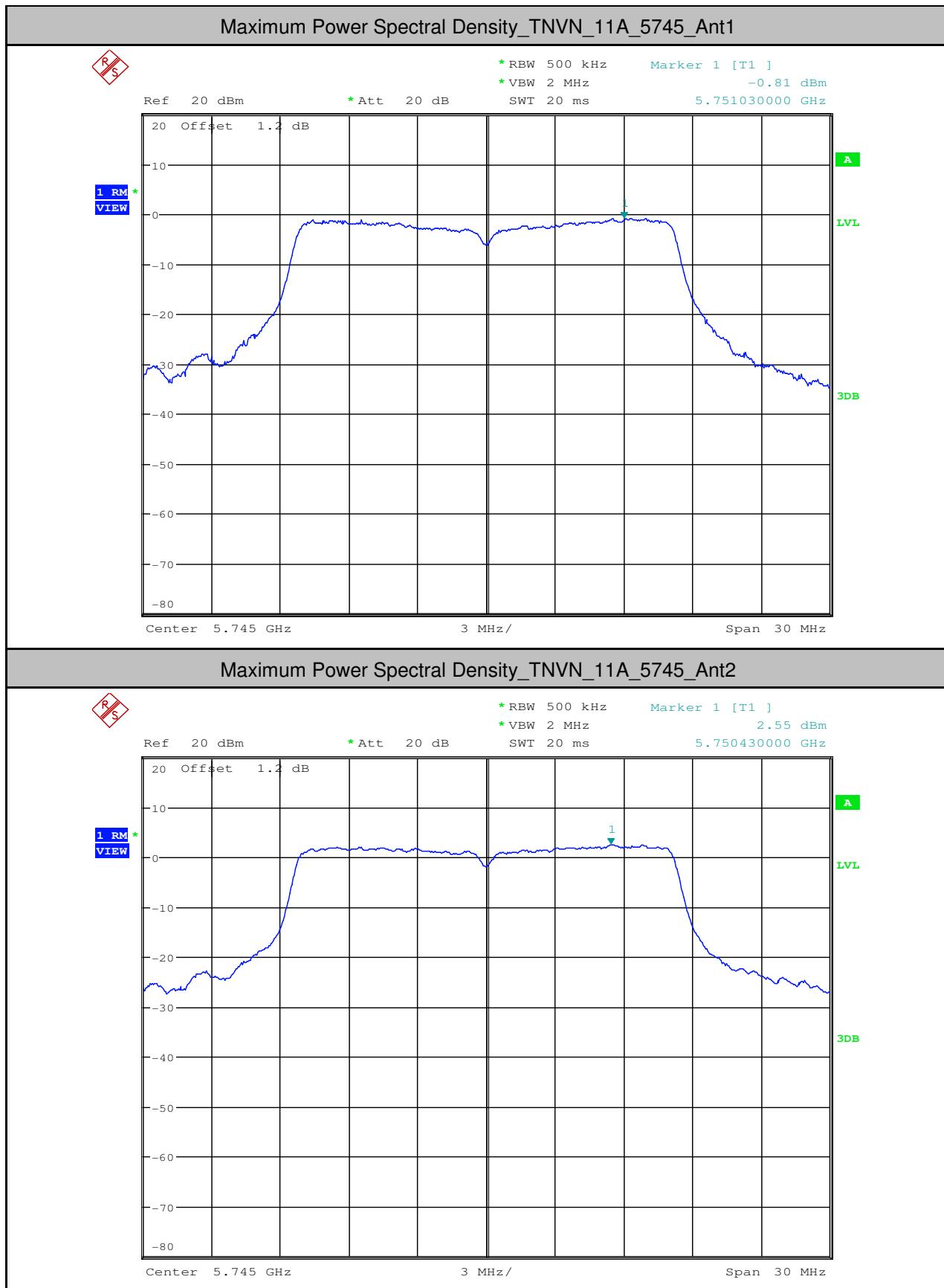


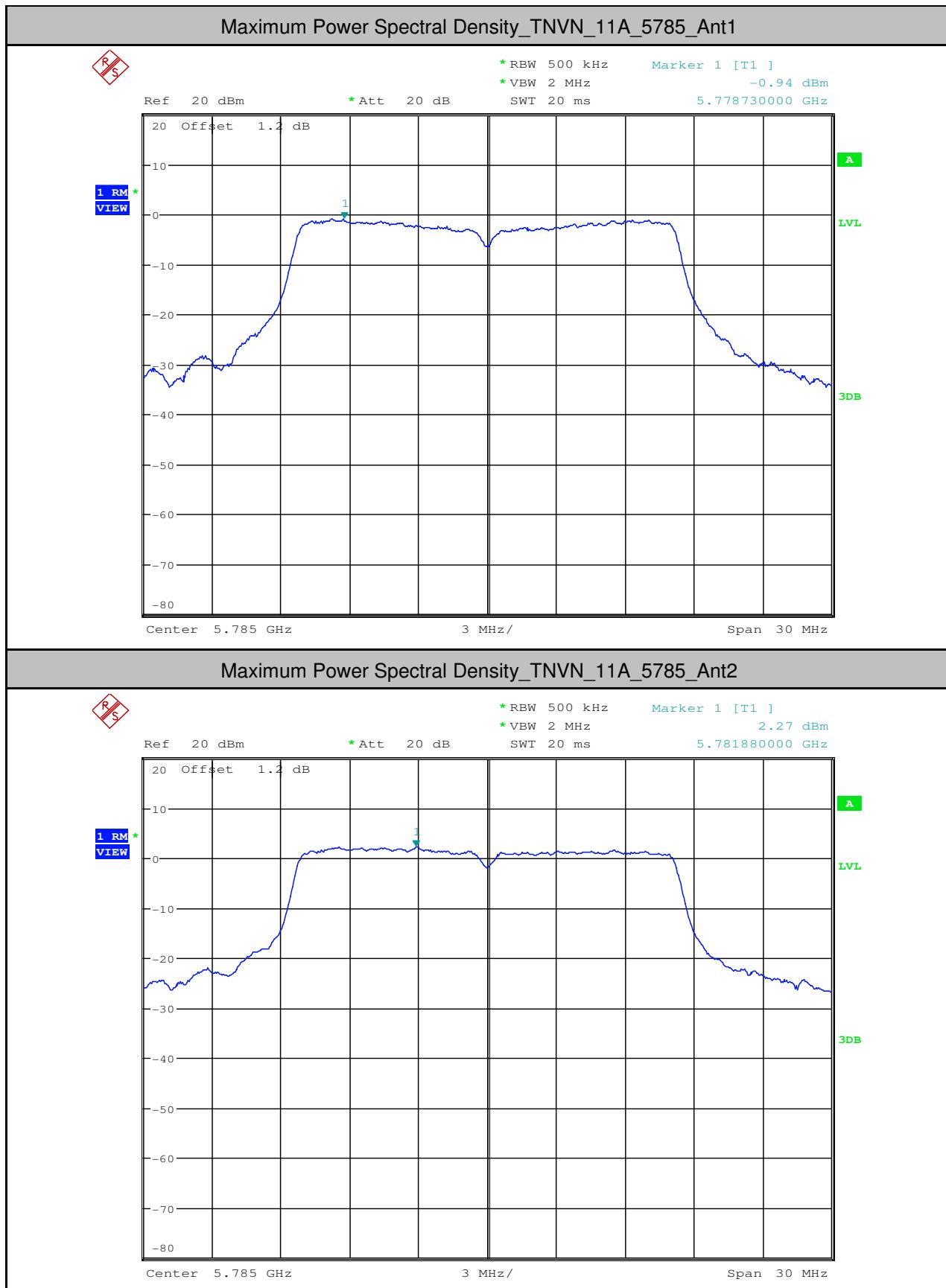


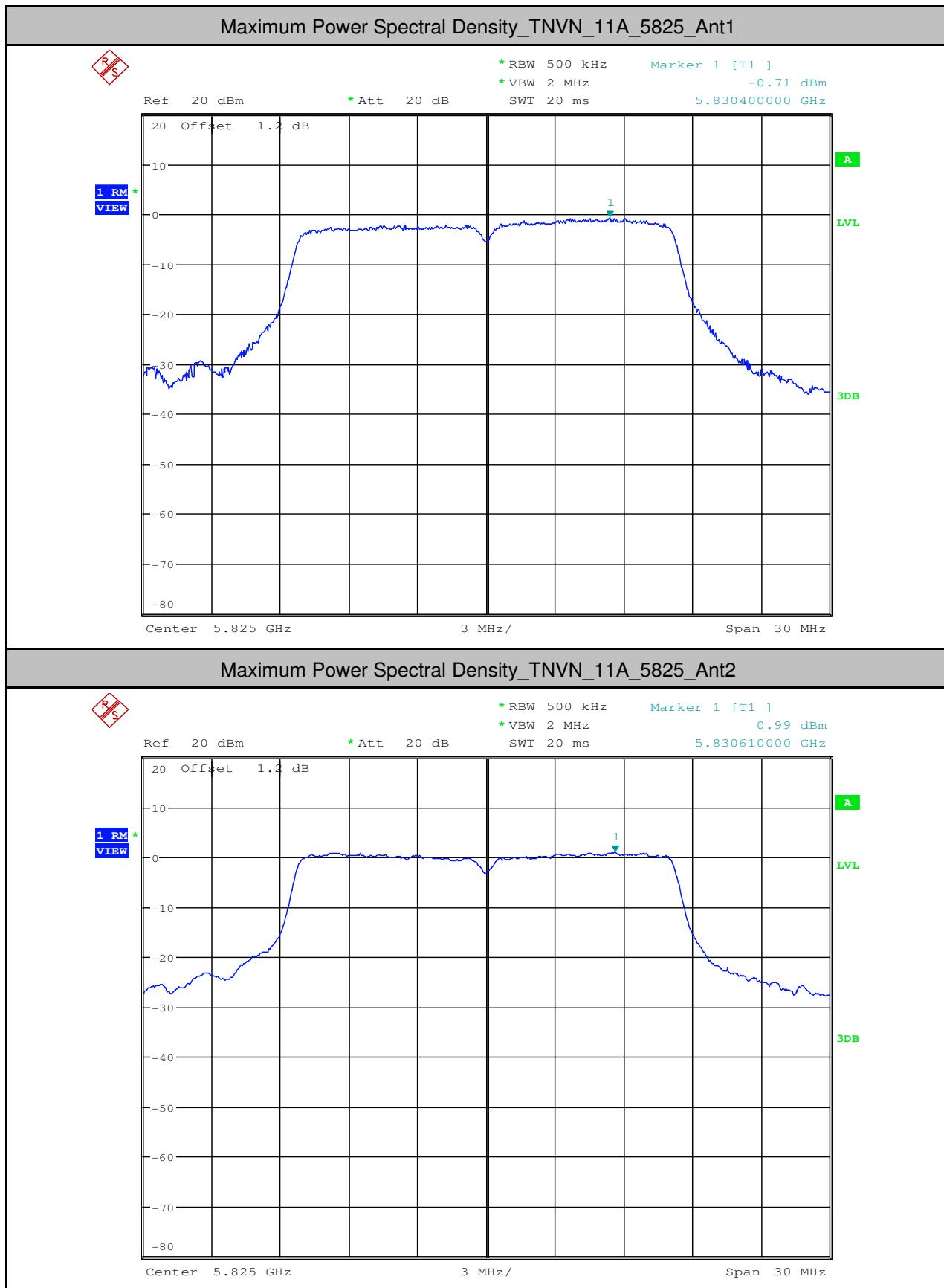


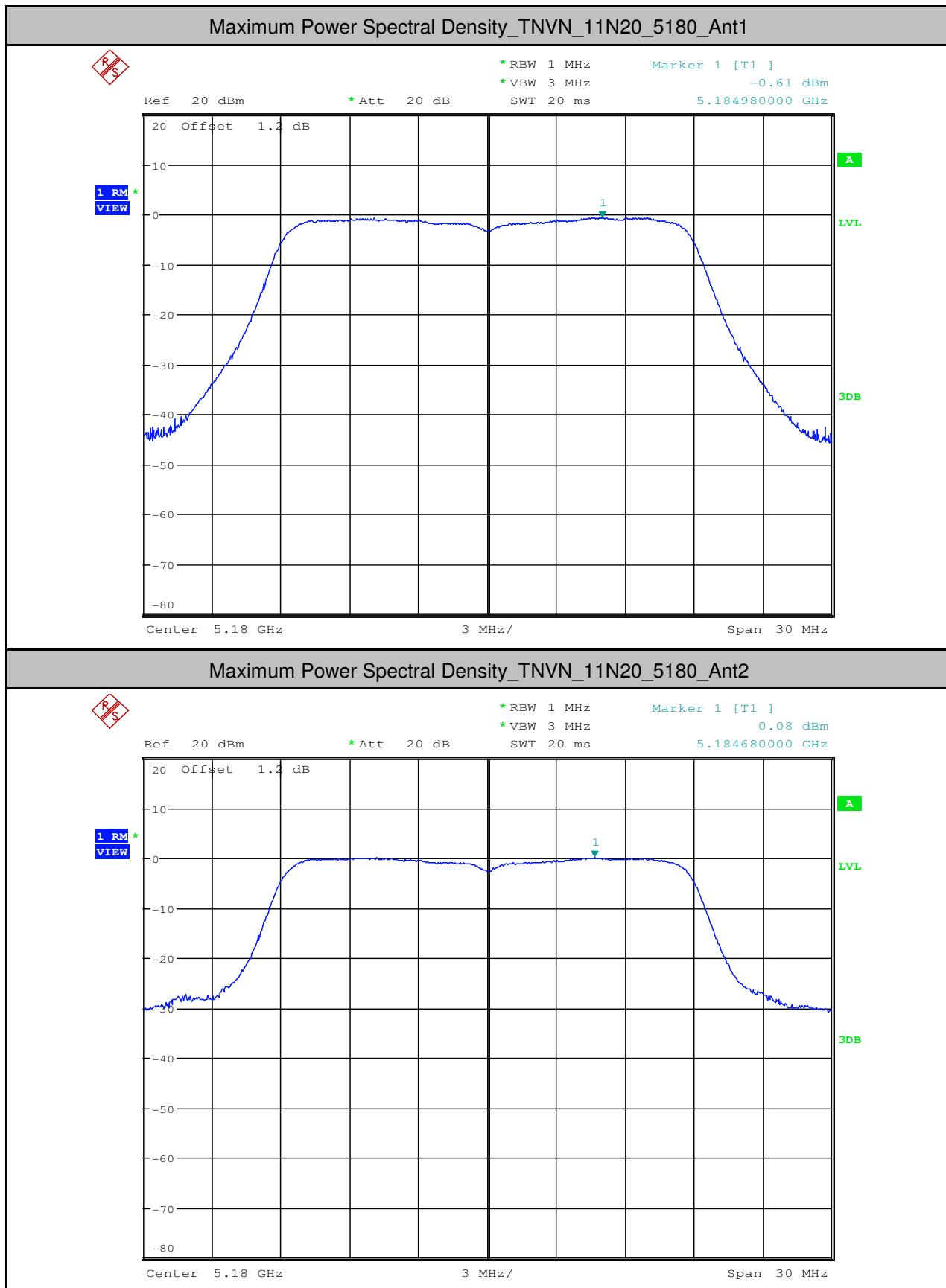


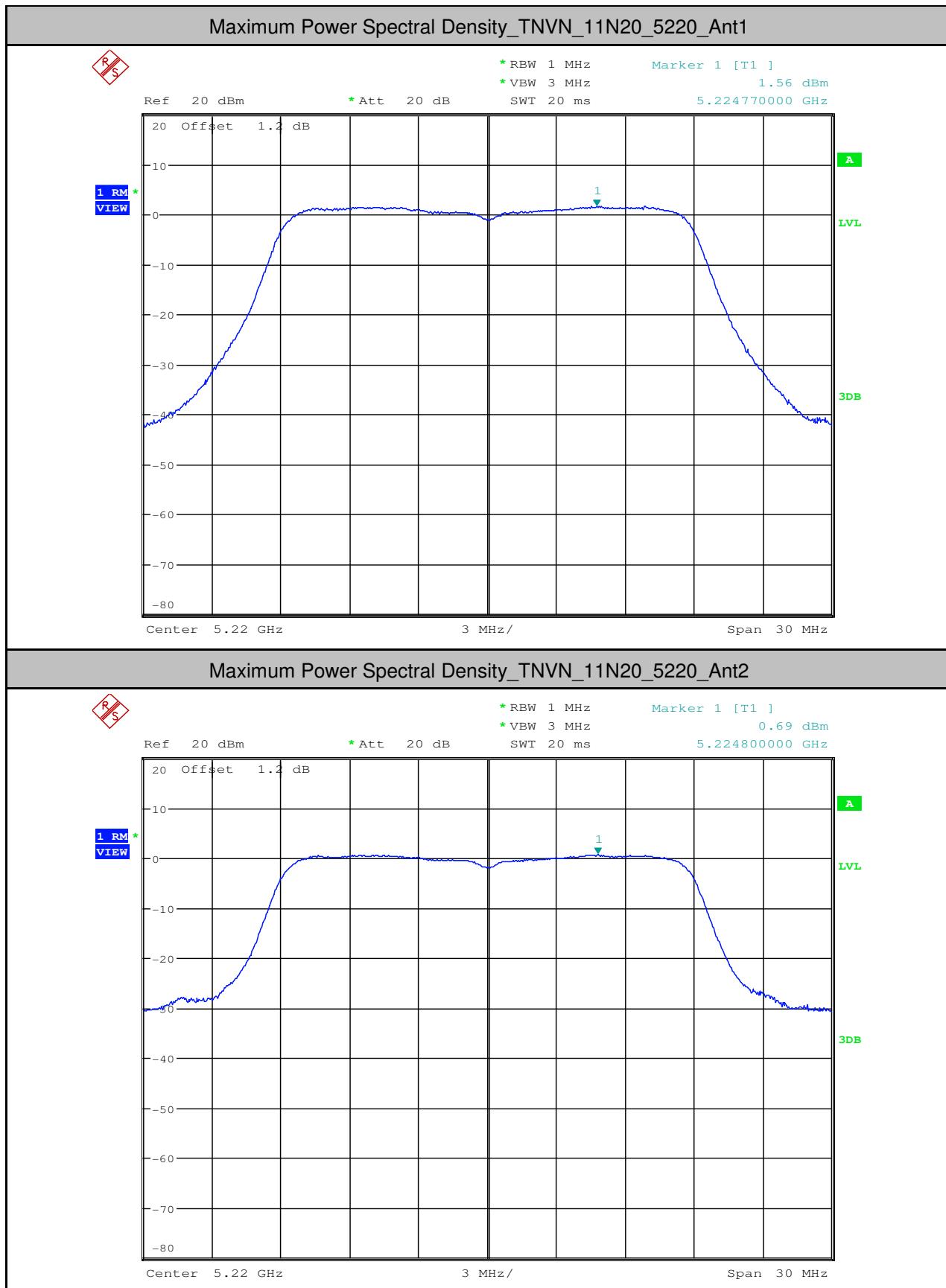


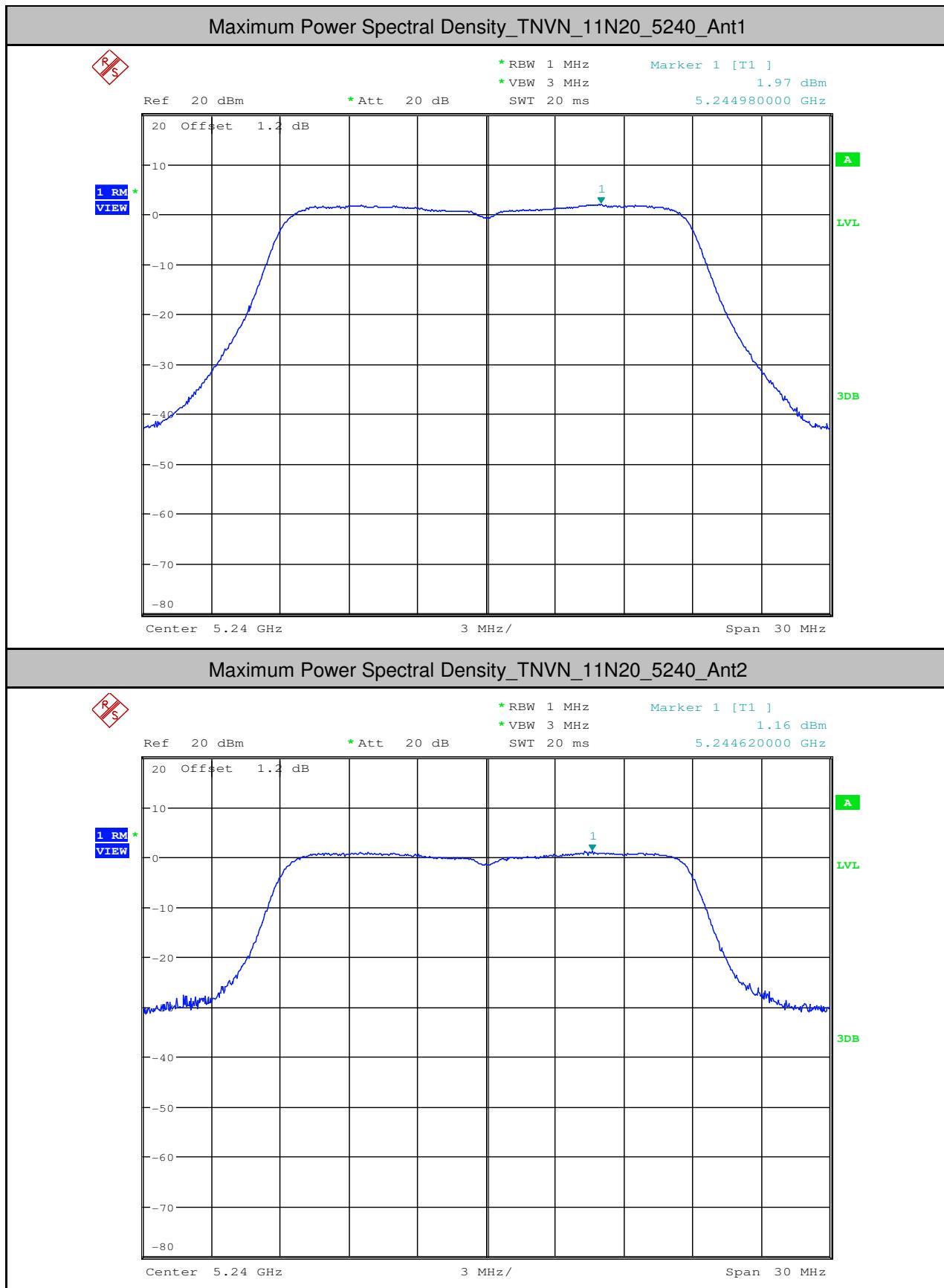


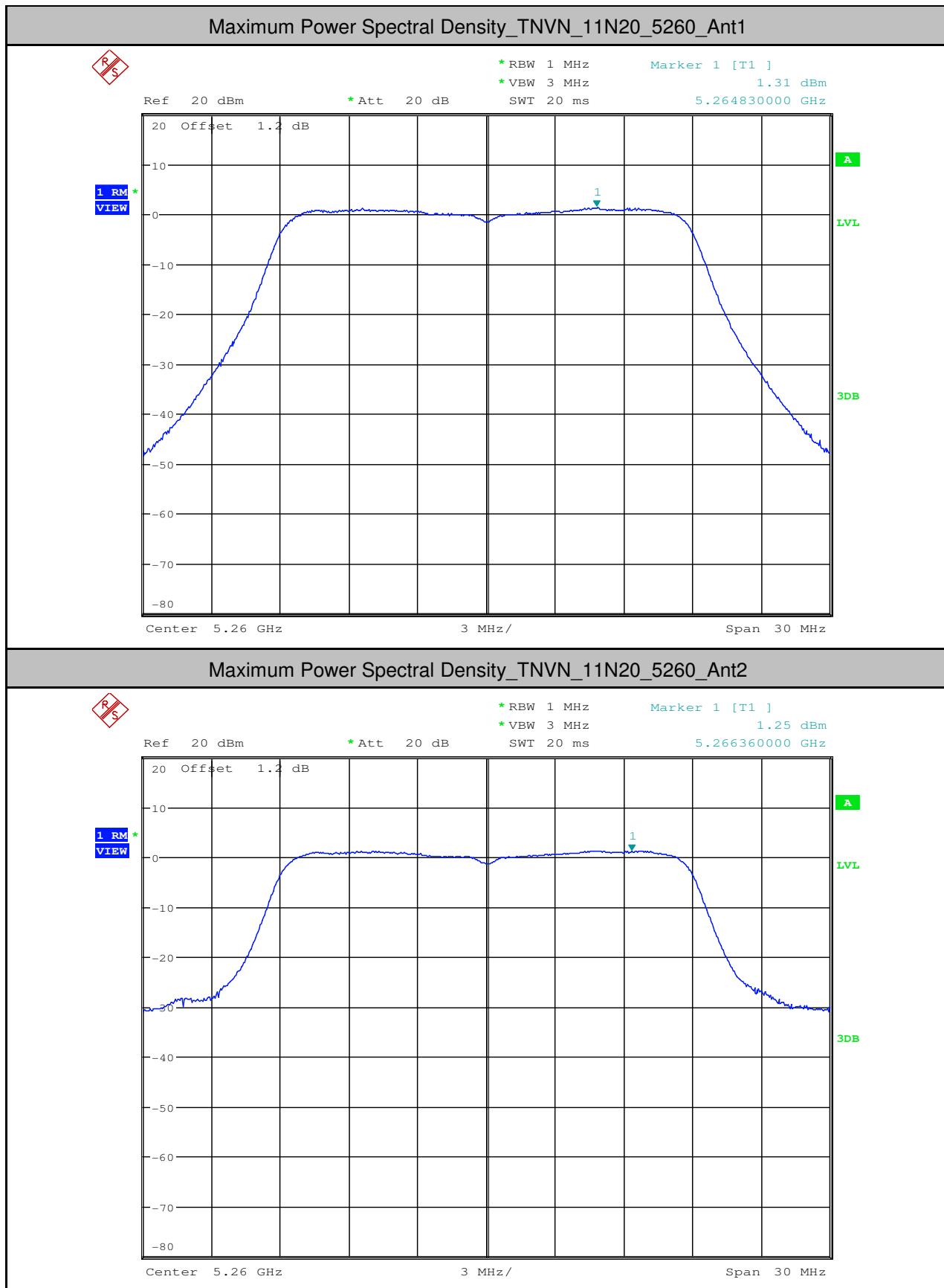


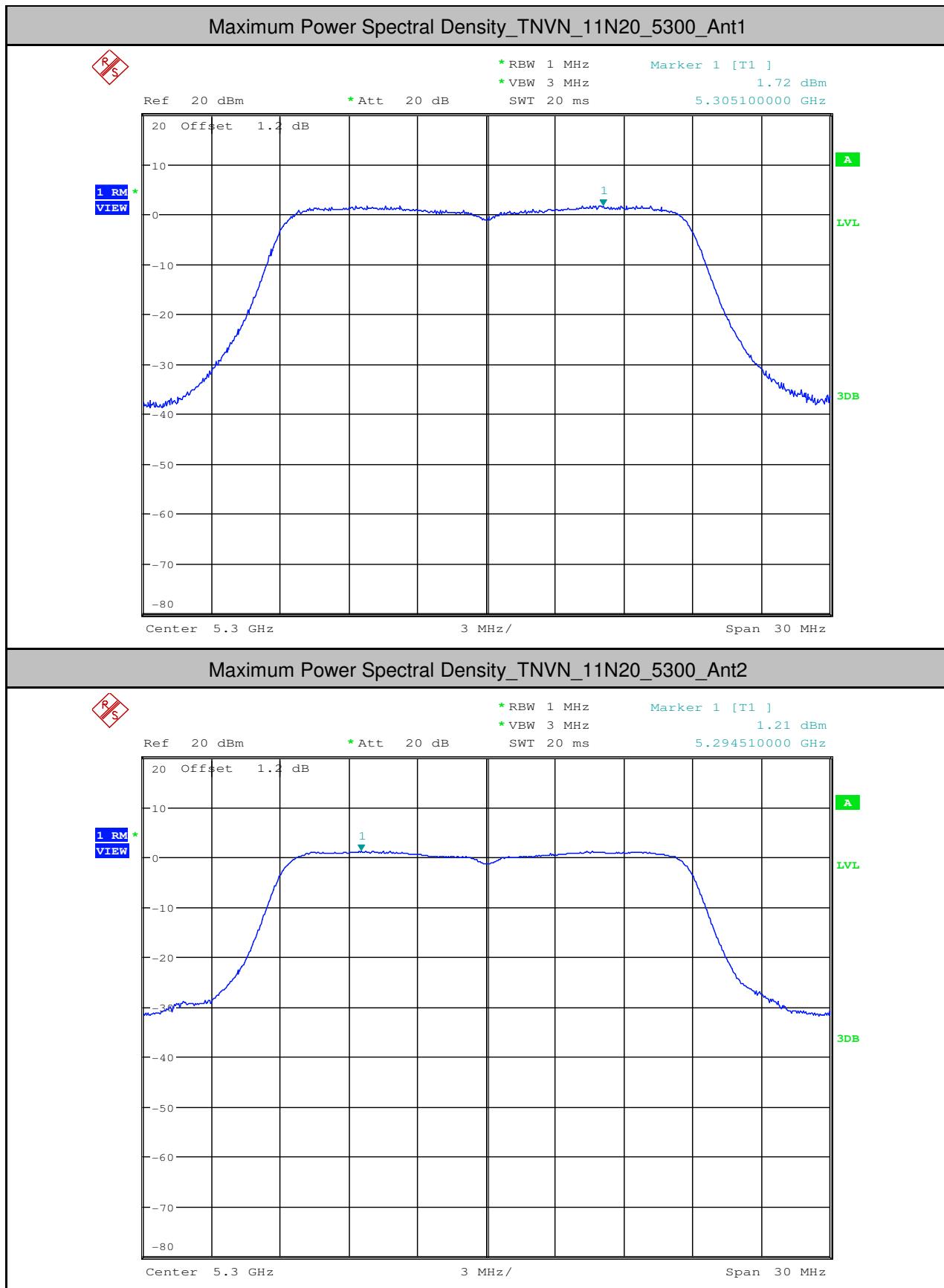


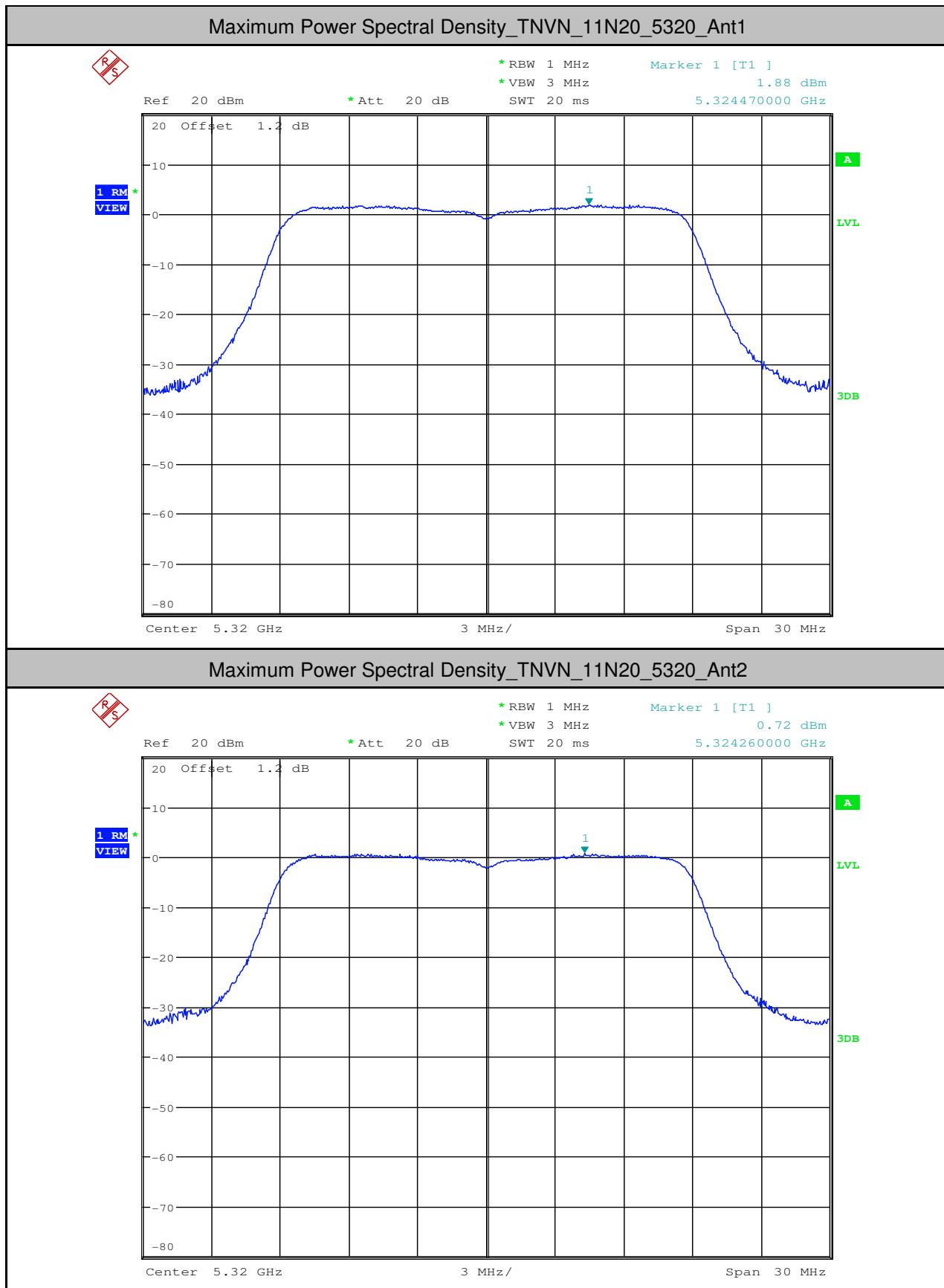


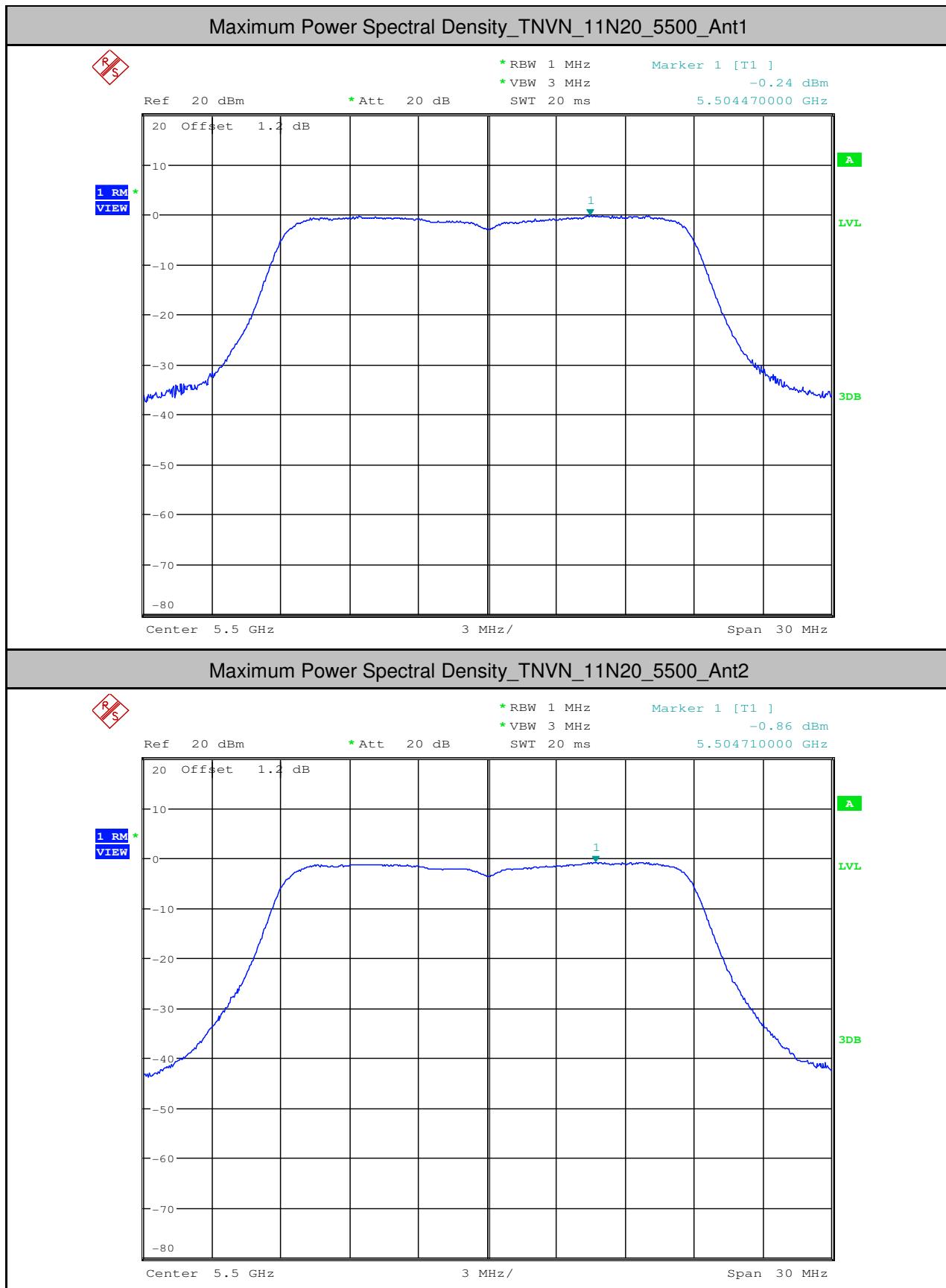


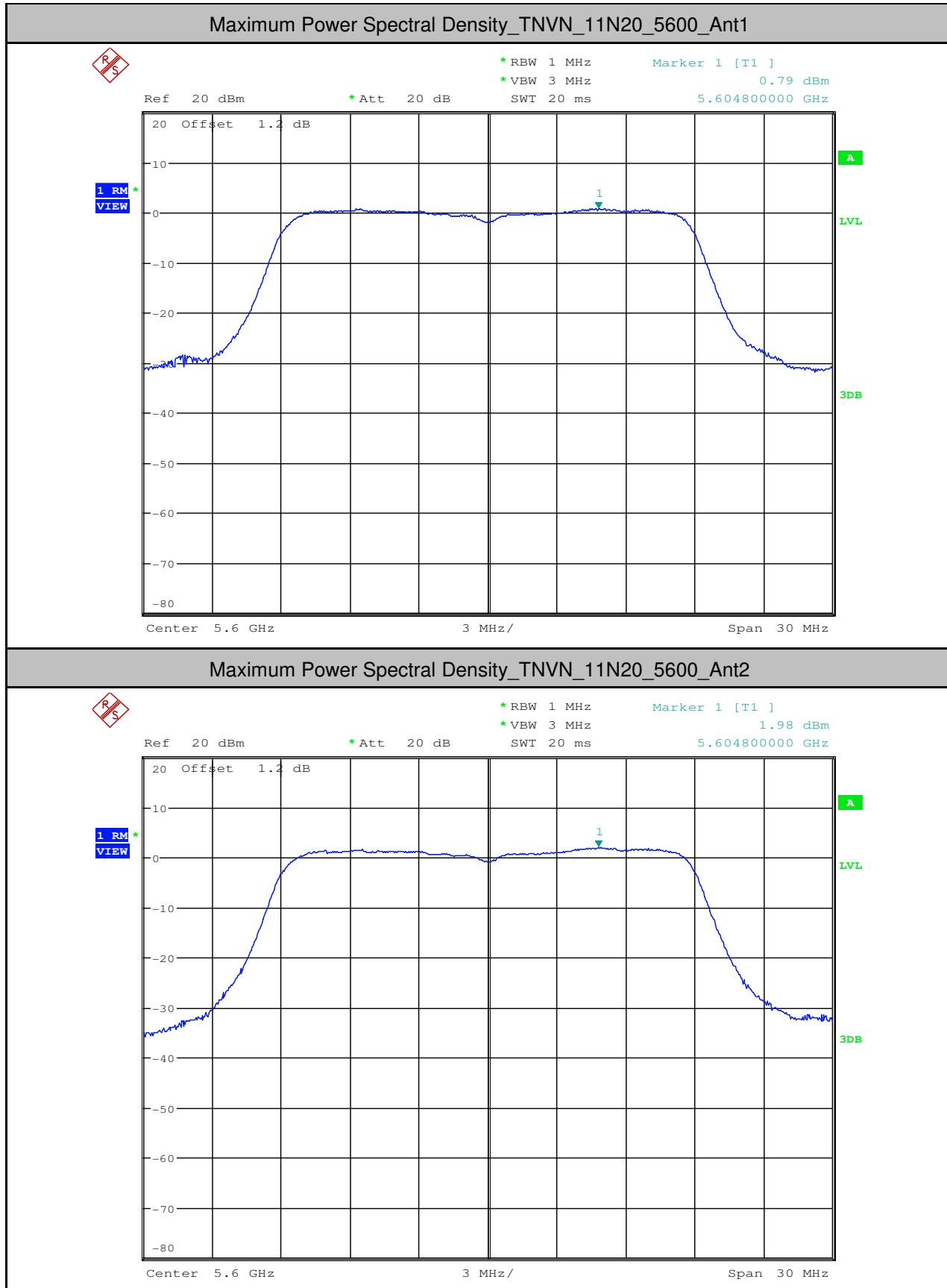


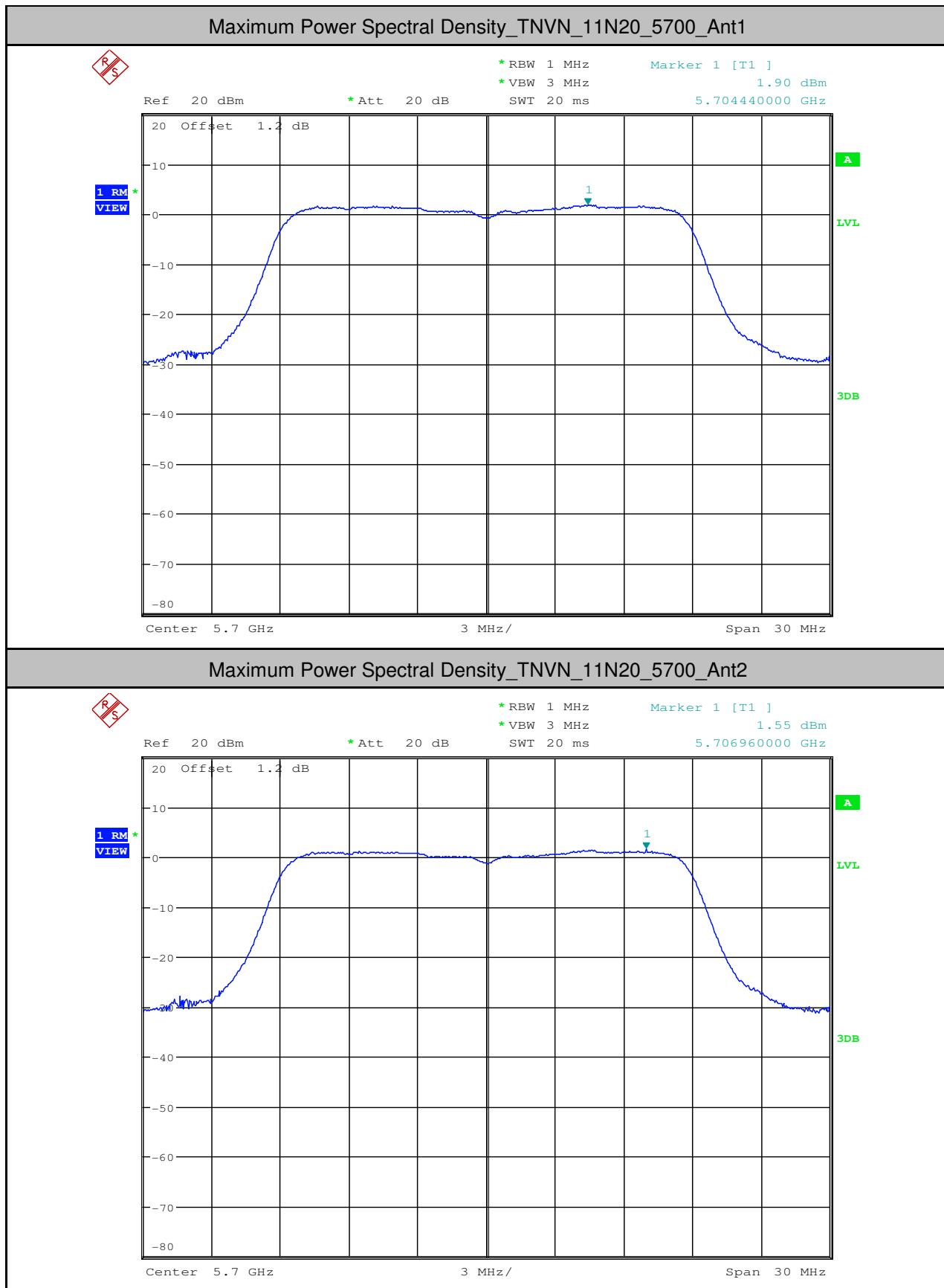


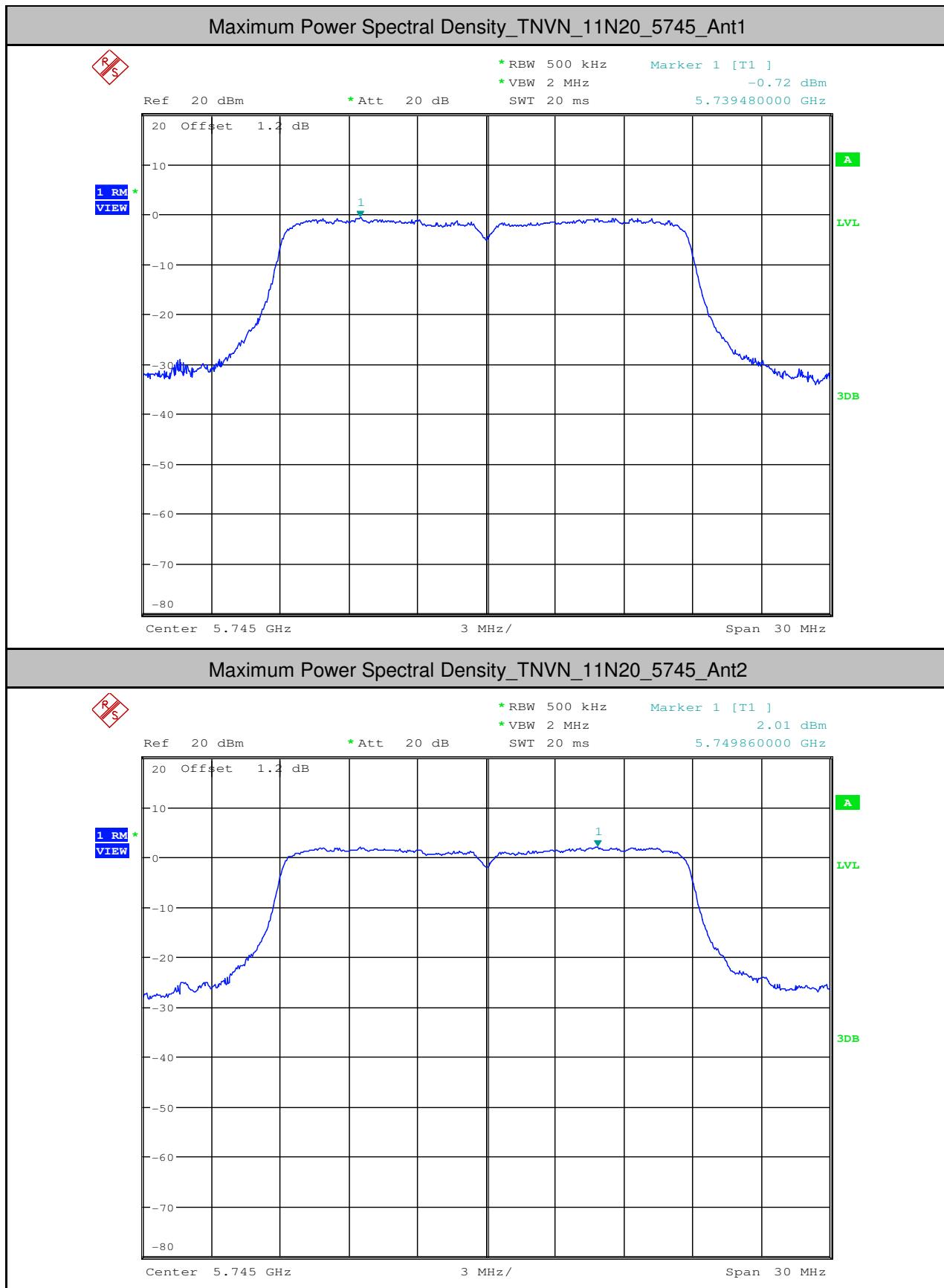


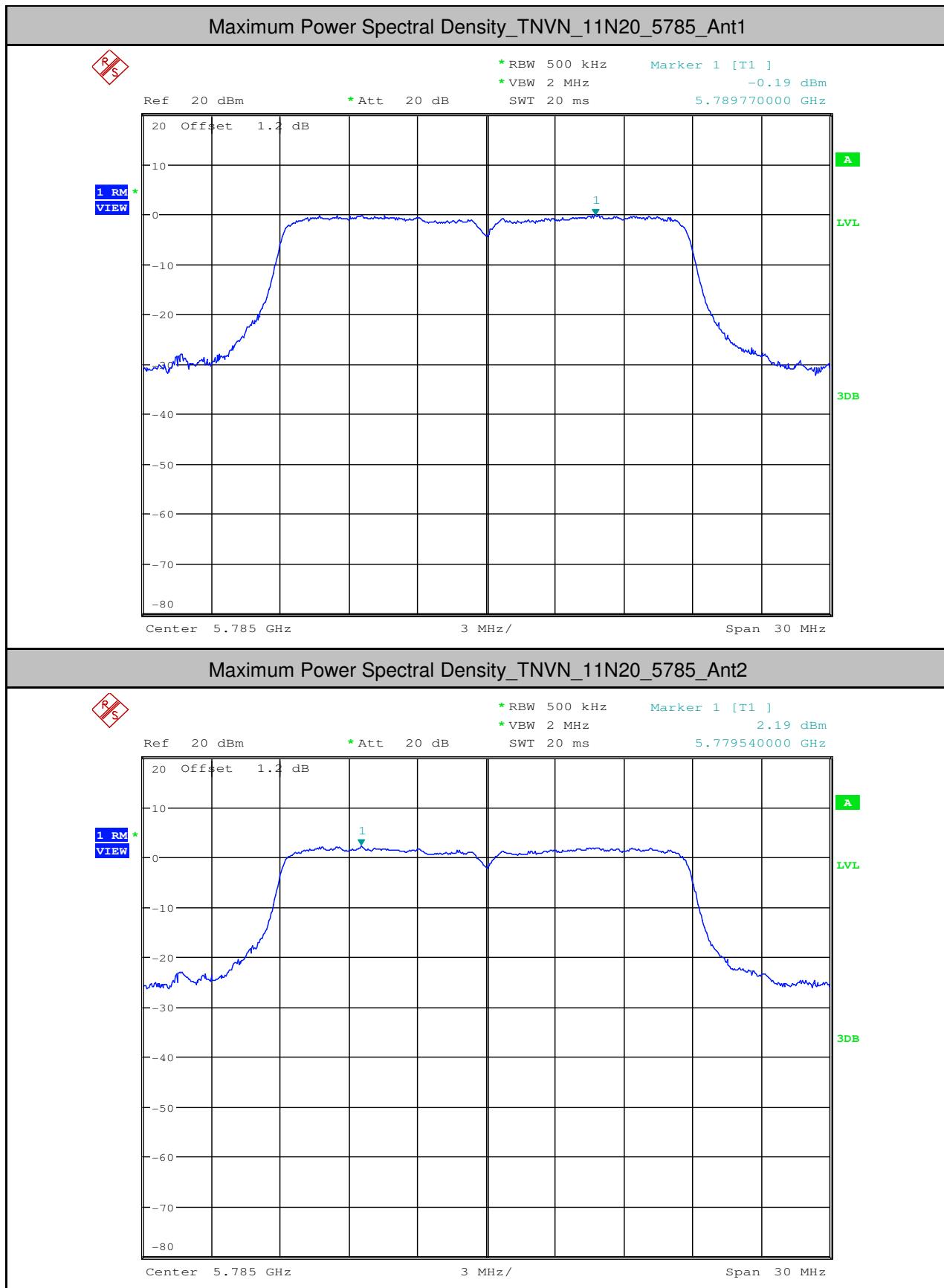


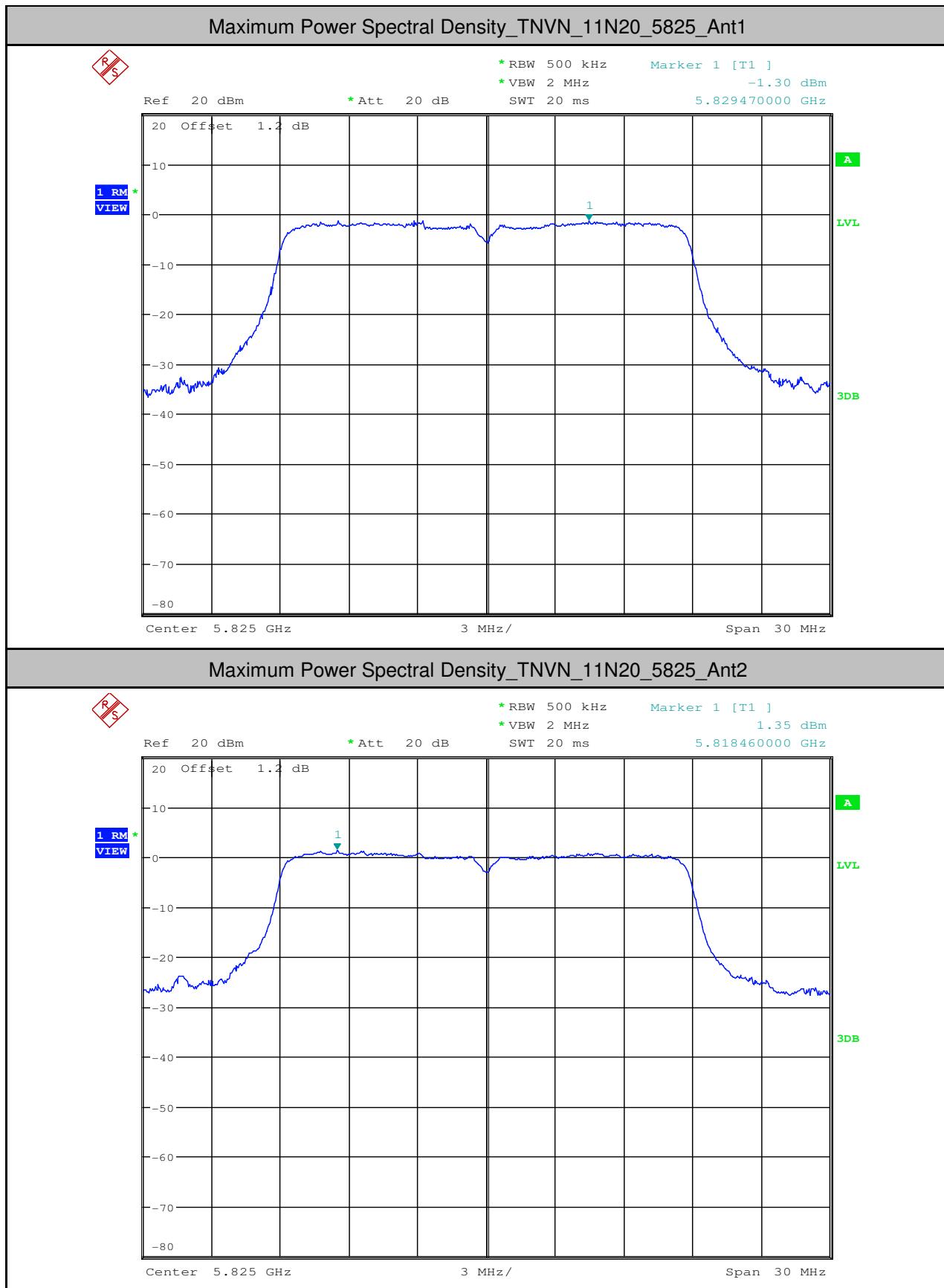


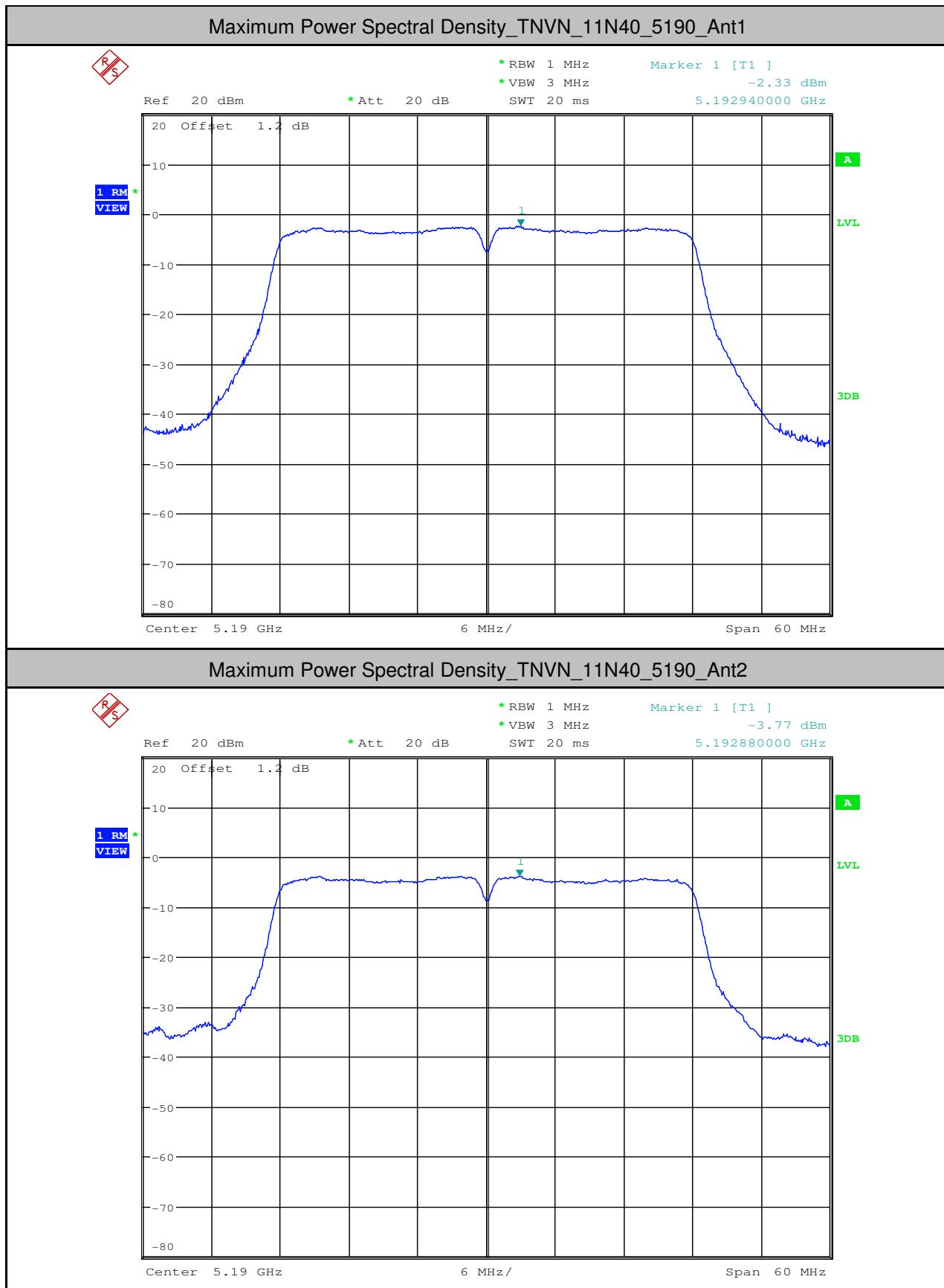


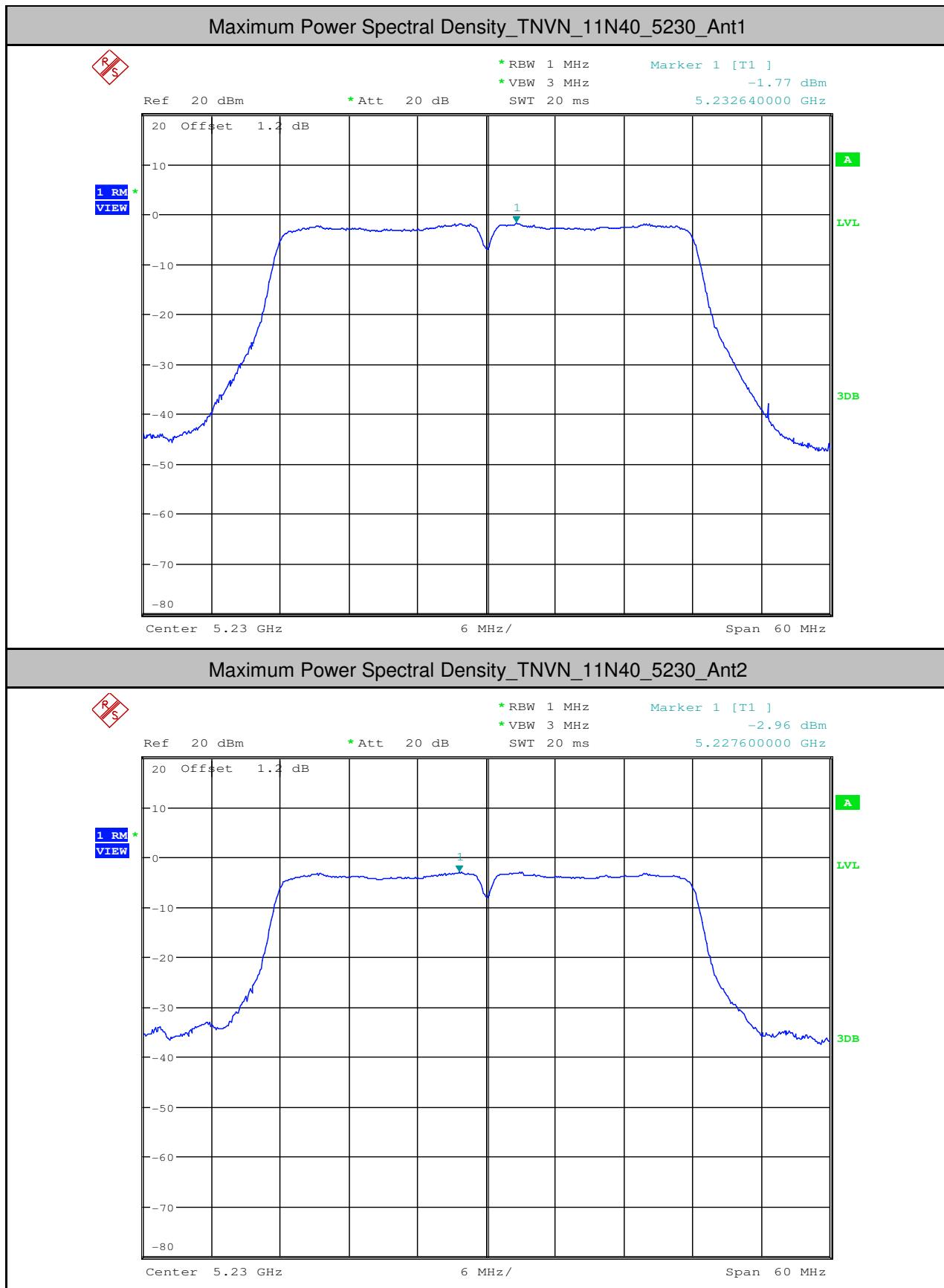


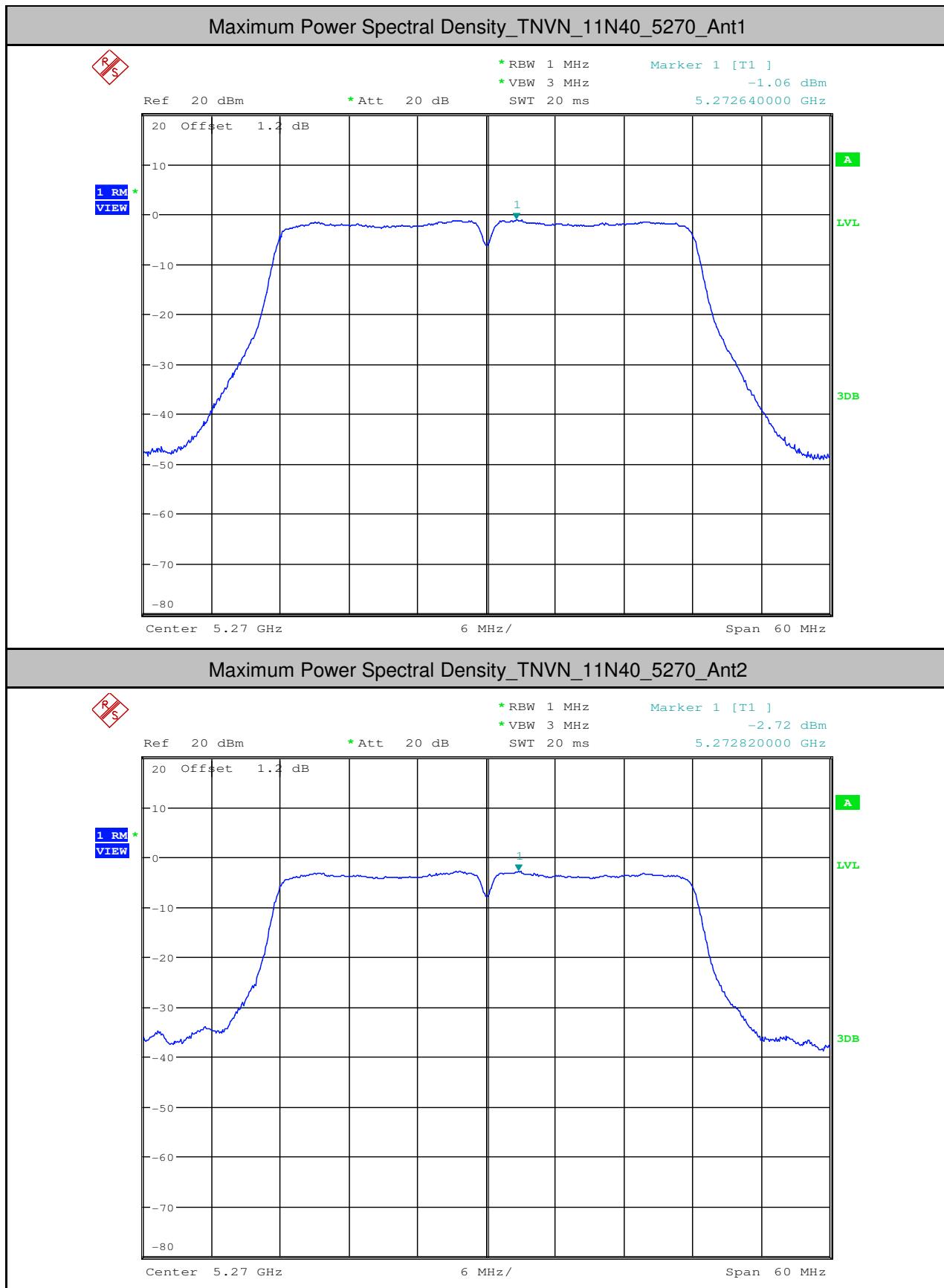


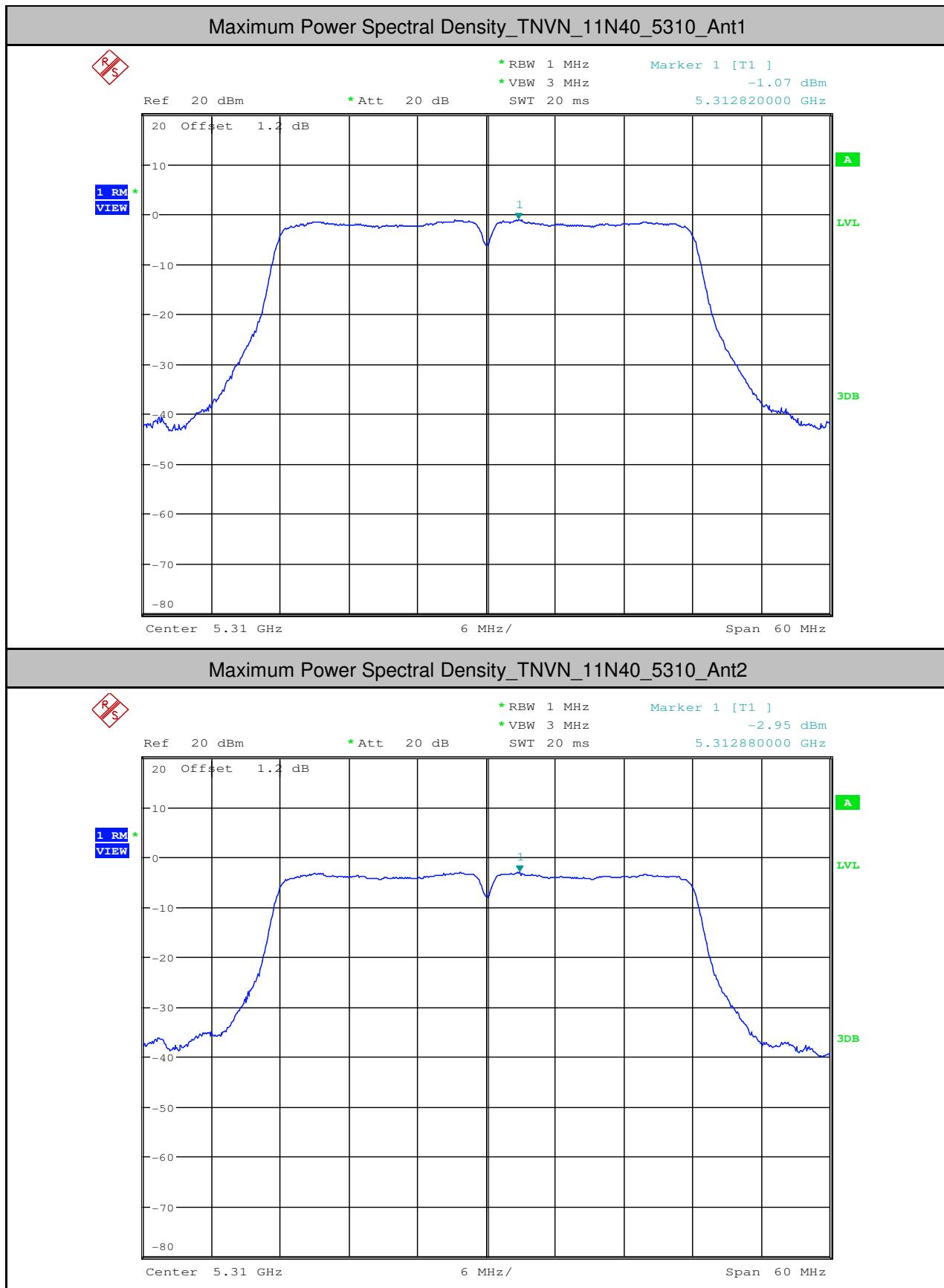


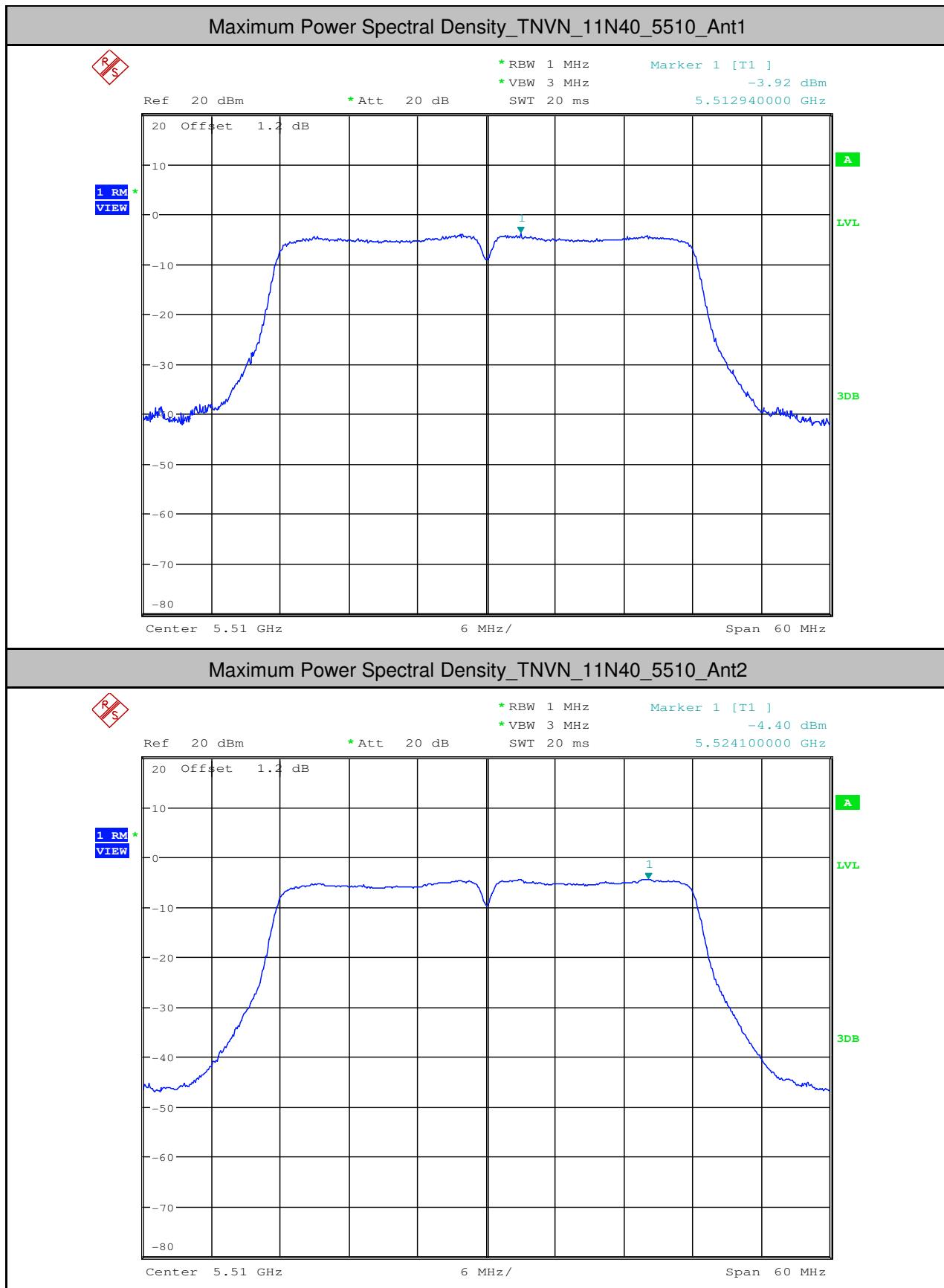


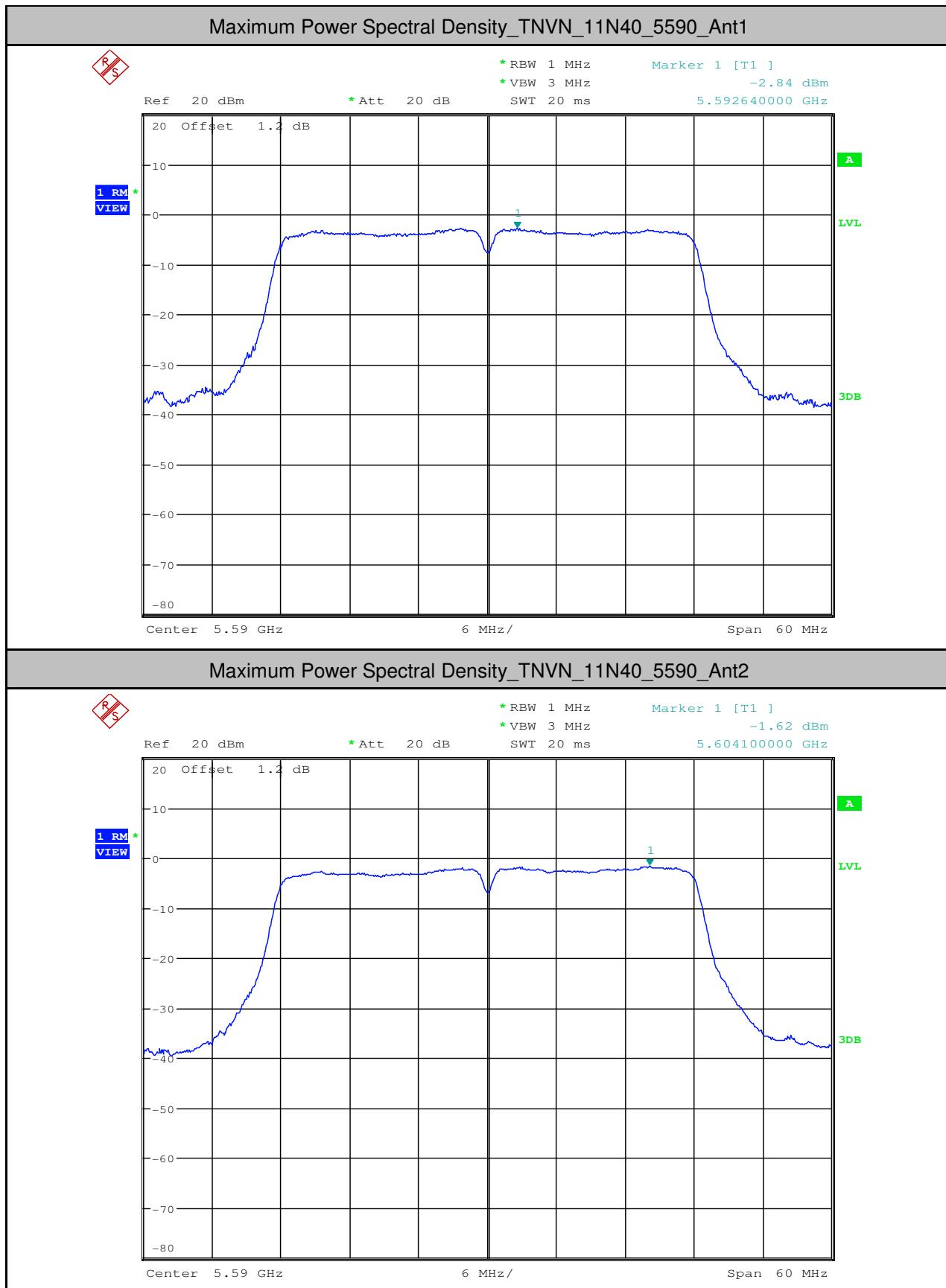


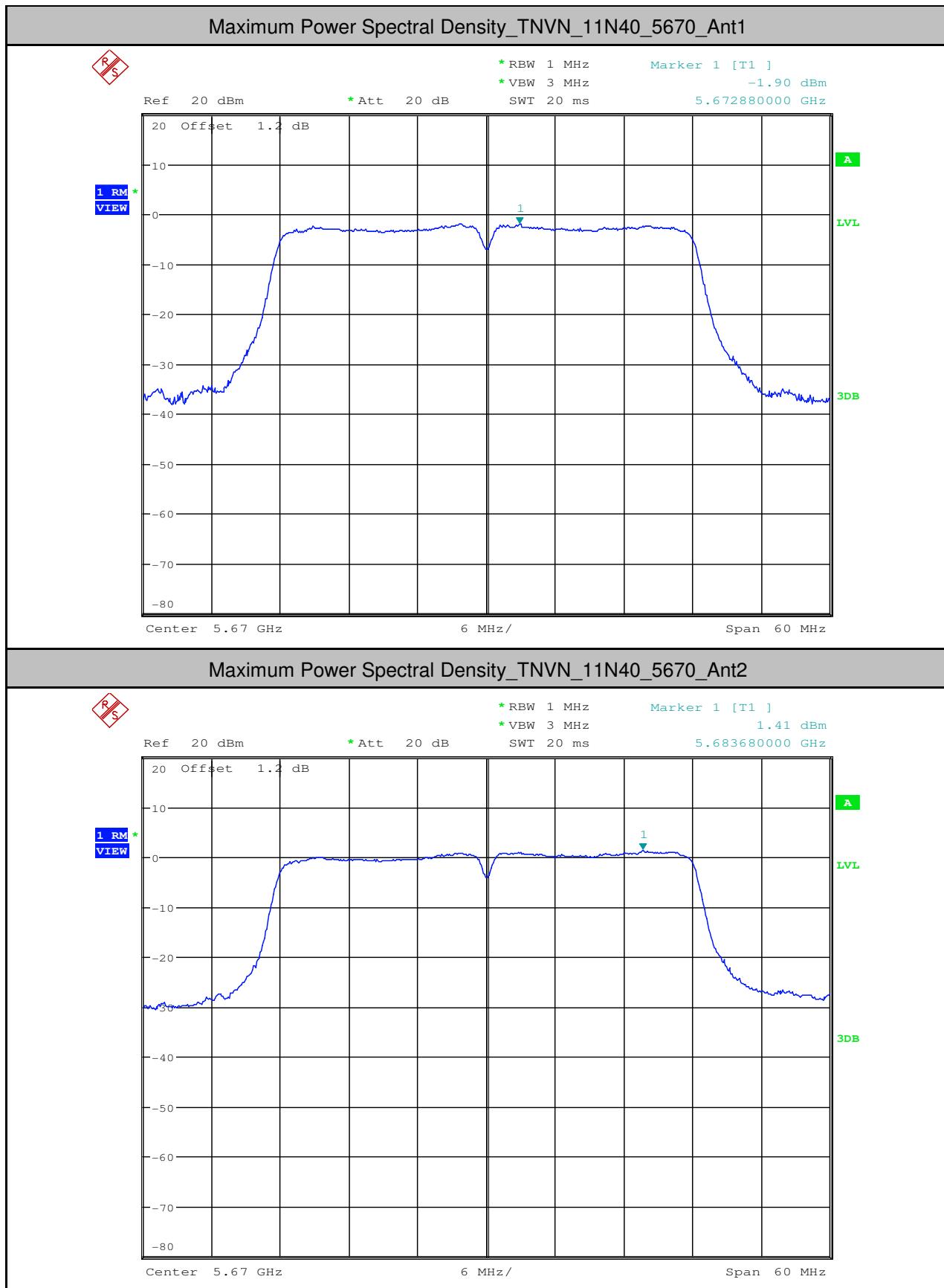


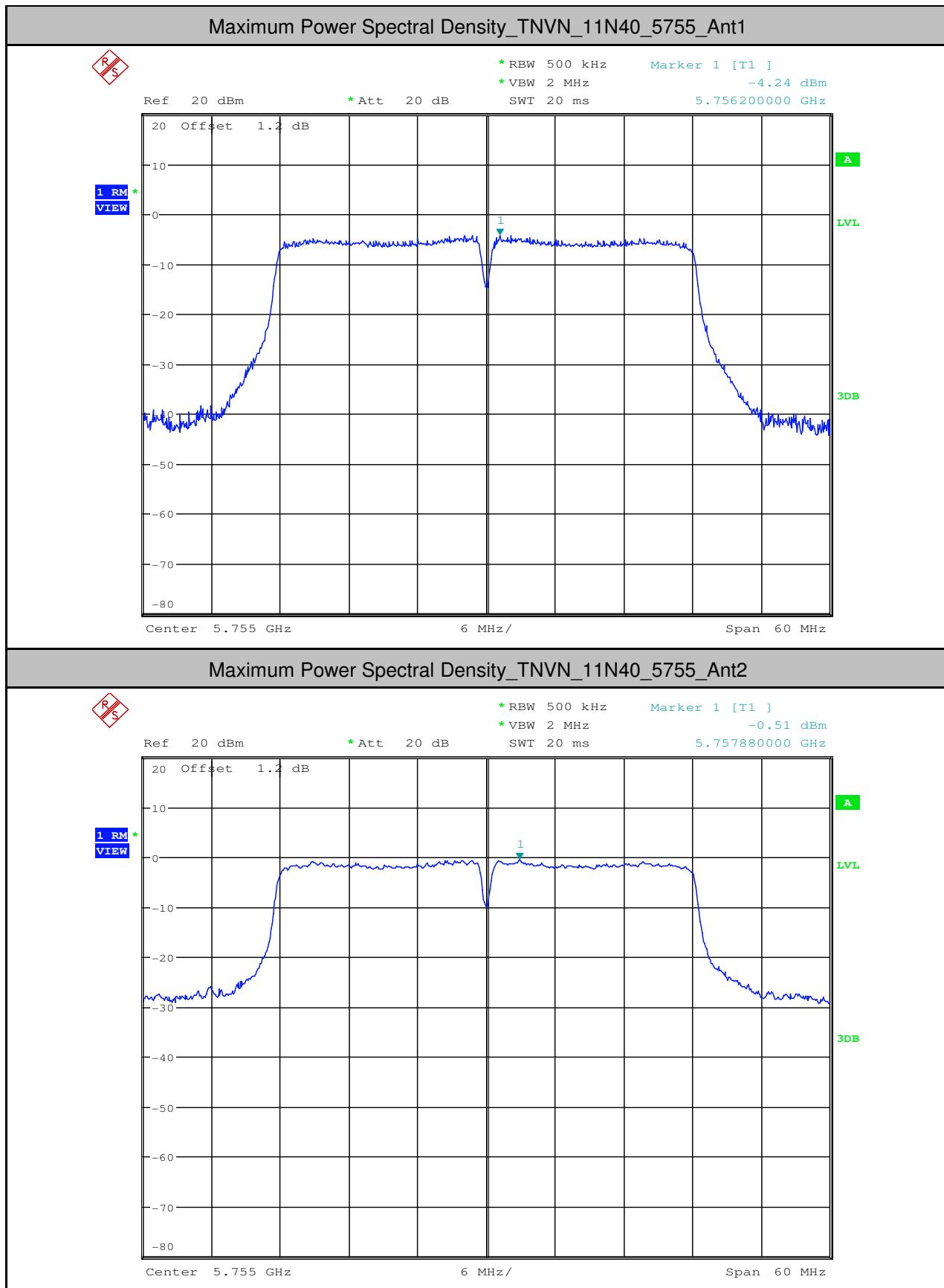


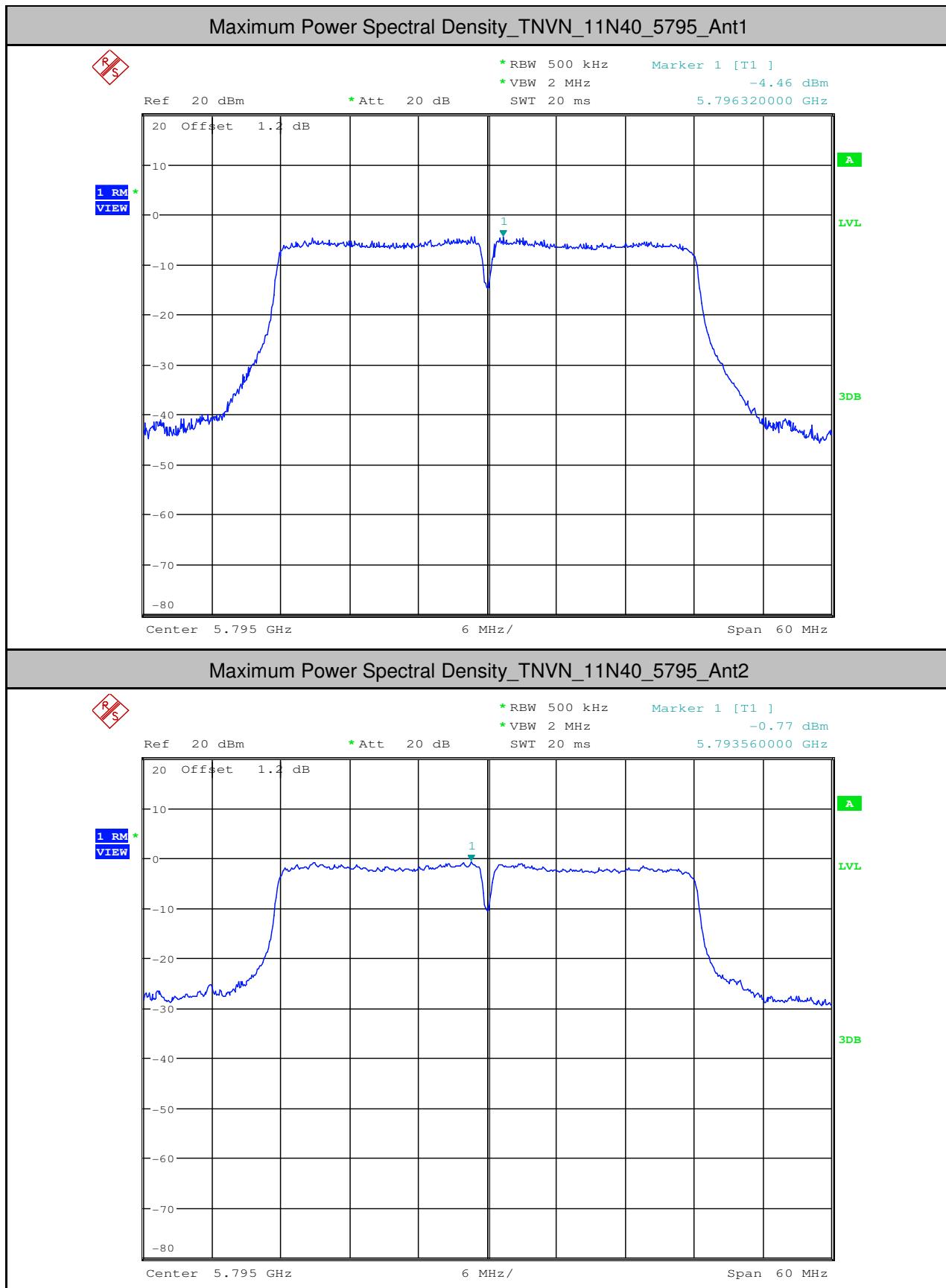


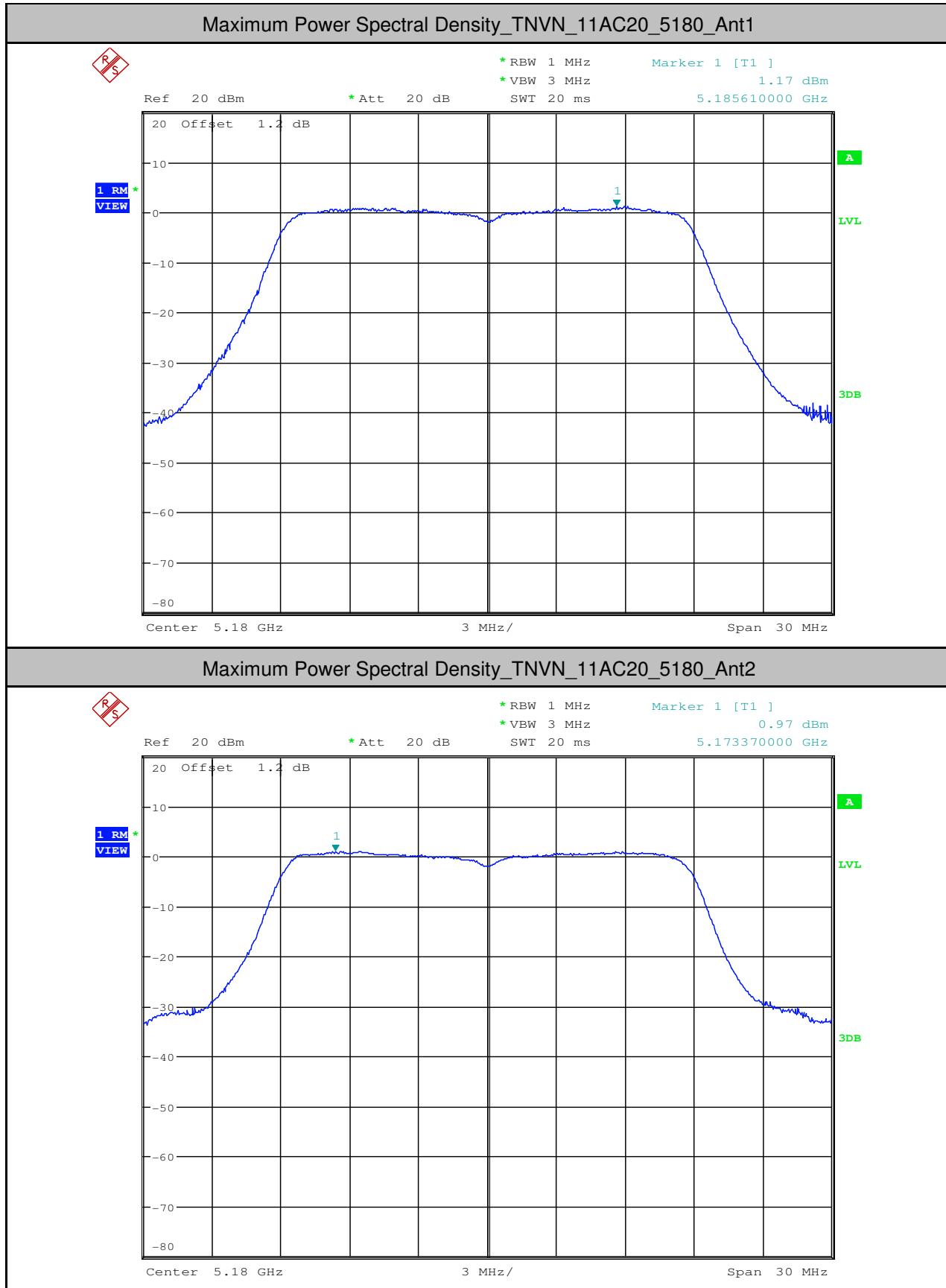


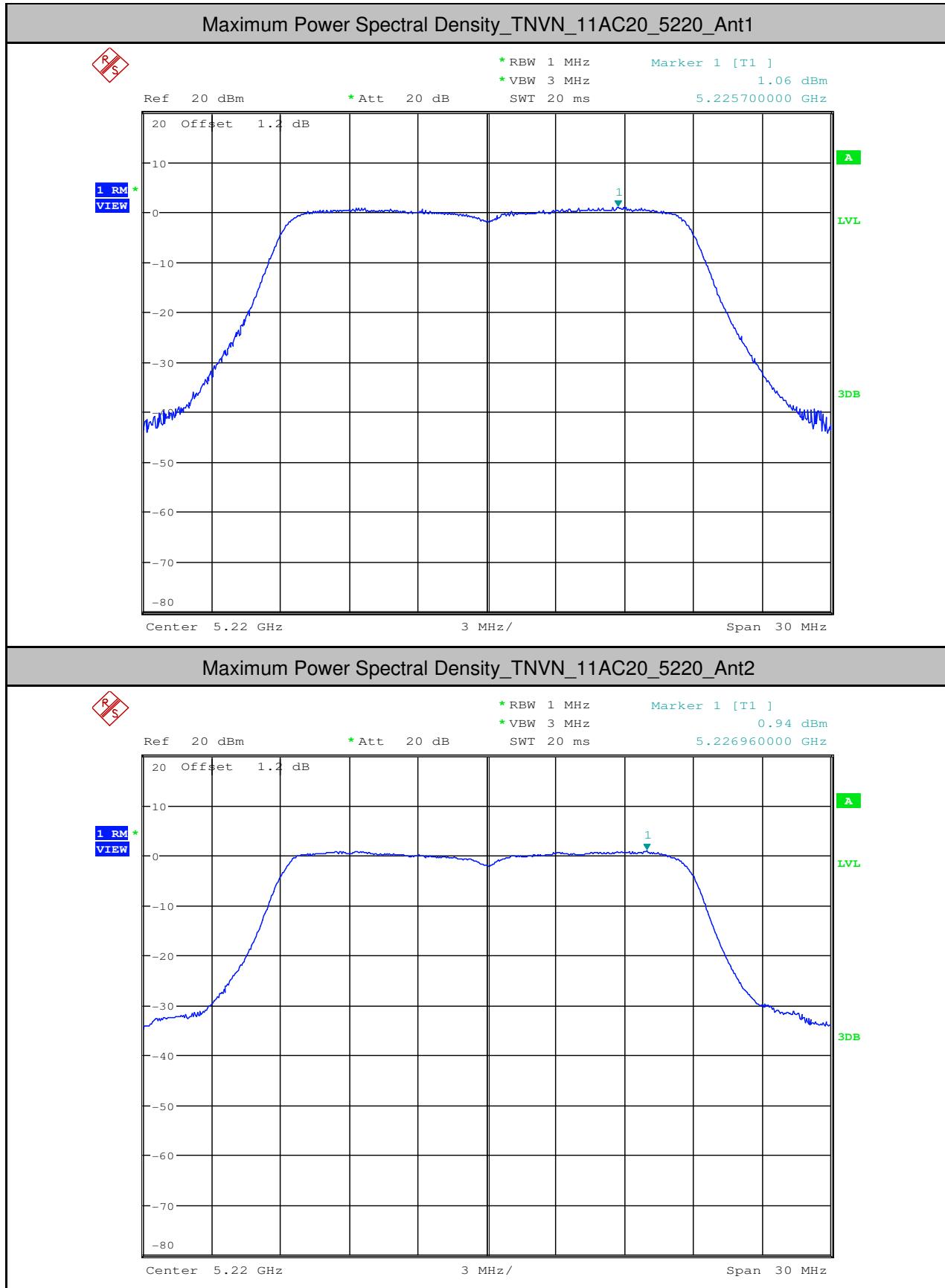


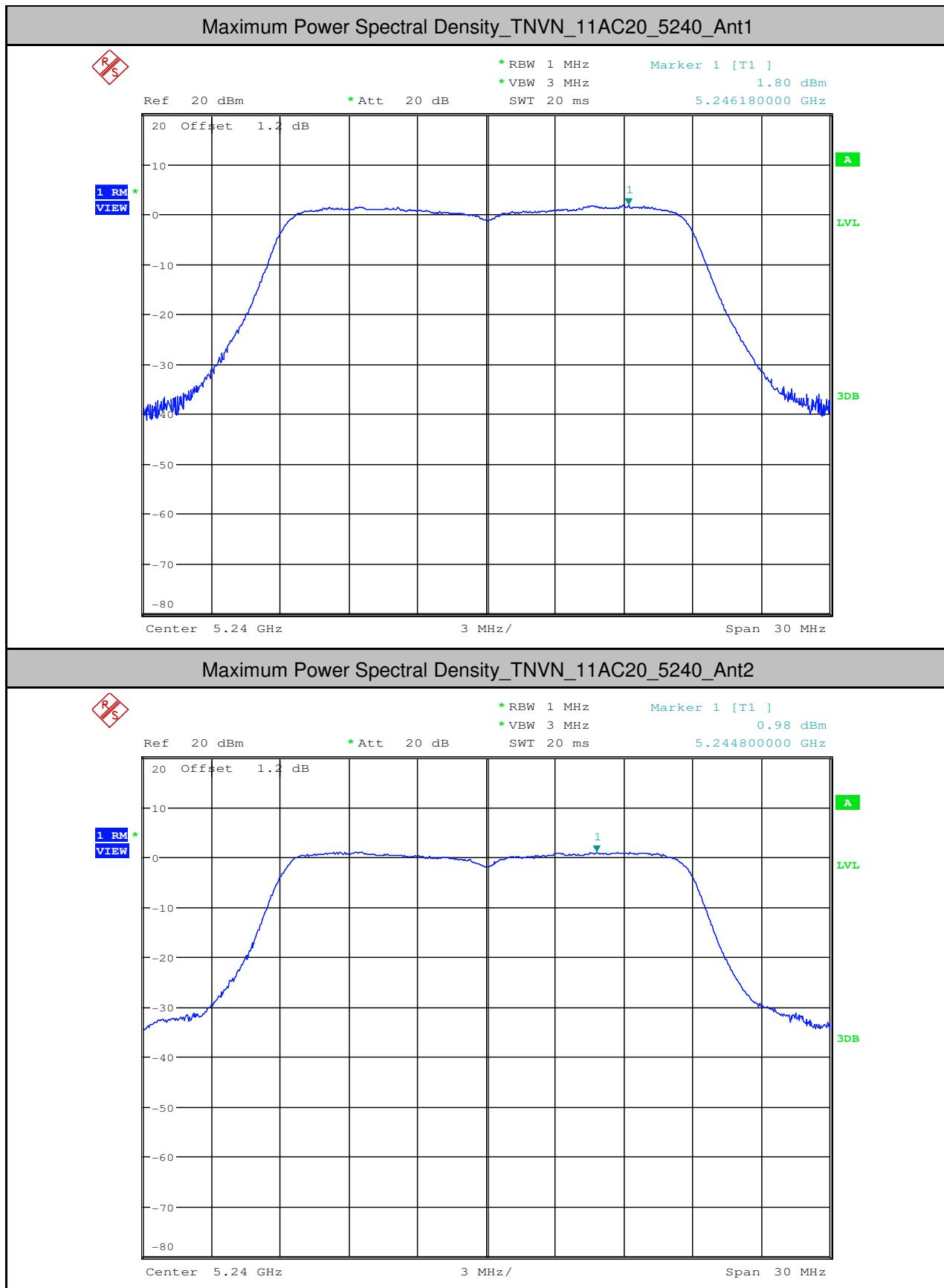


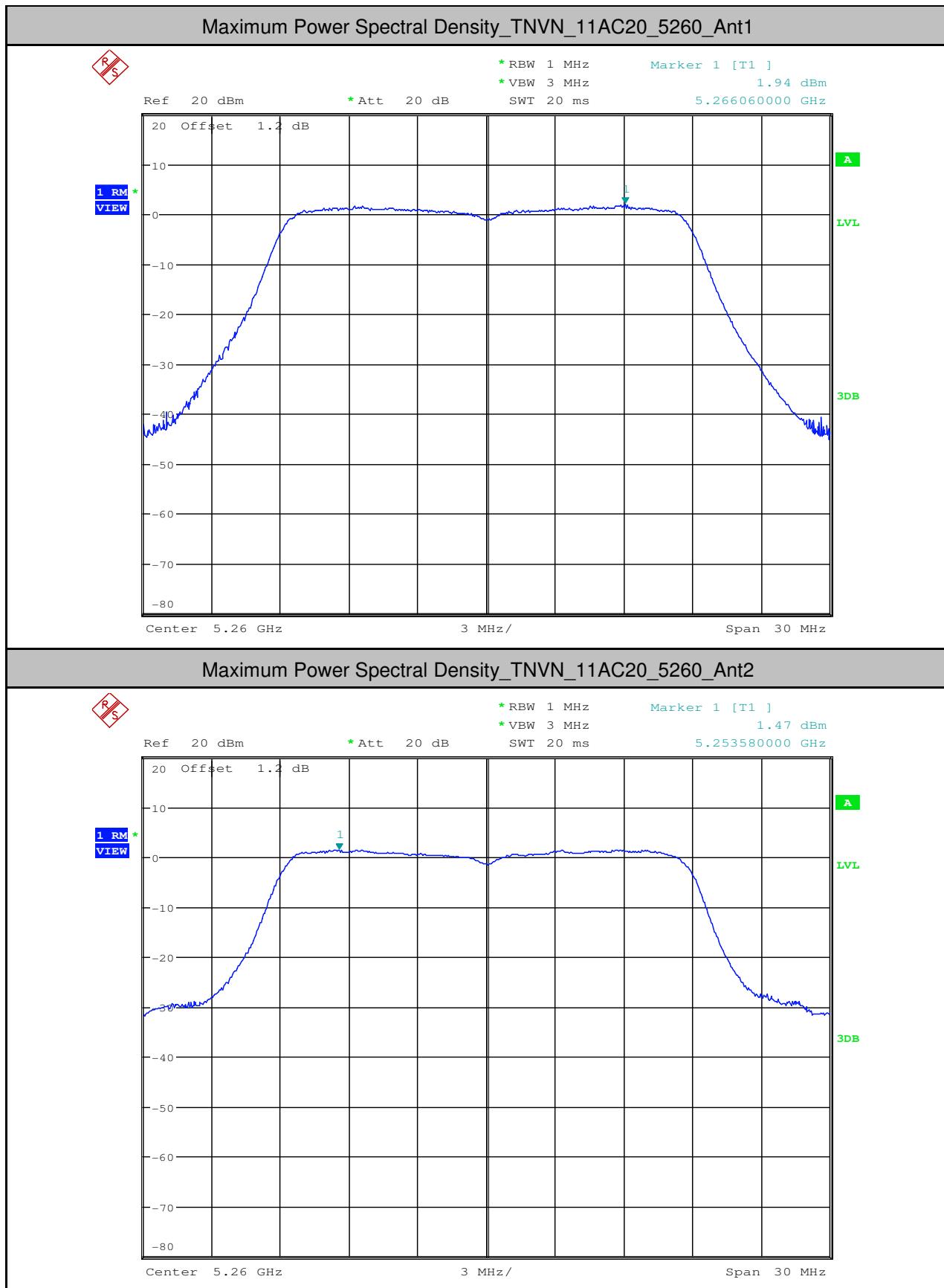


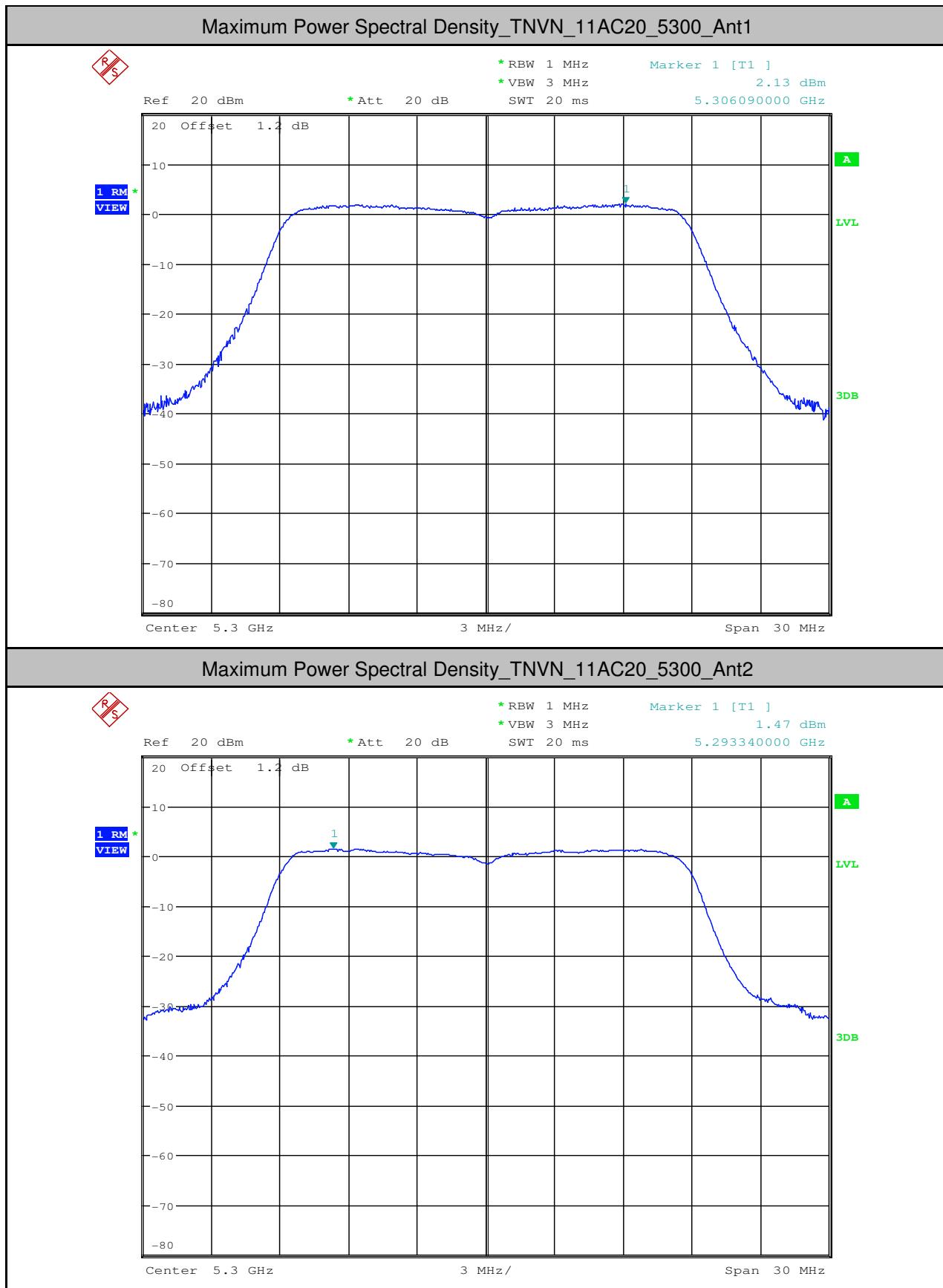


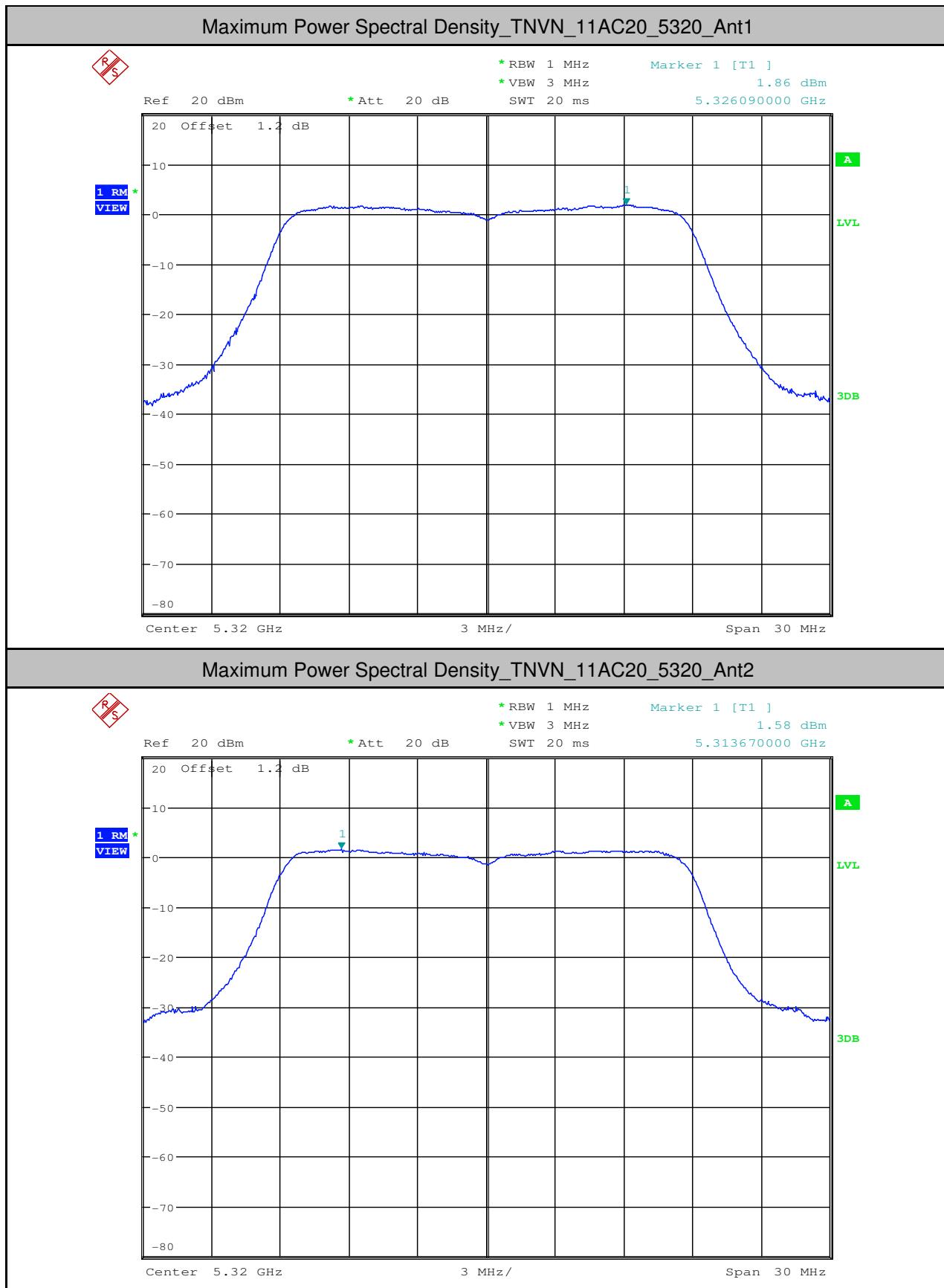


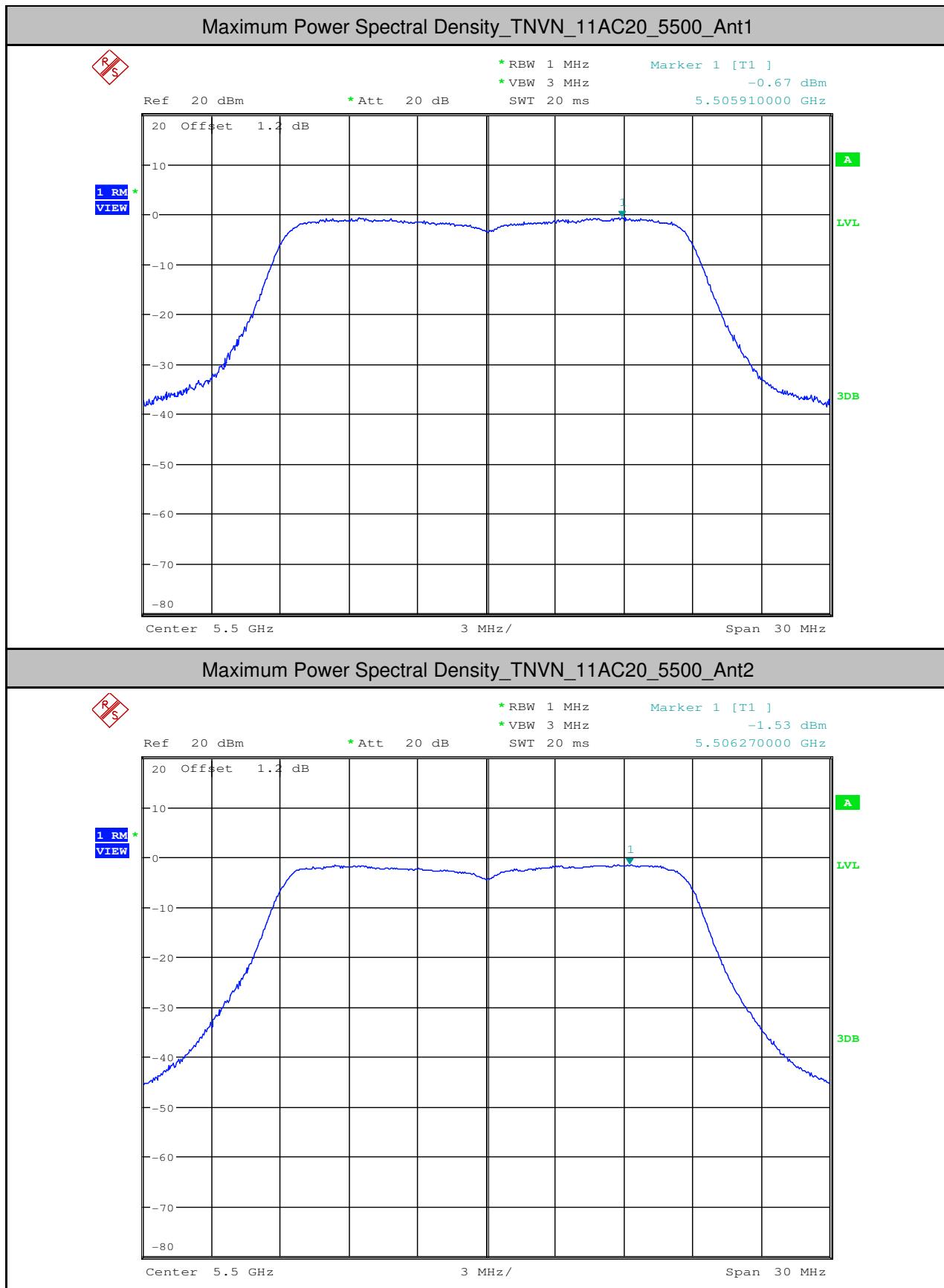


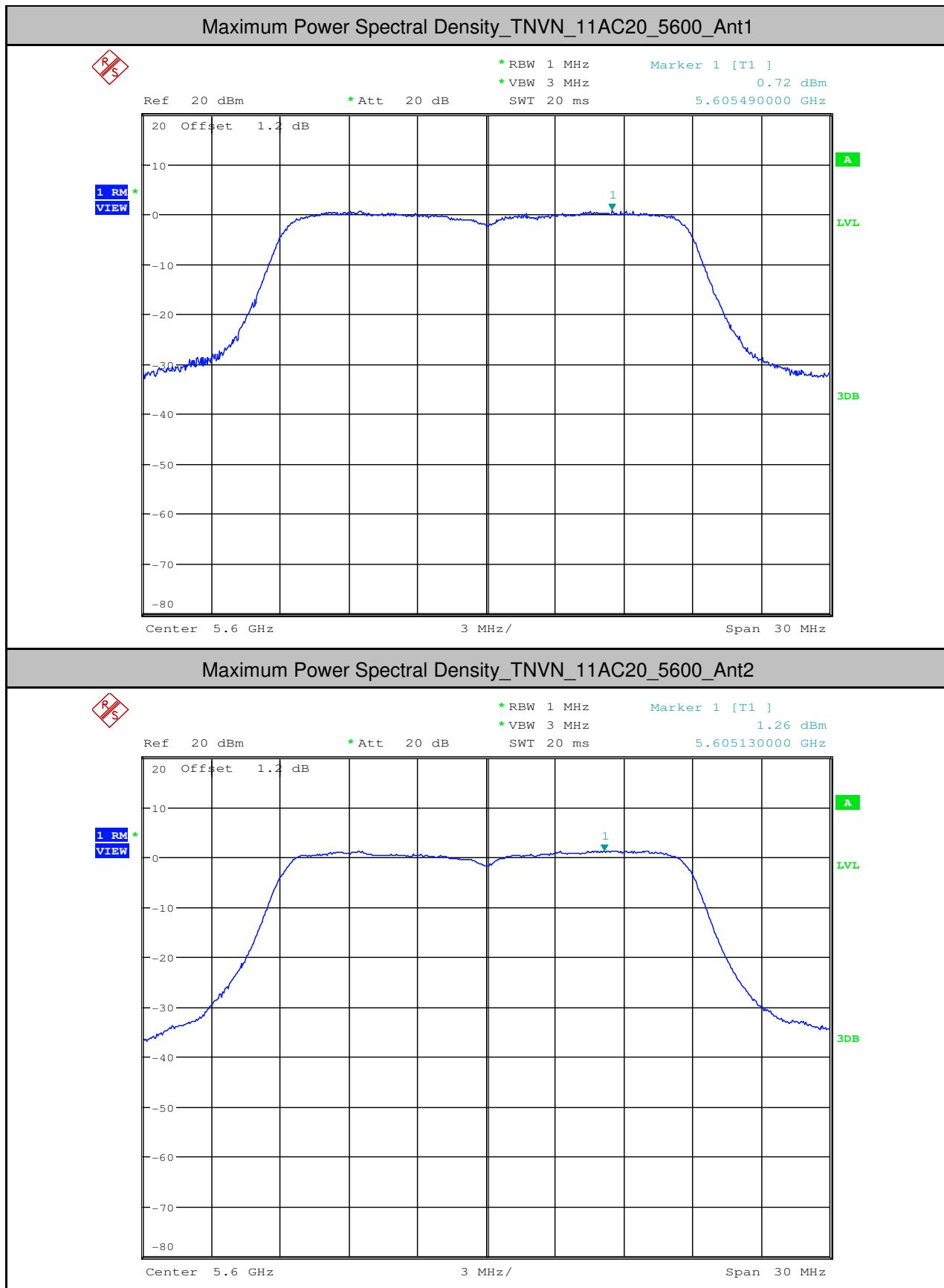


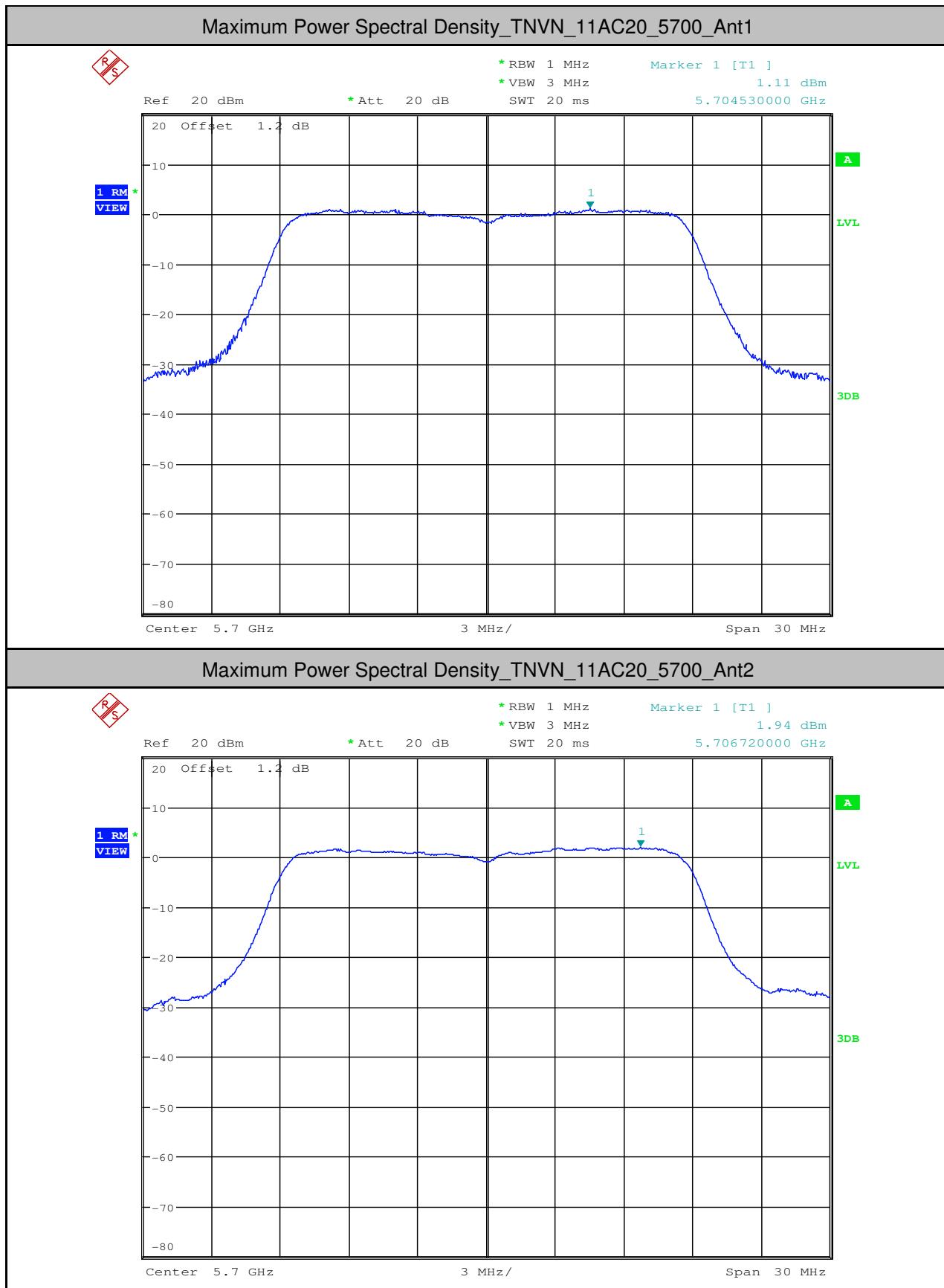


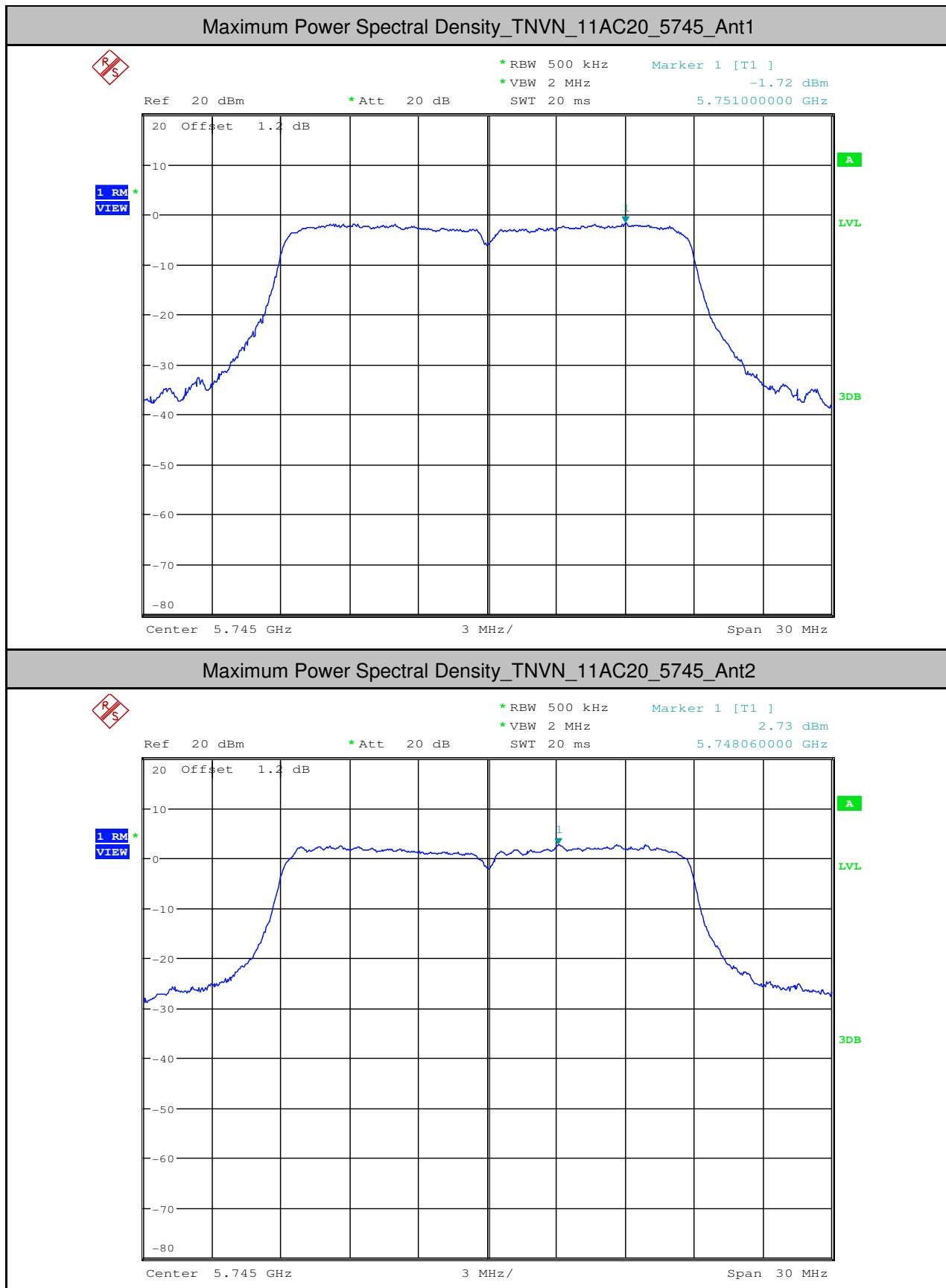


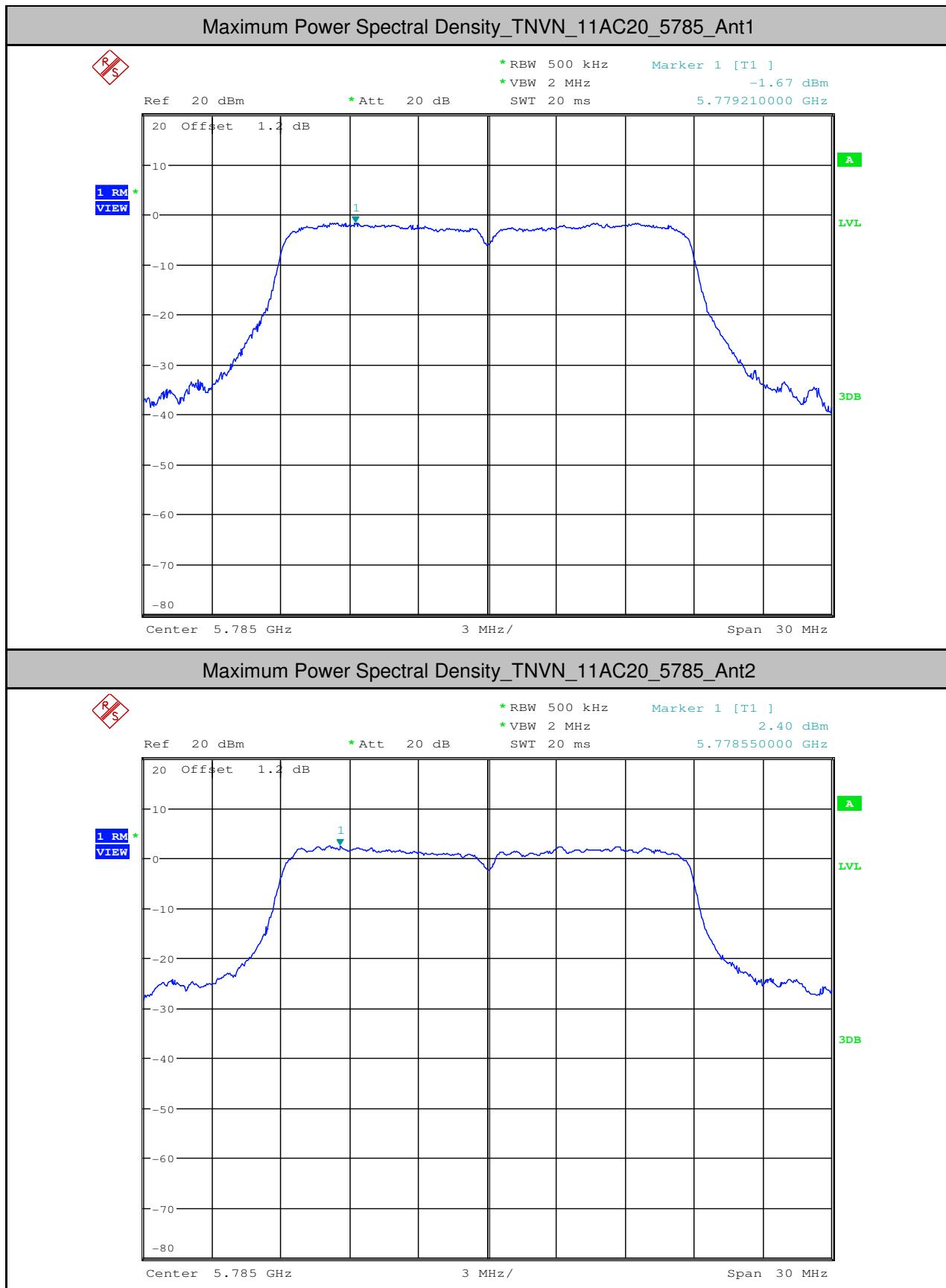


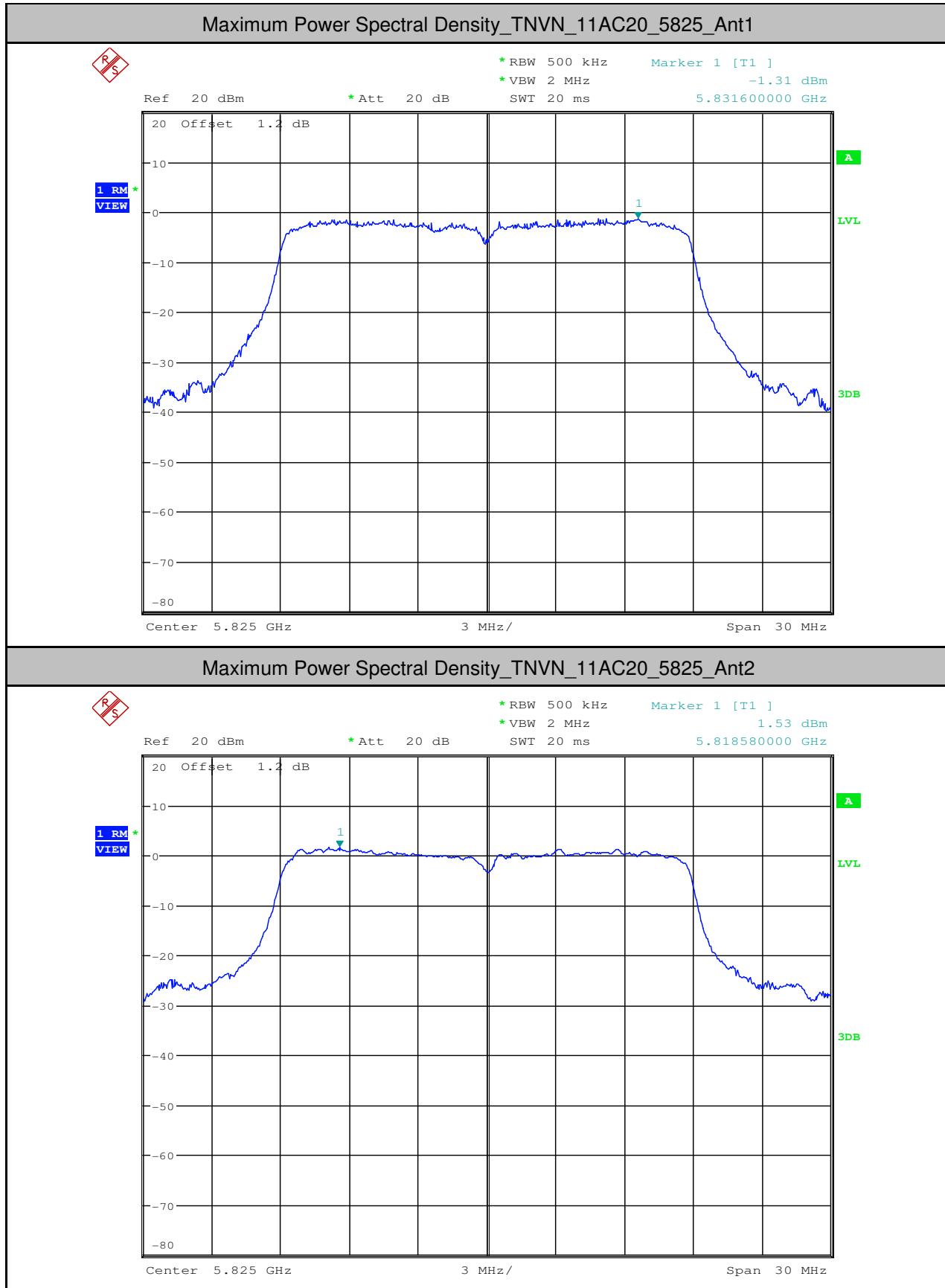


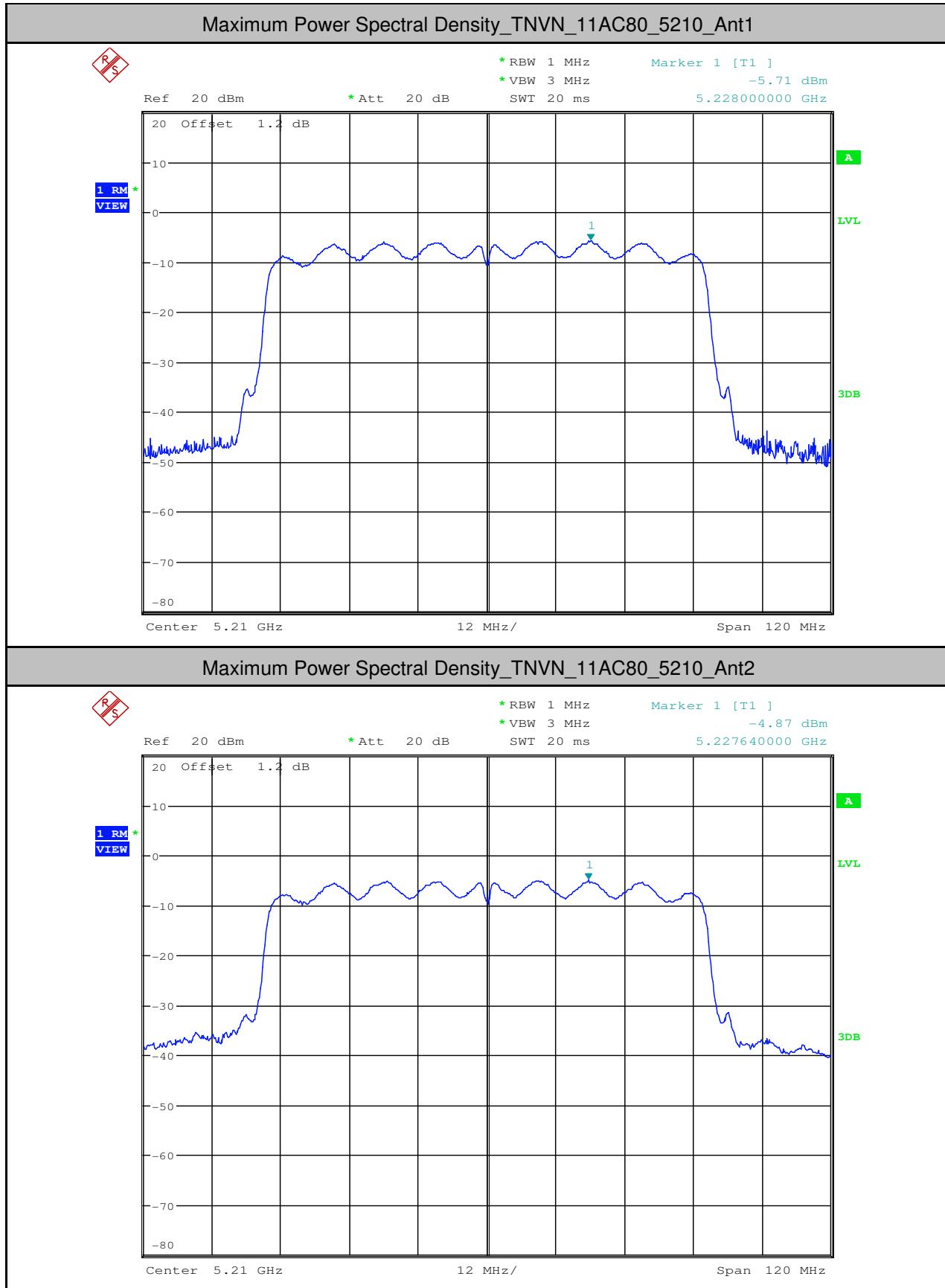


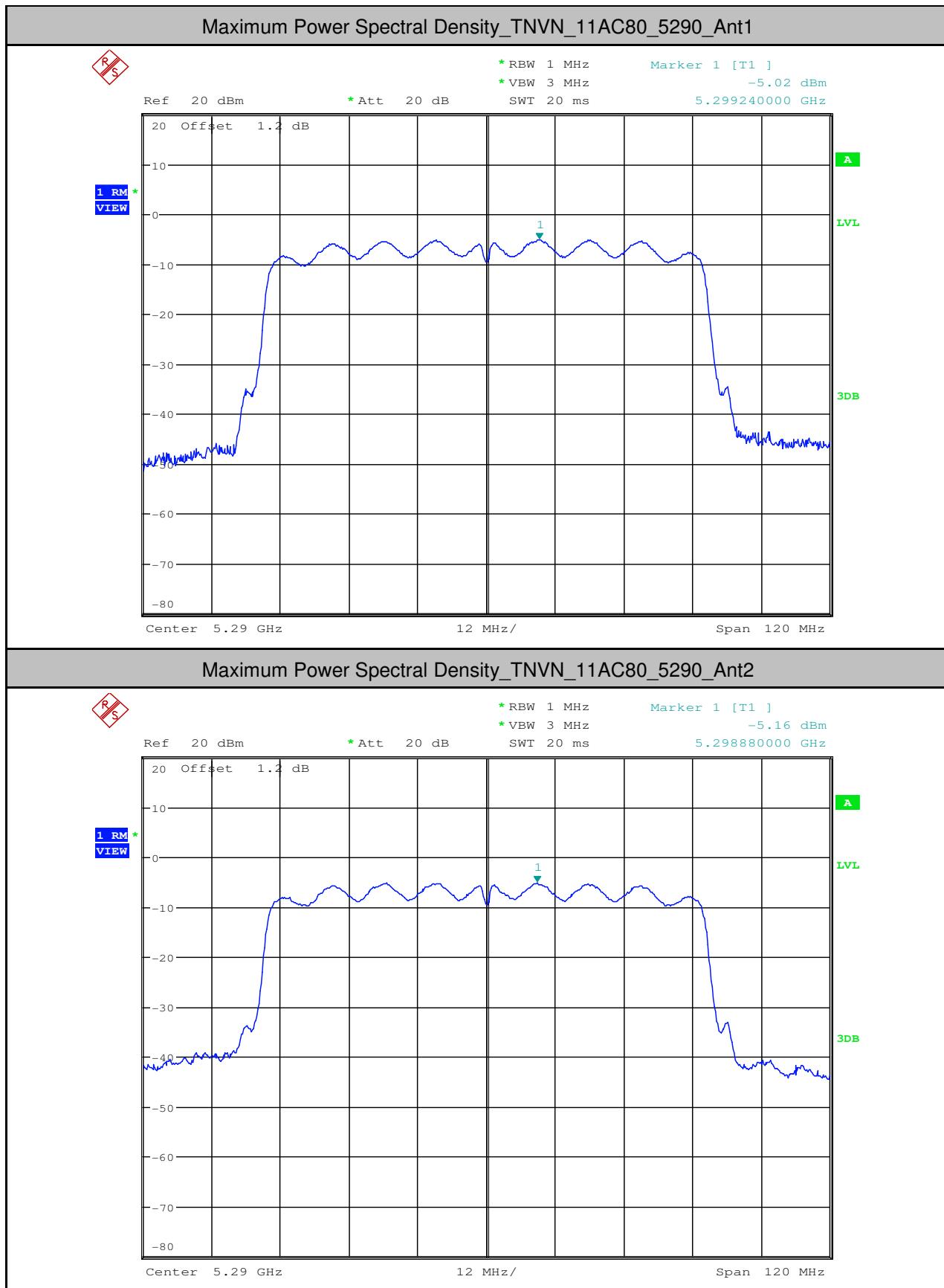


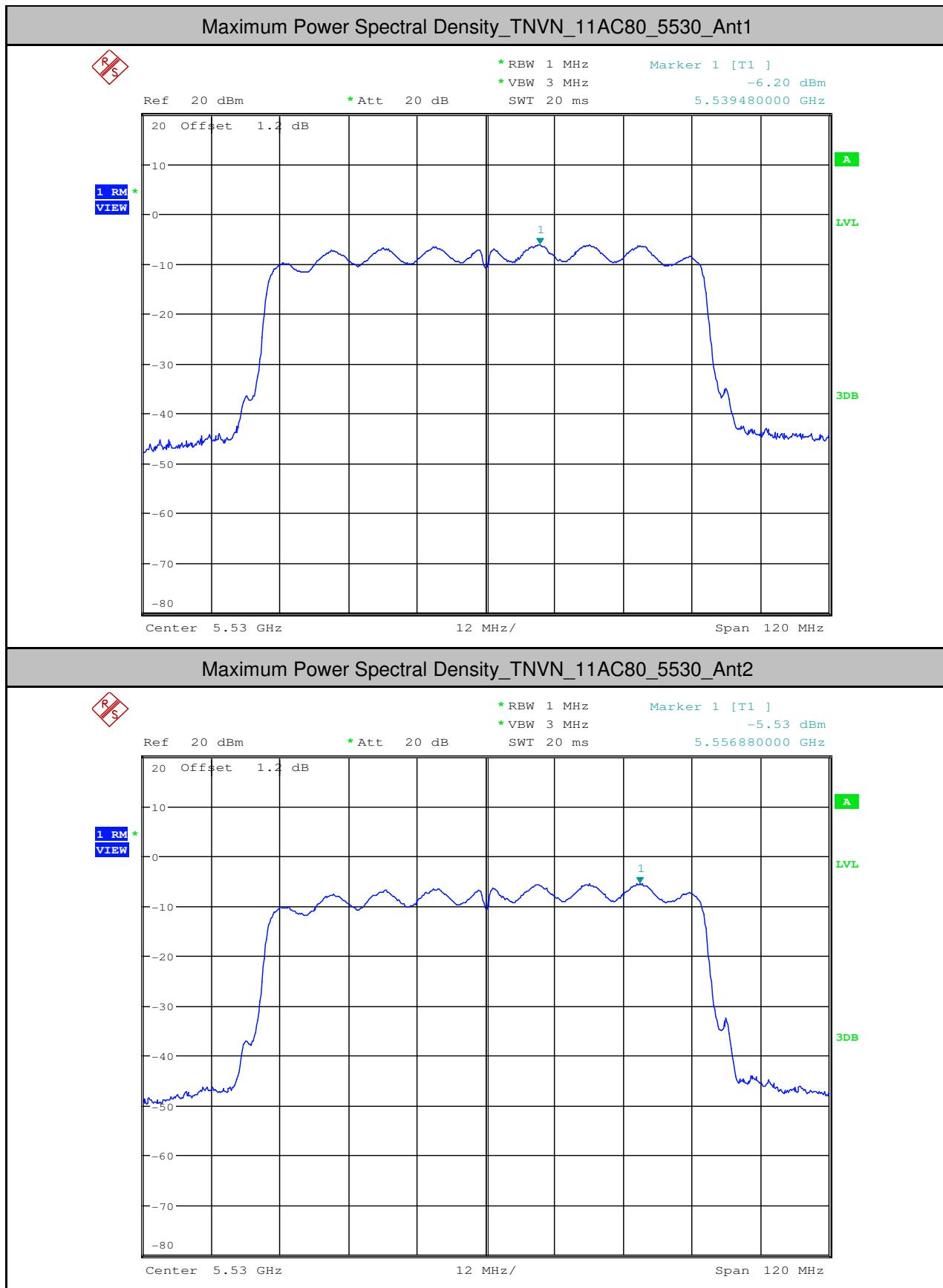


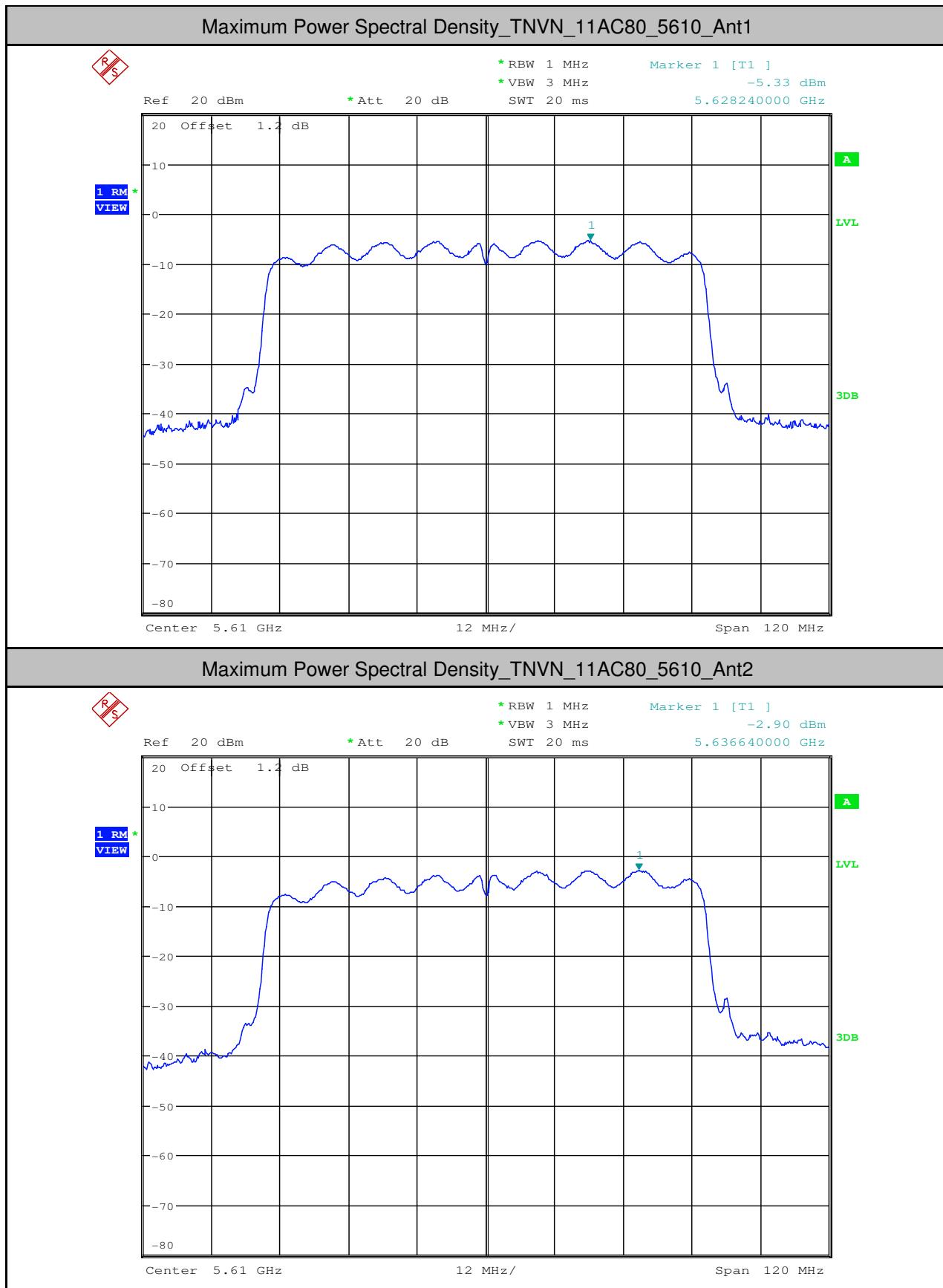


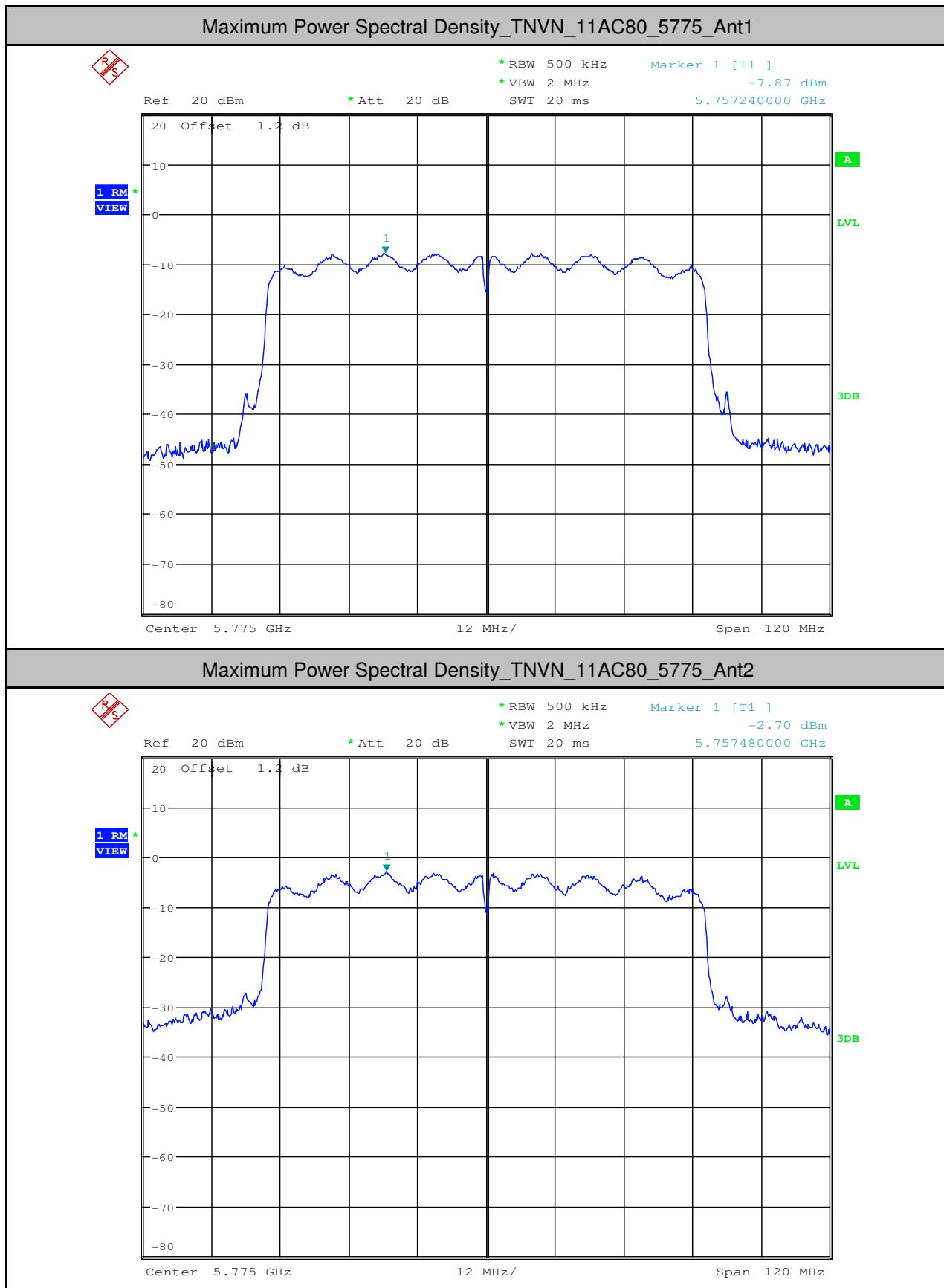


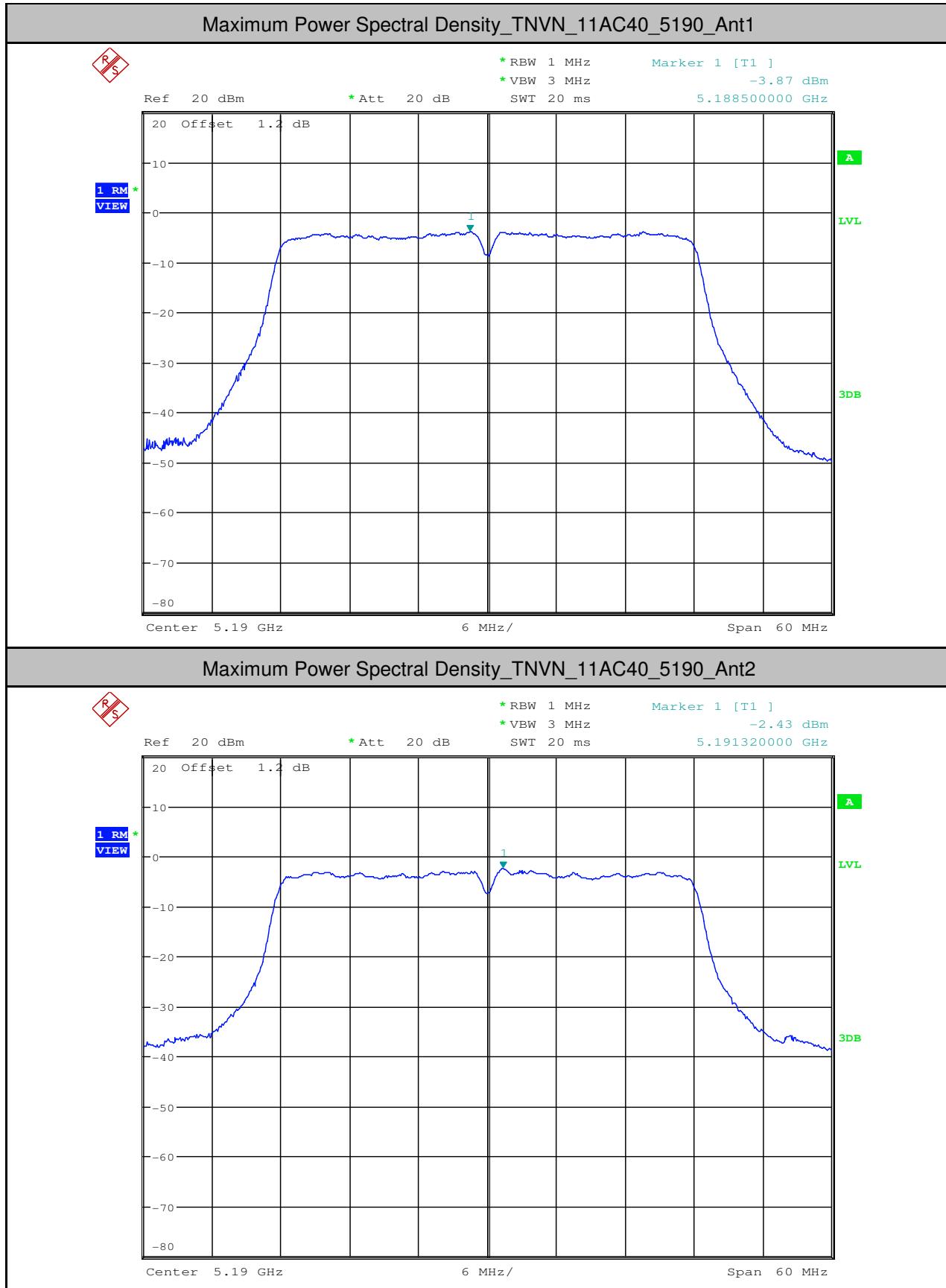


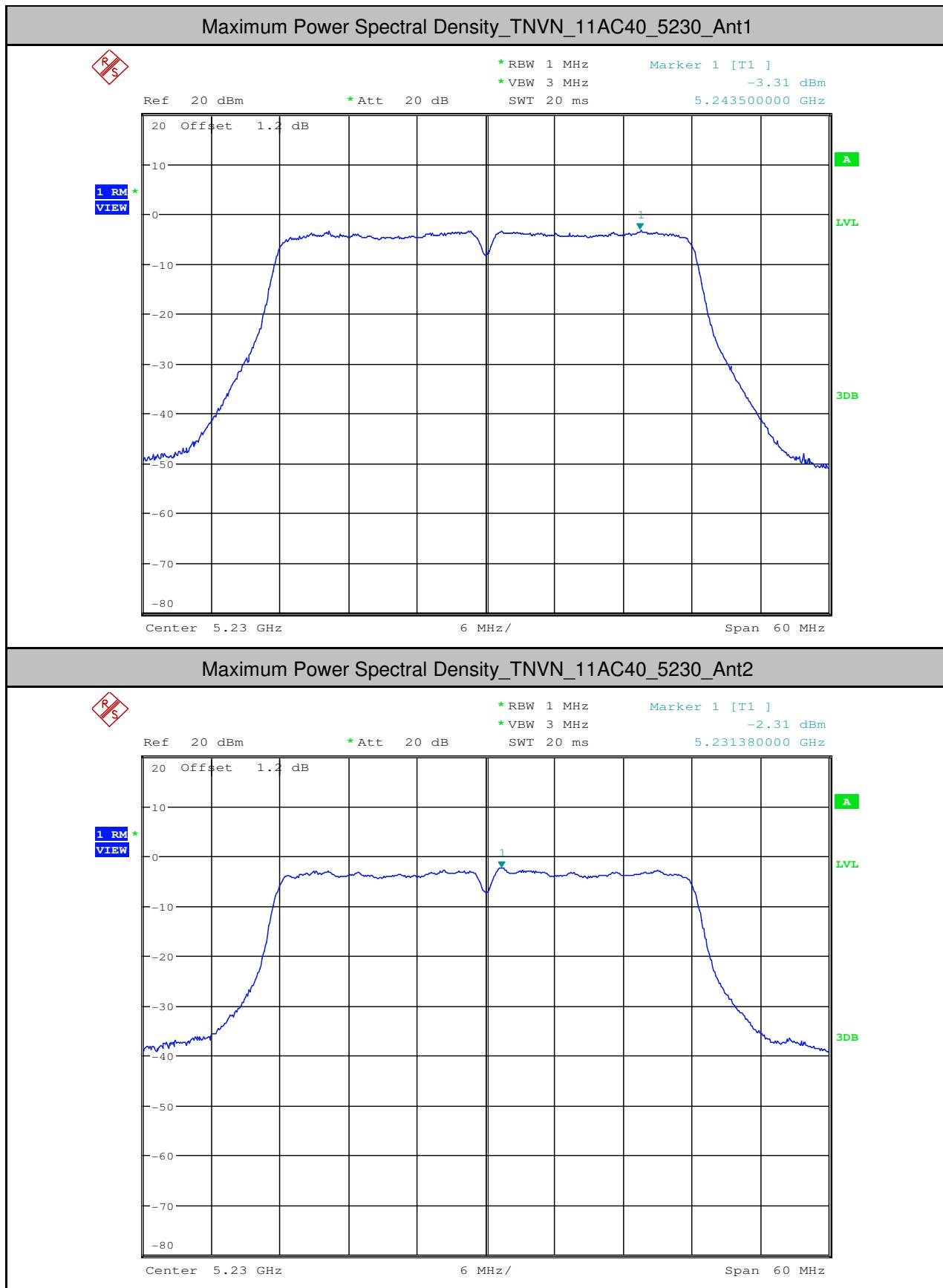


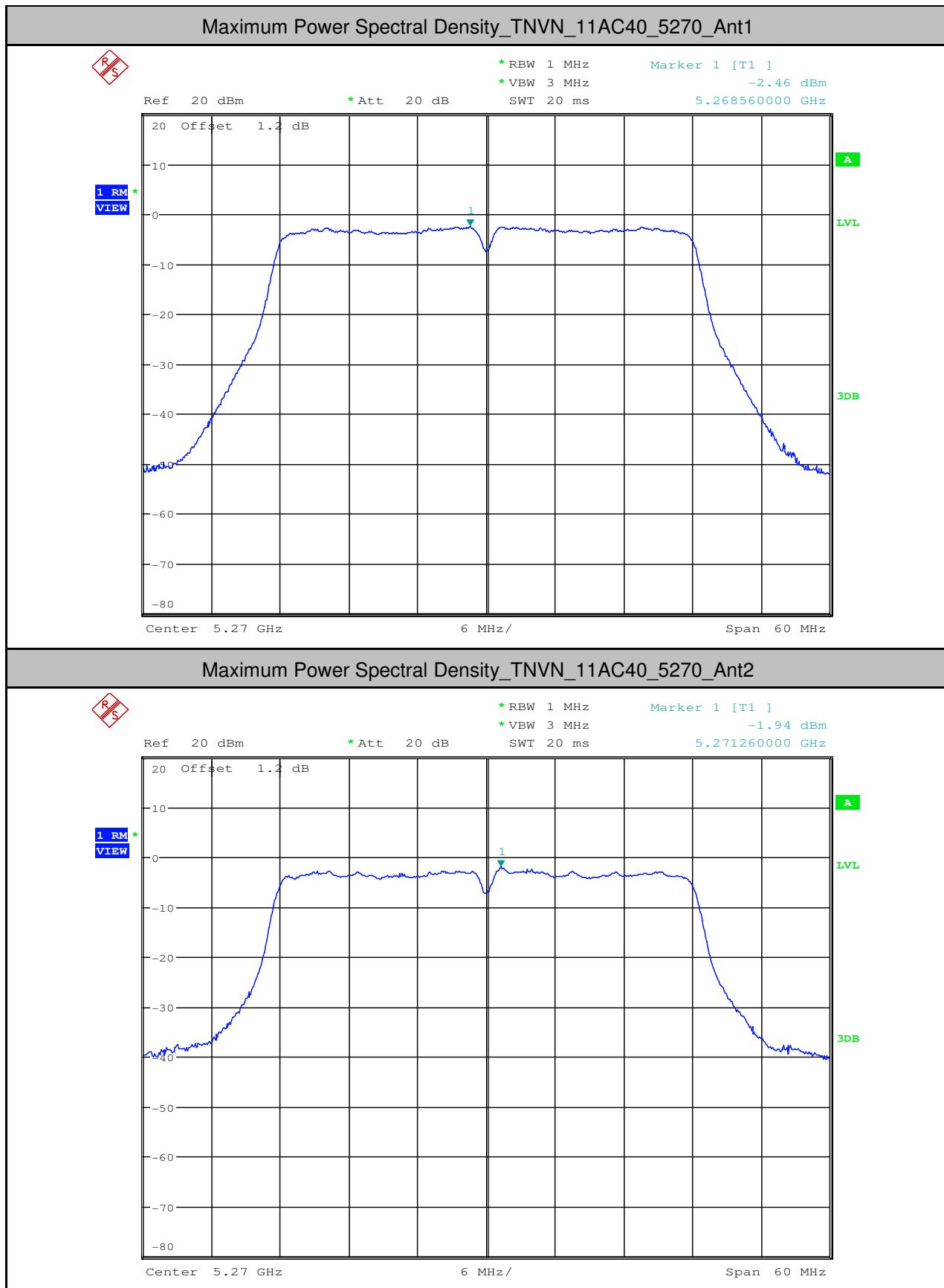


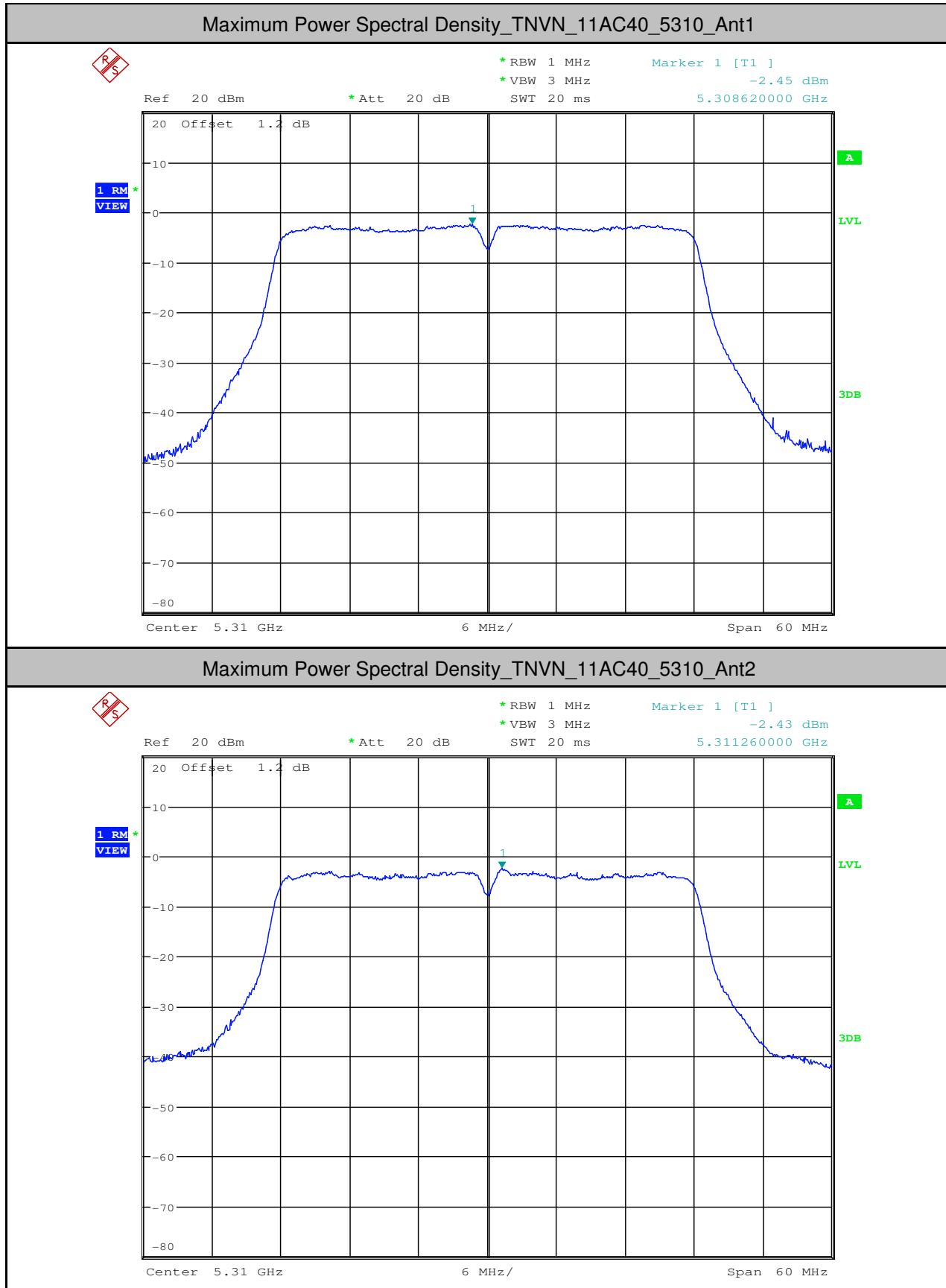


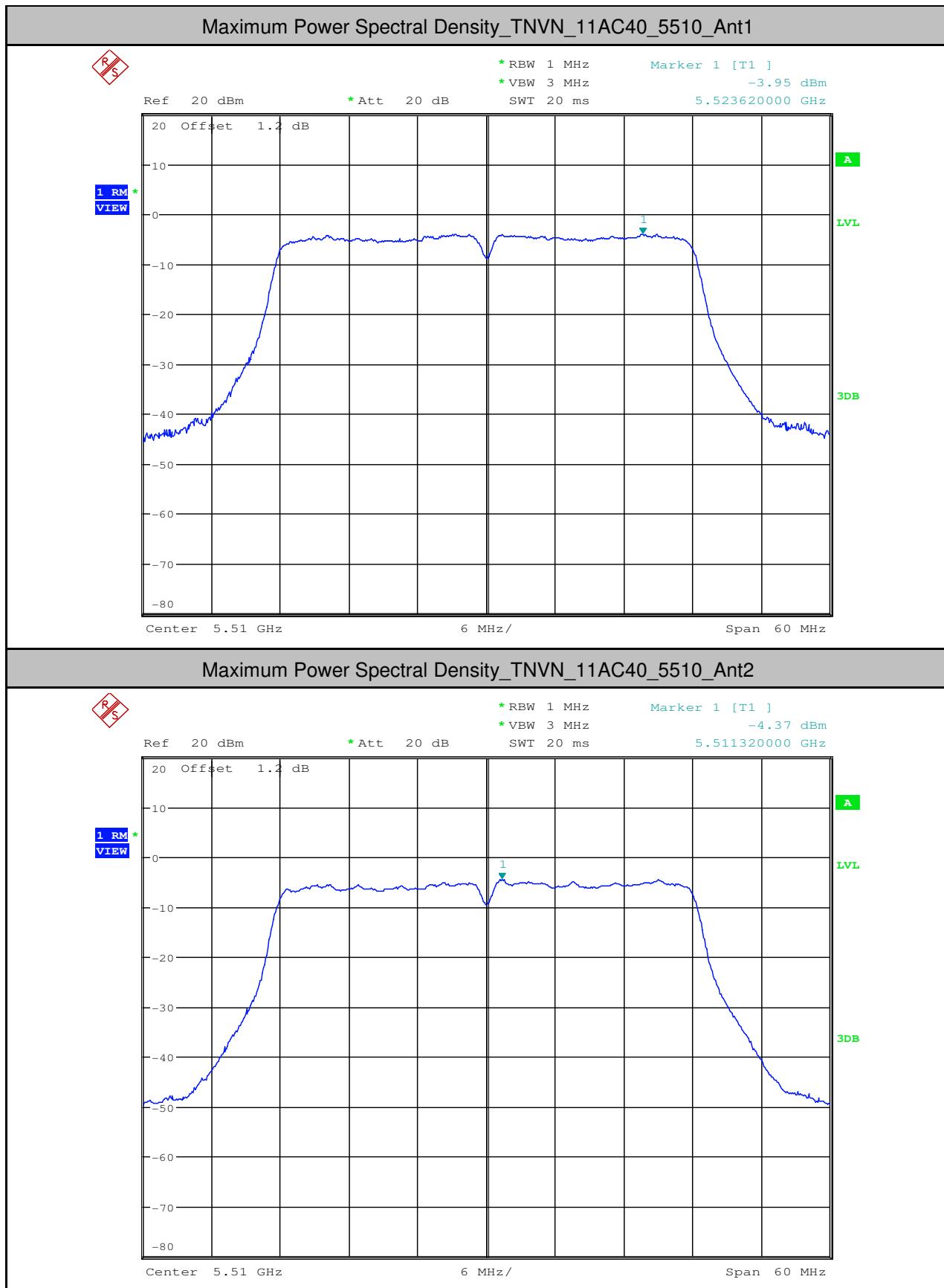


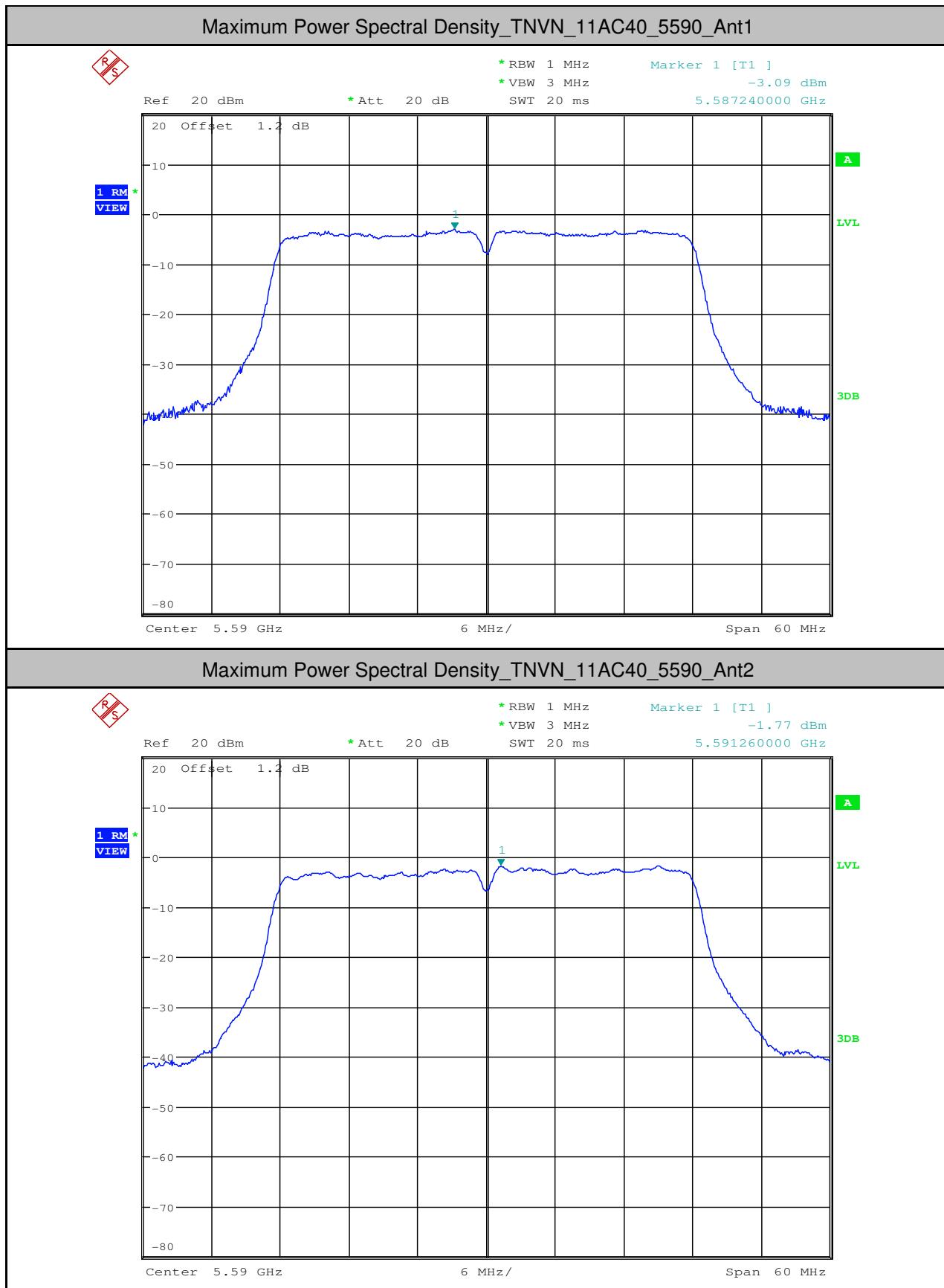


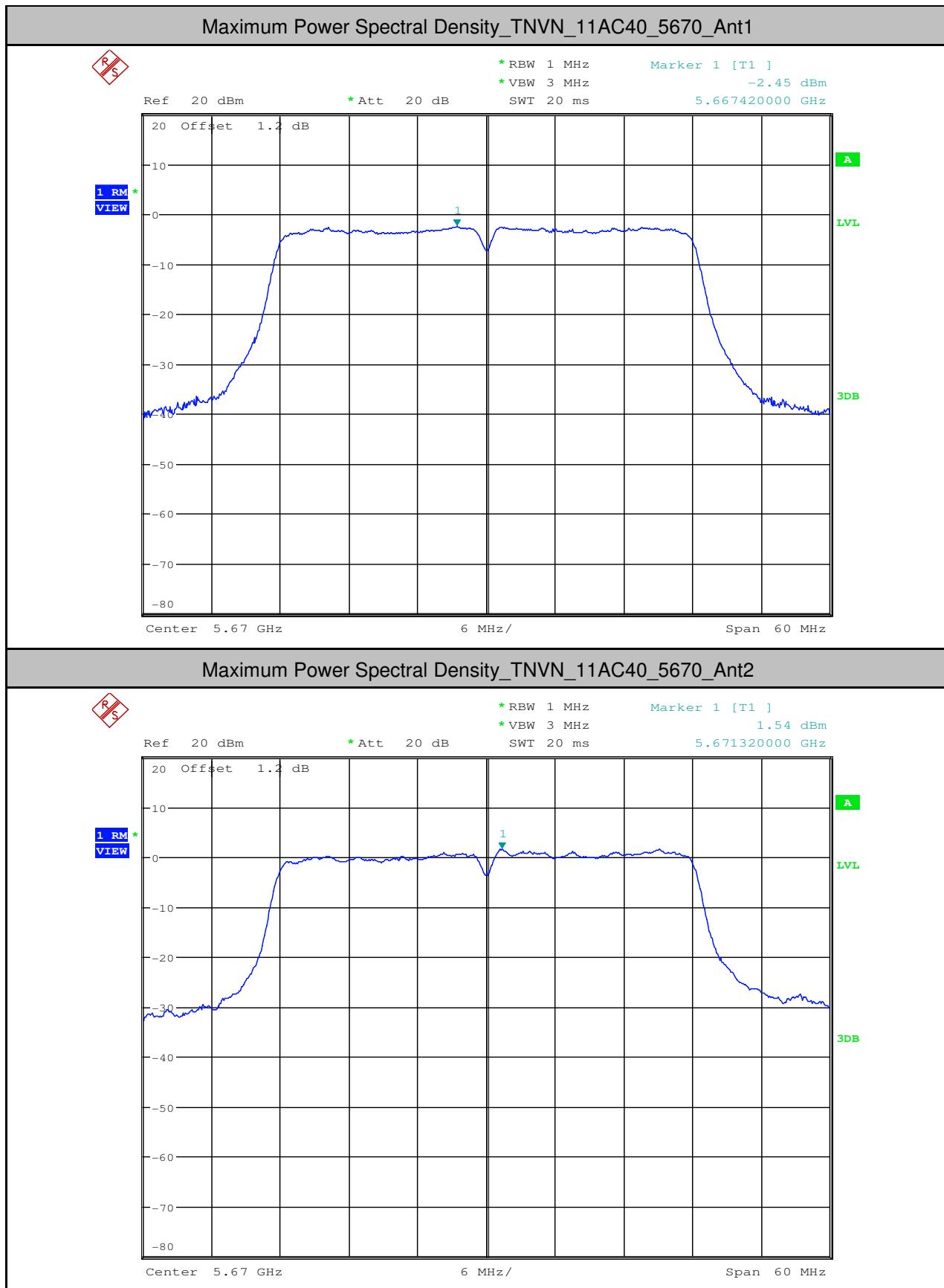


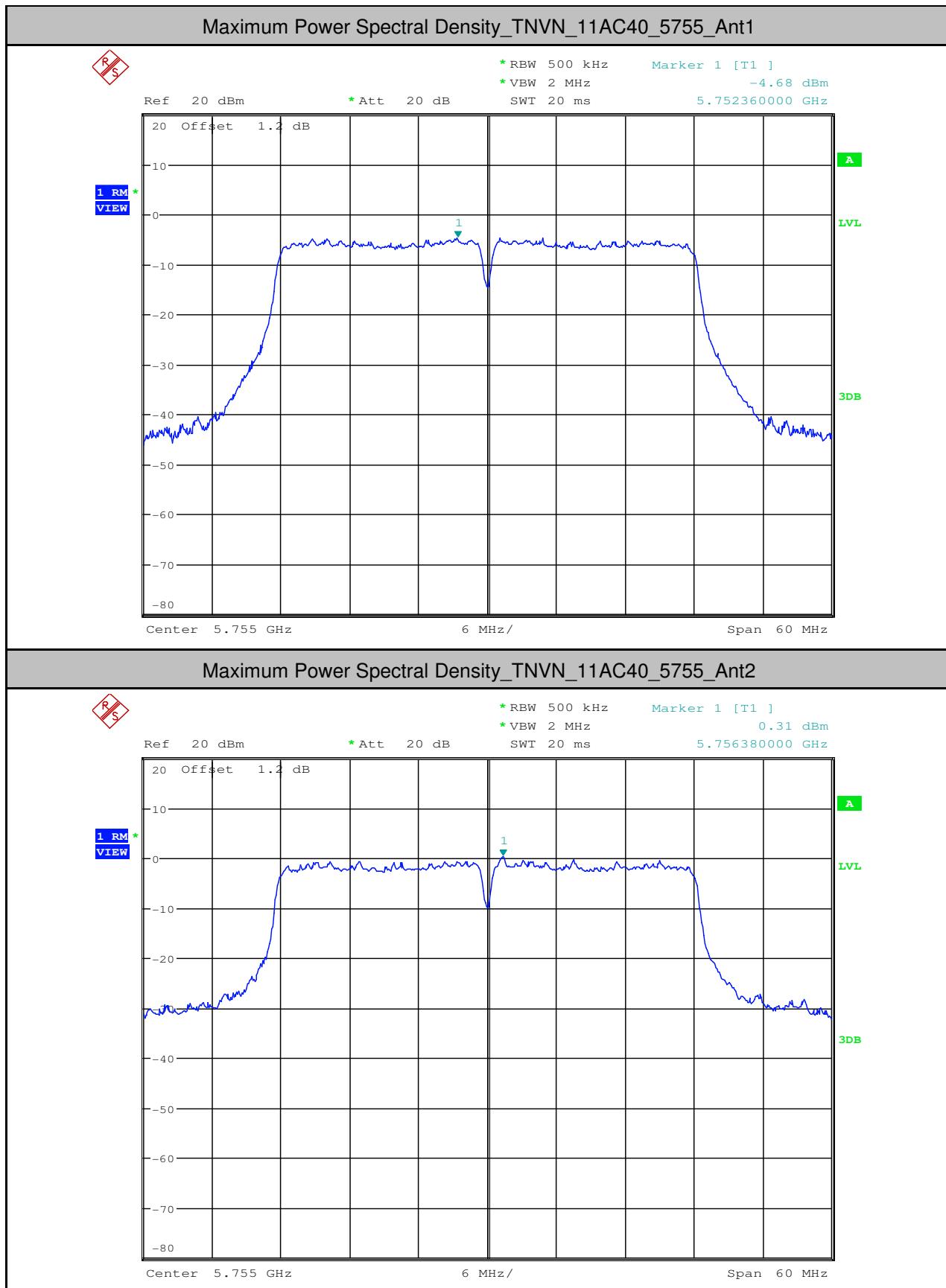


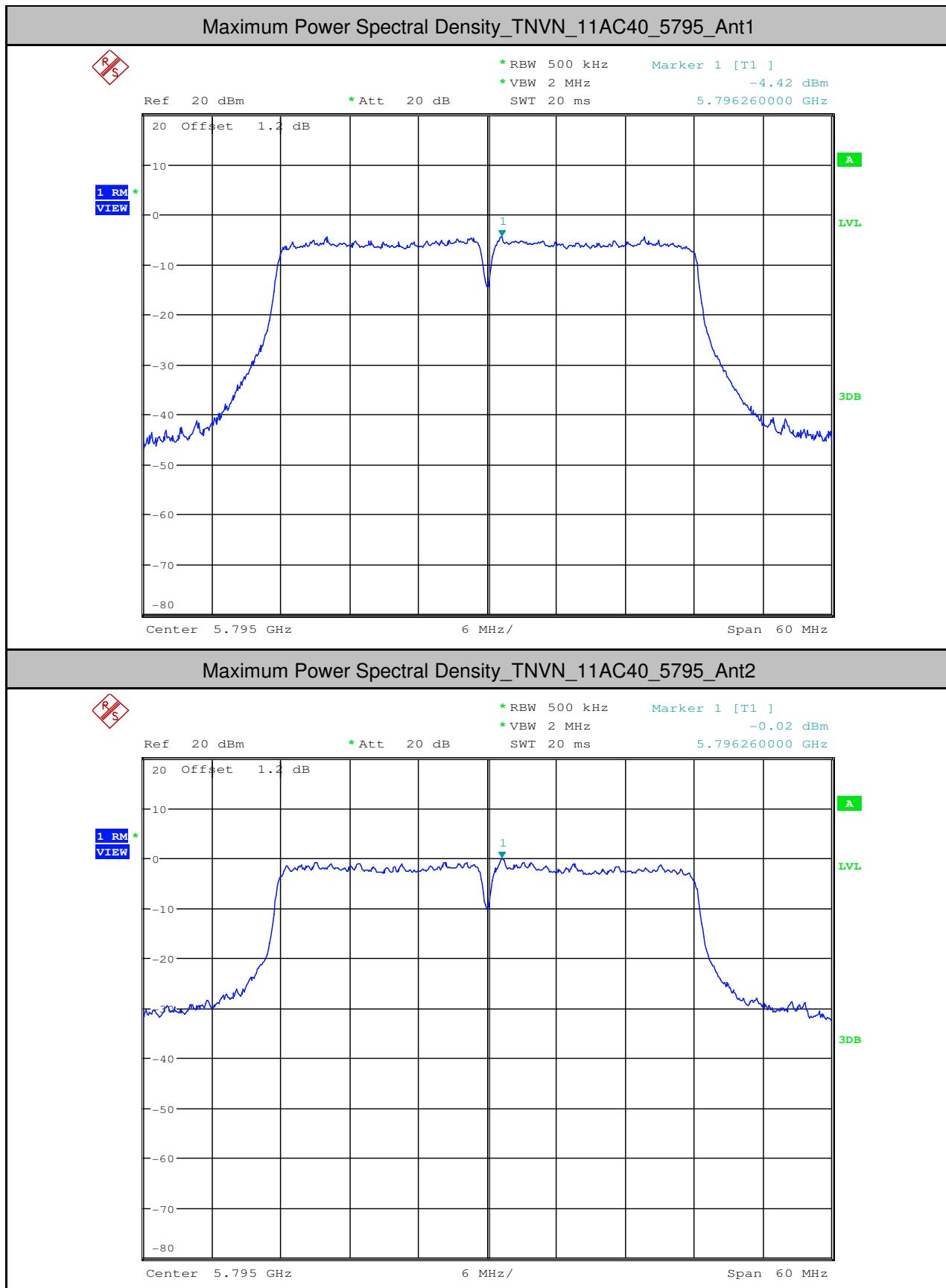














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Shenzhen Branch**

Report No.: SZEM180300232405
Page: 571 of 636

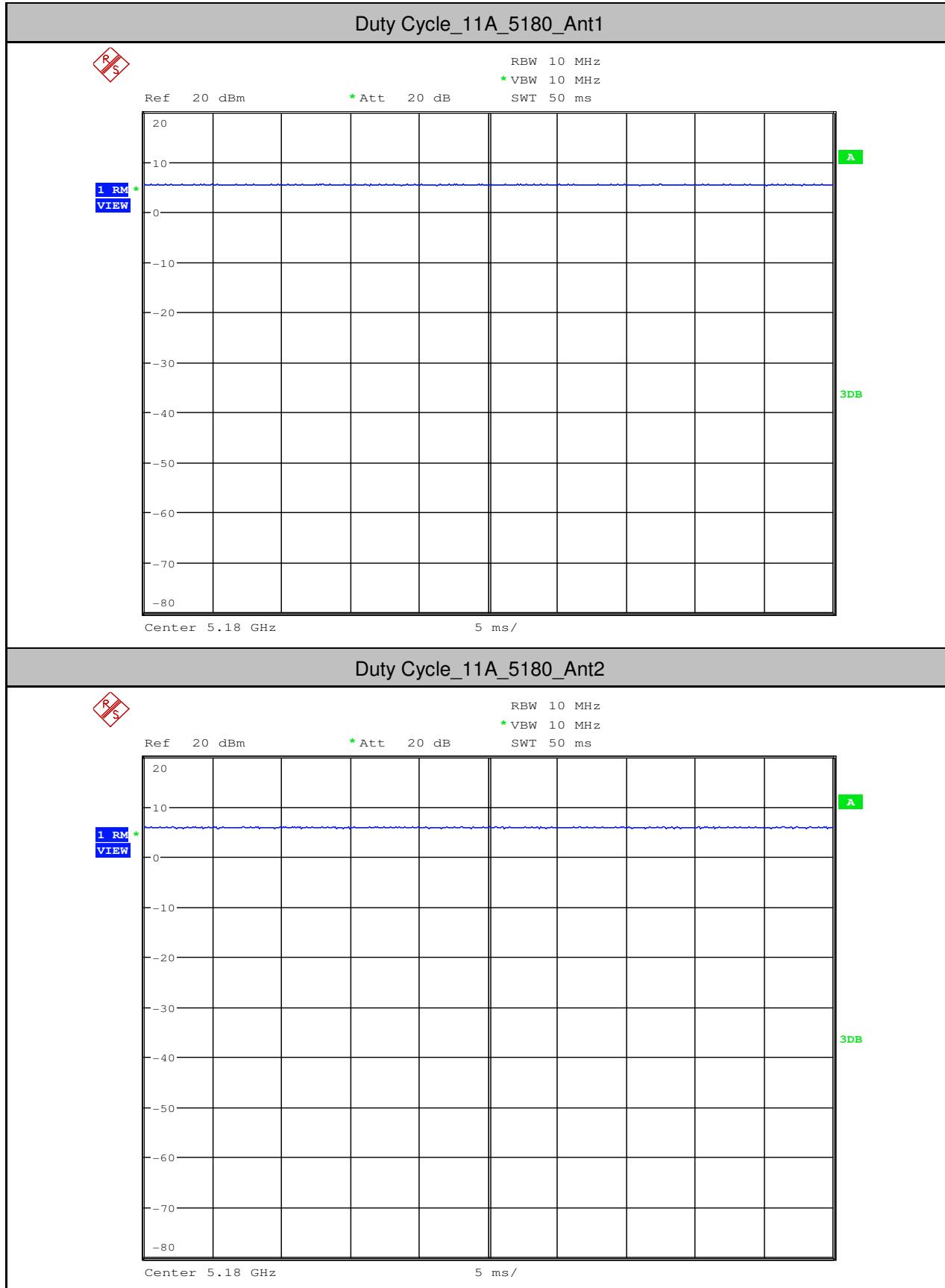
5.Duty Cycle (x)

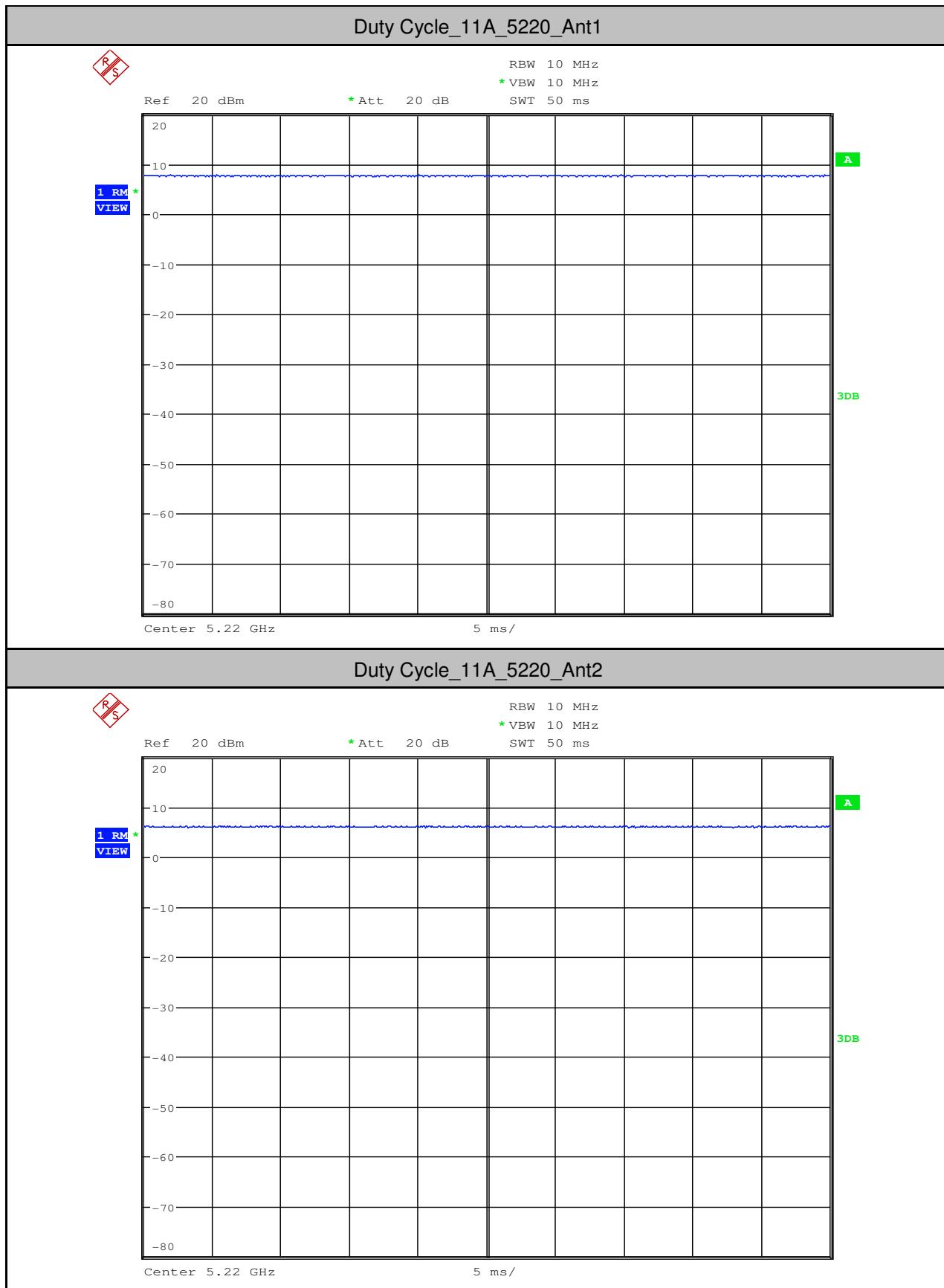
Test Mode	Test Channel	Ant	Duty Cycle[%]	10log(1/x) Factor[dB]
11A	5180	Ant1	100	0
11A	5180	Ant2	100	0
11A	5220	Ant1	100	0
11A	5220	Ant2	100	0
11A	5240	Ant1	100	0
11A	5240	Ant2	100	0
11A	5260	Ant1	100	0
11A	5260	Ant2	100	0
11A	5300	Ant1	100	0
11A	5300	Ant2	100	0
11A	5320	Ant1	100	0
11A	5320	Ant2	100	0
11A	5500	Ant1	100	0
11A	5500	Ant2	100	0
11A	5600	Ant1	100	0
11A	5600	Ant2	100	0
11A	5700	Ant1	100	0
11A	5700	Ant2	100	0
11A	5745	Ant1	100	0
11A	5745	Ant2	100	0
11A	5785	Ant1	100	0
11A	5785	Ant2	100	0
11A	5825	Ant1	100	0
11A	5825	Ant2	100	0
11N20	5180	Ant1	100	0
11N20	5180	Ant2	100	0
11N20	5220	Ant1	100	0
11N20	5220	Ant2	100	0
11N20	5240	Ant1	100	0
11N20	5240	Ant2	100	0

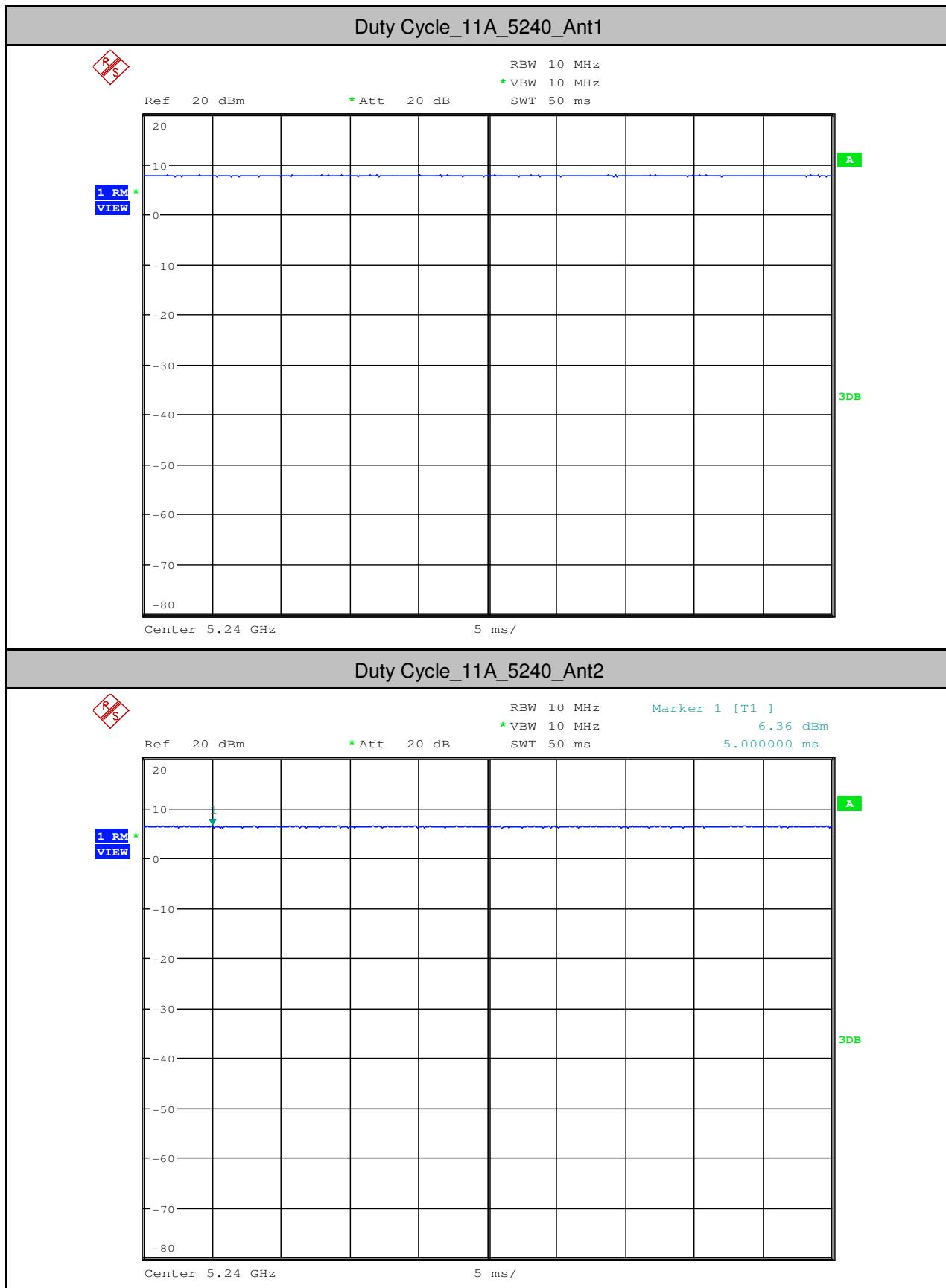
11N20	5260	Ant1	100	0
11N20	5260	Ant2	100	0
11N20	5300	Ant1	100	0
11N20	5300	Ant2	100	0
11N20	5320	Ant1	100	0
11N20	5320	Ant2	100	0
11N20	5500	Ant1	100	0
11N20	5500	Ant2	100	0
11N20	5600	Ant1	100	0
11N20	5600	Ant2	100	0
11N20	5700	Ant1	100	0
11N20	5700	Ant2	100	0
11N20	5745	Ant1	100	0
11N20	5745	Ant2	100	0
11N20	5785	Ant1	100	0
11N20	5785	Ant2	100	0
11N20	5825	Ant1	100	0
11N20	5825	Ant2	100	0
11N40	5190	Ant1	100	0
11N40	5190	Ant2	100	0
11N40	5230	Ant1	100	0
11N40	5230	Ant2	100	0
11N40	5270	Ant1	100	0
11N40	5270	Ant2	100	0
11N40	5310	Ant1	100	0
11N40	5310	Ant2	100	0
11N40	5510	Ant1	100	0
11N40	5510	Ant2	100	0
11N40	5590	Ant1	100	0
11N40	5590	Ant2	100	0
11N40	5670	Ant1	100	0
11N40	5670	Ant2	100	0
11N40	5755	Ant1	100	0
11N40	5755	Ant2	100	0

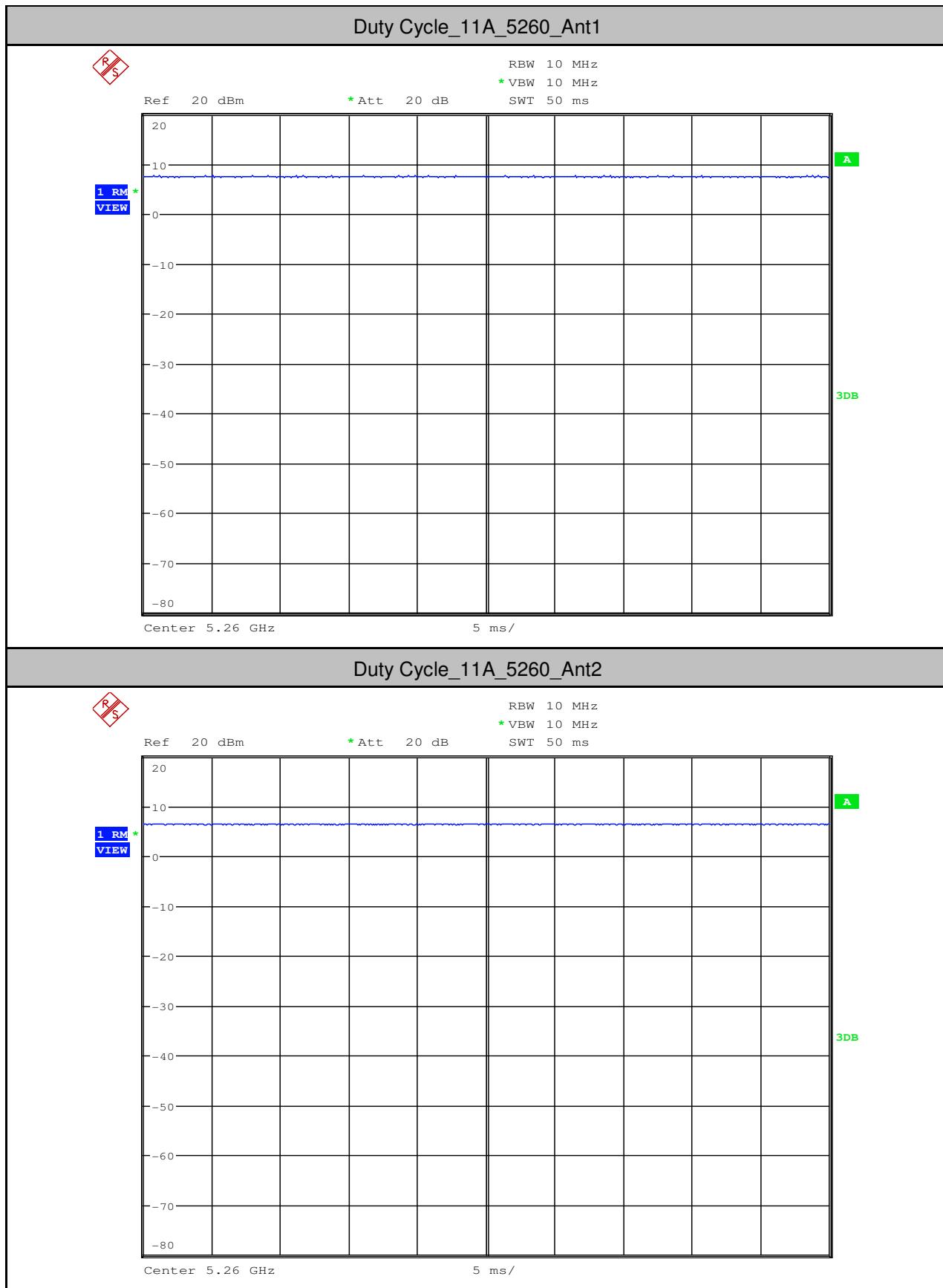
11N40	5795	Ant1	100	0
11N40	5795	Ant2	100	0
11AC20	5180	Ant1	100	0
11AC20	5180	Ant2	100	0
11AC20	5220	Ant1	100	0
11AC20	5220	Ant2	100	0
11AC20	5240	Ant1	100	0
11AC20	5240	Ant2	100	0
11AC20	5260	Ant1	100	0
11AC20	5260	Ant2	100	0
11AC20	5300	Ant1	100	0
11AC20	5300	Ant2	100	0
11AC20	5320	Ant1	100	0
11AC20	5320	Ant2	100	0
11AC20	5500	Ant1	100	0
11AC20	5500	Ant2	100	0
11AC20	5600	Ant1	100	0
11AC20	5600	Ant2	100	0
11AC20	5700	Ant1	100	0
11AC20	5700	Ant2	100	0
11AC20	5745	Ant1	100	0
11AC20	5745	Ant2	100	0
11AC20	5785	Ant1	100	0
11AC20	5785	Ant2	100	0
11AC20	5825	Ant1	100	0
11AC20	5825	Ant2	100	0
11AC80	5210	Ant1	100	0
11AC80	5210	Ant2	100	0
11AC80	5290	Ant1	100	0
11AC80	5290	Ant2	100	0
11AC80	5530	Ant1	100	0
11AC80	5530	Ant2	100	0
11AC80	5610	Ant1	100	0
11AC80	5610	Ant2	100	0

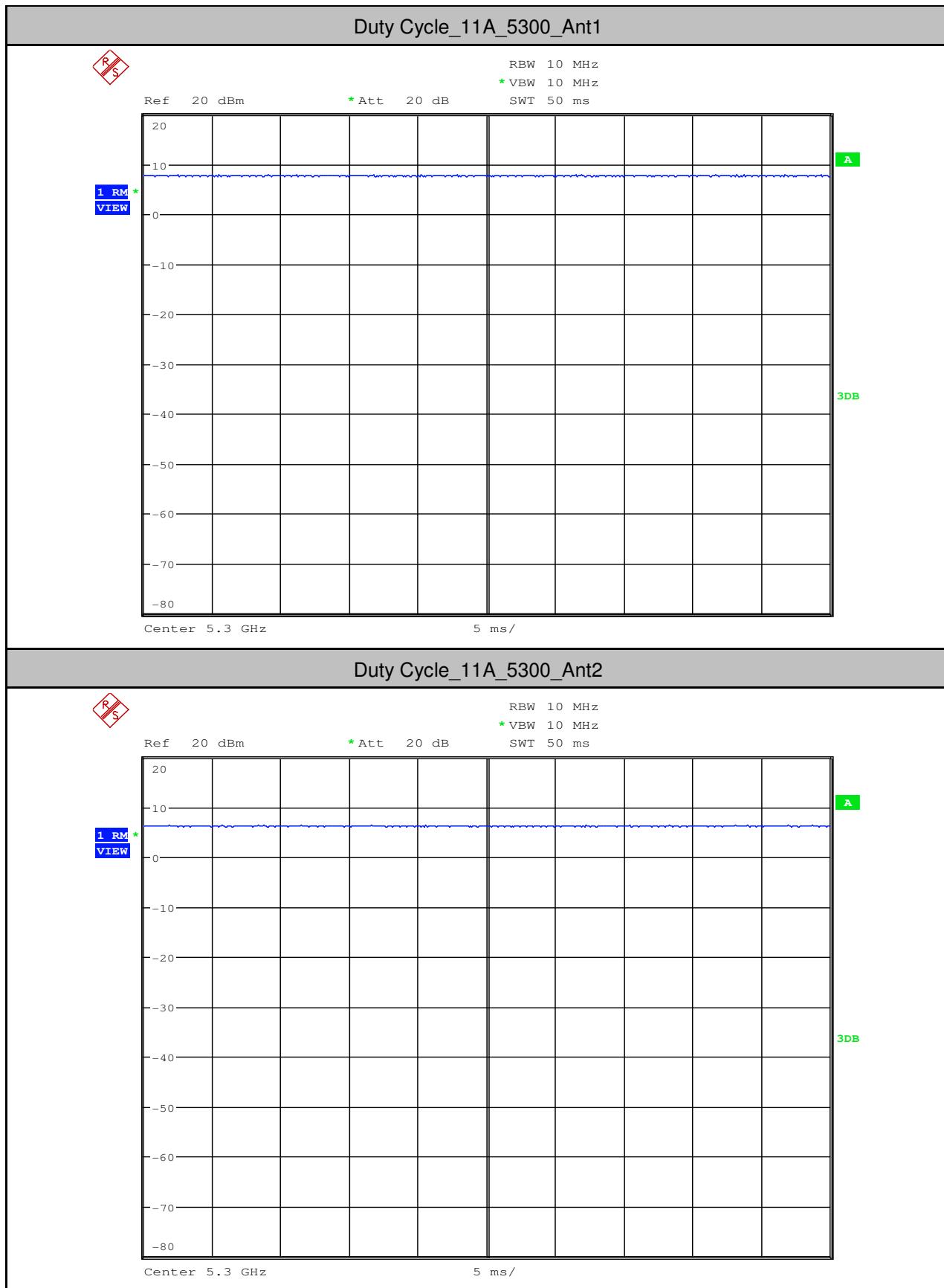
11AC80	5775	Ant1	100	0
11AC80	5775	Ant2	100	0
11AC40	5190	Ant1	100	0
11AC40	5190	Ant2	100	0
11AC40	5230	Ant1	100	0
11AC40	5230	Ant2	100	0
11AC40	5270	Ant1	100	0
11AC40	5270	Ant2	100	0
11AC40	5310	Ant1	100	0
11AC40	5310	Ant2	100	0
11AC40	5510	Ant1	100	0
11AC40	5510	Ant2	100	0
11AC40	5590	Ant1	100	0
11AC40	5590	Ant2	100	0
11AC40	5670	Ant1	100	0
11AC40	5670	Ant2	100	0
11AC40	5755	Ant1	100	0
11AC40	5755	Ant2	100	0
11AC40	5795	Ant1	100	0
11AC40	5795	Ant2	100	0

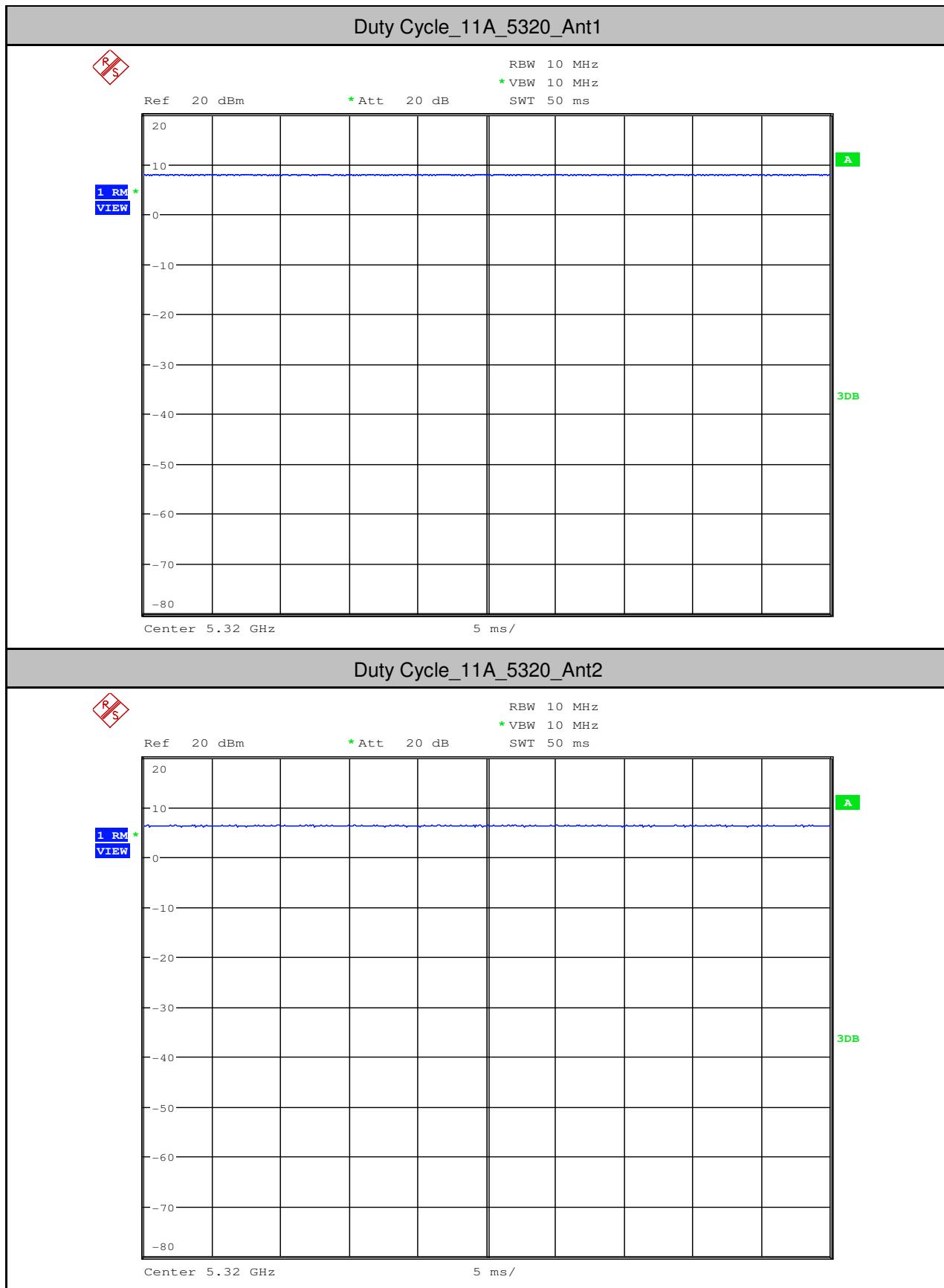


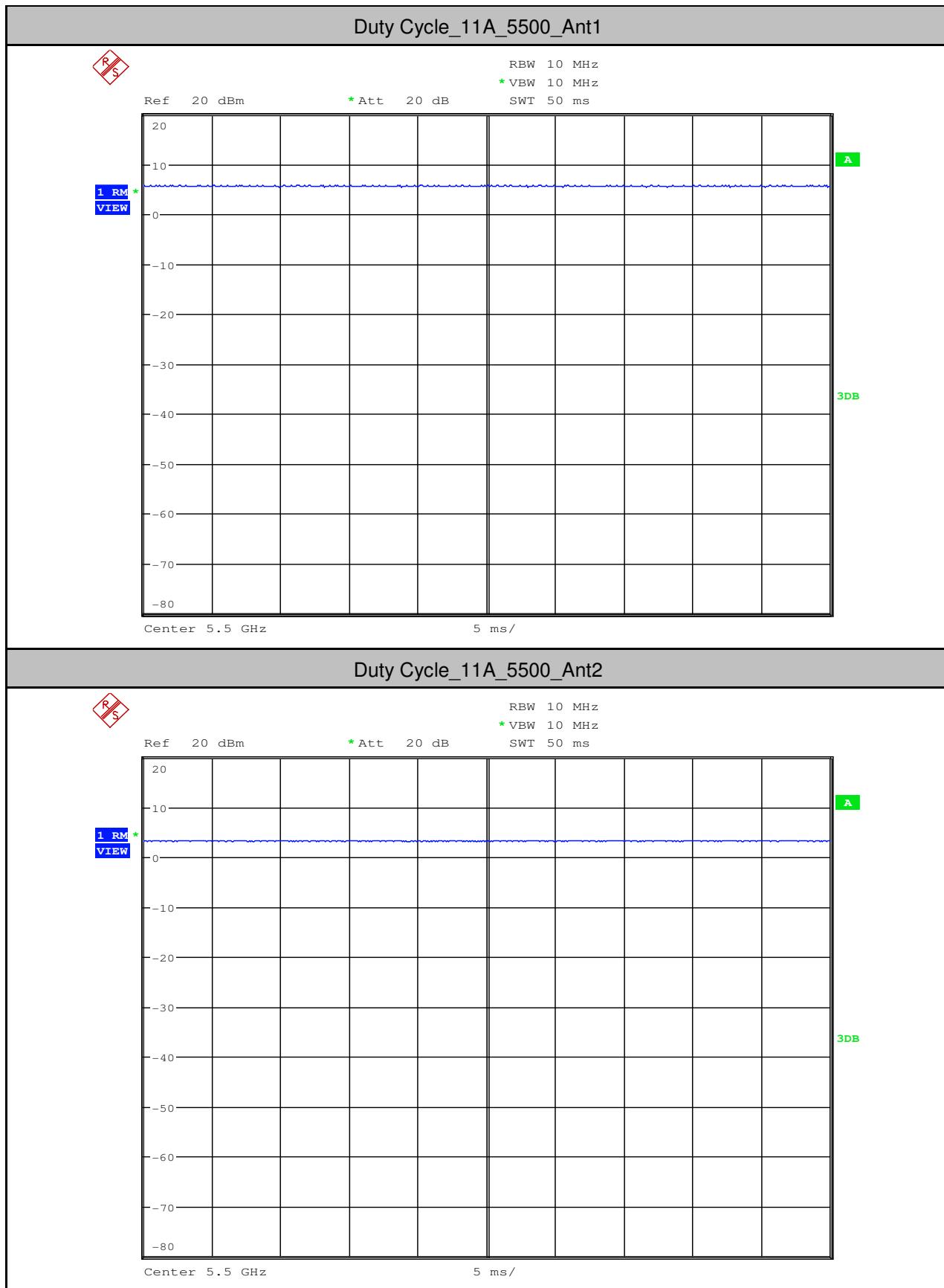


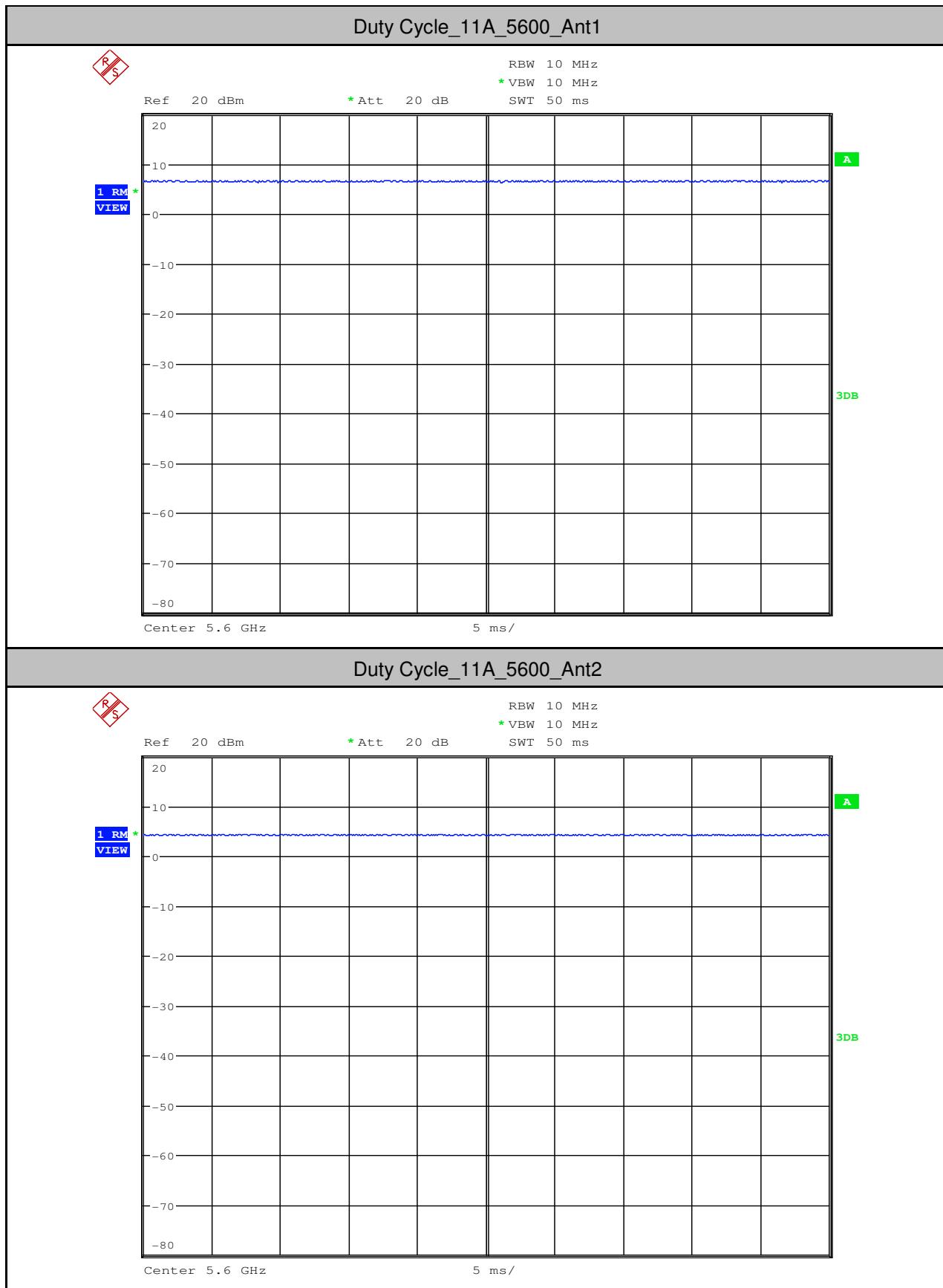


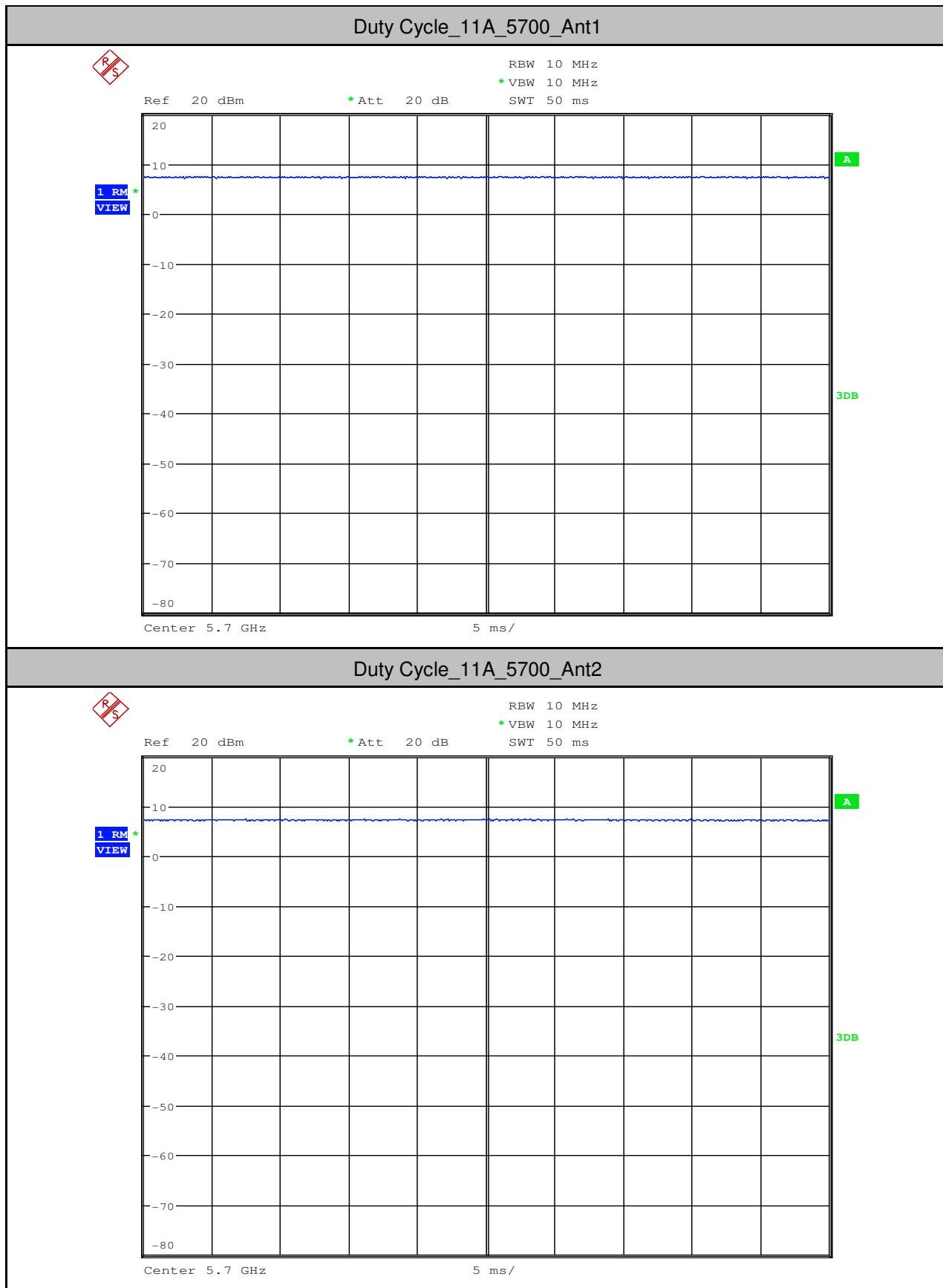


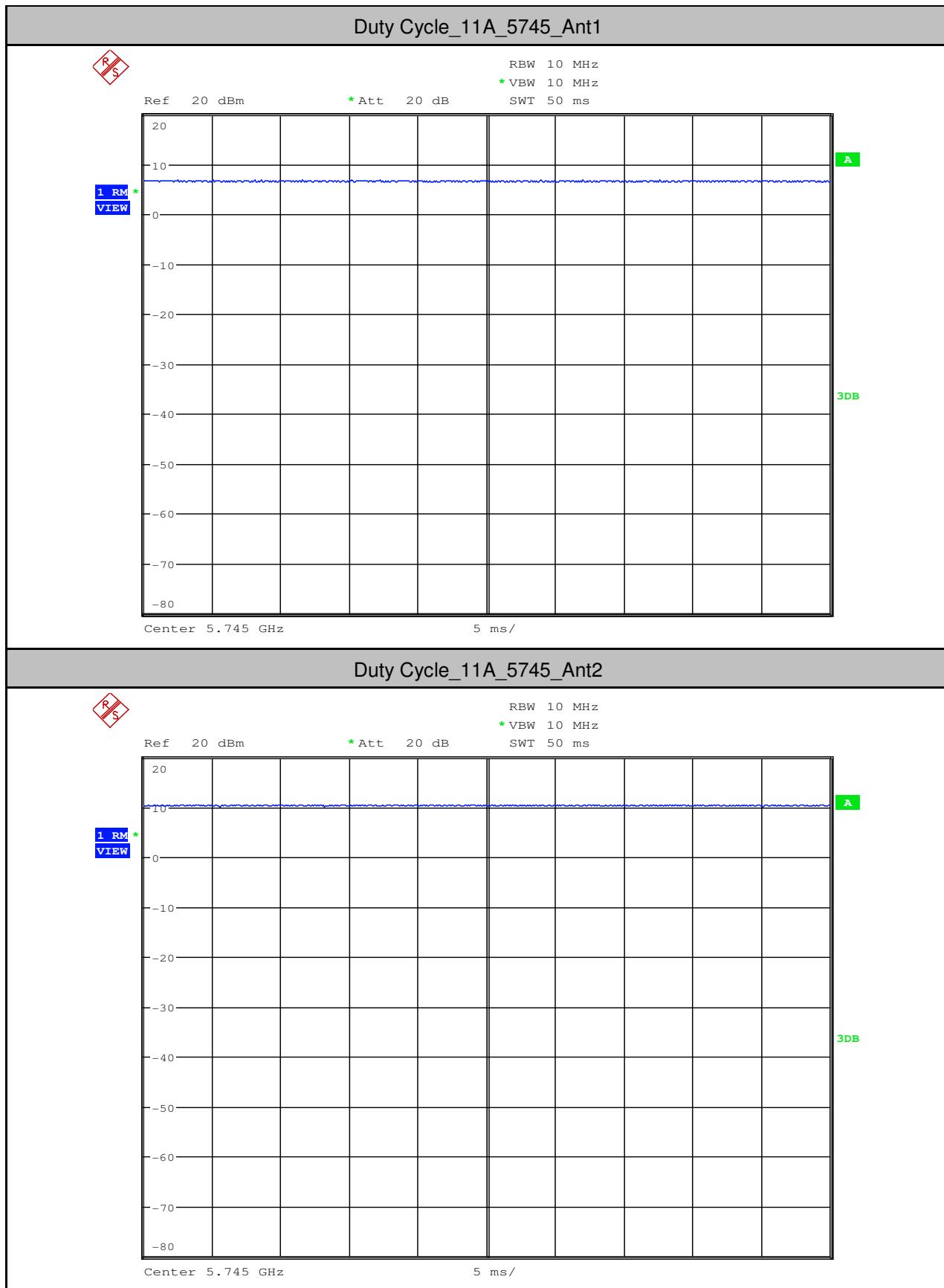


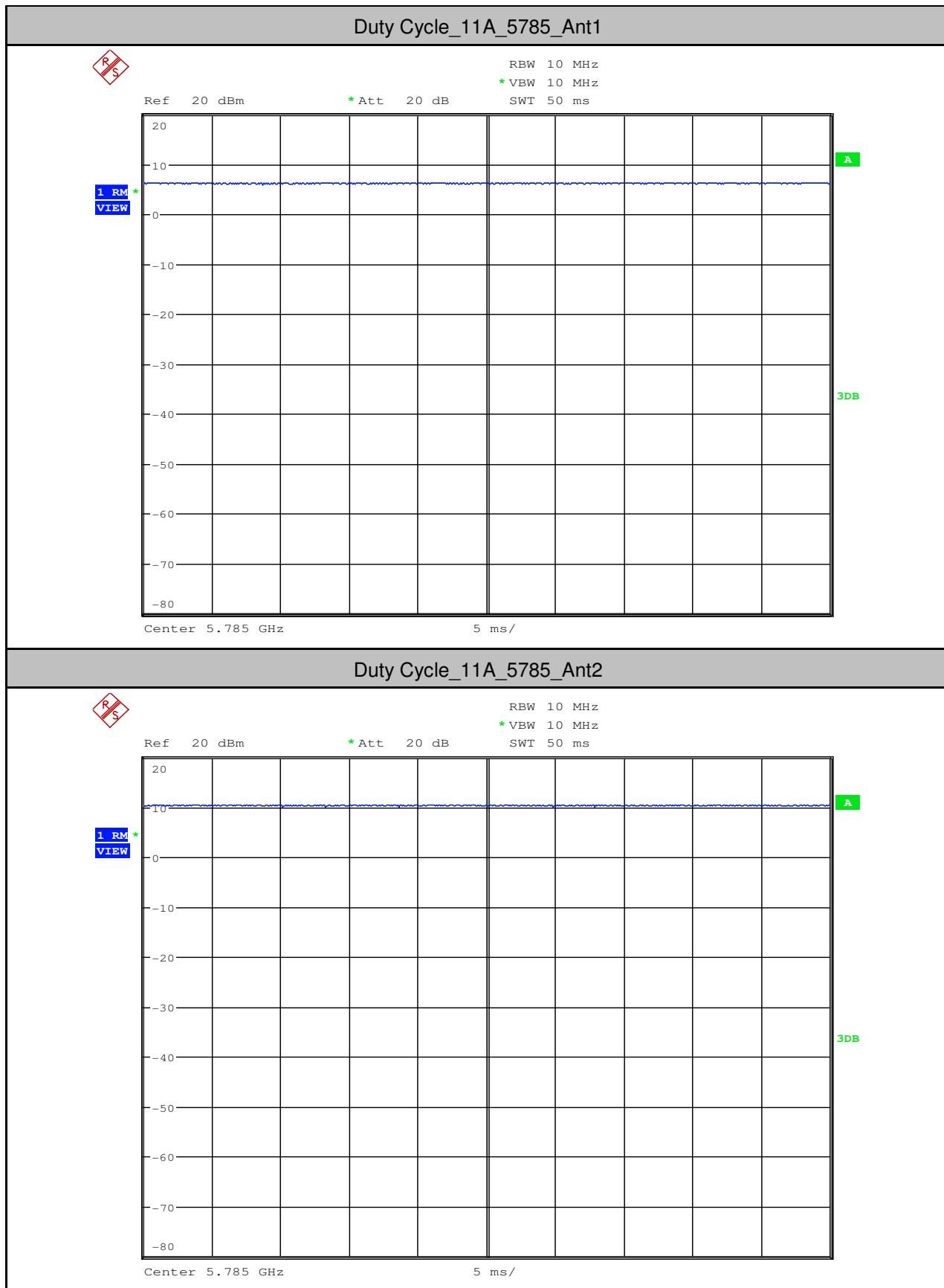


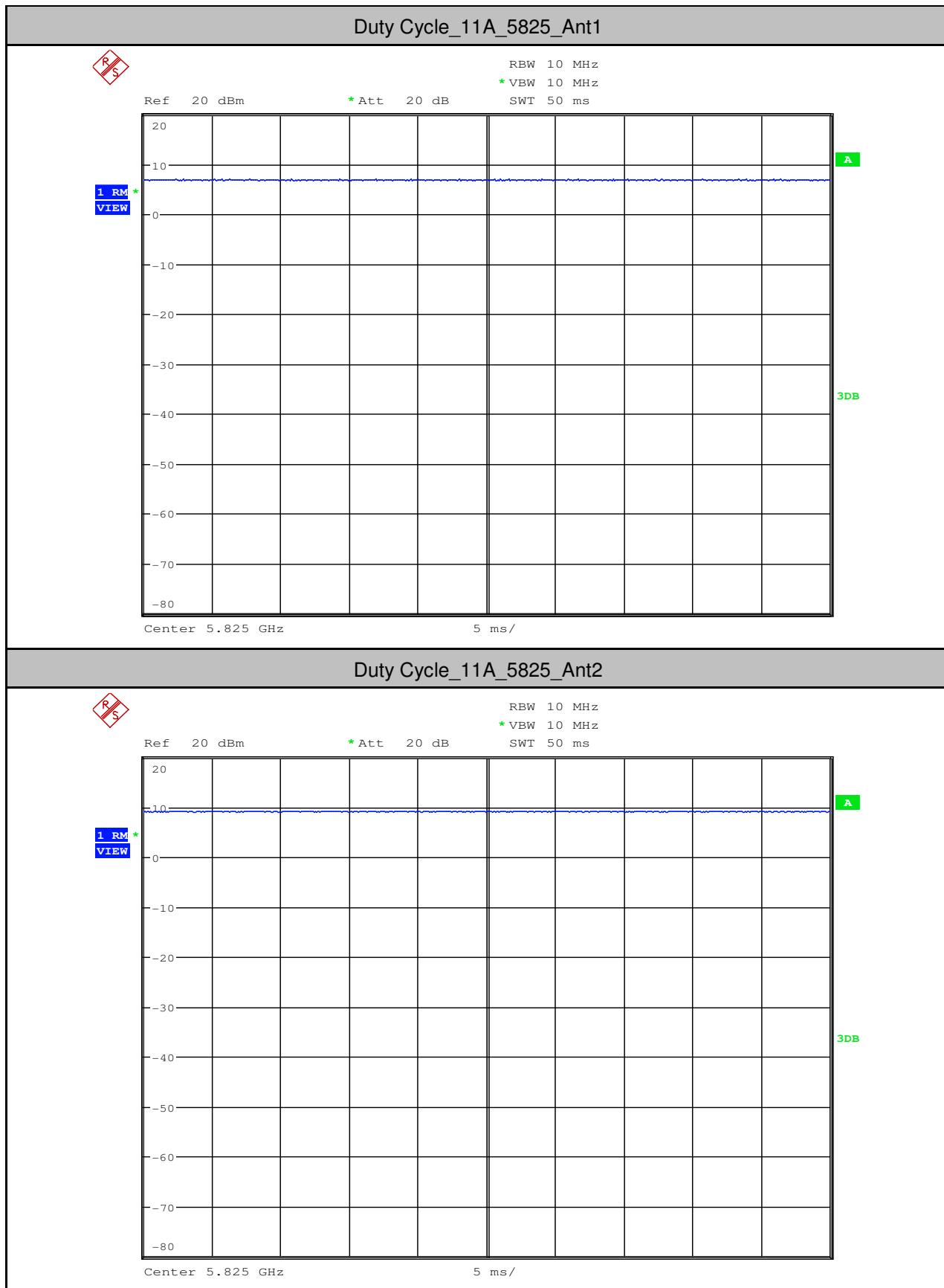


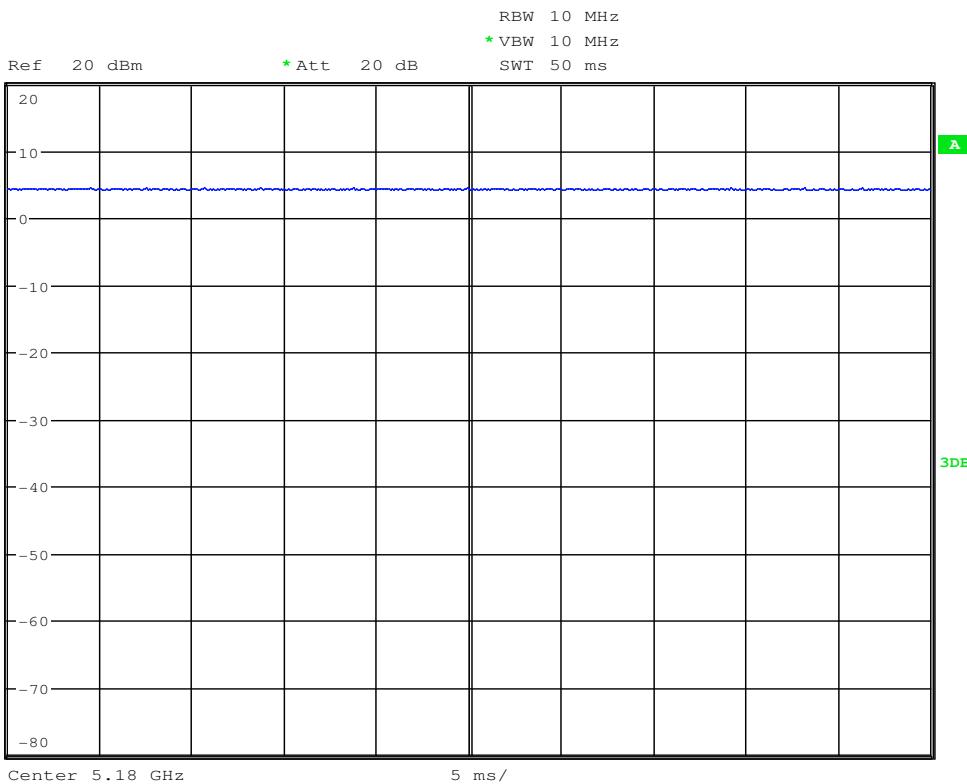
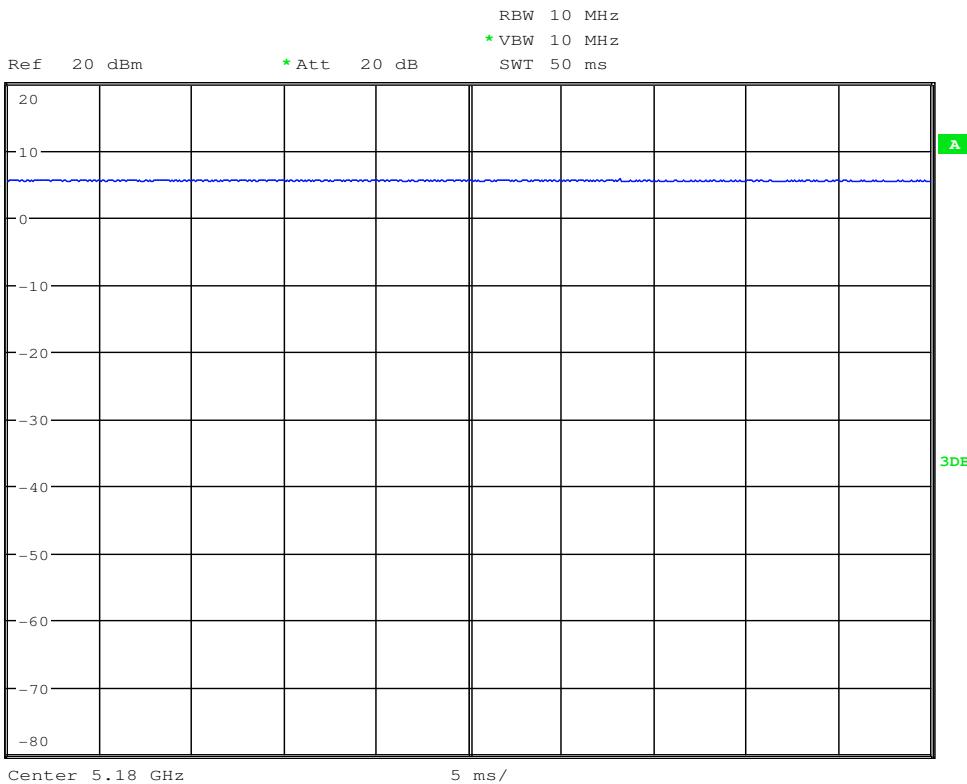


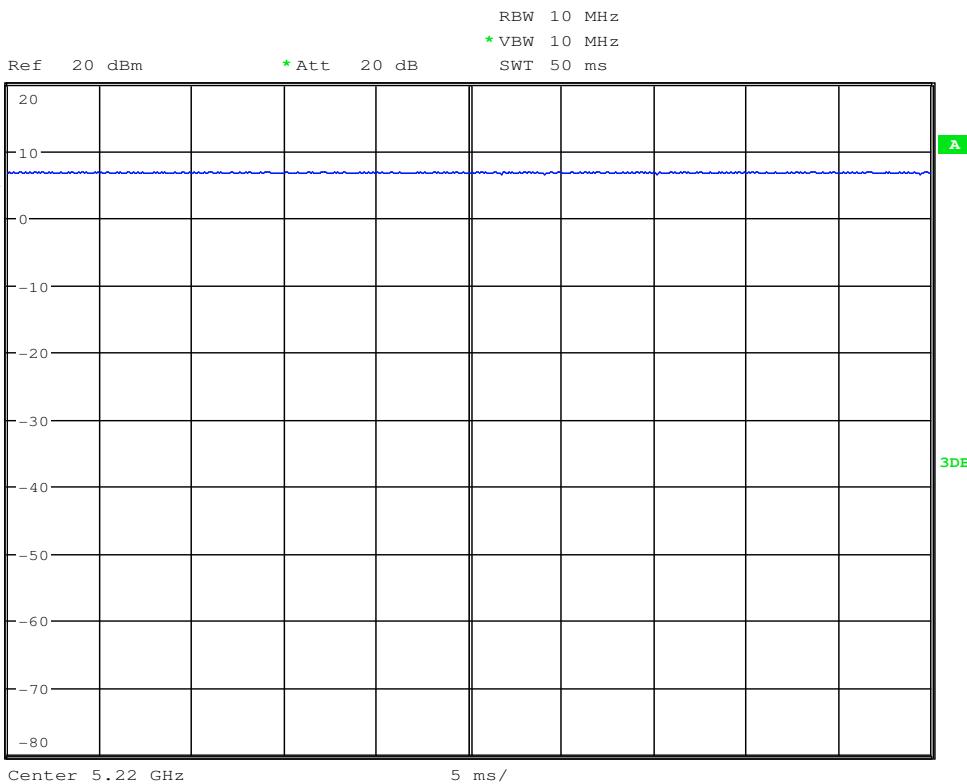
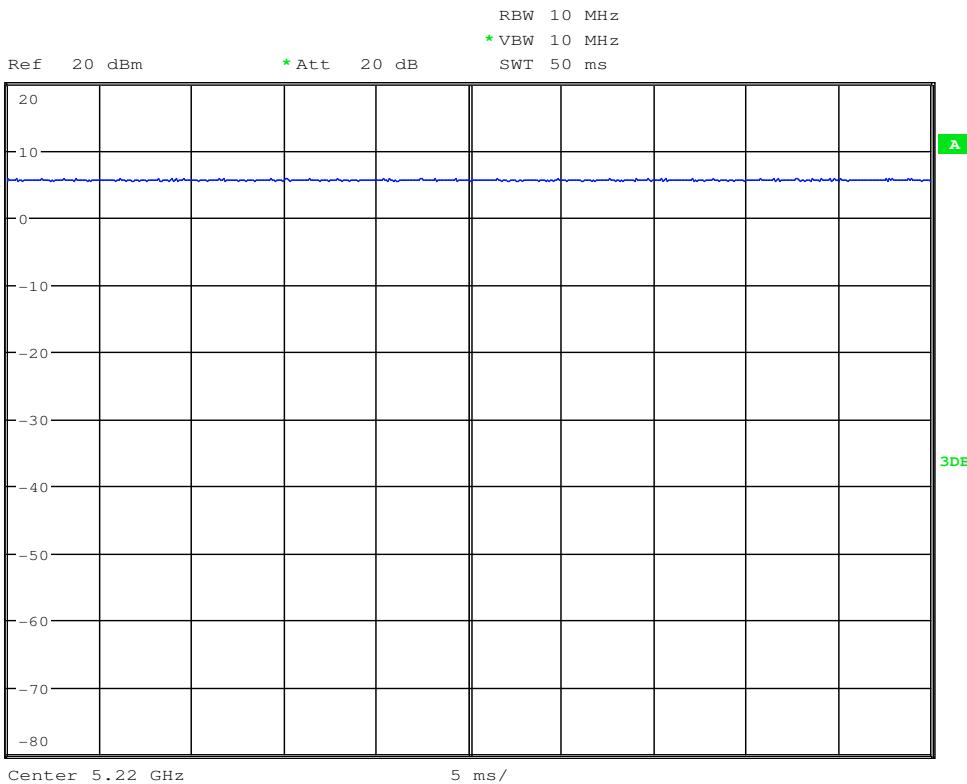


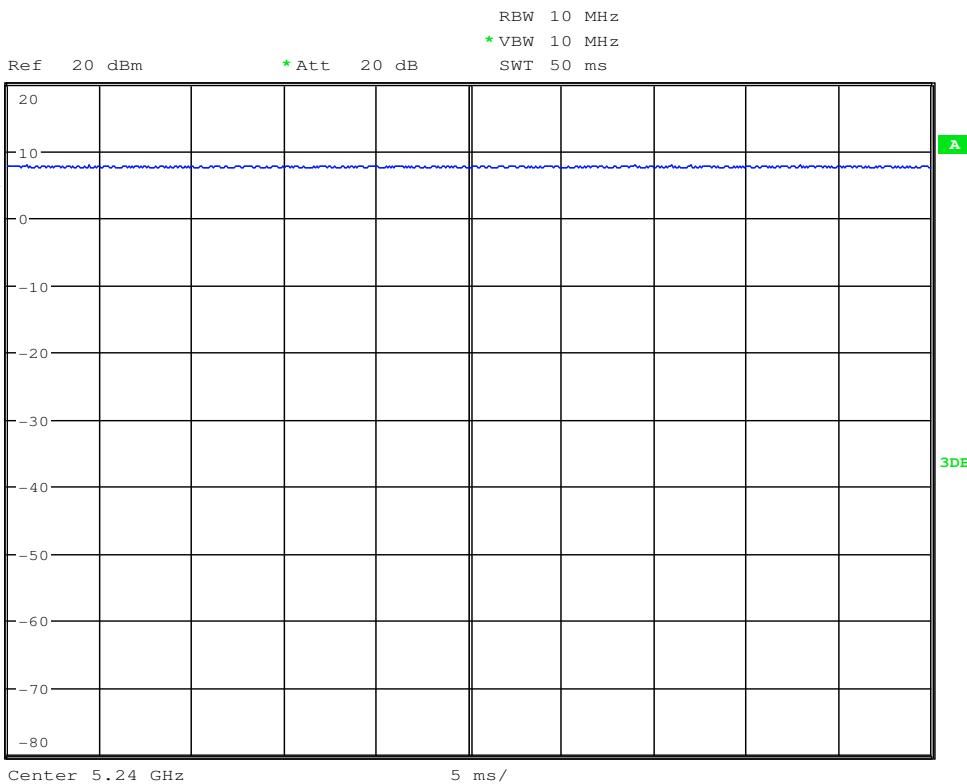
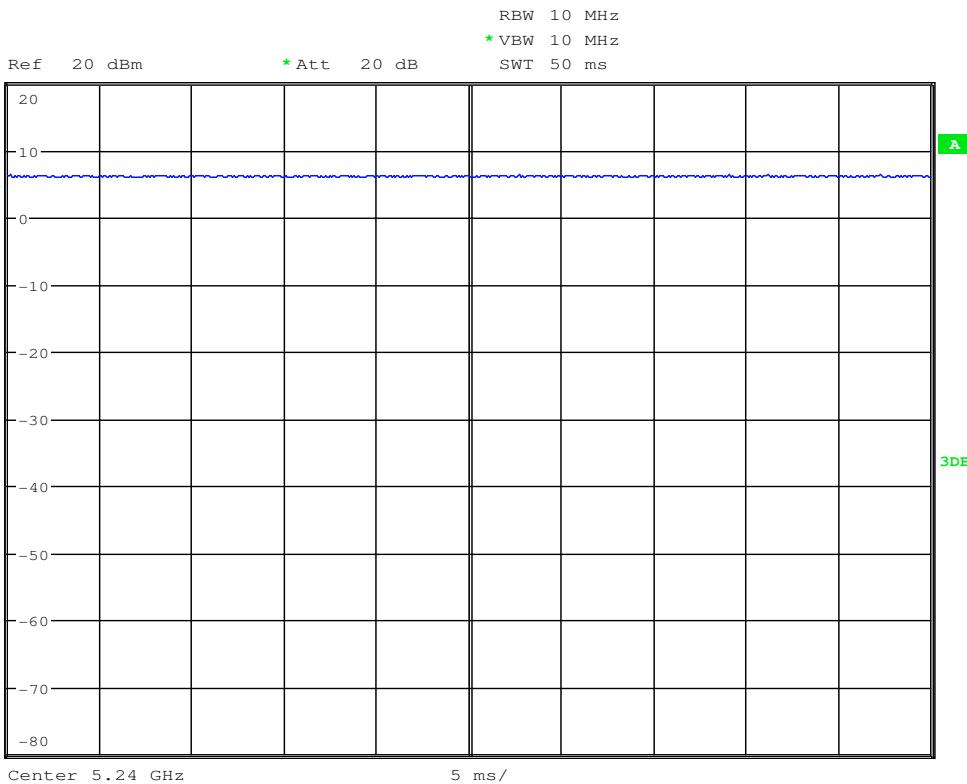


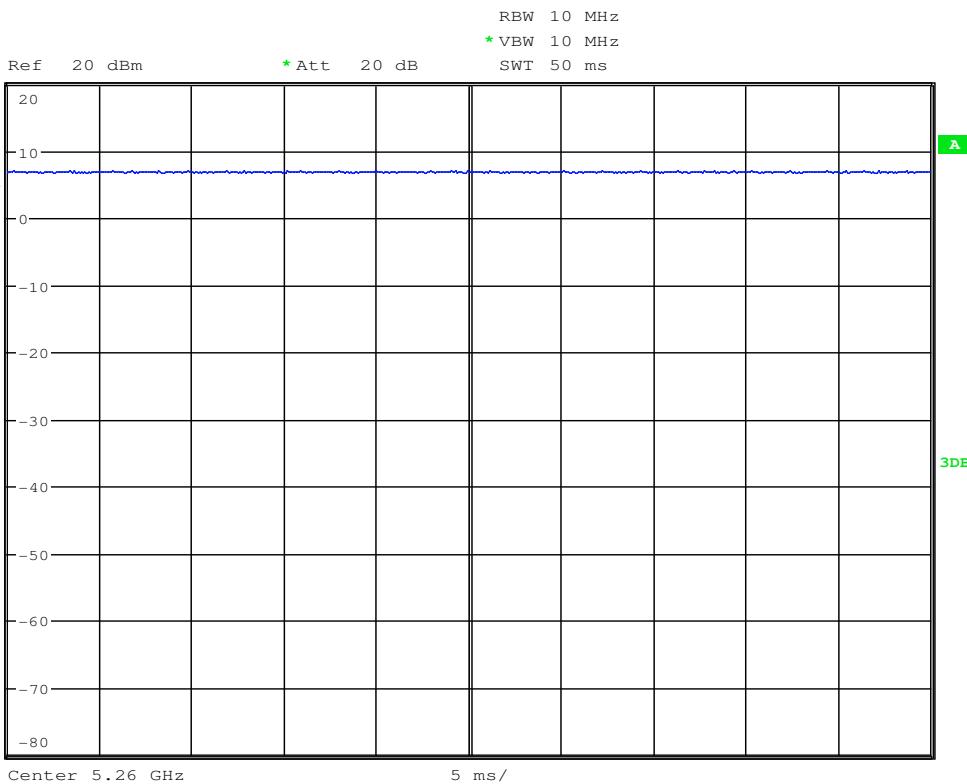
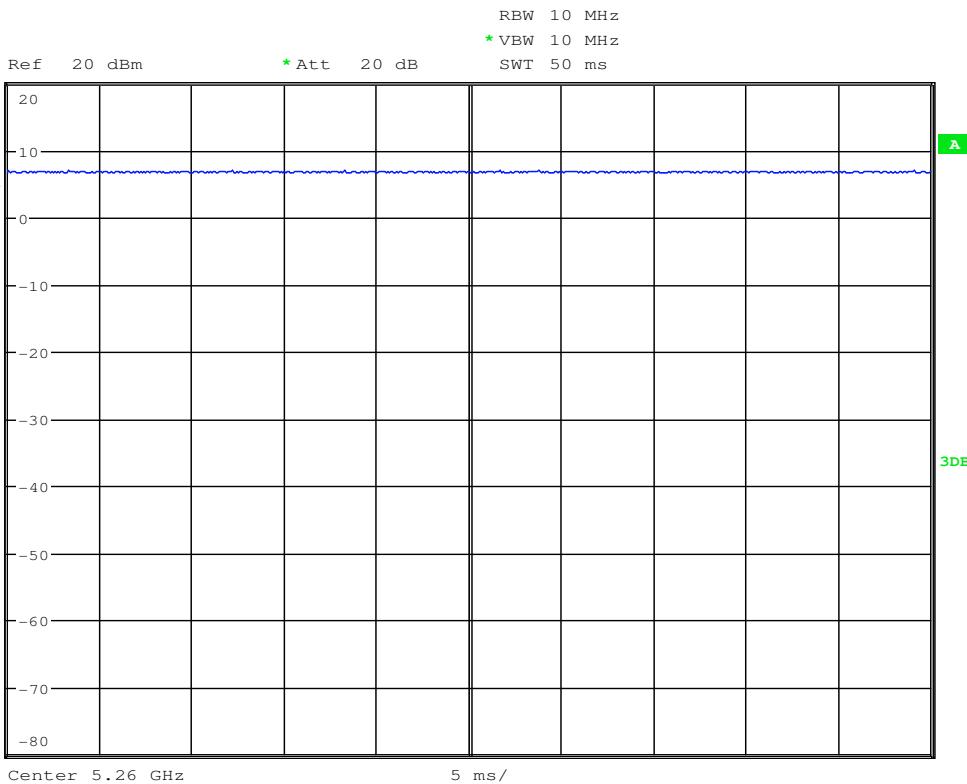


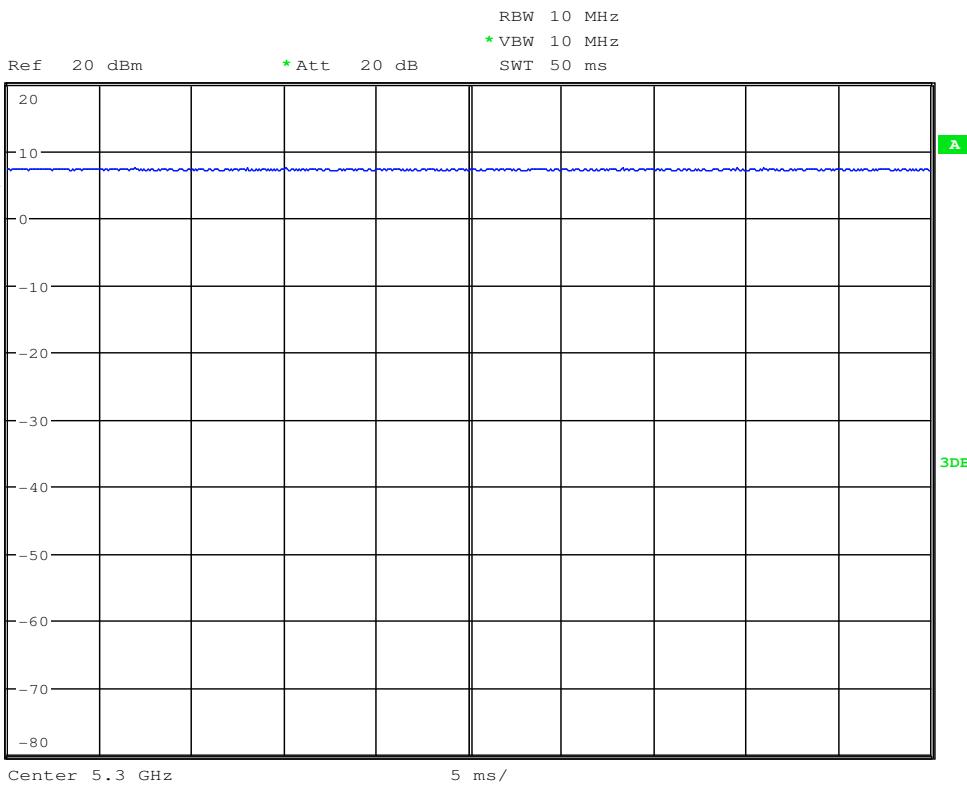
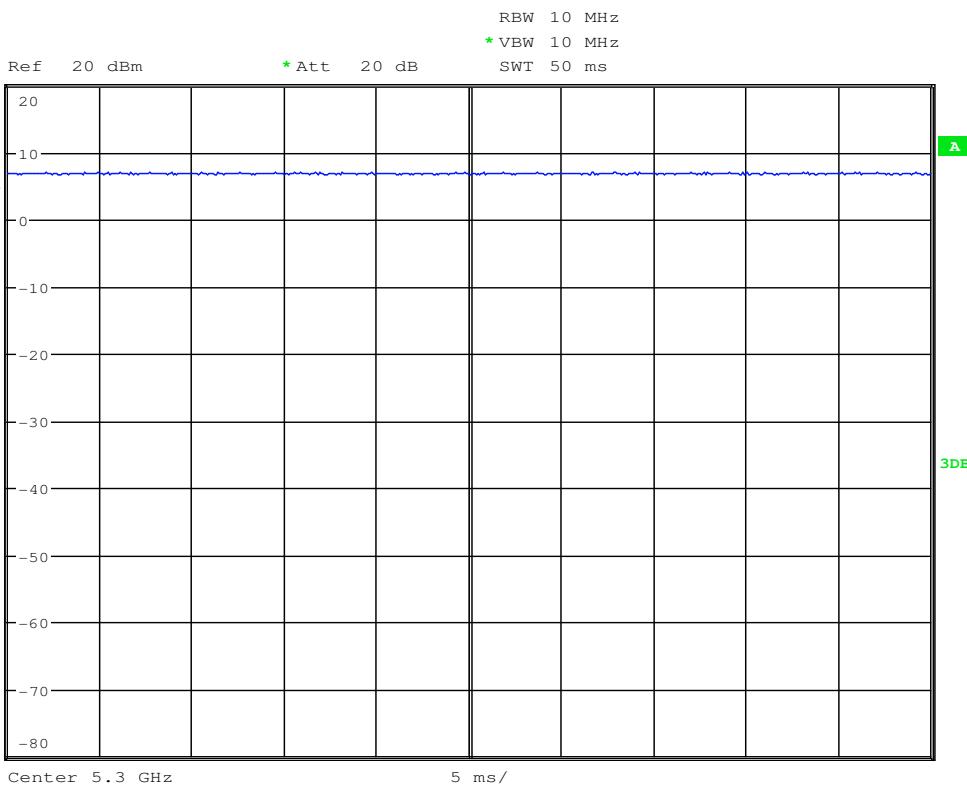


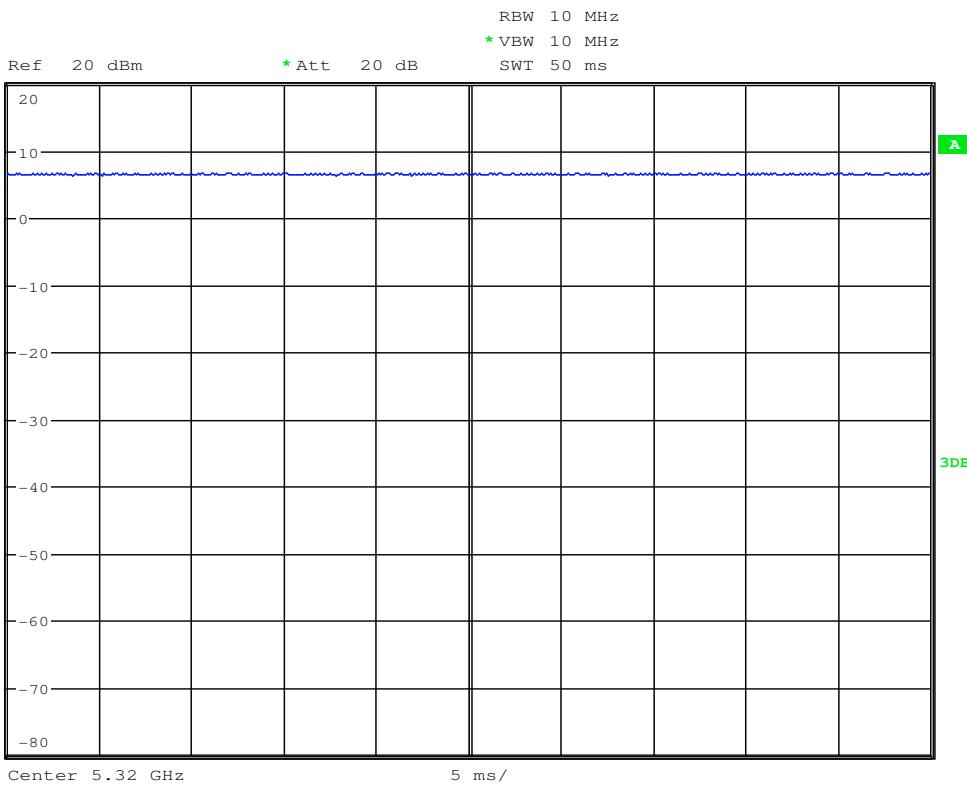
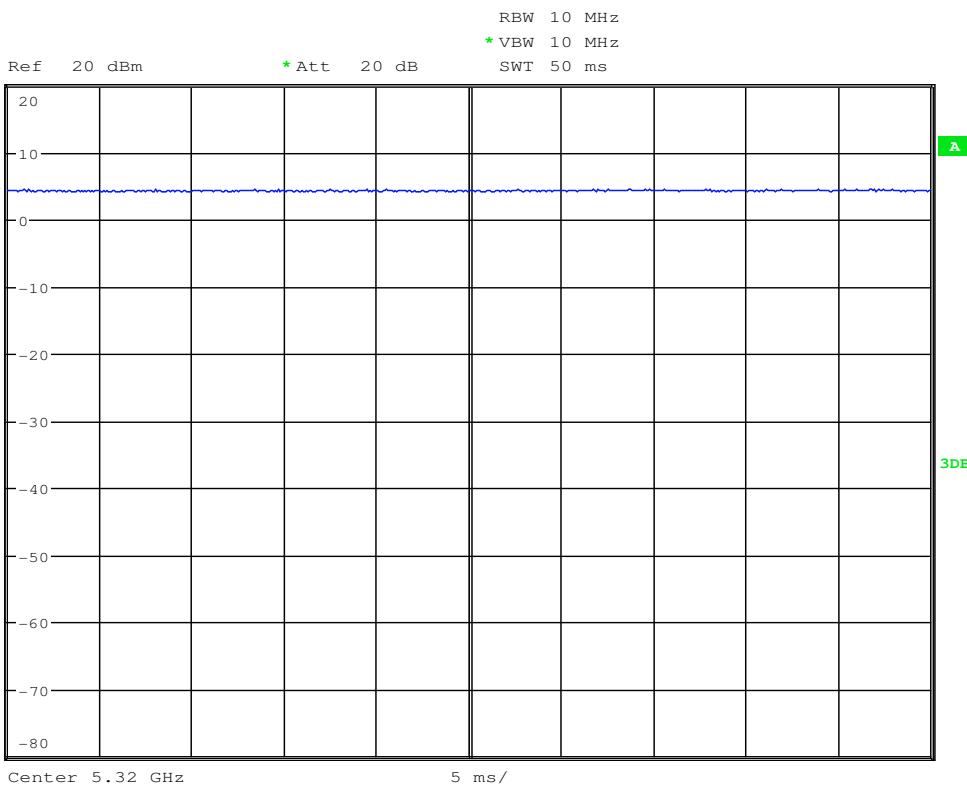
Duty Cycle_11N20_5180_Ant1**Duty Cycle_11N20_5180_Ant2**

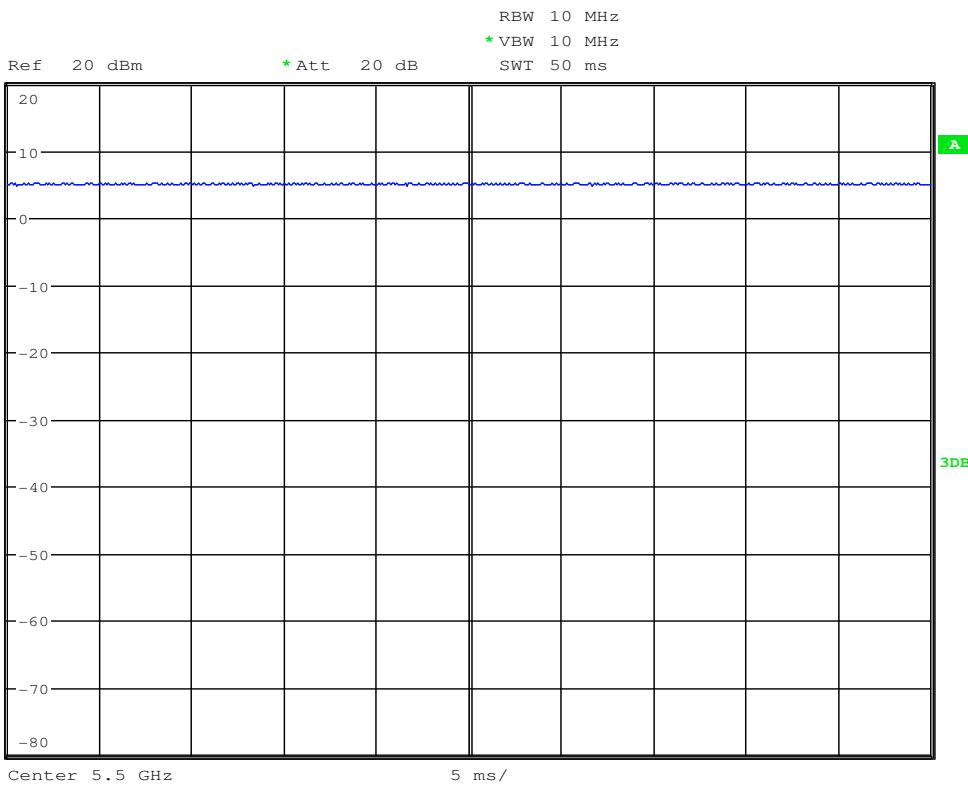
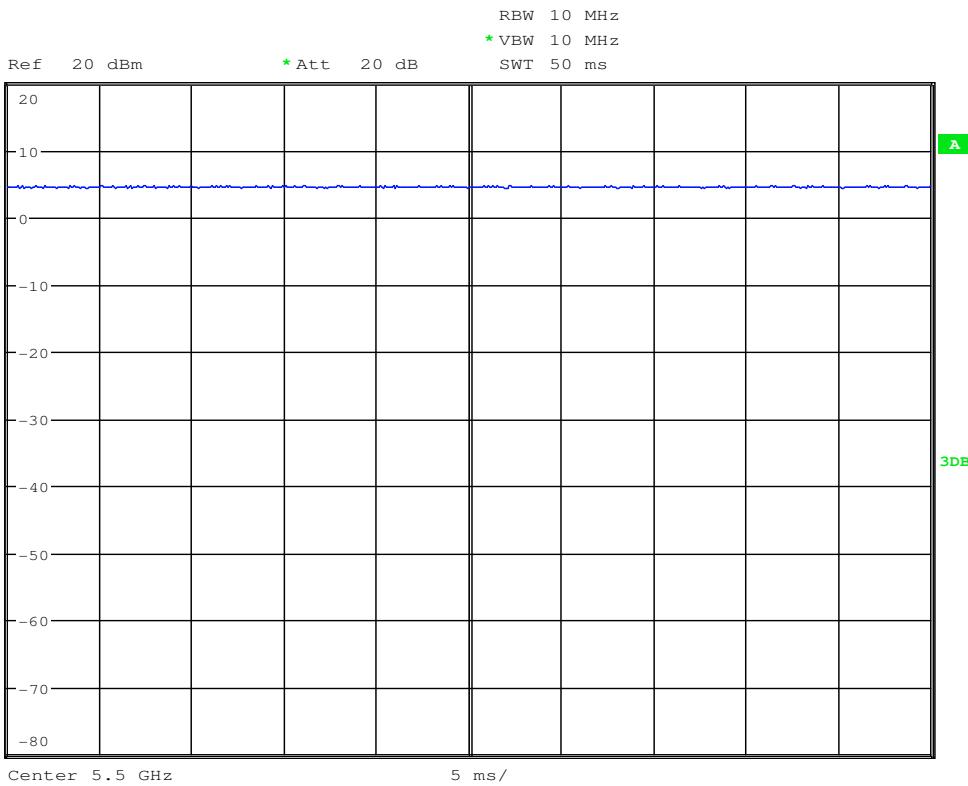
Duty Cycle_11N20_5220_Ant1**Duty Cycle_11N20_5220_Ant2**

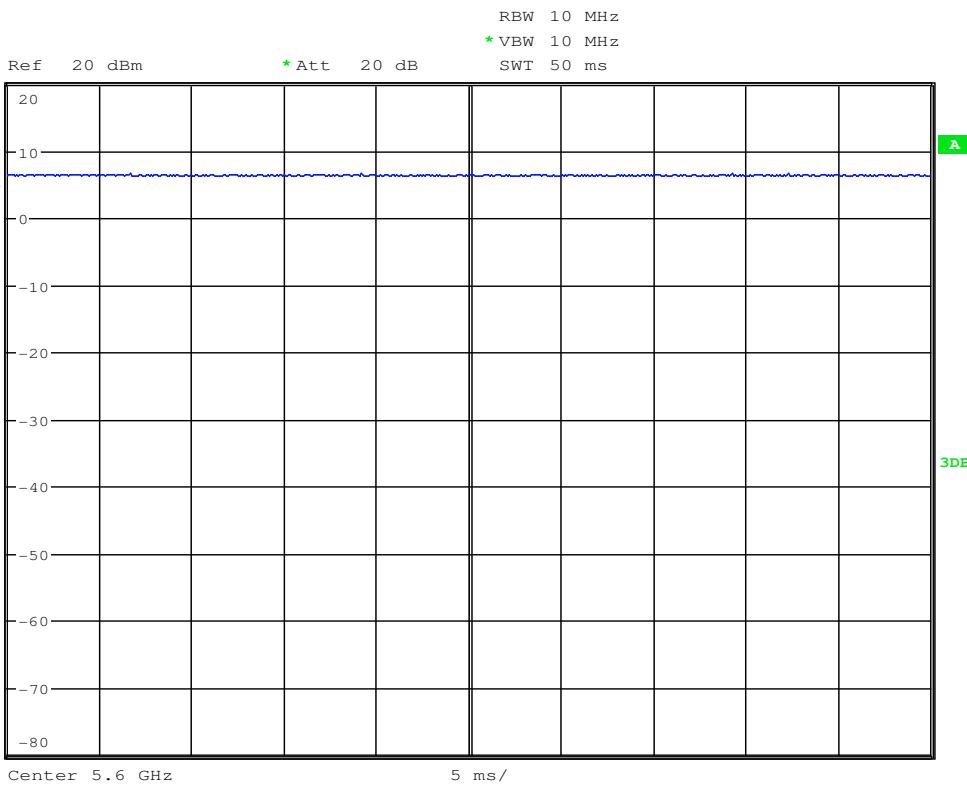
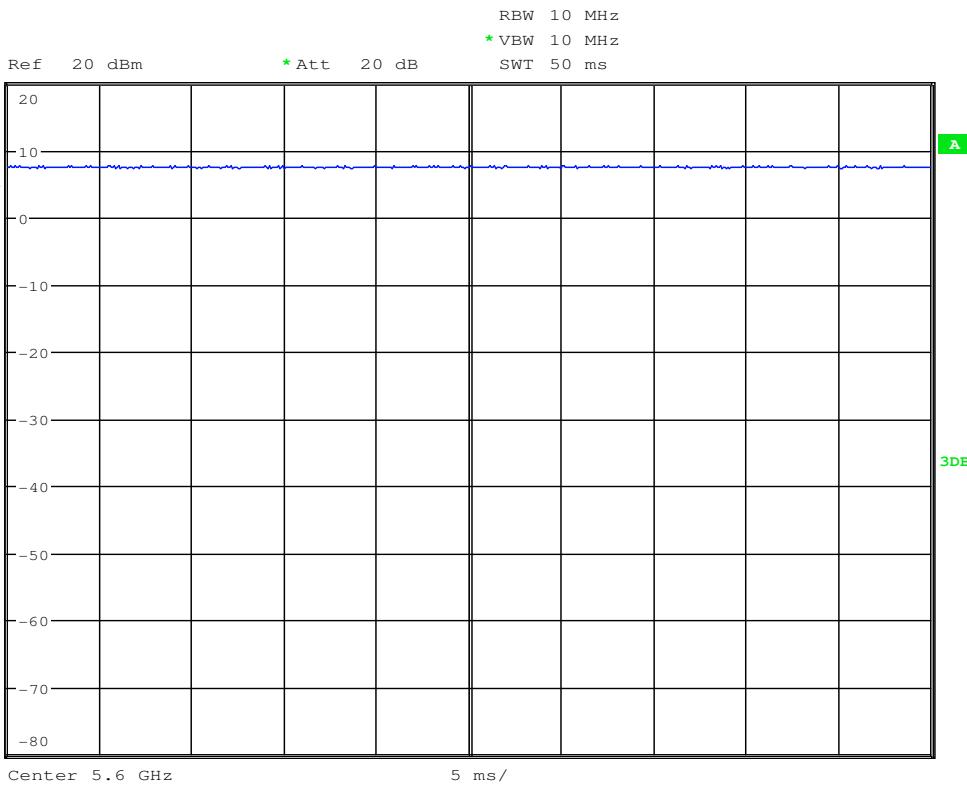
Duty Cycle_11N20_5240_Ant1**Duty Cycle_11N20_5240_Ant2**

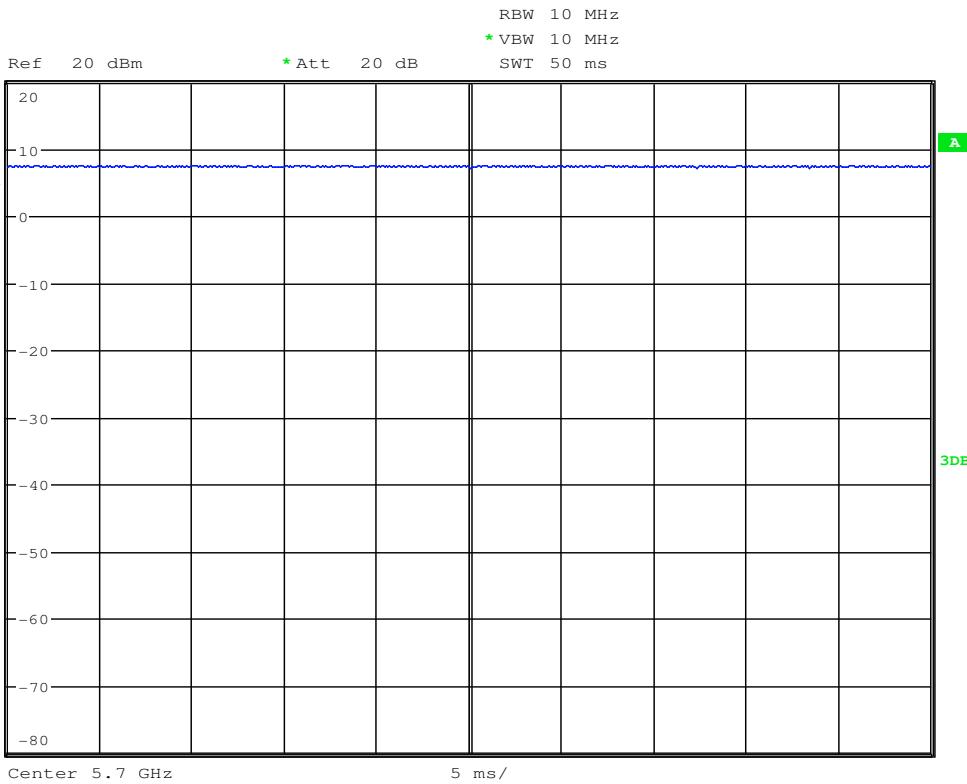
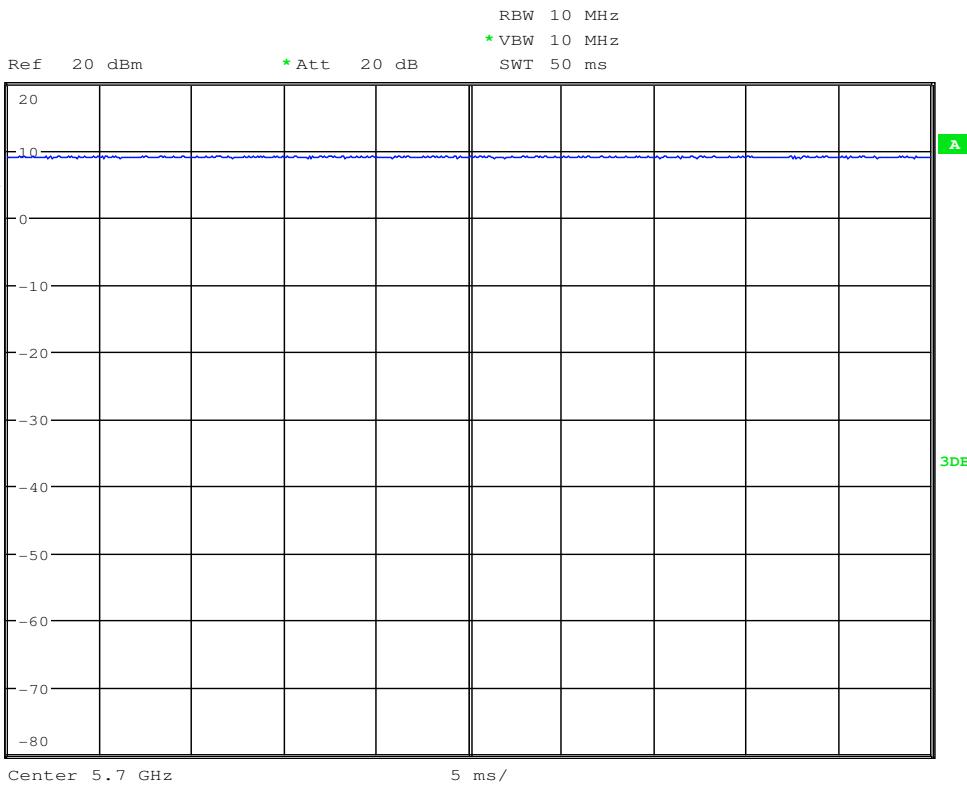
Duty Cycle_11N20_5260_Ant1**Duty Cycle_11N20_5260_Ant2**

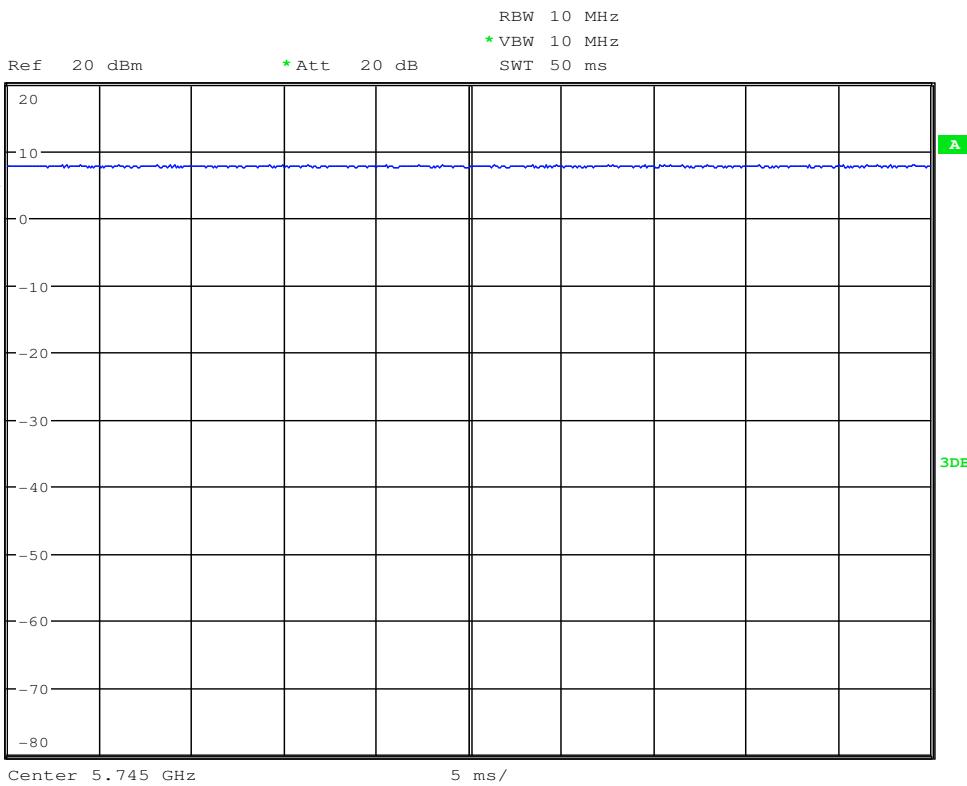
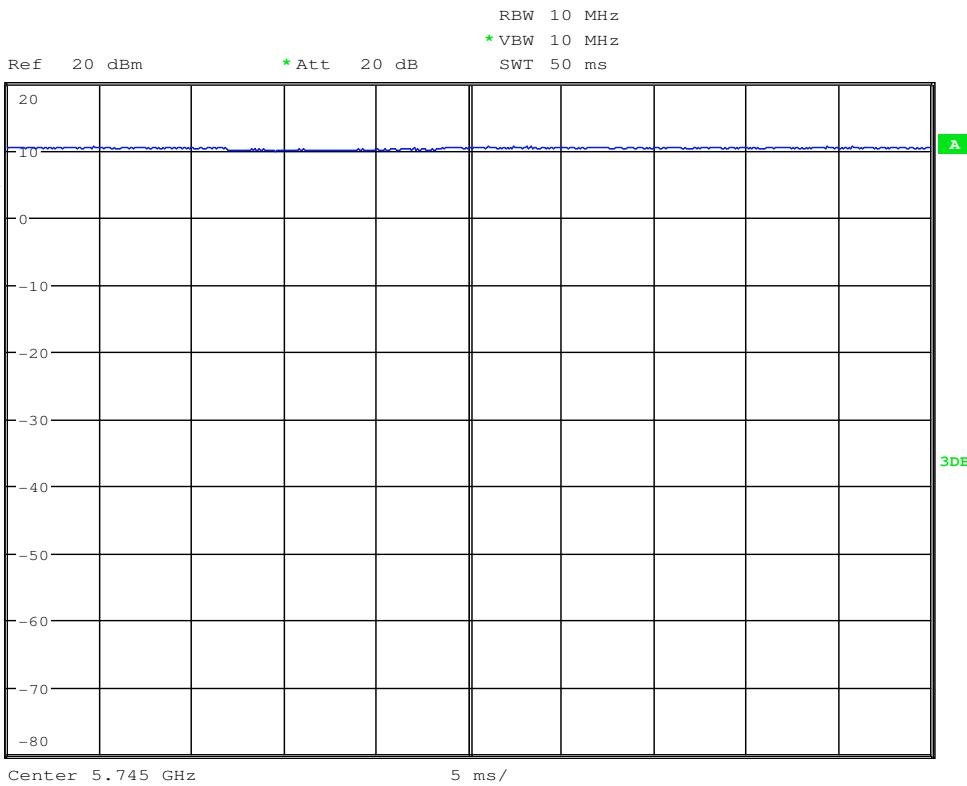
Duty Cycle_11N20_5300_Ant1**Duty Cycle_11N20_5300_Ant2**

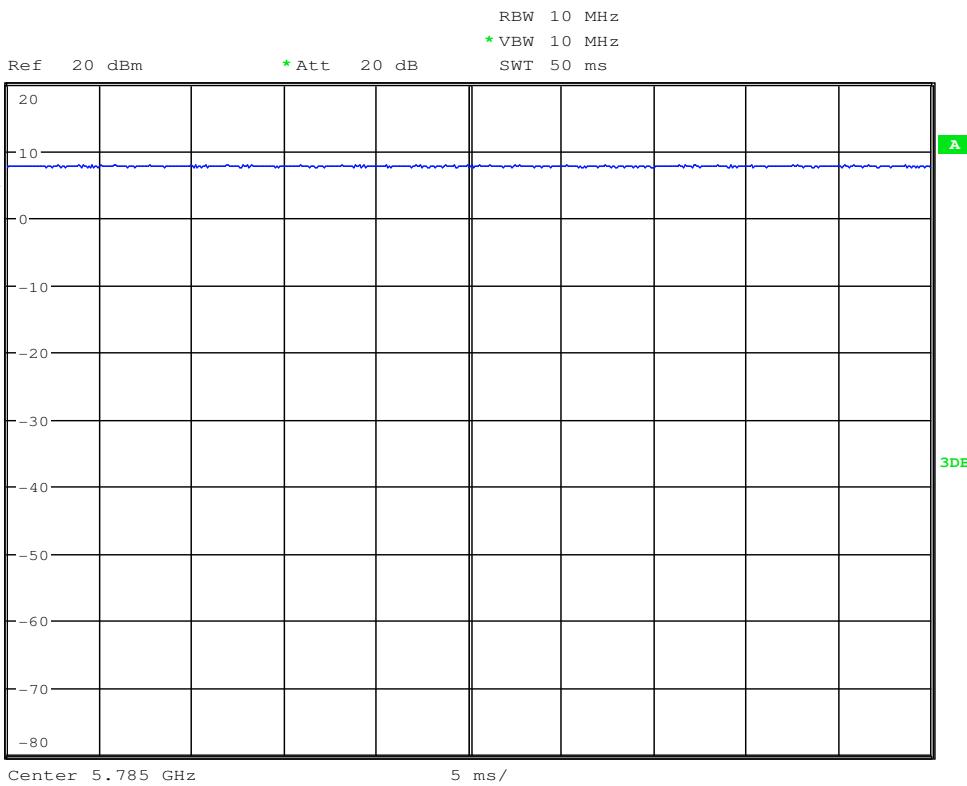
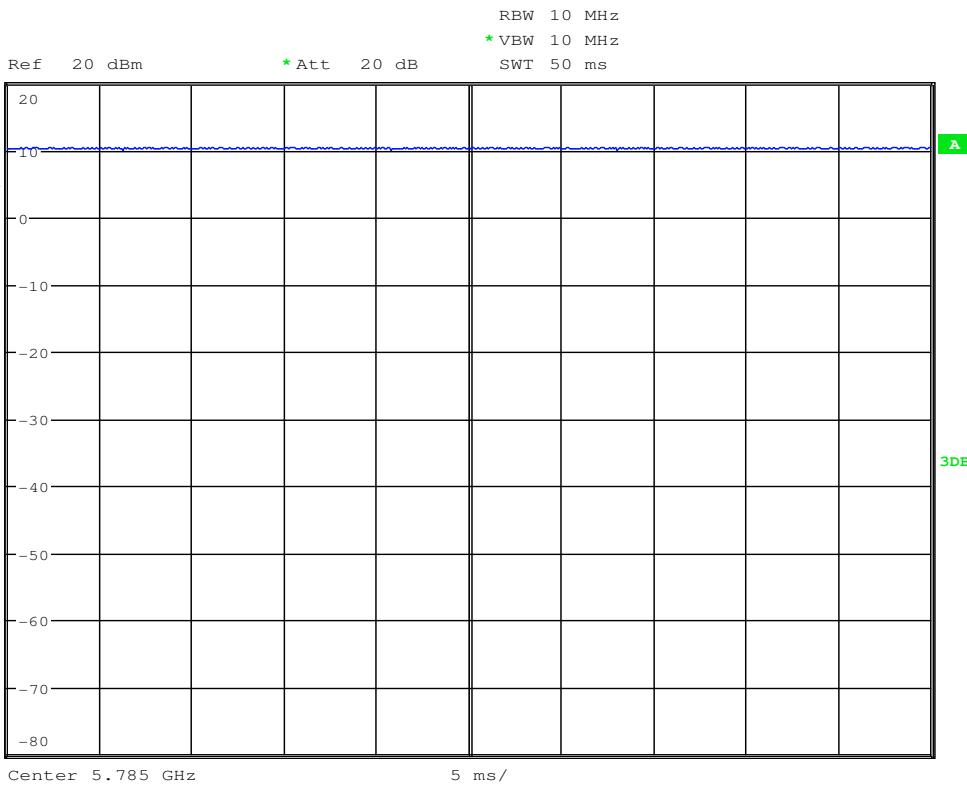
Duty Cycle_11N20_5320_Ant1**Duty Cycle_11N20_5320_Ant2**

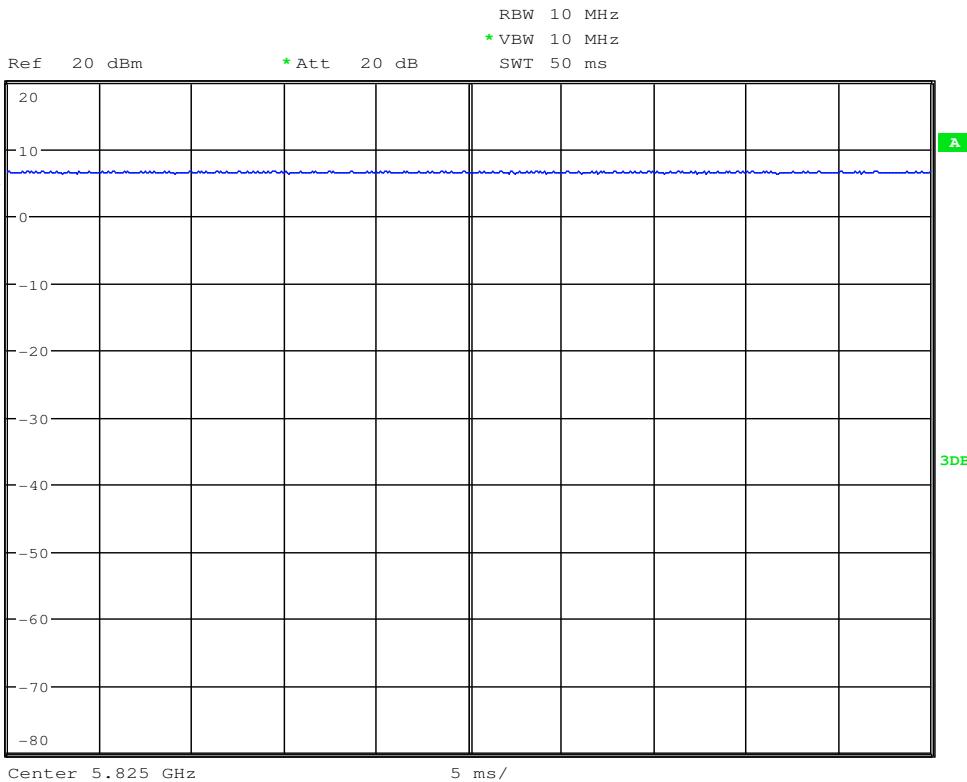
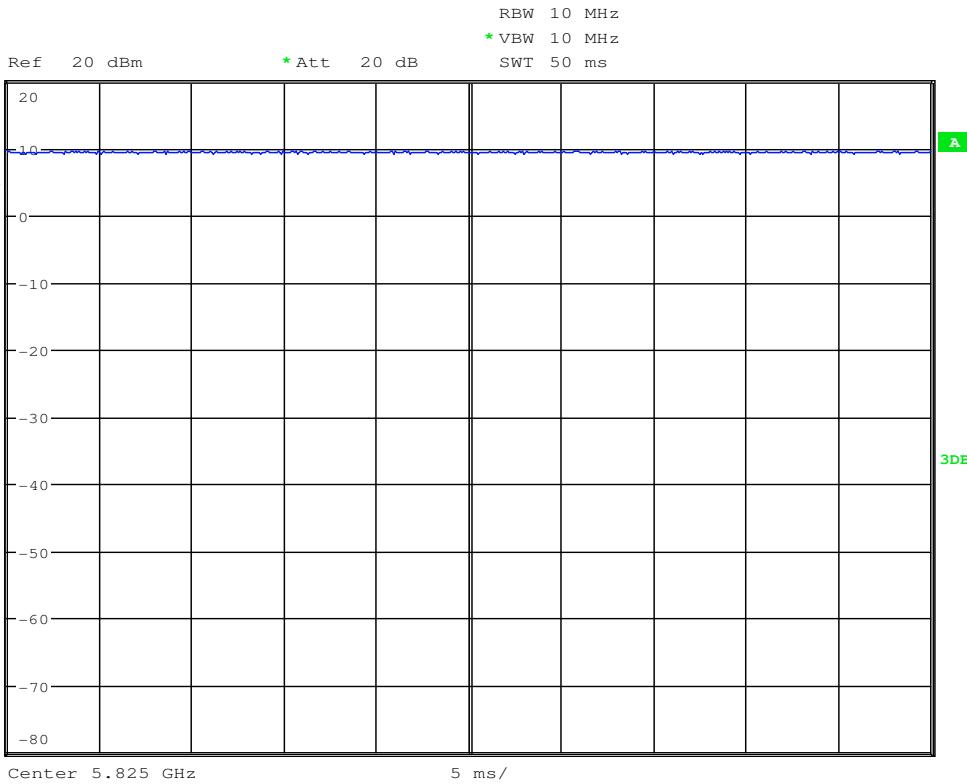
Duty Cycle_11N20_5500_Ant1**Duty Cycle_11N20_5500_Ant2**

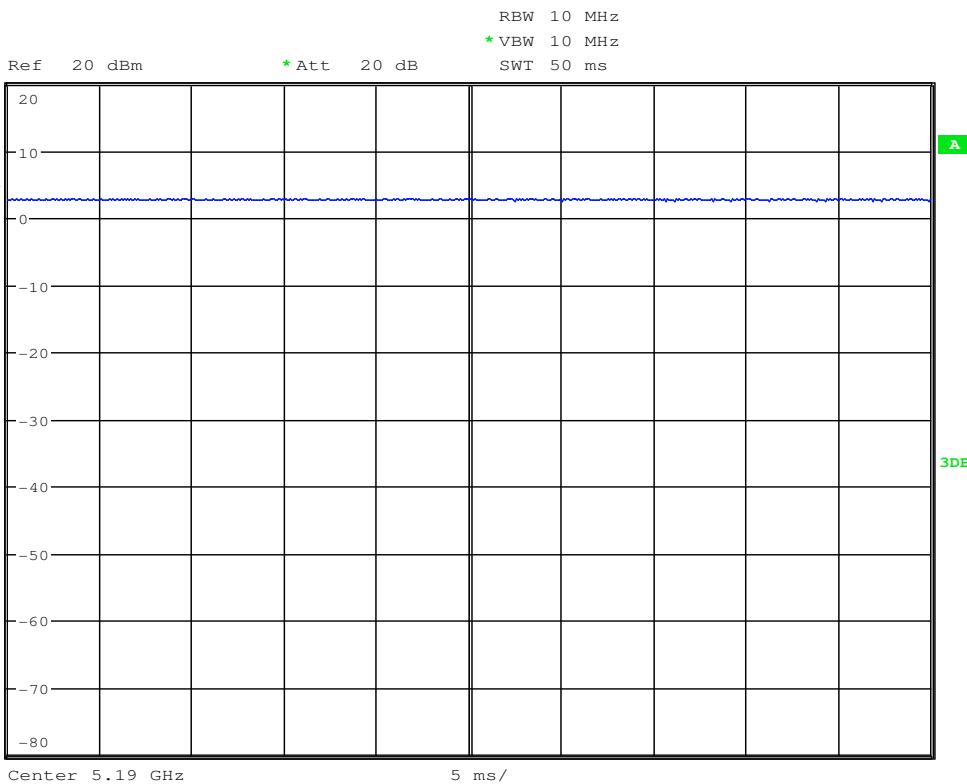
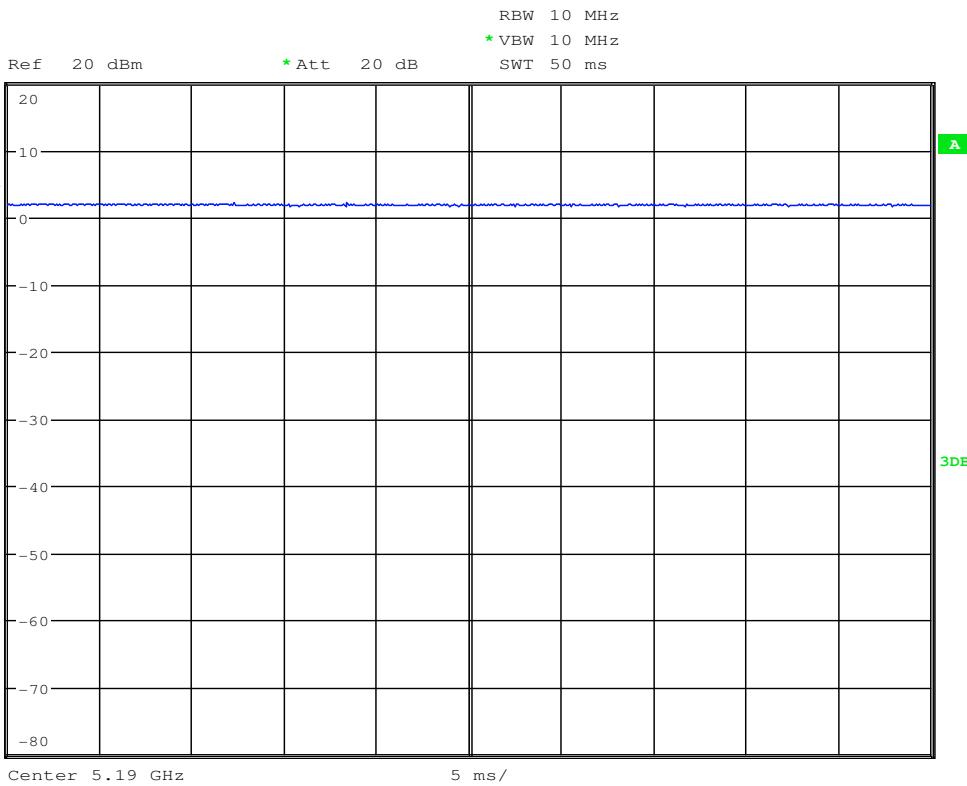
Duty Cycle_11N20_5600_Ant1**Duty Cycle_11N20_5600_Ant2**

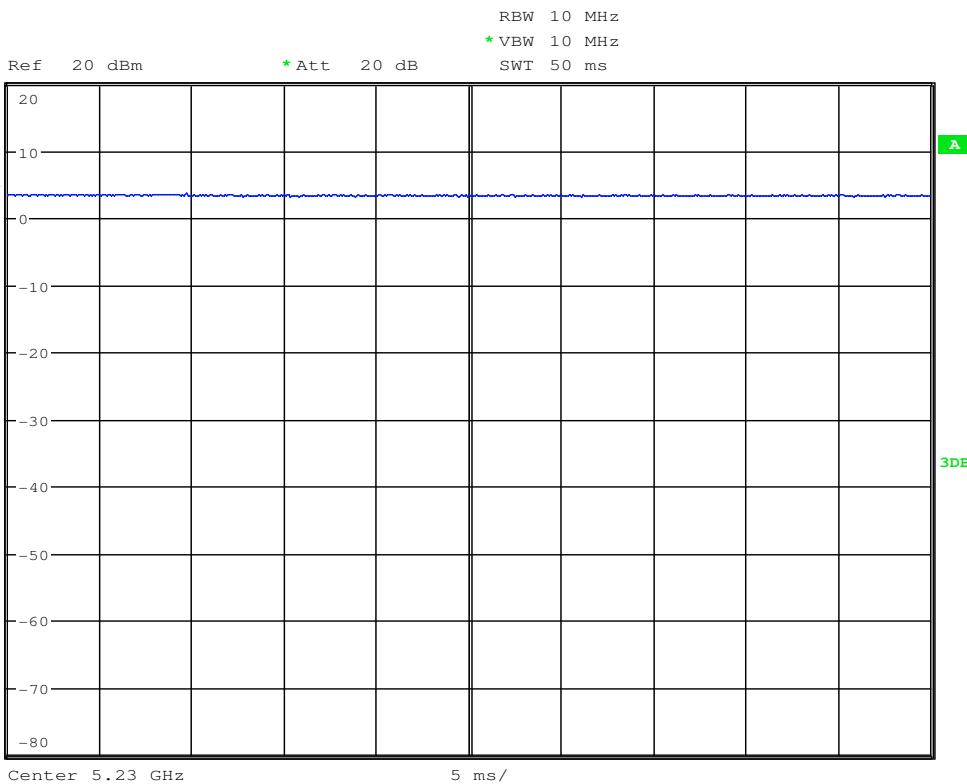
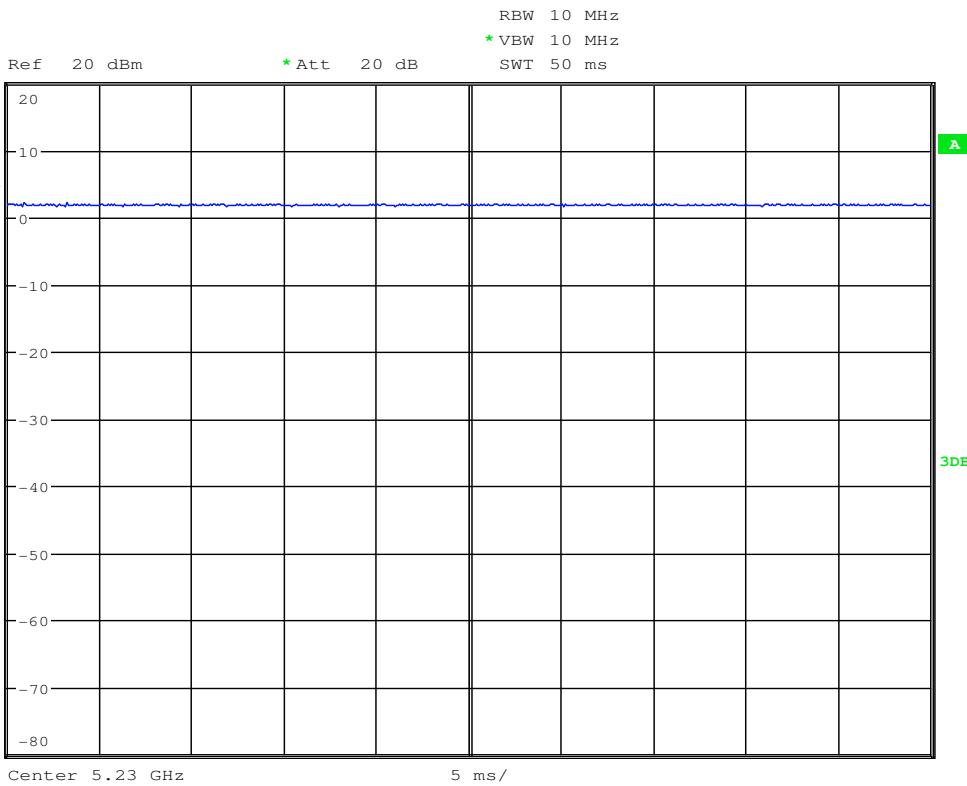
Duty Cycle_11N20_5700_Ant1**Duty Cycle_11N20_5700_Ant2**

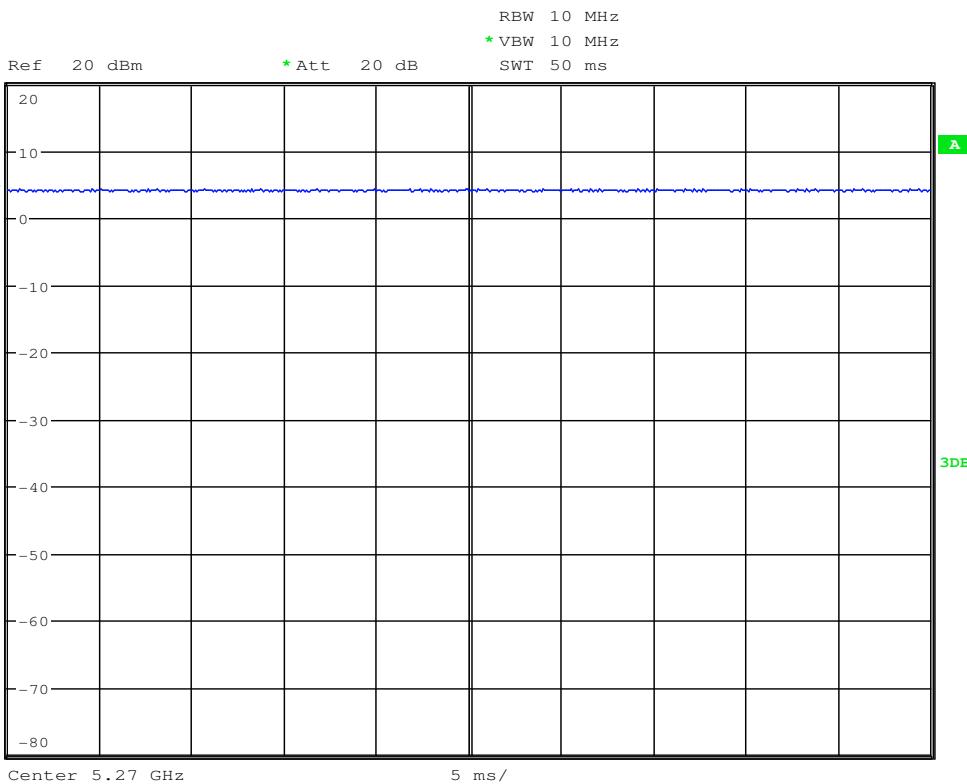
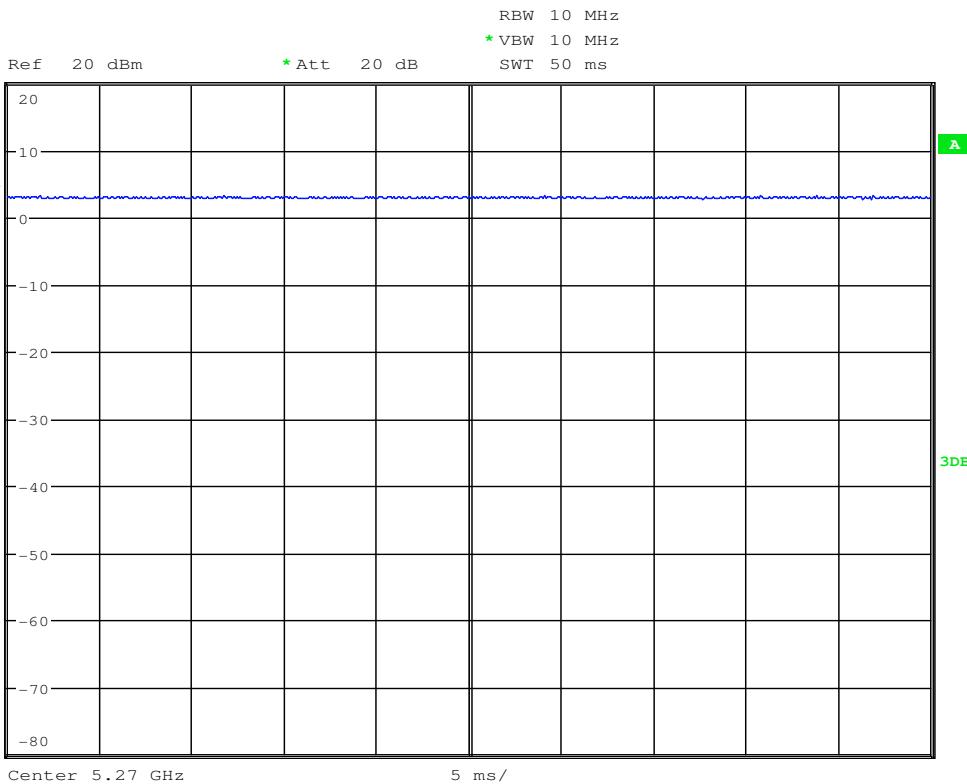
Duty Cycle_11N20_5745_Ant1**Duty Cycle_11N20_5745_Ant2**

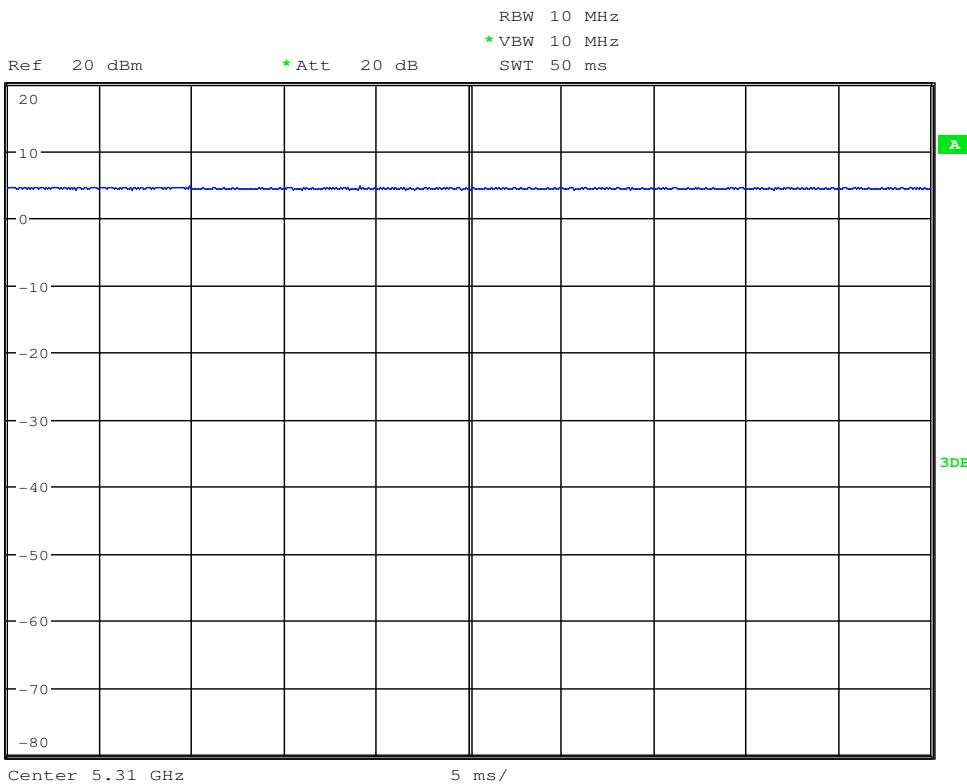
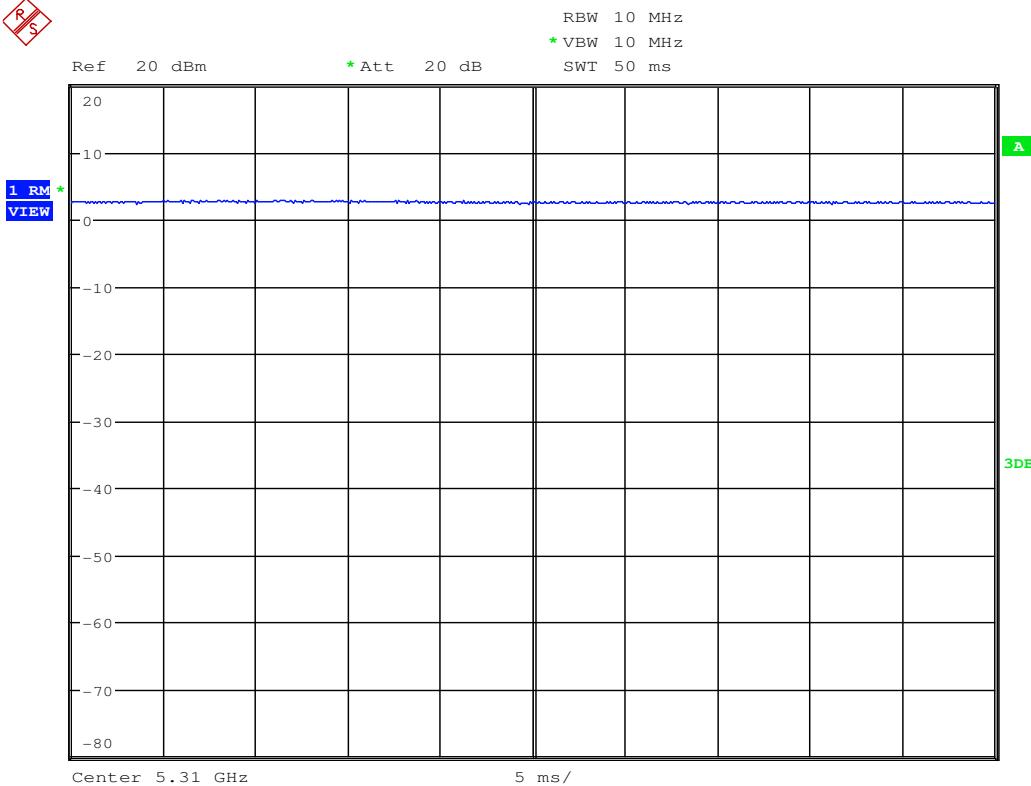
Duty Cycle_11N20_5785_Ant1**Duty Cycle_11N20_5785_Ant2**

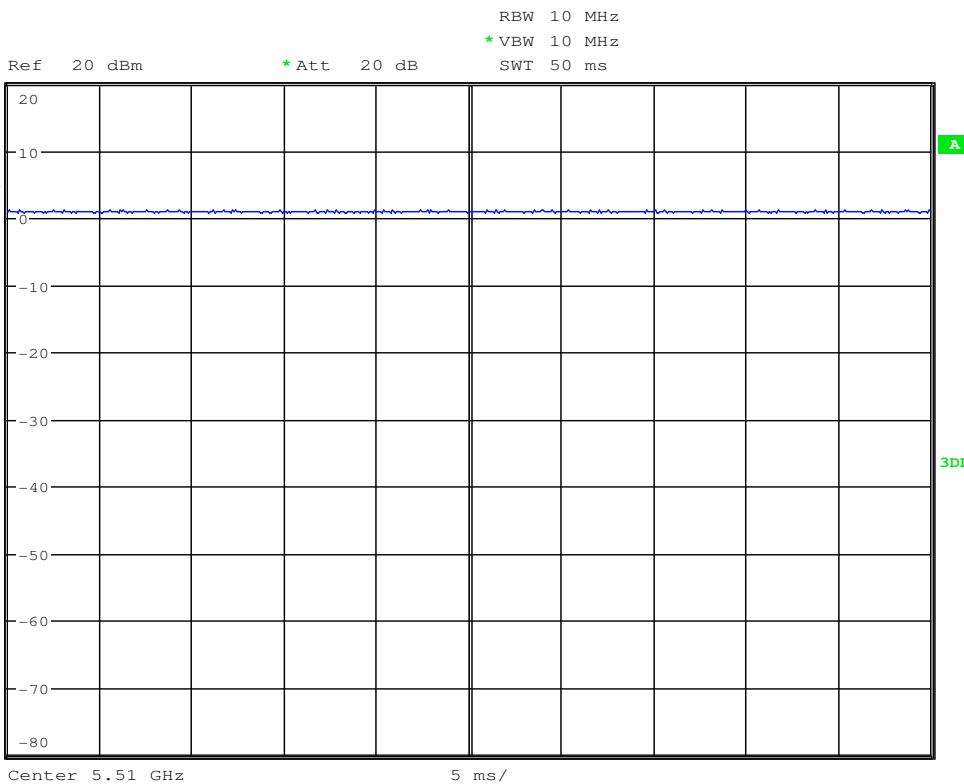
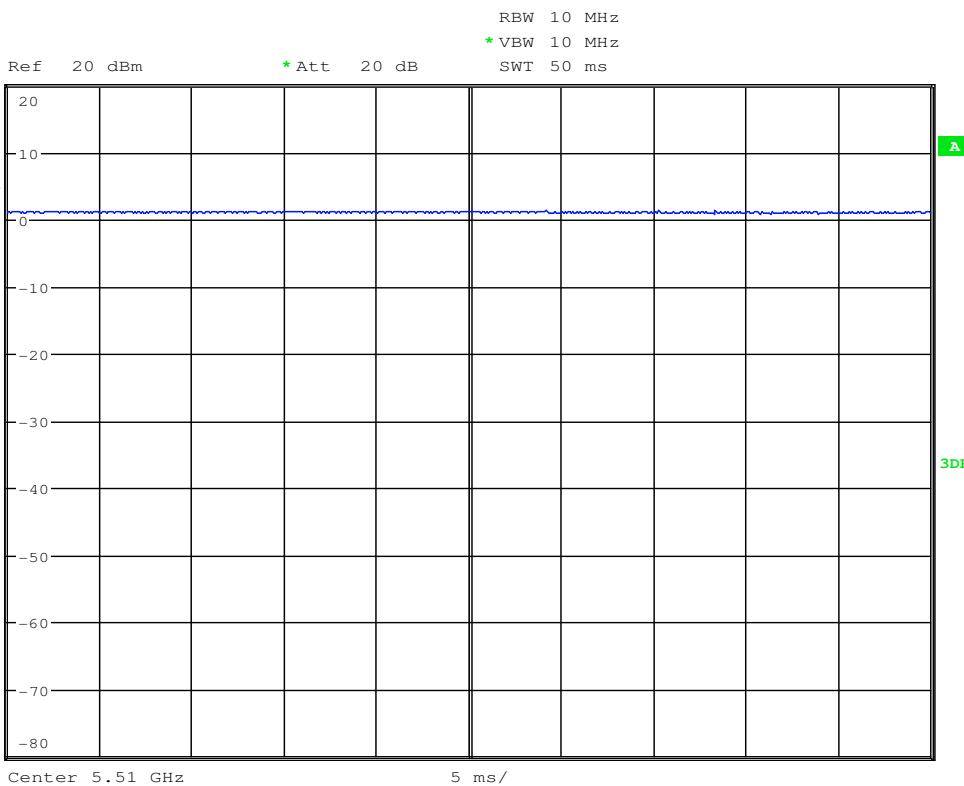
Duty Cycle_11N20_5825_Ant1**Duty Cycle_11N20_5825_Ant2**

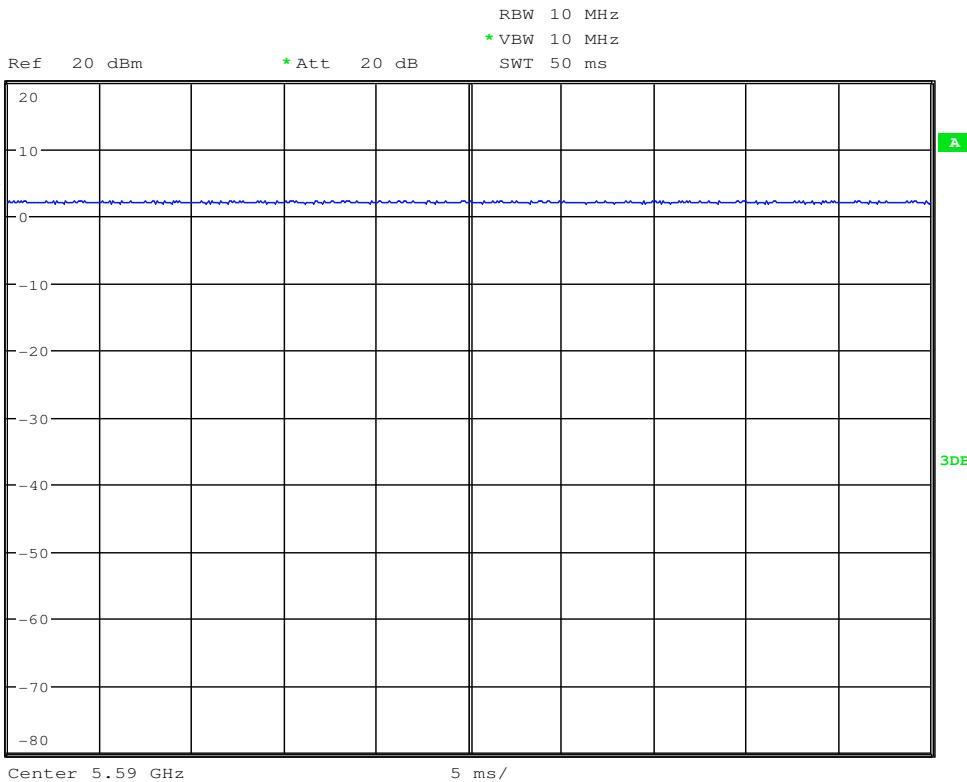
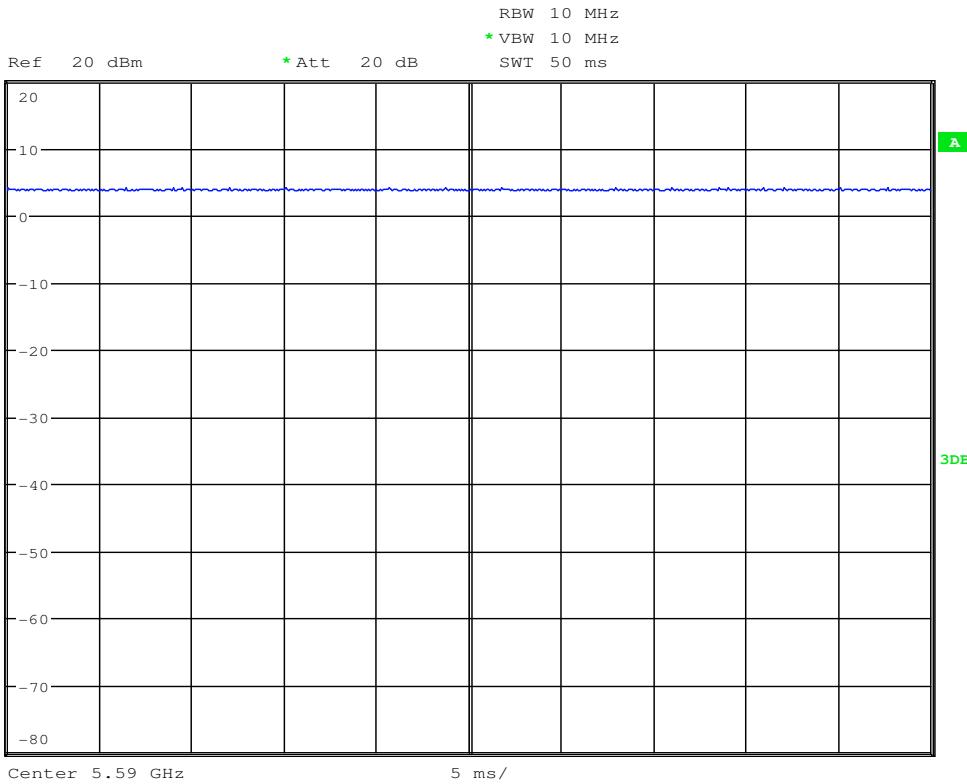
Duty Cycle_11N40_5190_Ant1**Duty Cycle_11N40_5190_Ant2**

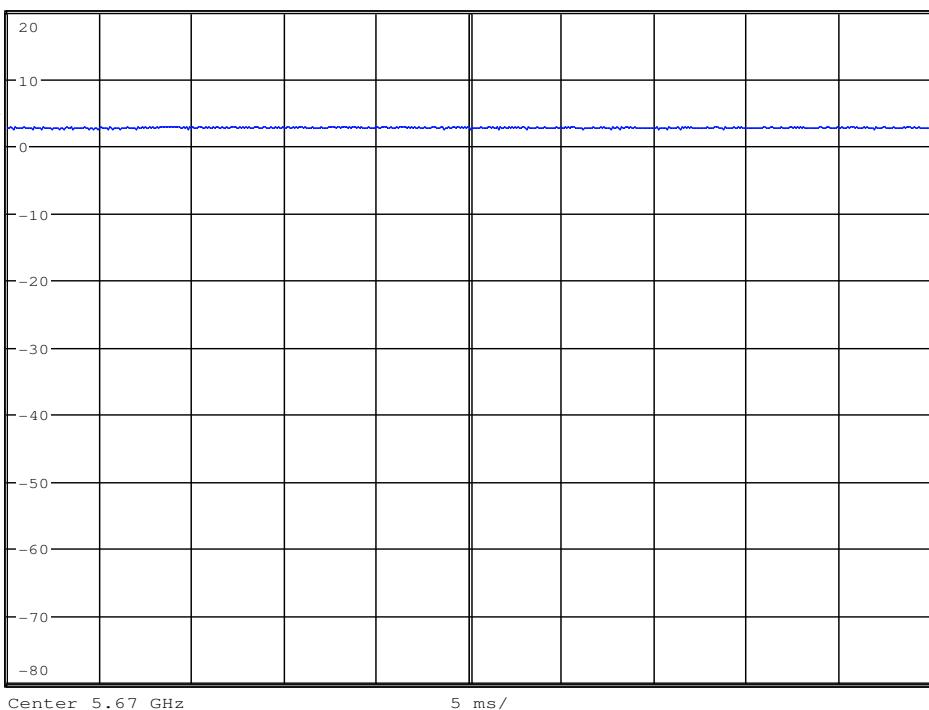
Duty Cycle_11N40_5230_Ant1**Duty Cycle_11N40_5230_Ant2**

Duty Cycle_11N40_5270_Ant1**Duty Cycle_11N40_5270_Ant2**

Duty Cycle_11N40_5310_Ant1**Duty Cycle_11N40_5310_Ant2**

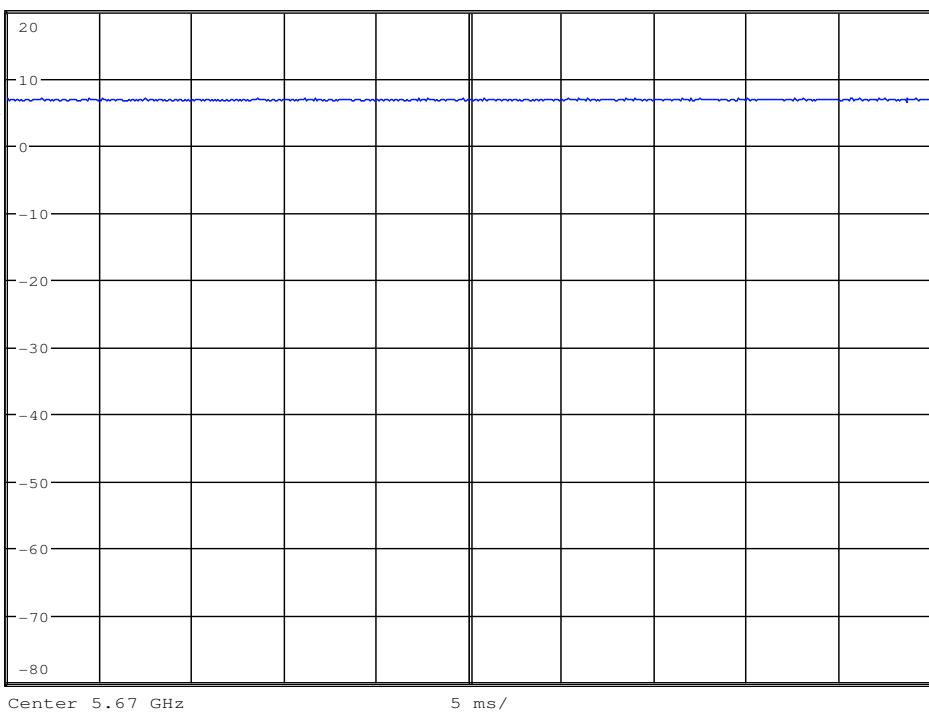
Duty Cycle_11N40_5510_Ant1**Duty Cycle_11N40_5510_Ant2**

Duty Cycle_11N40_5590_Ant1**Duty Cycle_11N40_5590_Ant2**

Duty Cycle_11N40_5670_Ant1RBW 10 MHz
* VBW 10 MHz
SWT 50 msRef 20 dBm
1 RM *
VIEW

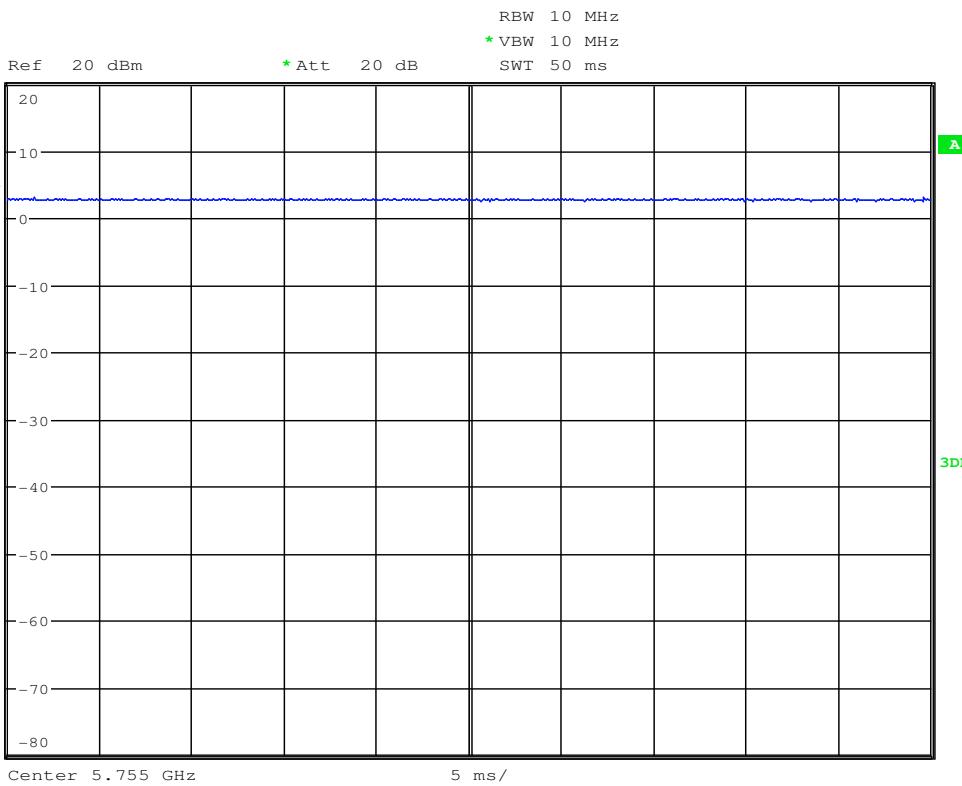
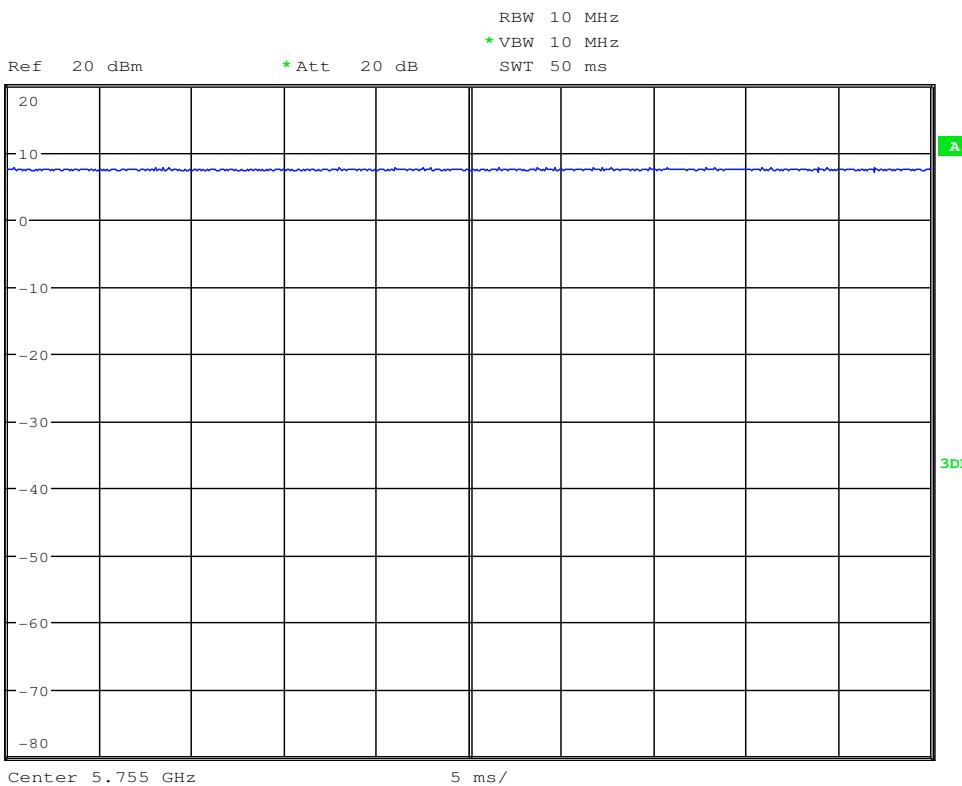
A

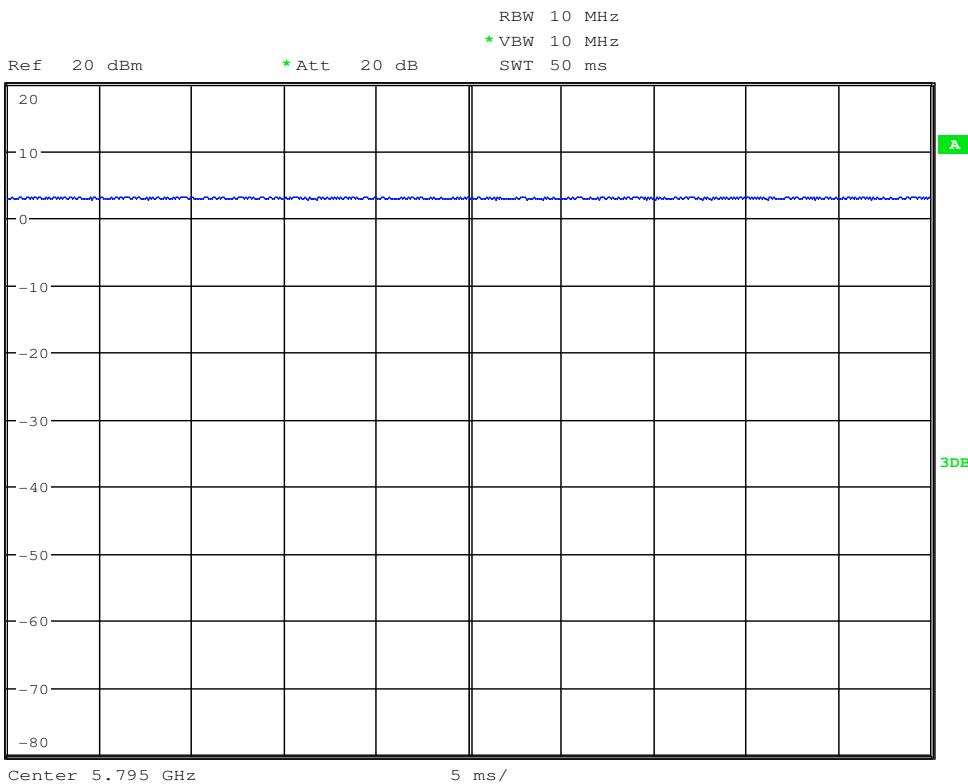
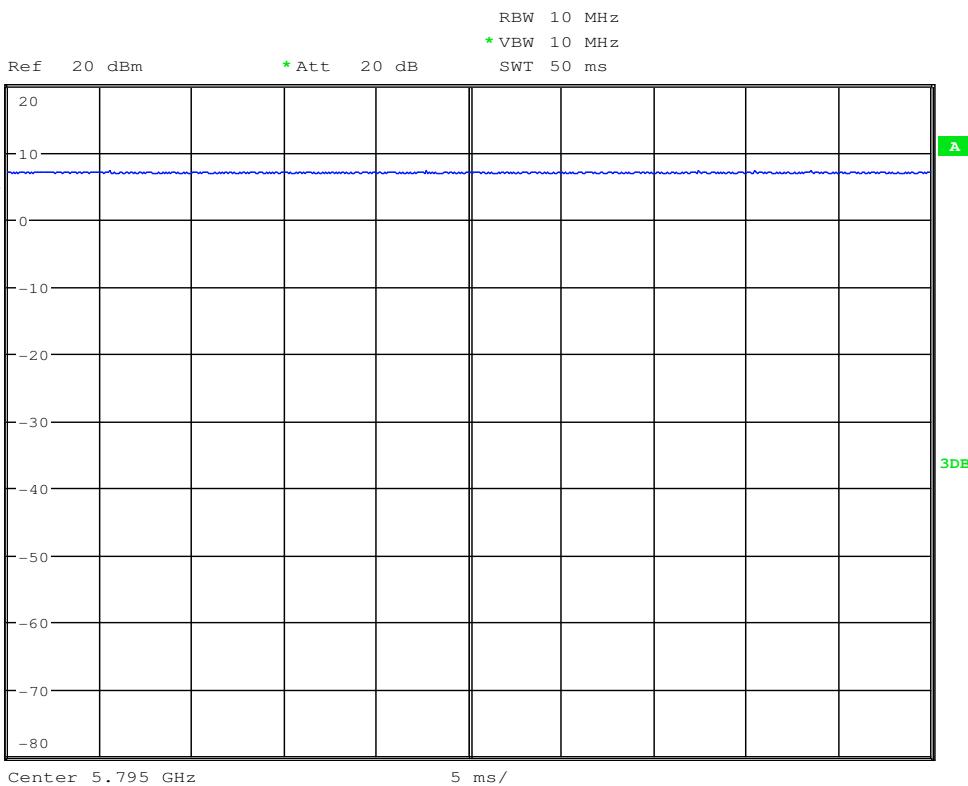
3dB

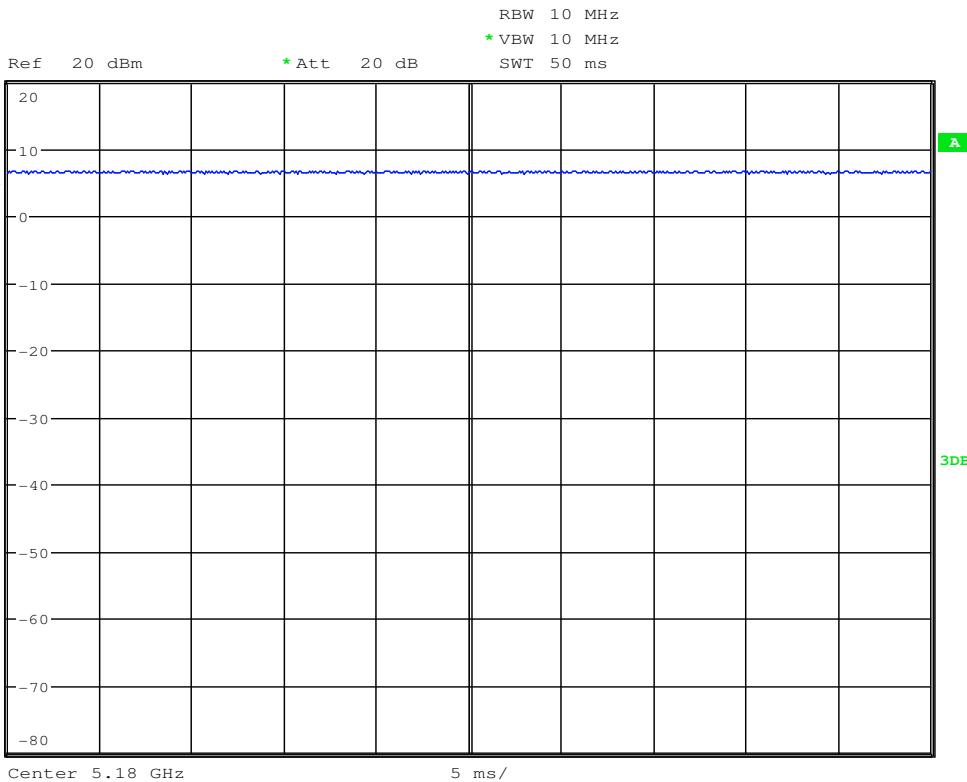
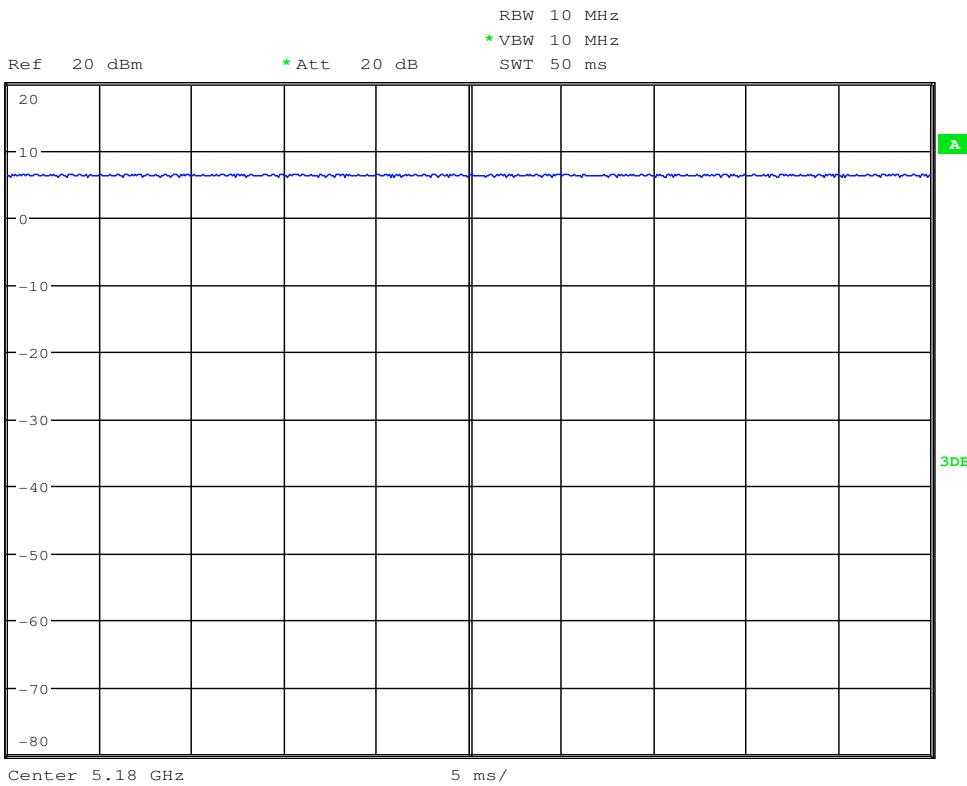
Duty Cycle_11N40_5670_Ant2RBW 10 MHz
* VBW 10 MHz
SWT 50 msRef 20 dBm
1 RM *
VIEW

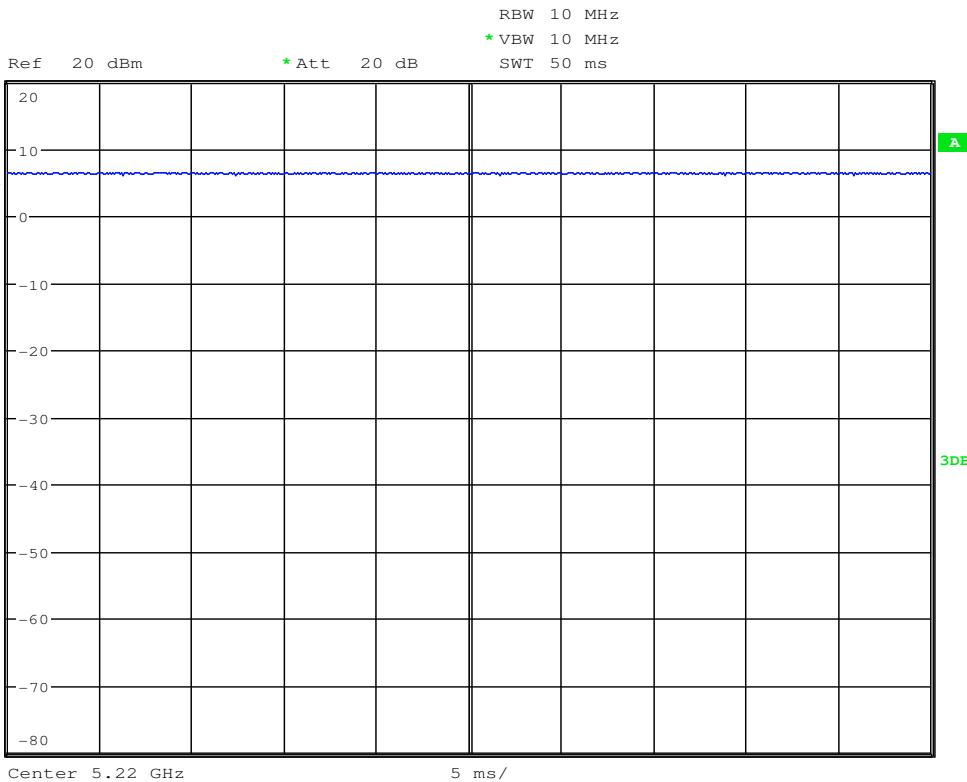
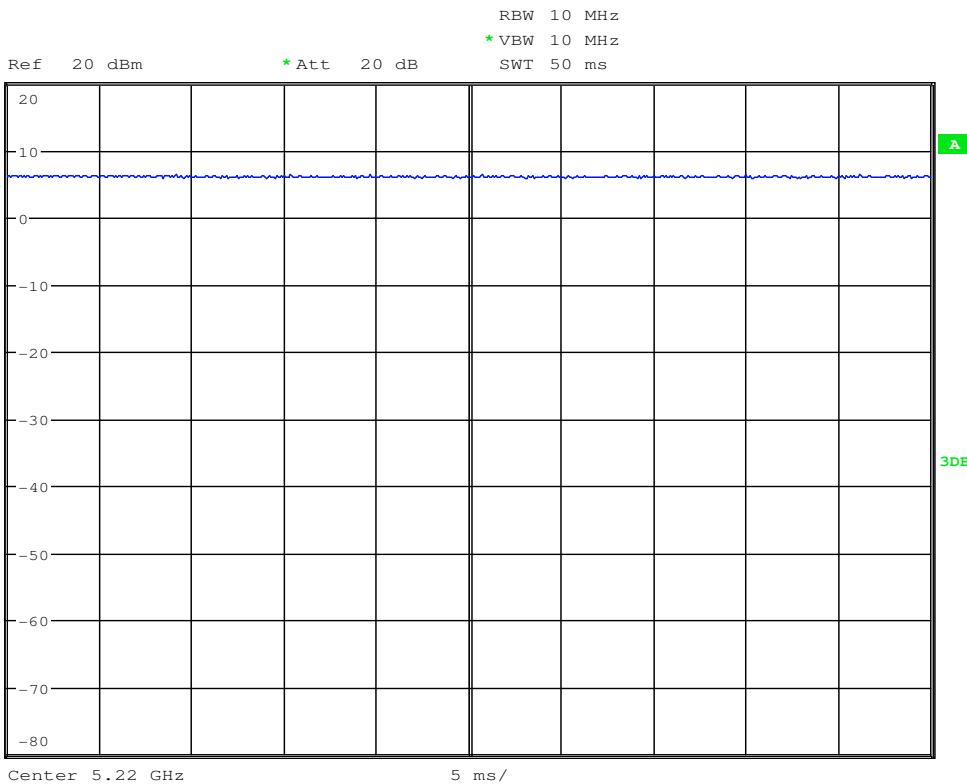
A

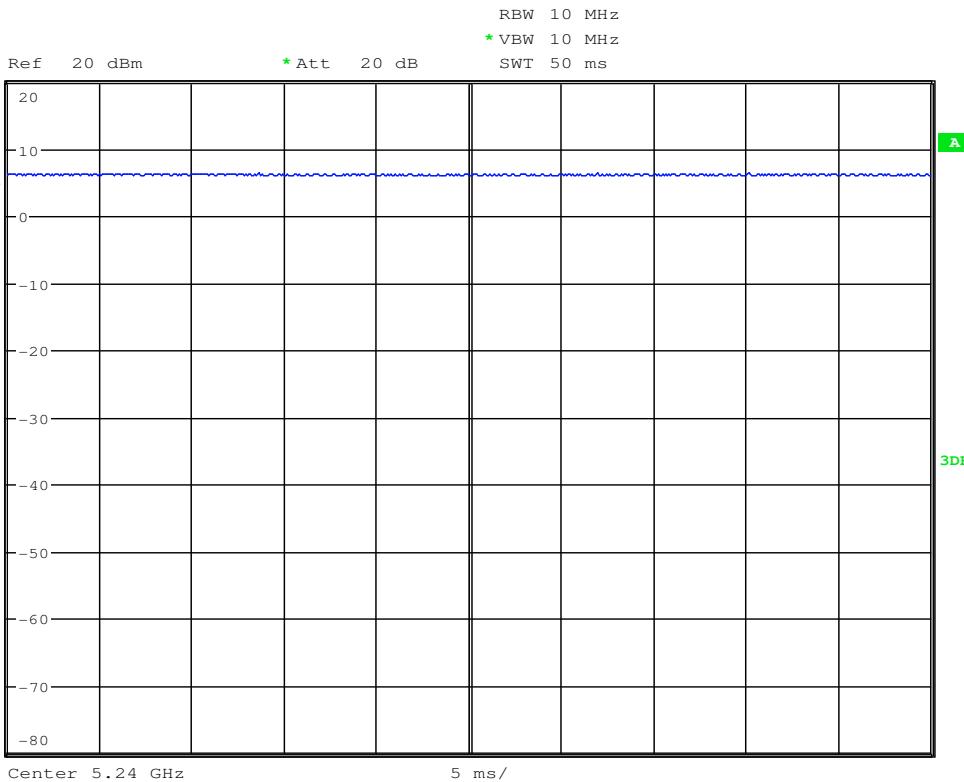
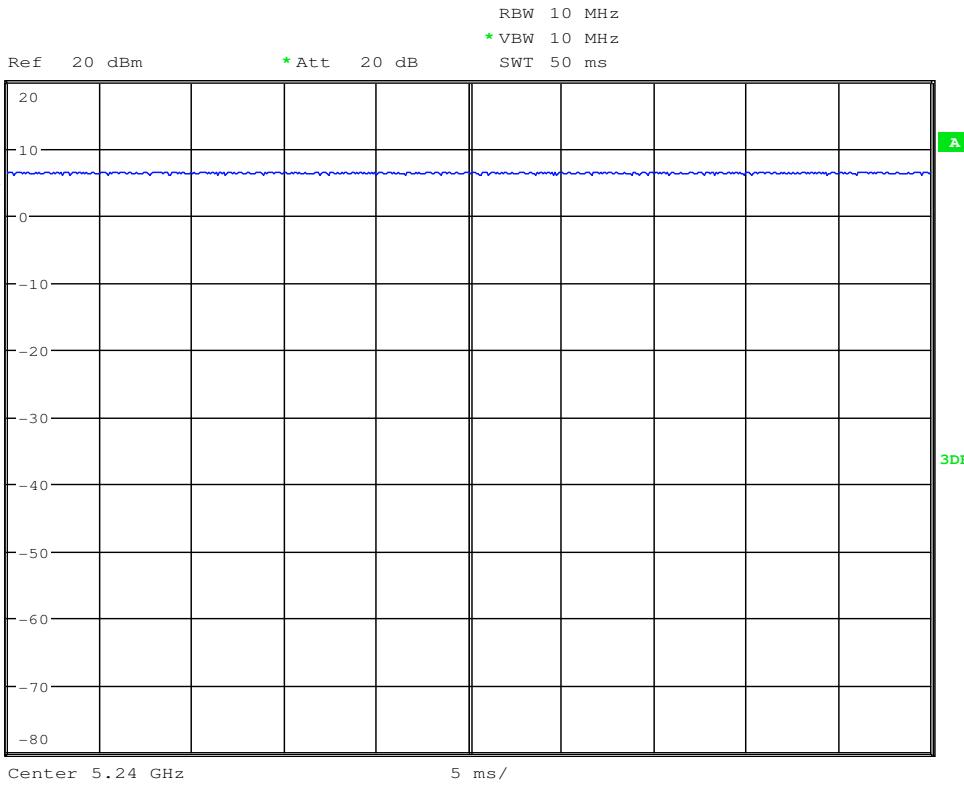
3dB

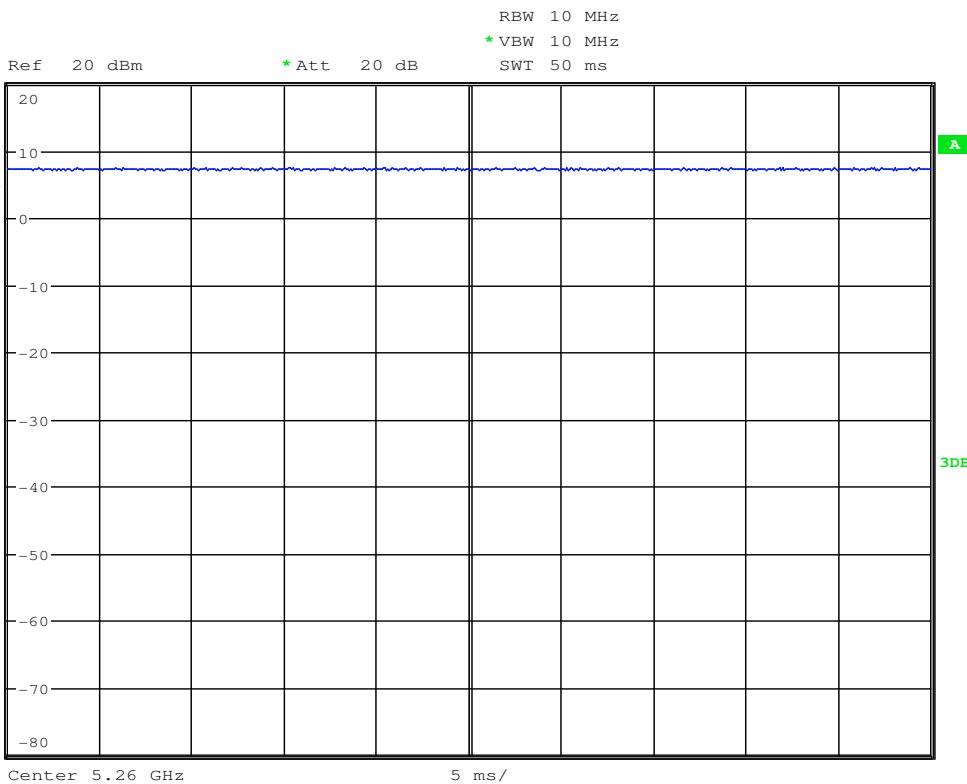
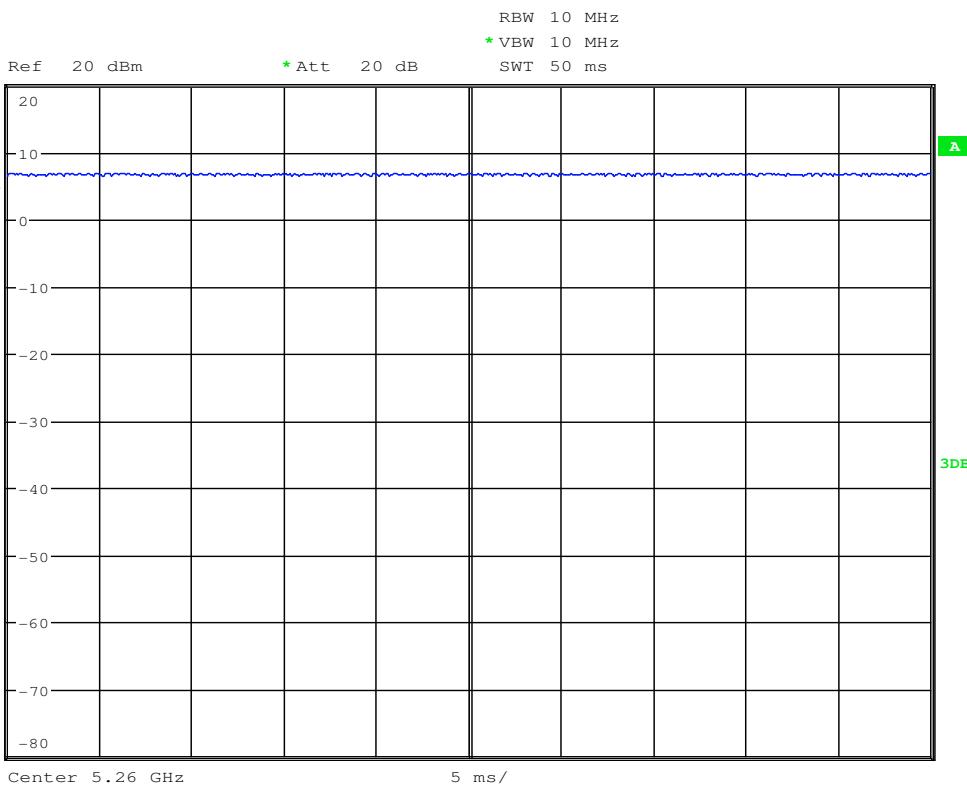
Duty Cycle_11N40_5755_Ant1**Duty Cycle_11N40_5755_Ant2**

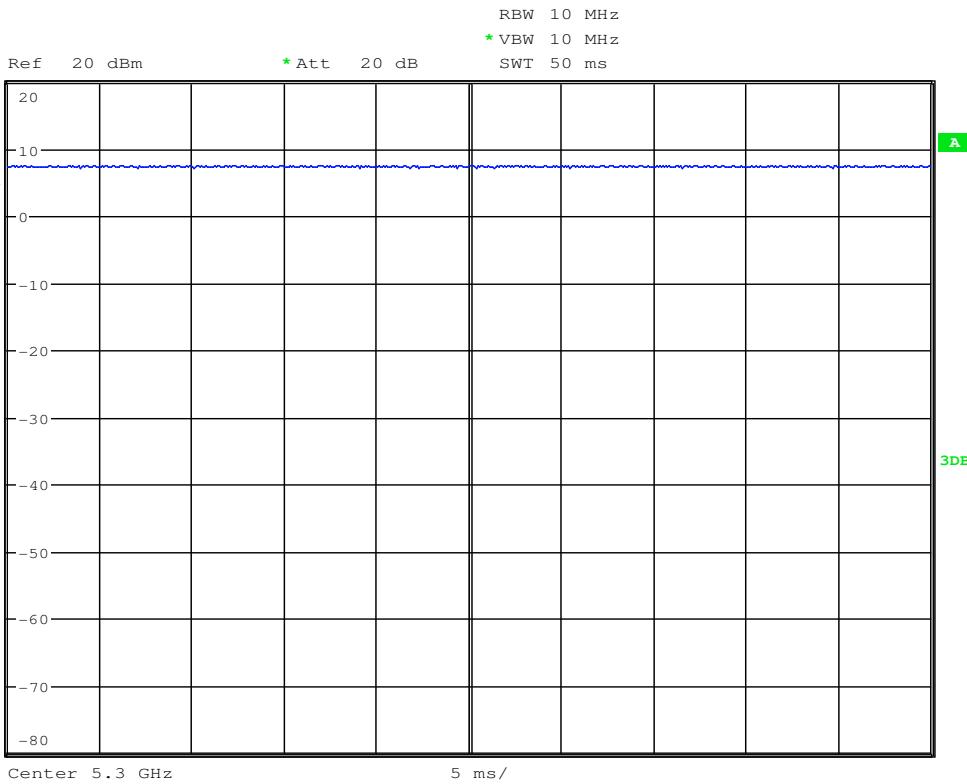
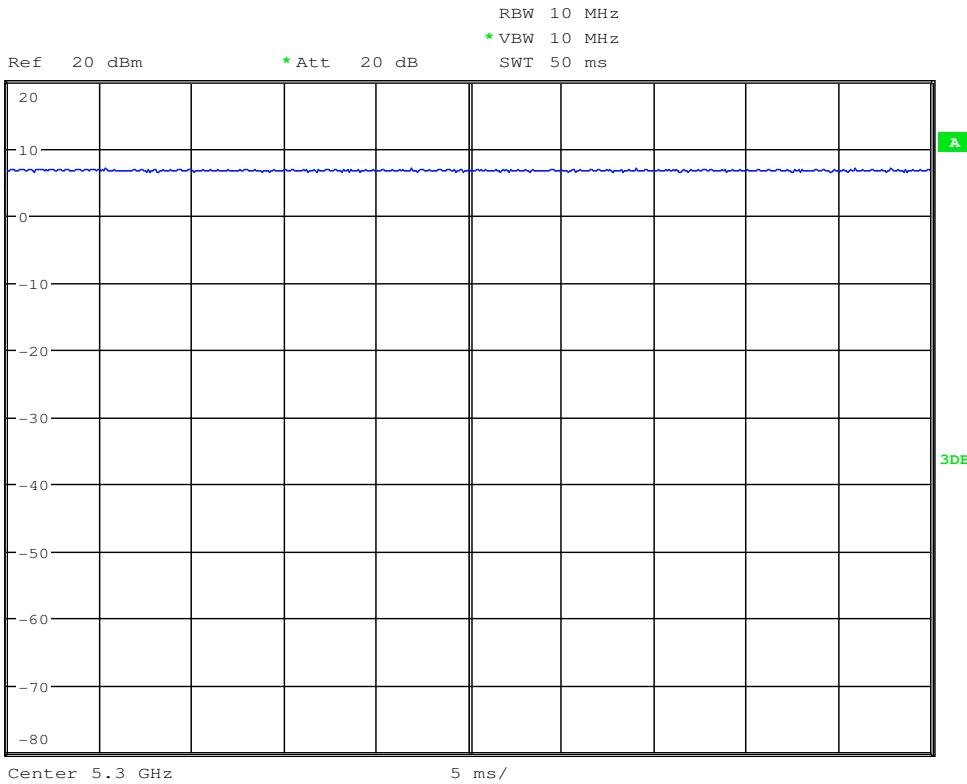
Duty Cycle_11N40_5795_Ant1**Duty Cycle_11N40_5795_Ant2**

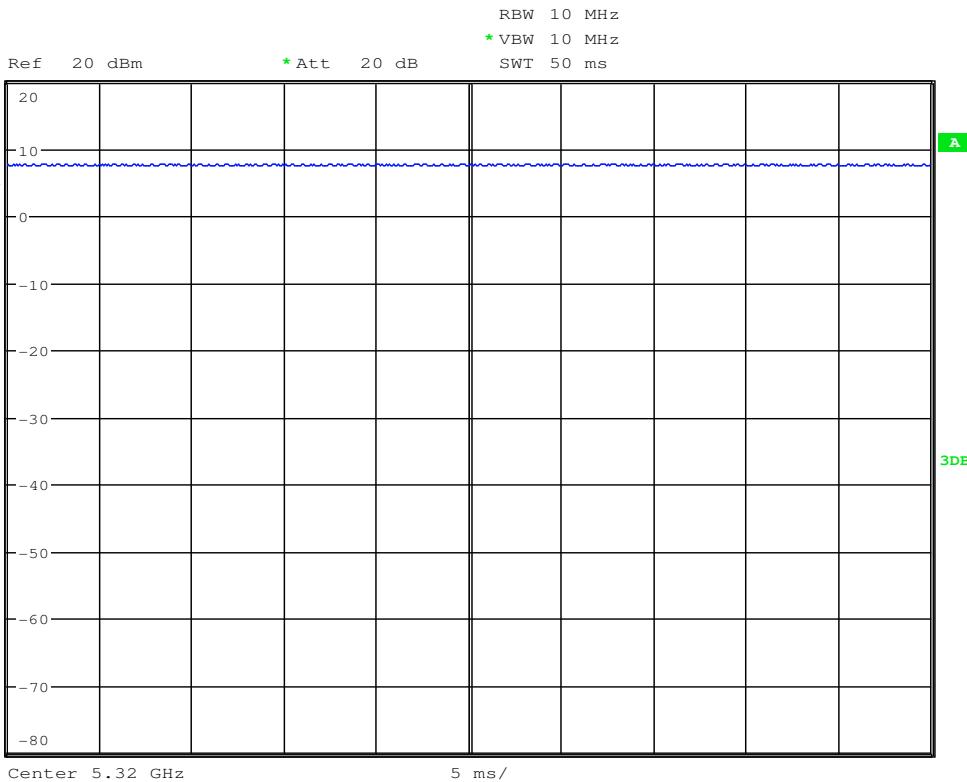
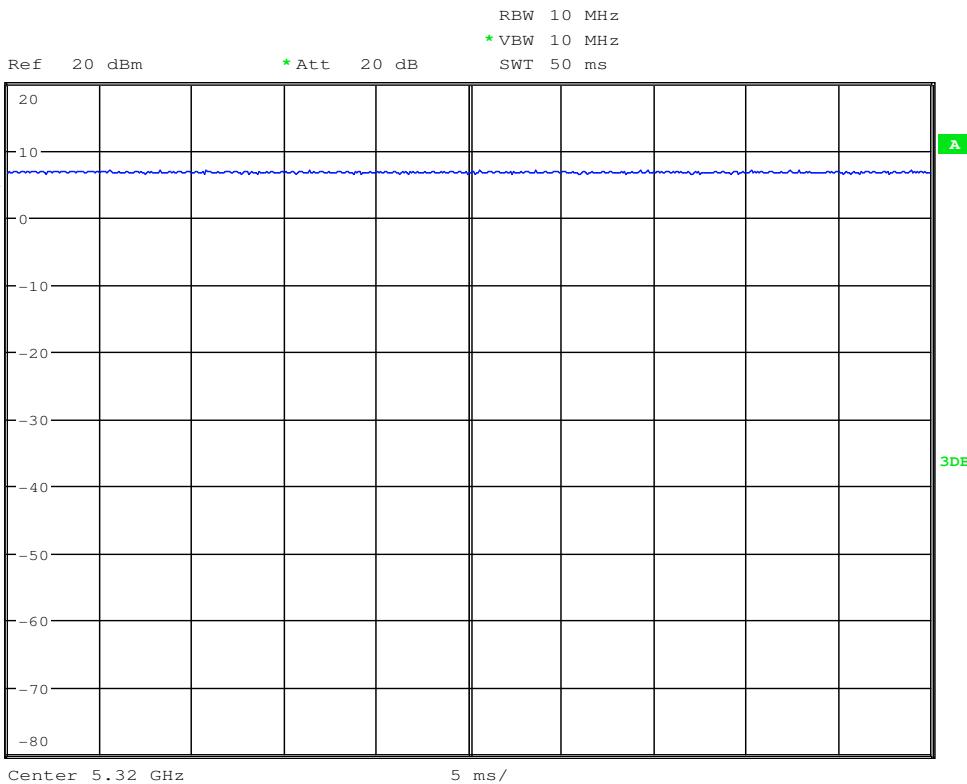
Duty Cycle_11AC20_5180_Ant1**Duty Cycle_11AC20_5180_Ant2**

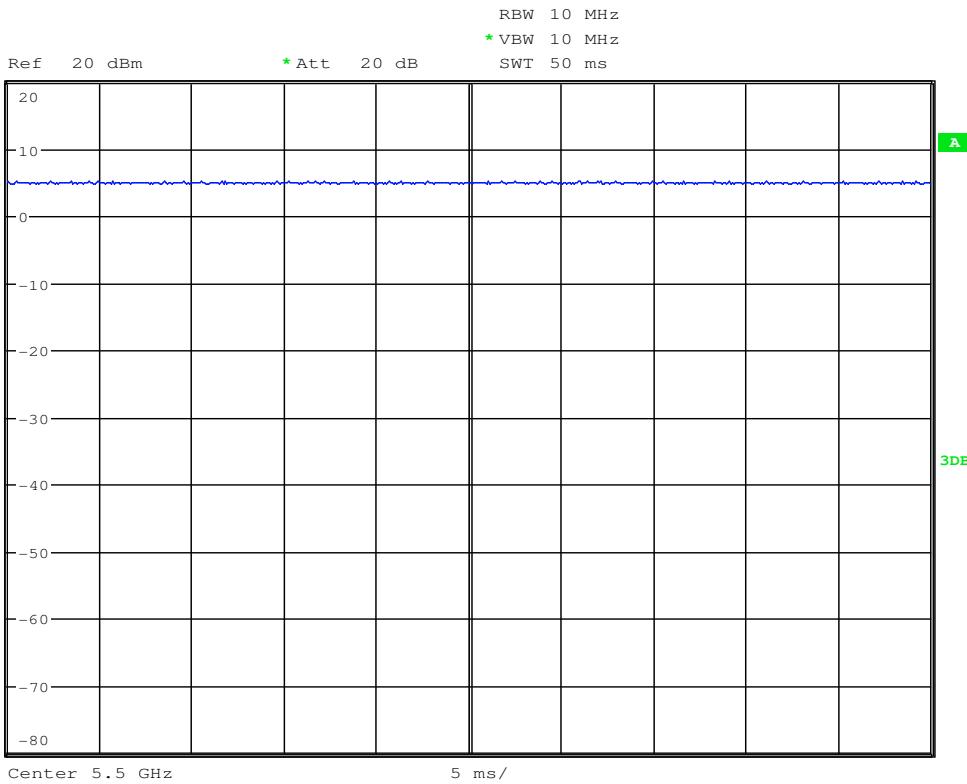
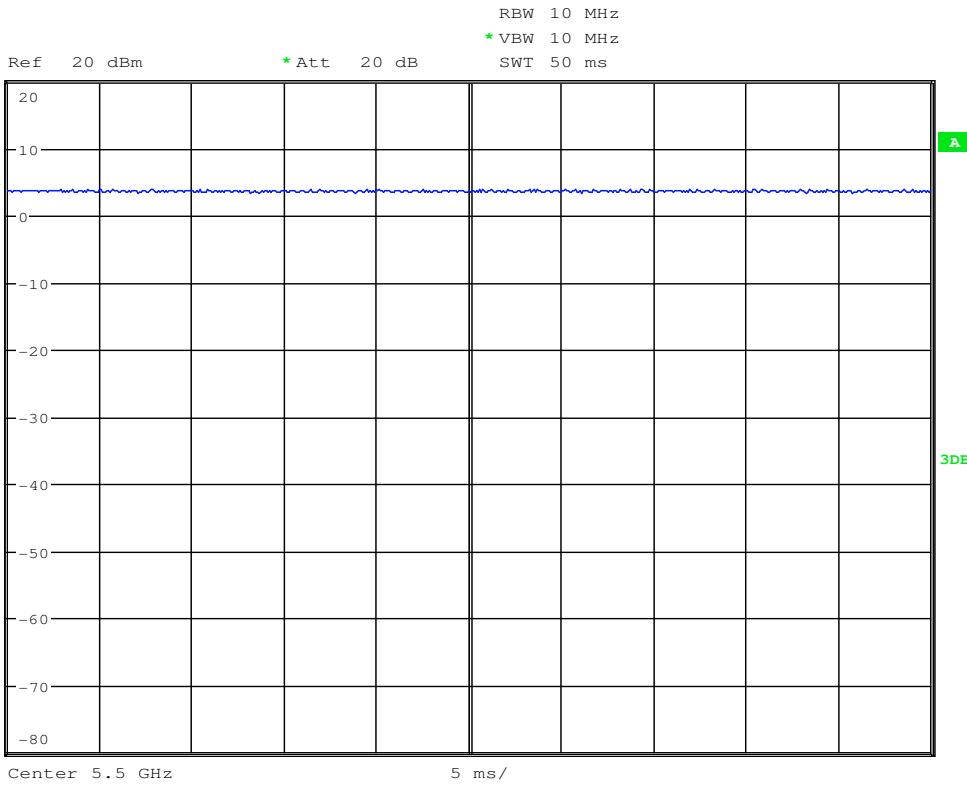
Duty Cycle_11AC20_5220_Ant1**Duty Cycle_11AC20_5220_Ant2**

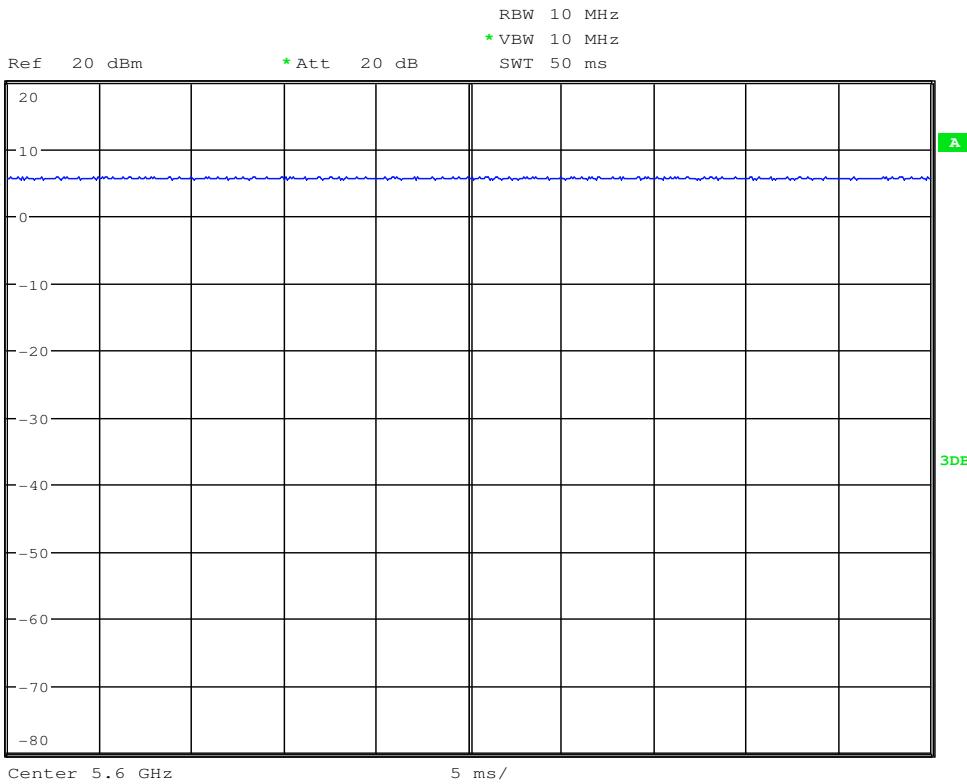
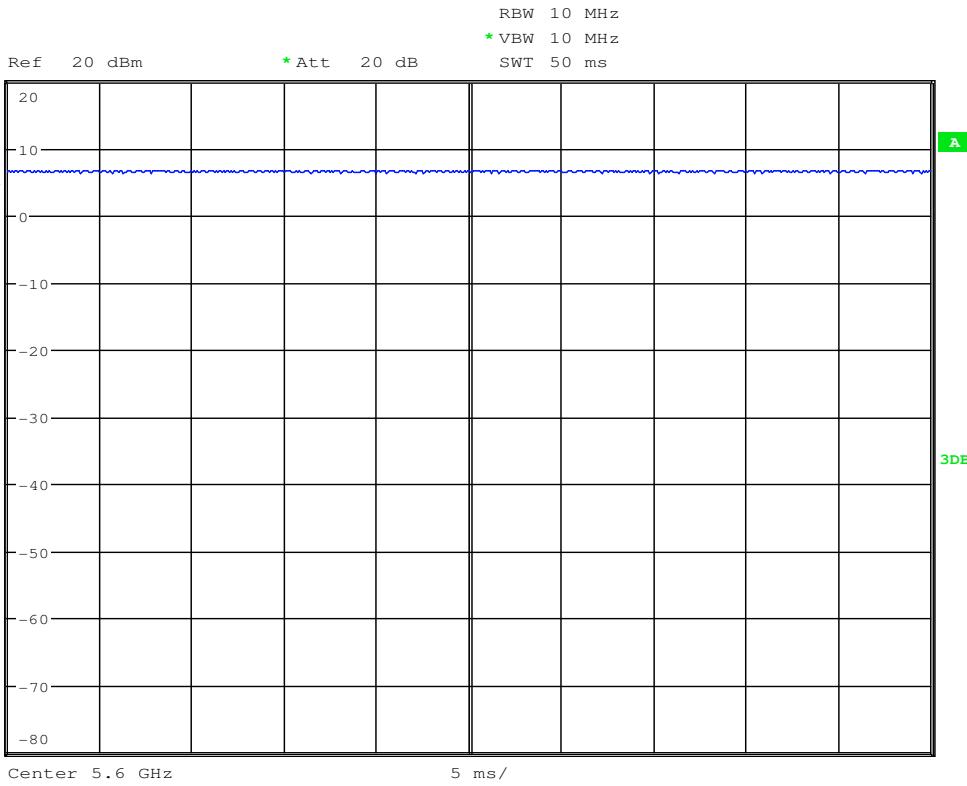
Duty Cycle_11AC20_5240_Ant1**Duty Cycle_11AC20_5240_Ant2**

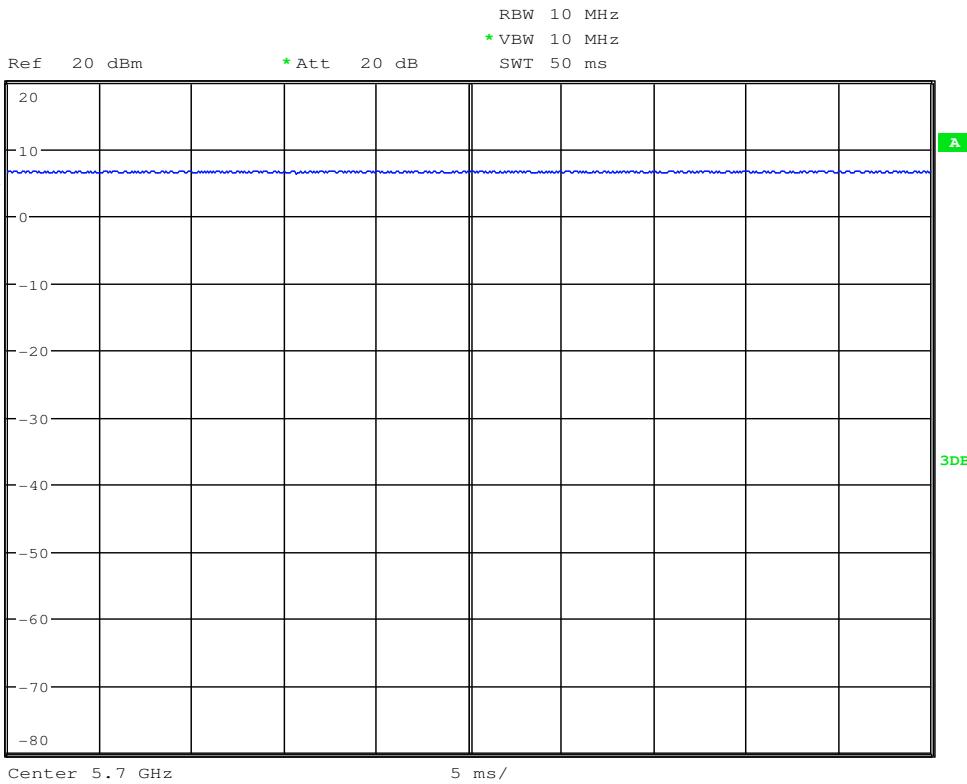
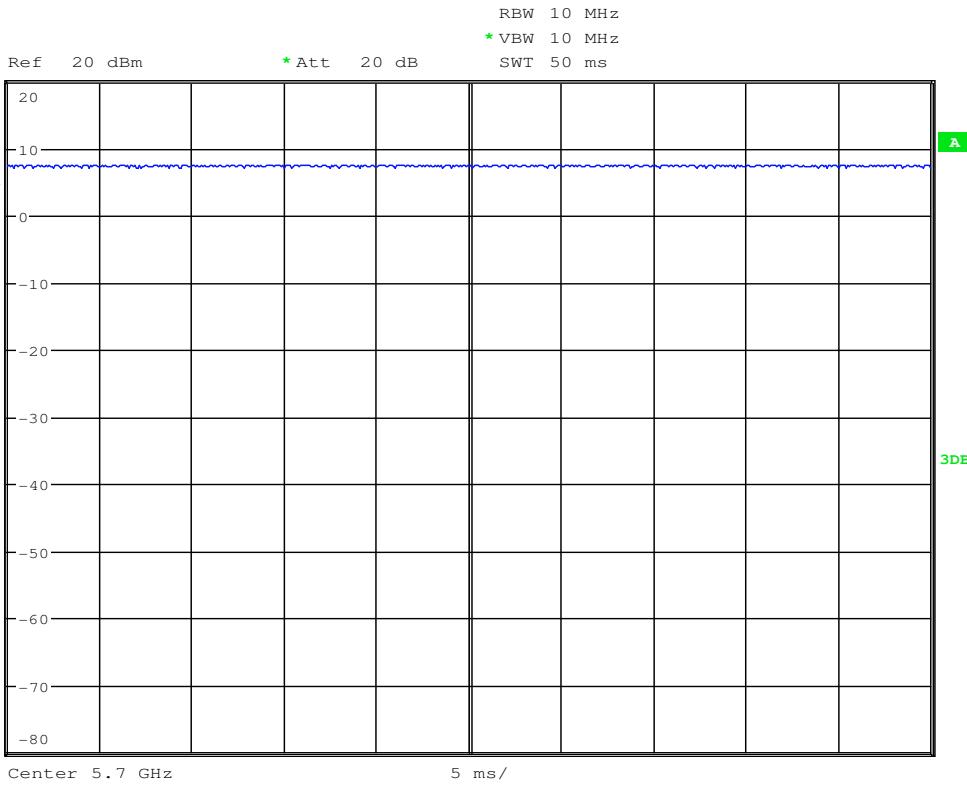
Duty Cycle_11AC20_5260_Ant1**Duty Cycle_11AC20_5260_Ant2**

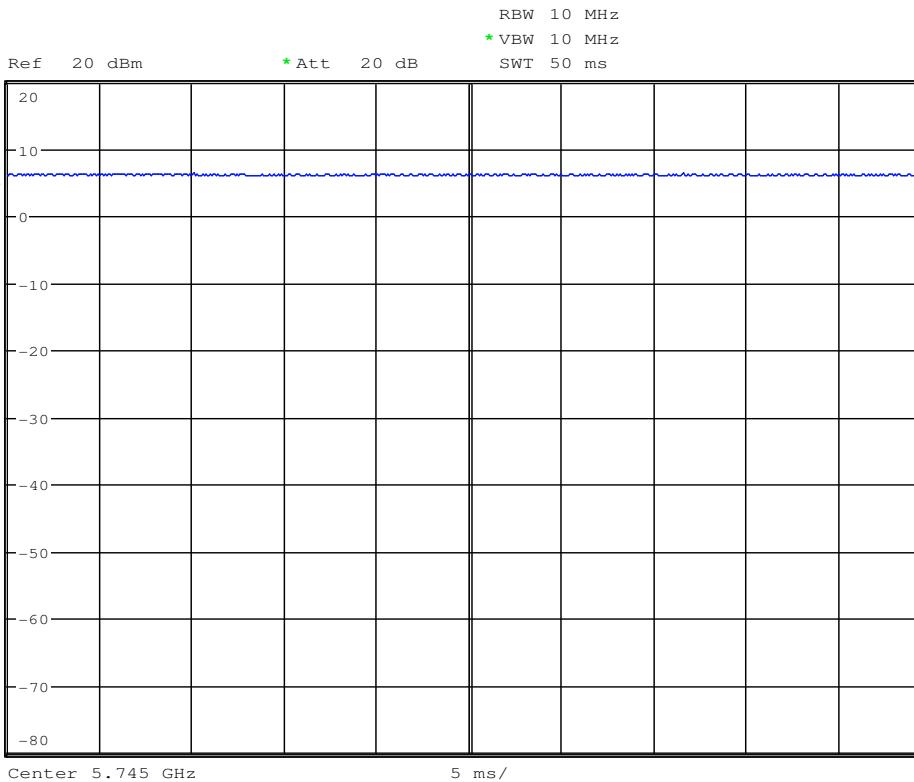
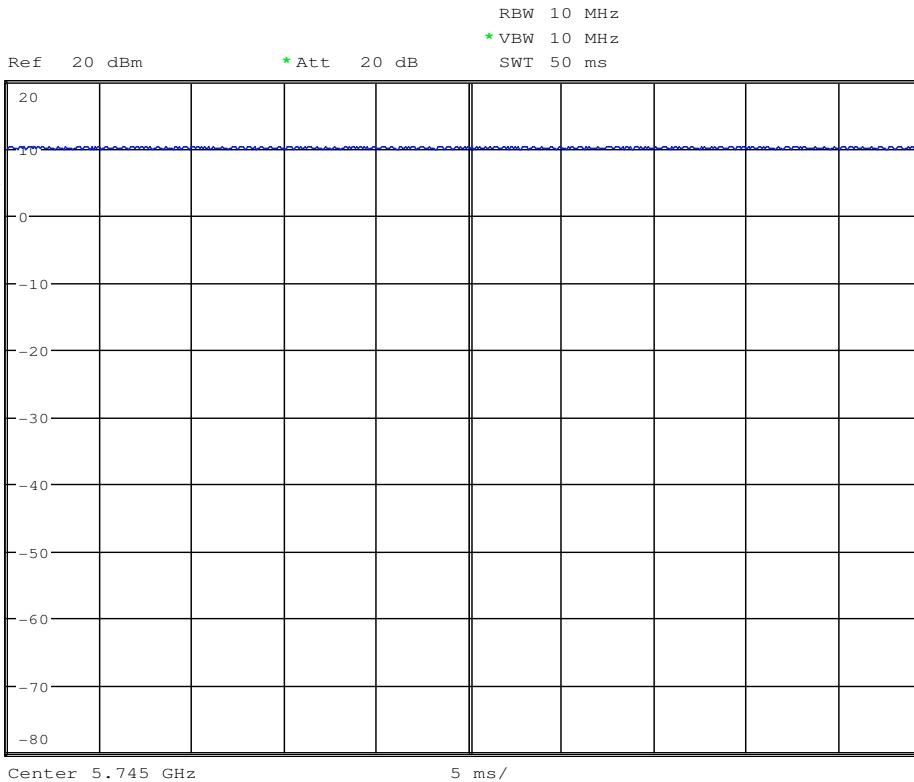
Duty Cycle_11AC20_5300_Ant1**Duty Cycle_11AC20_5300_Ant2**

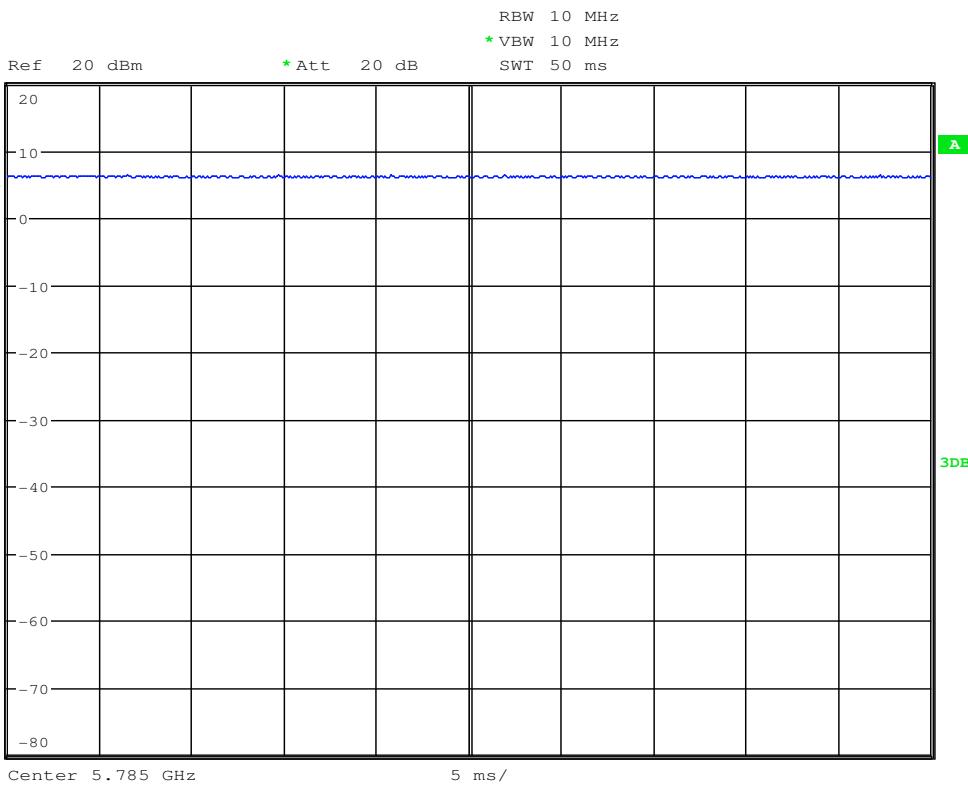
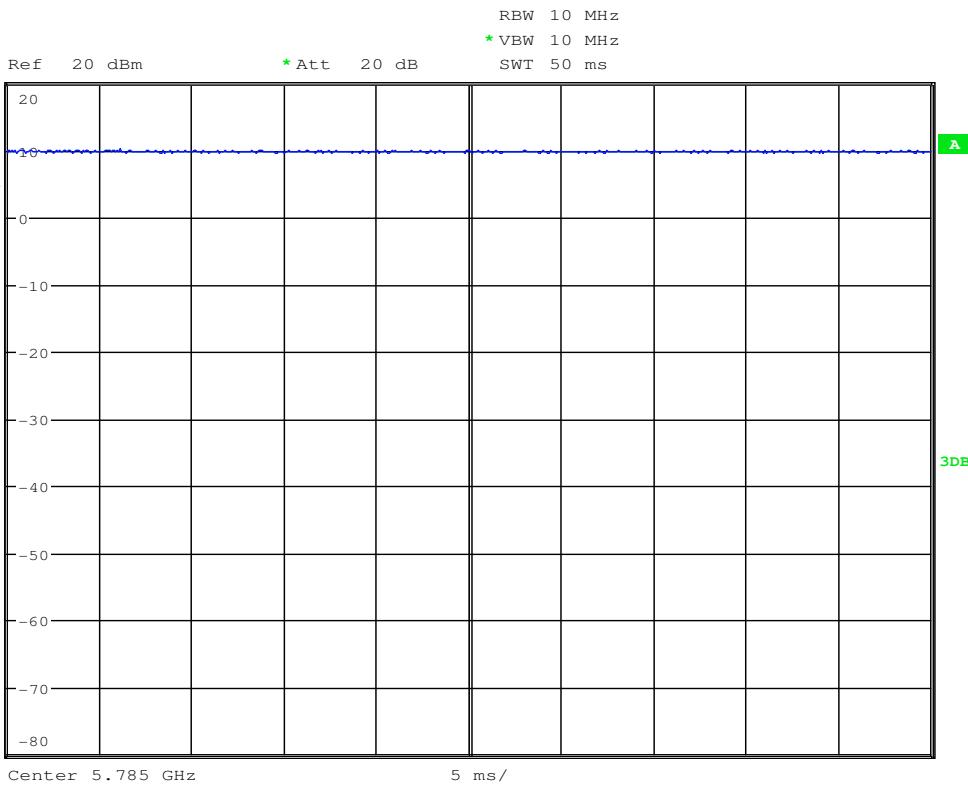
Duty Cycle_11AC20_5320_Ant1**Duty Cycle_11AC20_5320_Ant2**

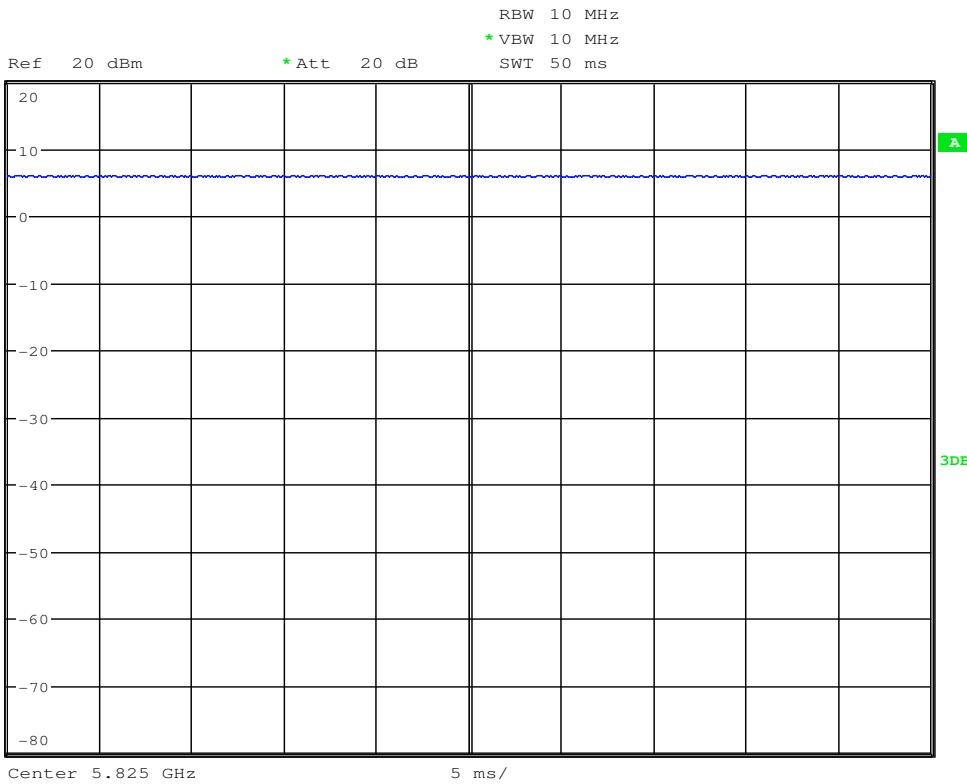
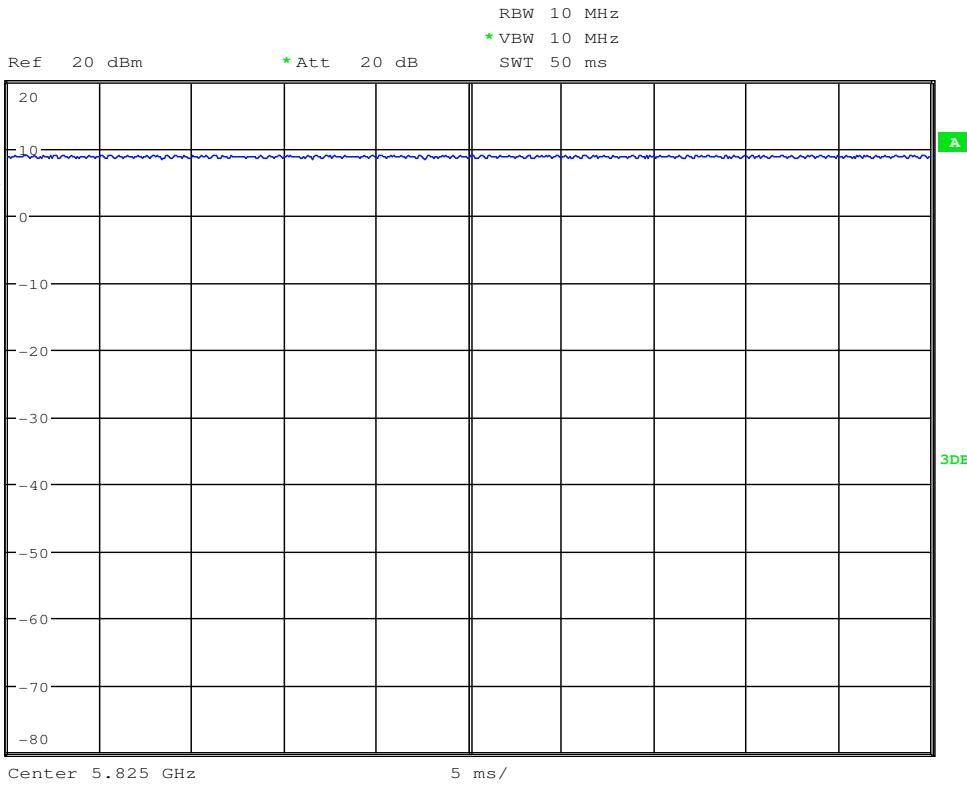
Duty Cycle_11AC20_5500_Ant1**Duty Cycle_11AC20_5500_Ant2**

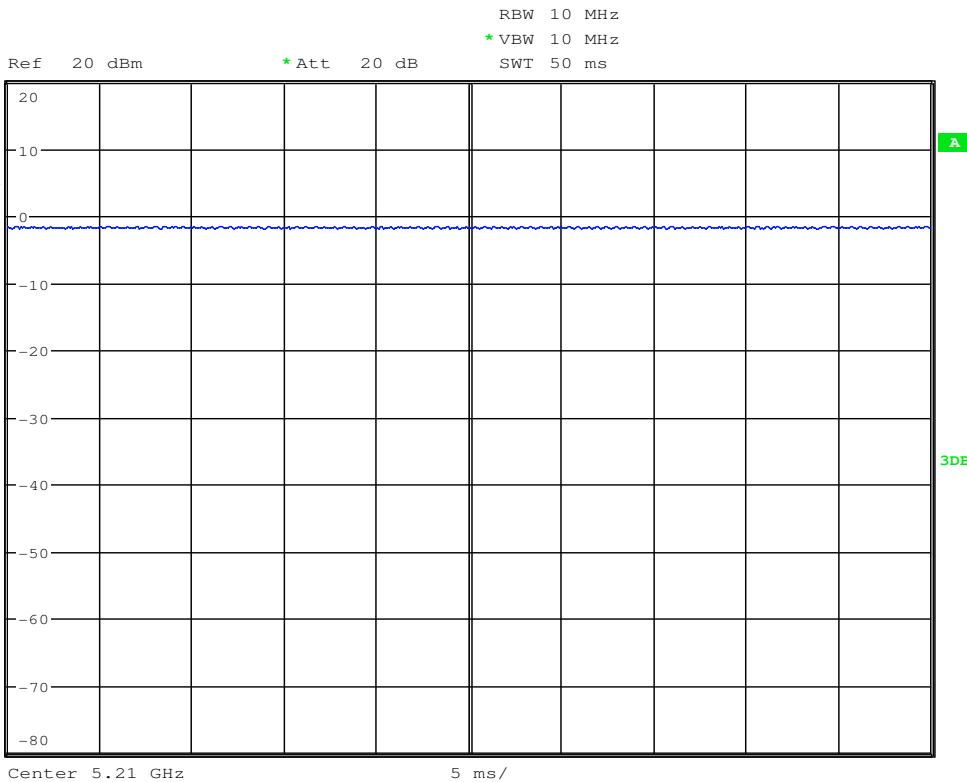
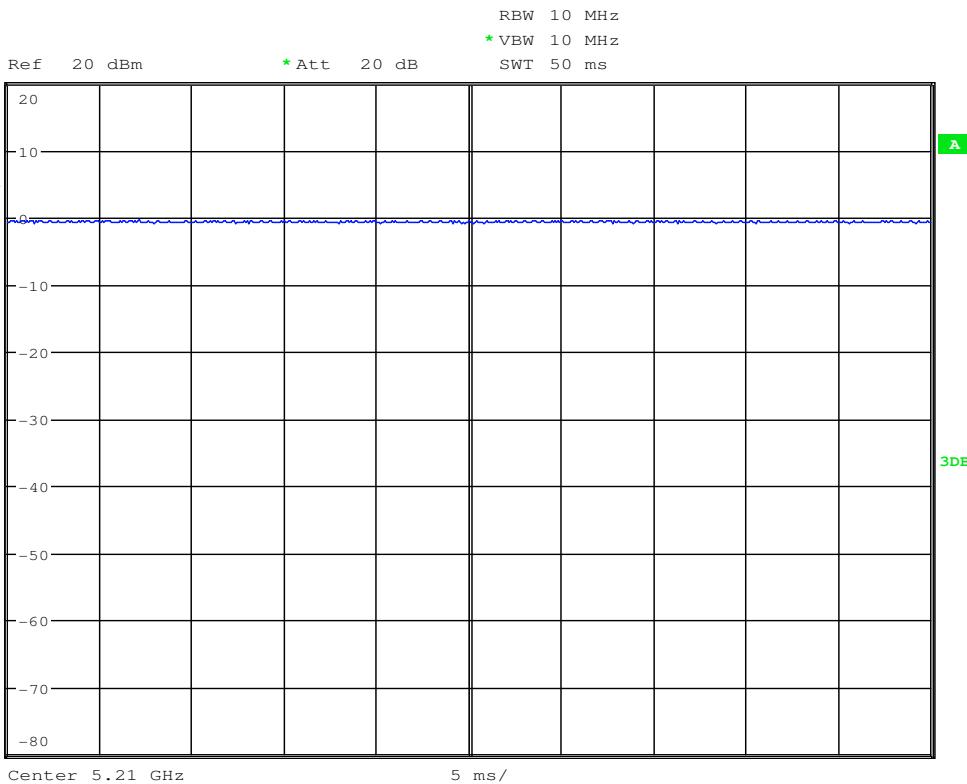
Duty Cycle_11AC20_5600_Ant1**A****3dB****Duty Cycle_11AC20_5600_Ant2****A****3dB**

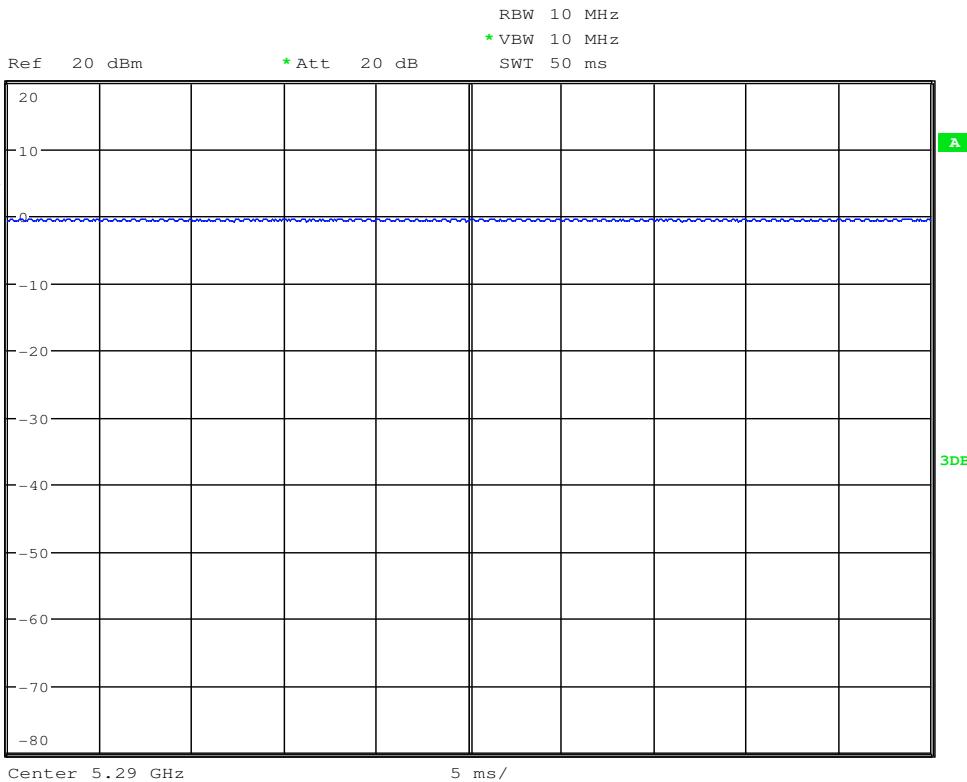
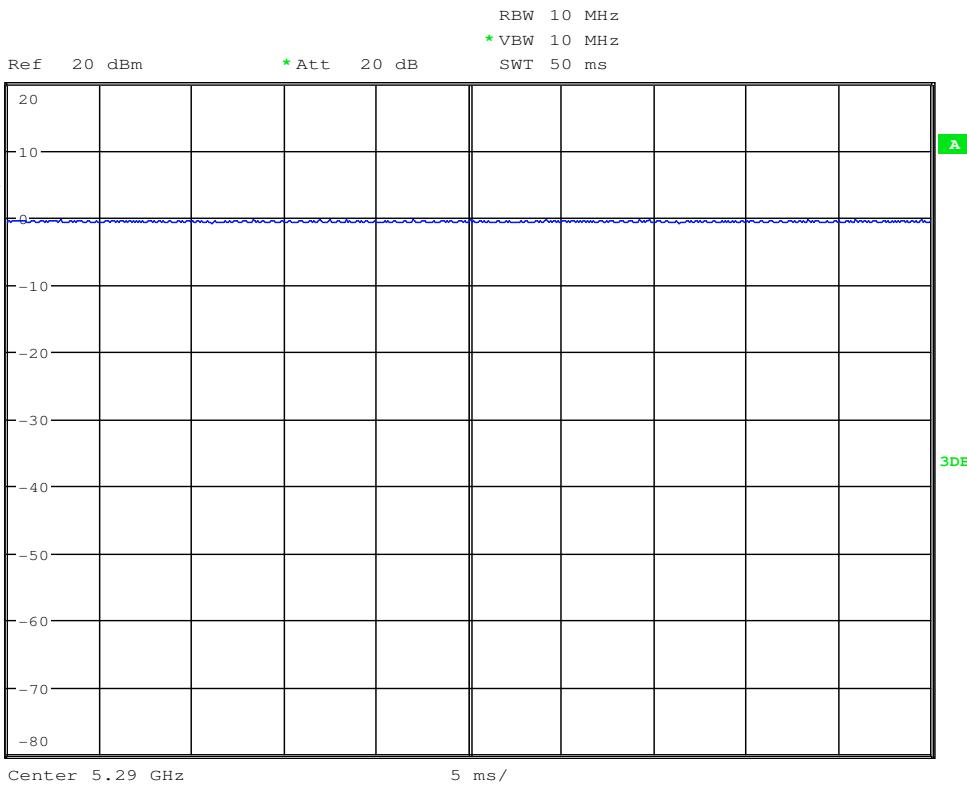
Duty Cycle_11AC20_5700_Ant1**Duty Cycle_11AC20_5700_Ant2**

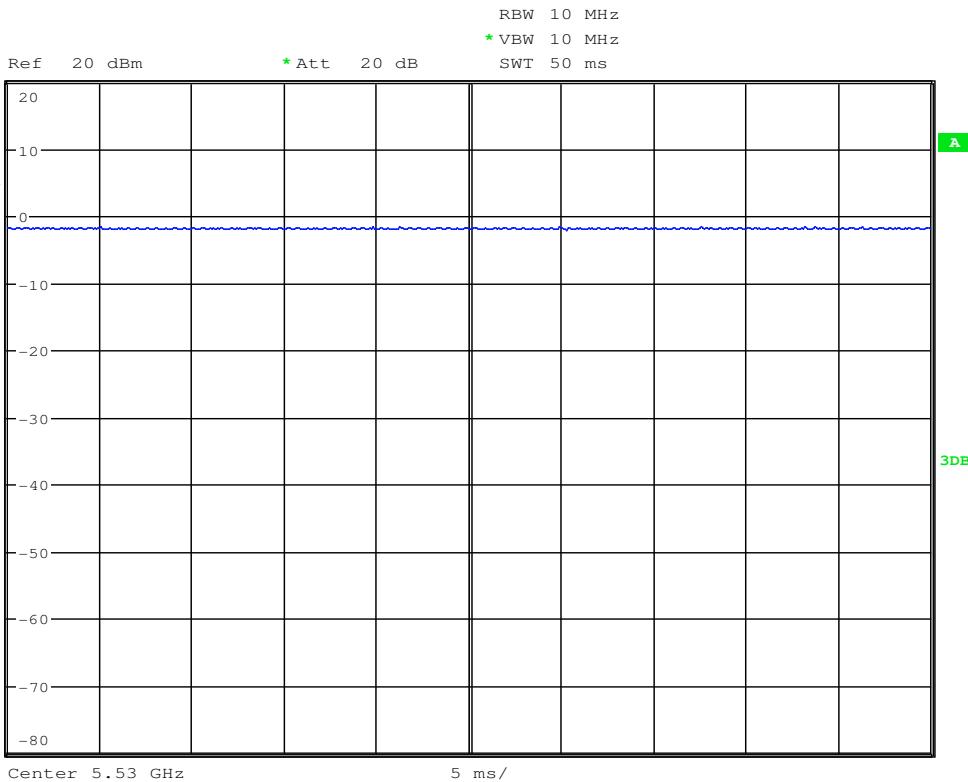
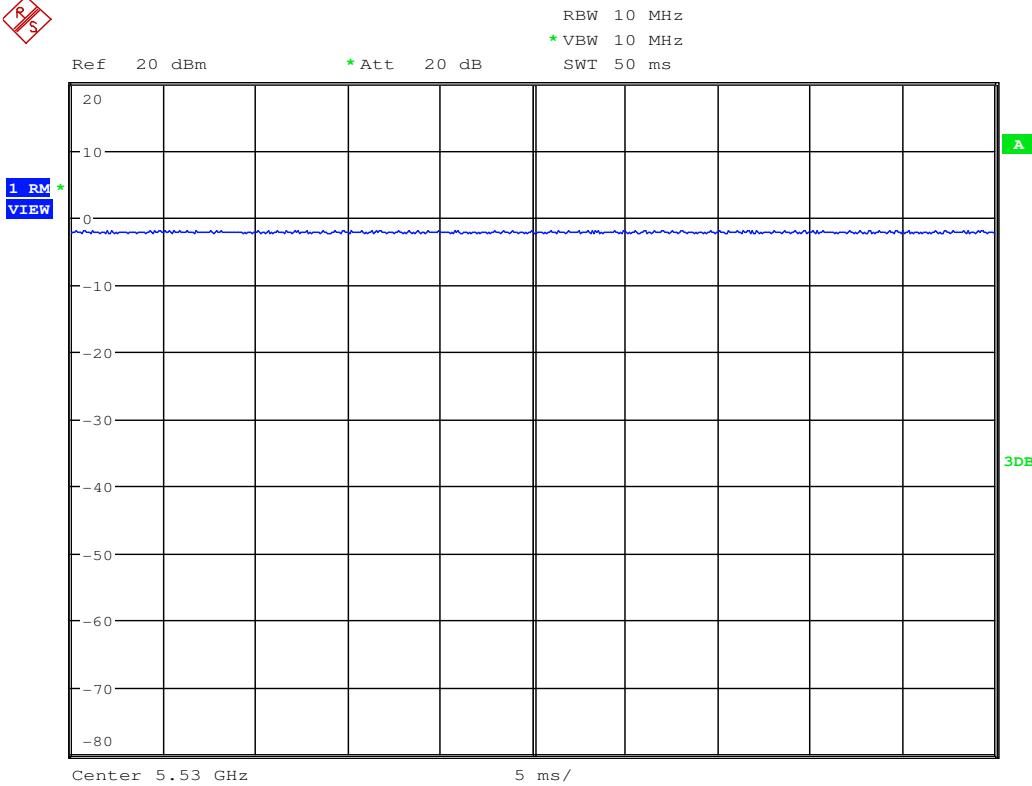
Duty Cycle_11AC20_5745_Ant1**Duty Cycle_11AC20_5745_Ant2**

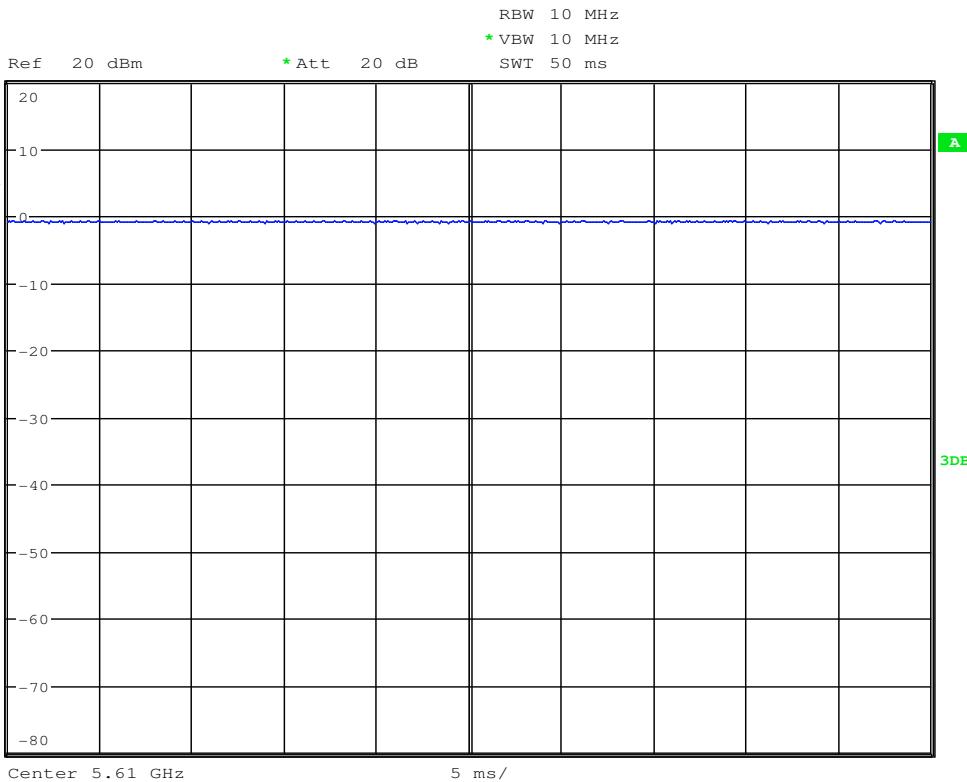
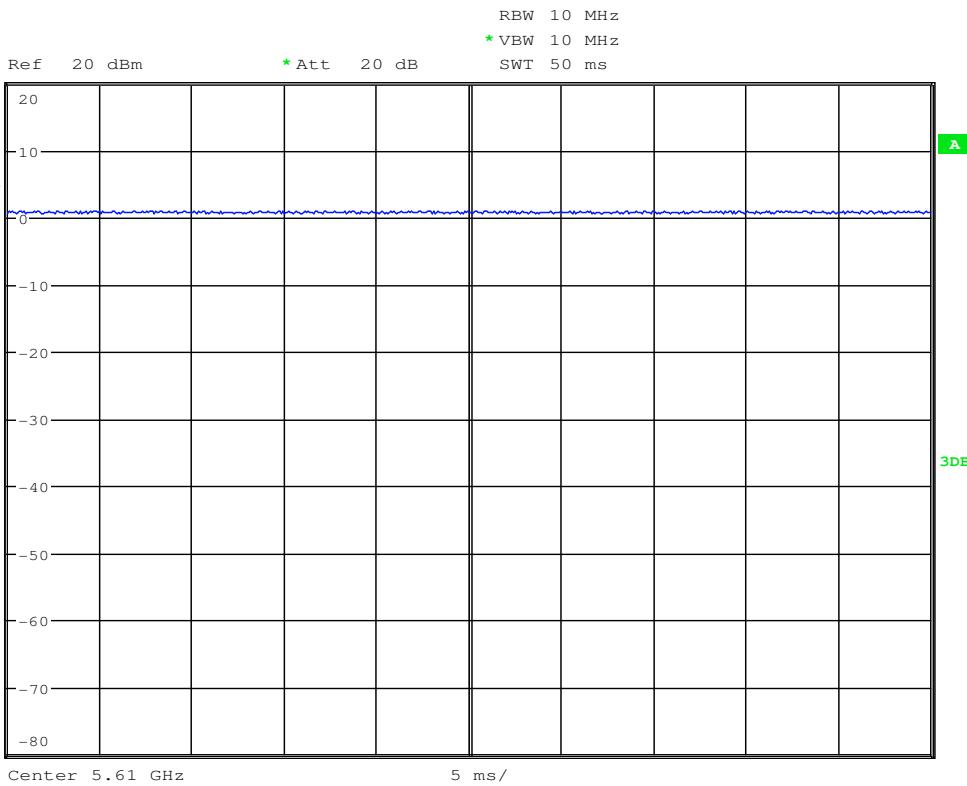
Duty Cycle_11AC20_5785_Ant1**Duty Cycle_11AC20_5785_Ant2**

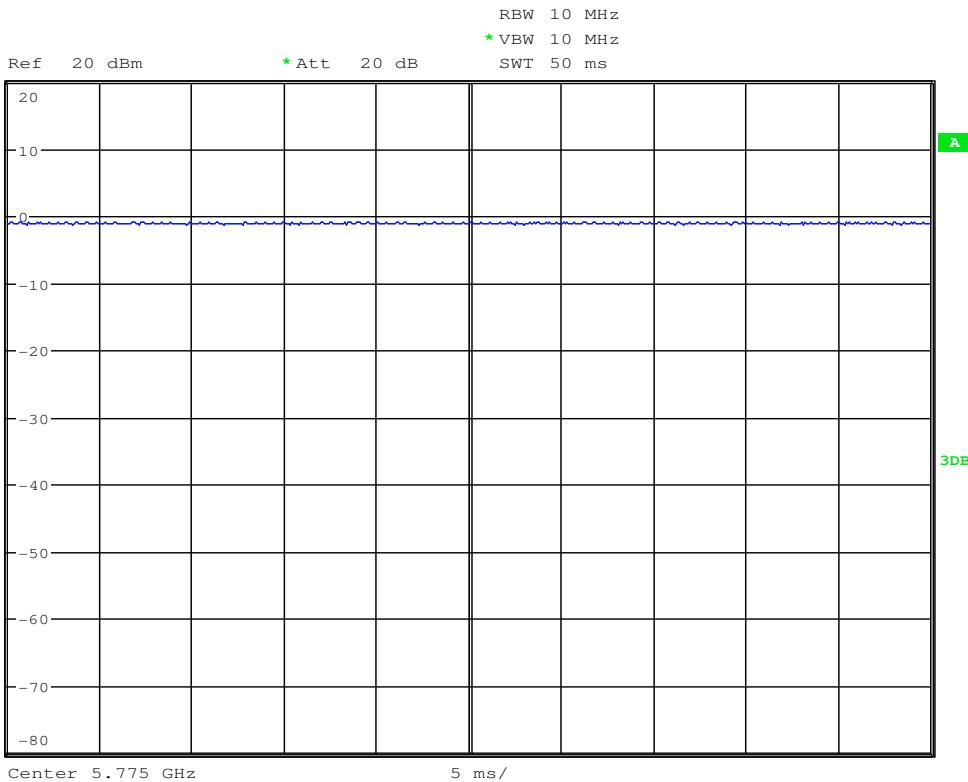
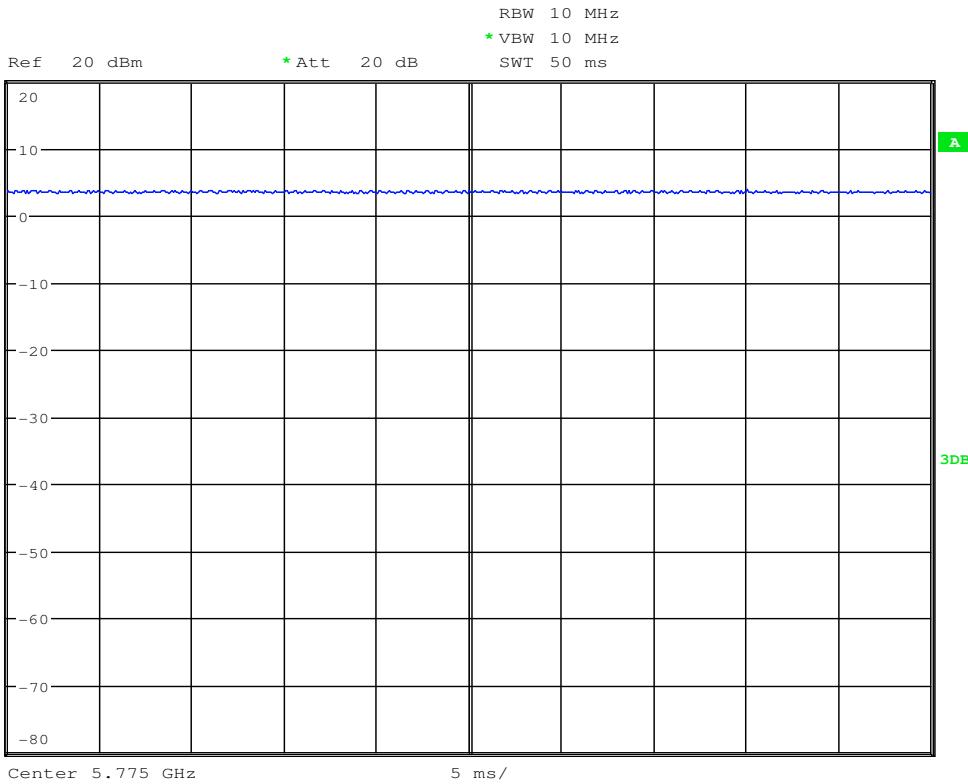
Duty Cycle_11AC20_5825_Ant1**Duty Cycle_11AC20_5825_Ant2**

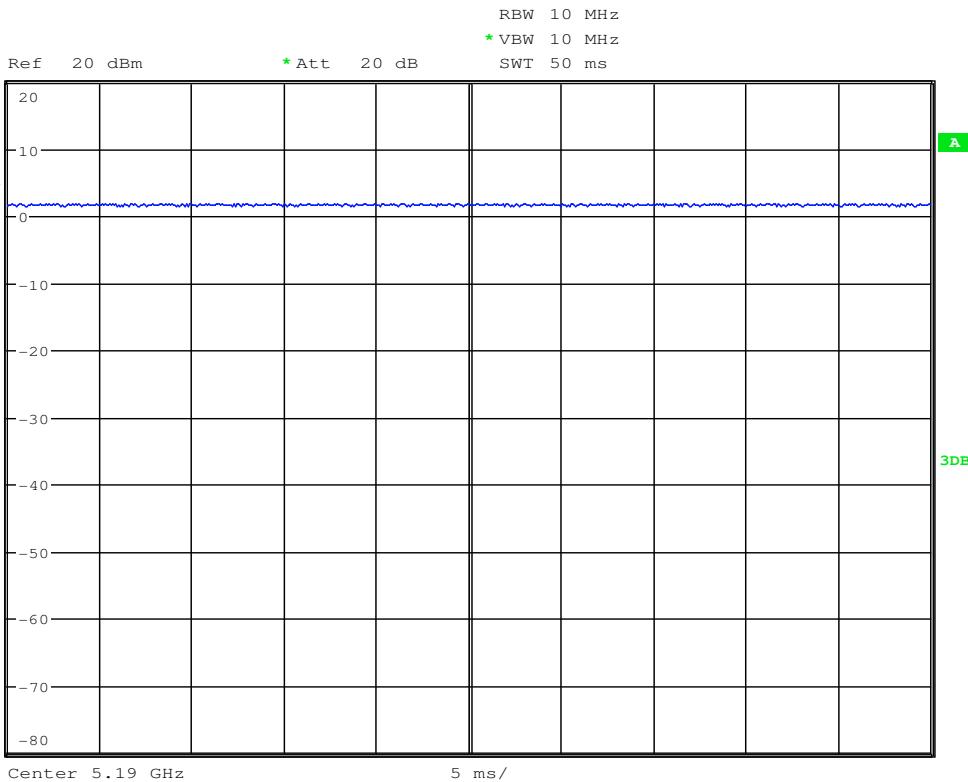
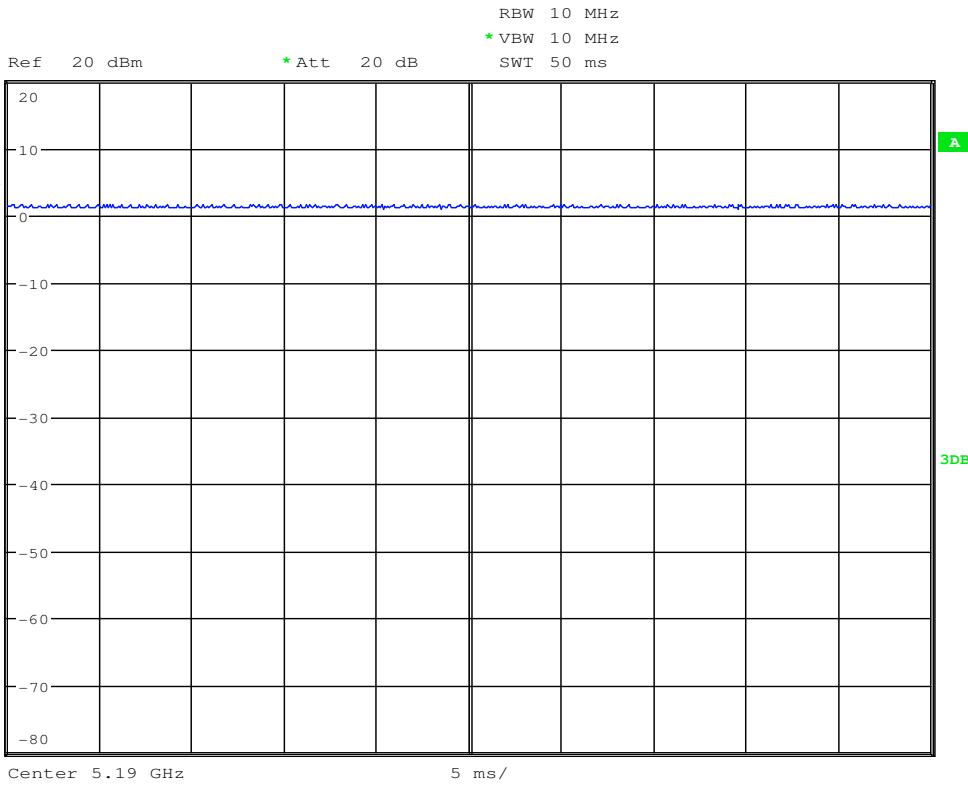
Duty Cycle_11AC80_5210_Ant1**Duty Cycle_11AC80_5210_Ant2**

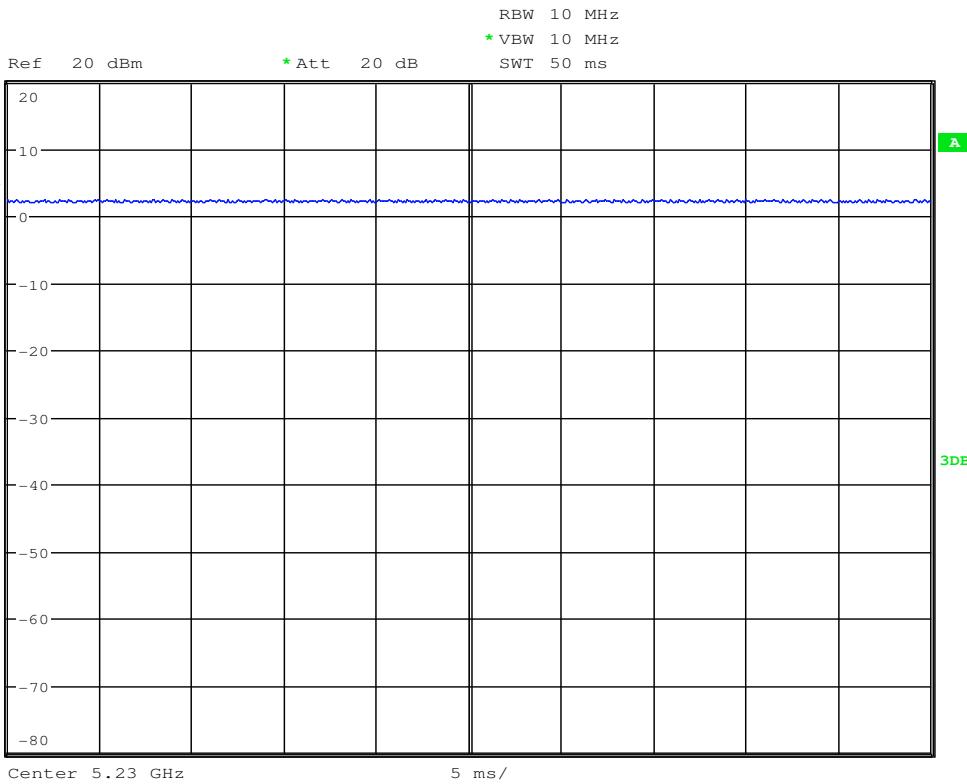
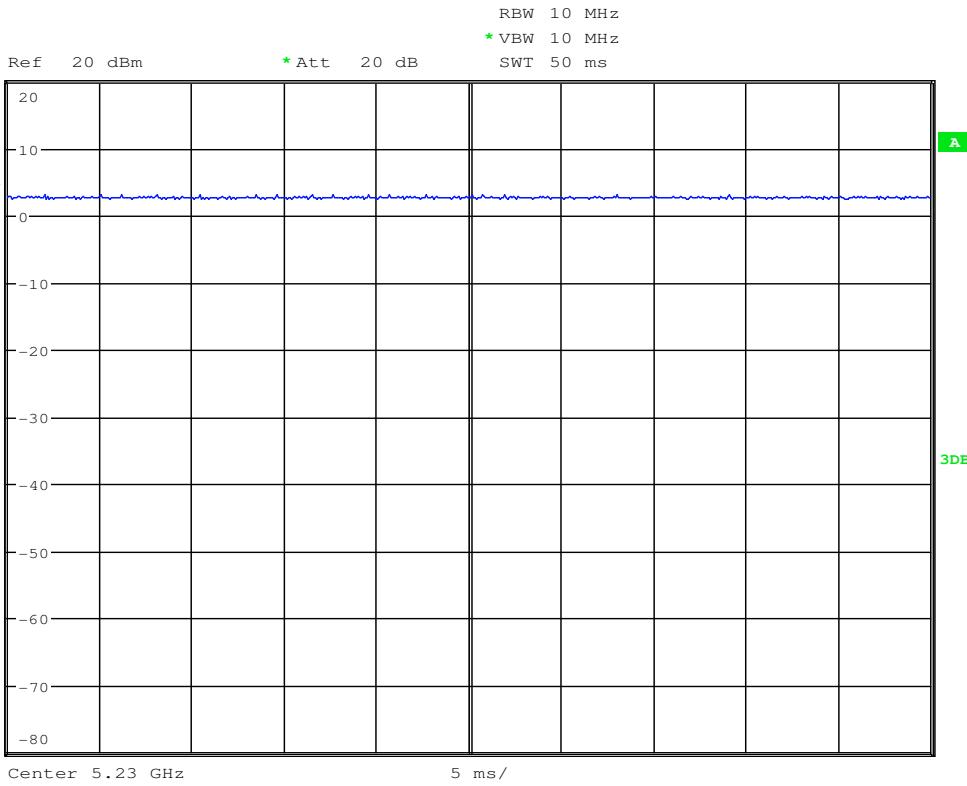
Duty Cycle_11AC80_5290_Ant1**Duty Cycle_11AC80_5290_Ant2**

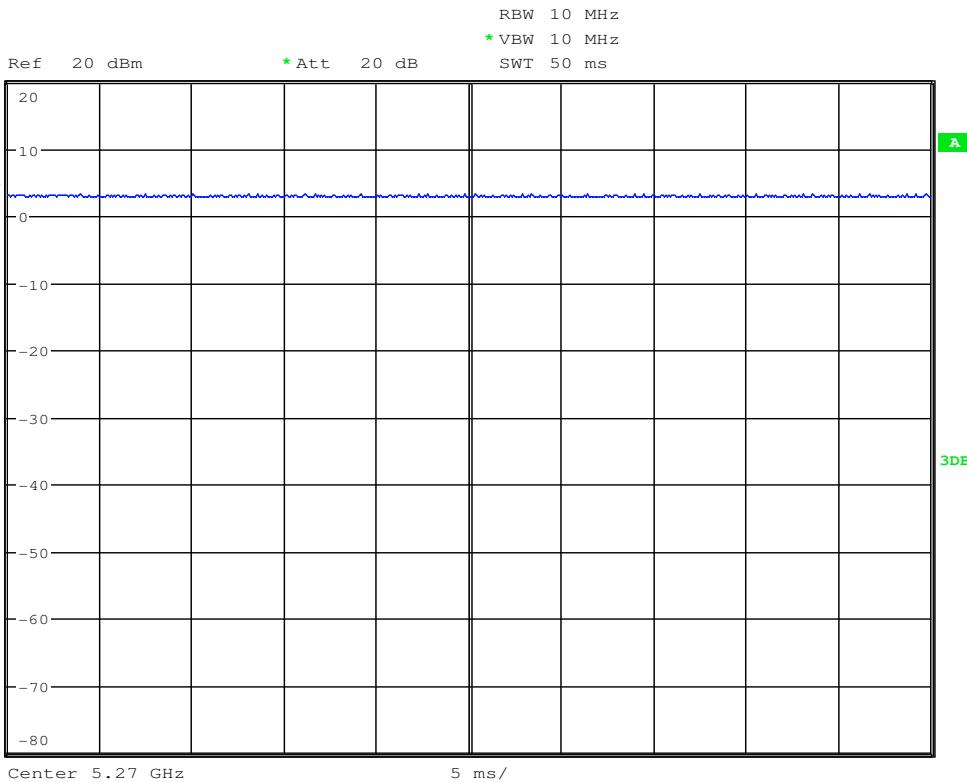
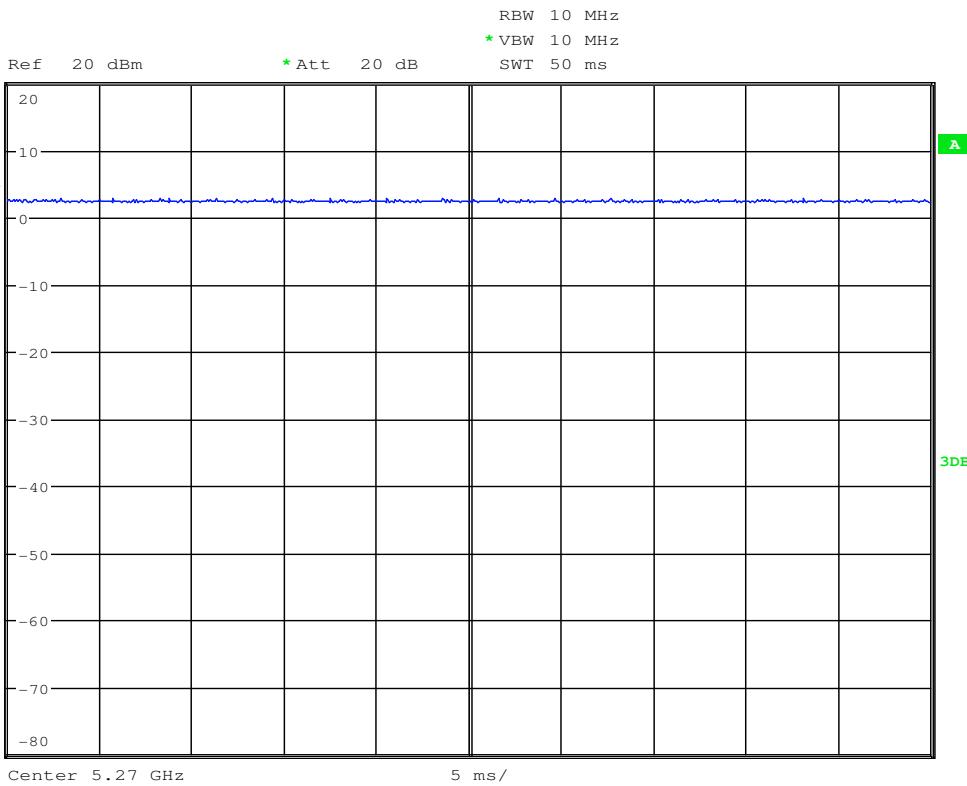
Duty Cycle_11AC80_5530_Ant1**Duty Cycle_11AC80_5530_Ant2**

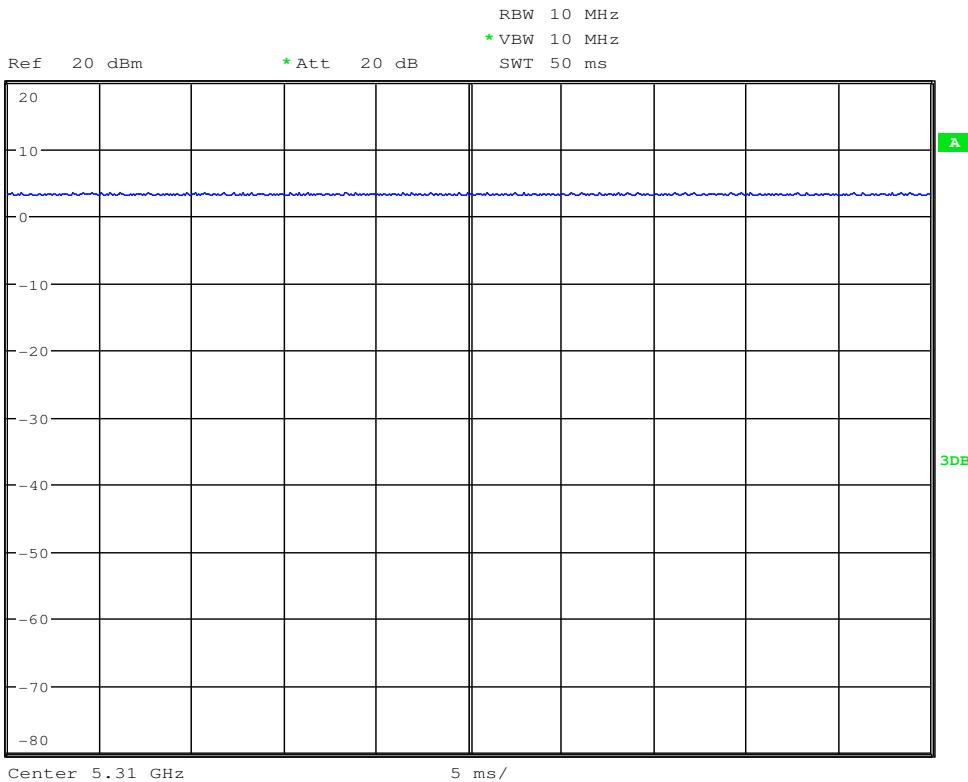
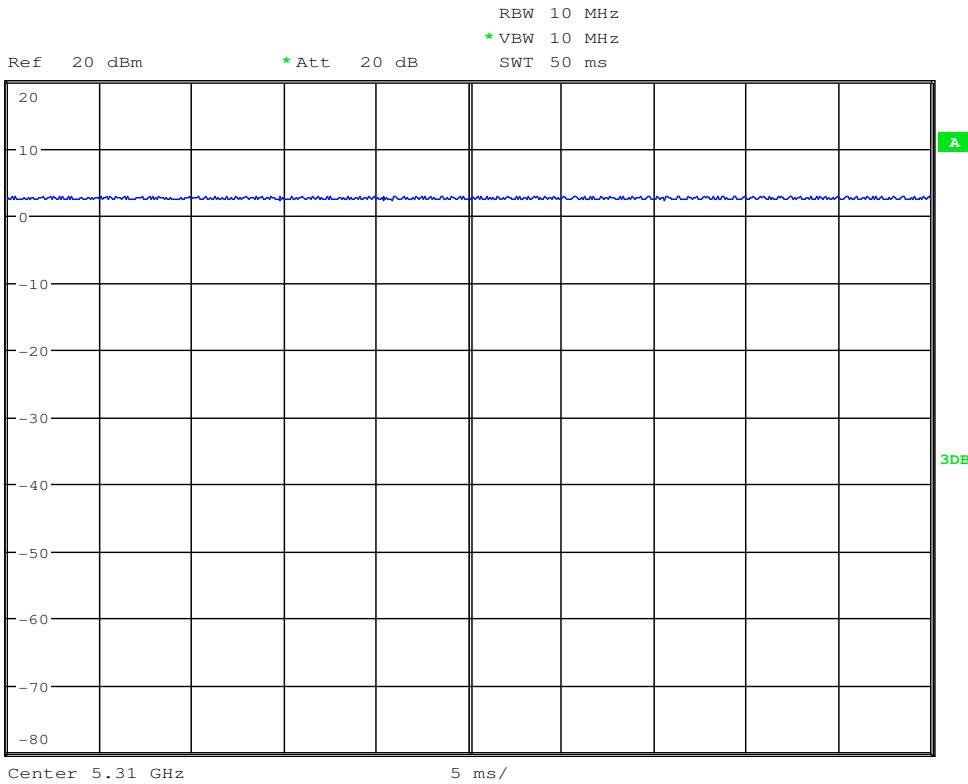
Duty Cycle_11AC80_5610_Ant1**Duty Cycle_11AC80_5610_Ant2**

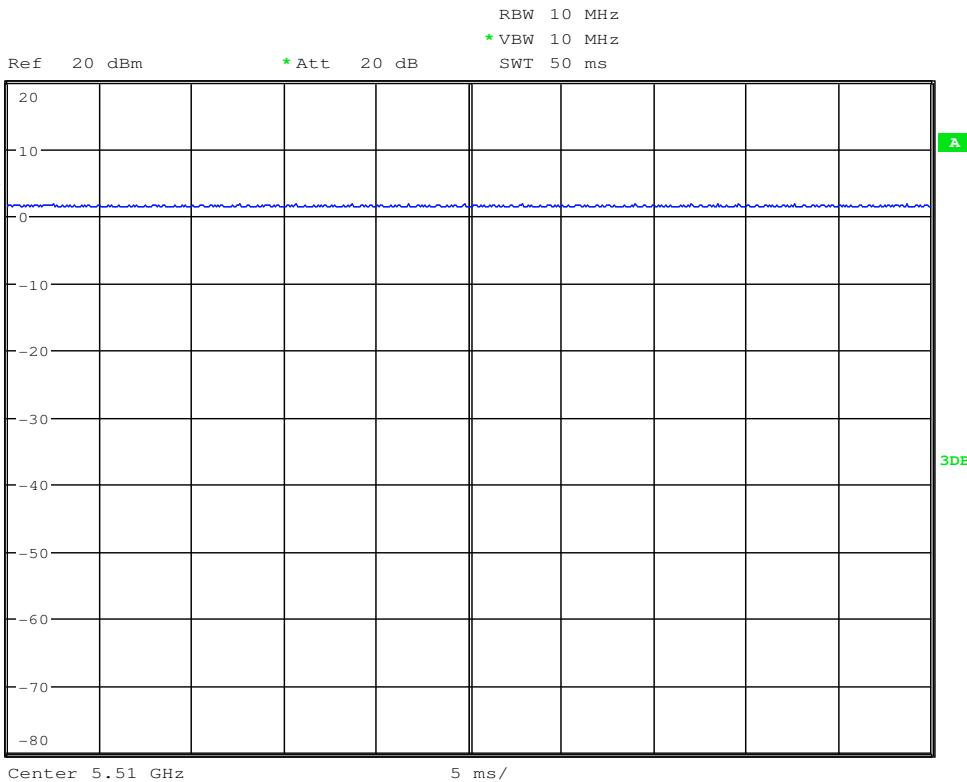
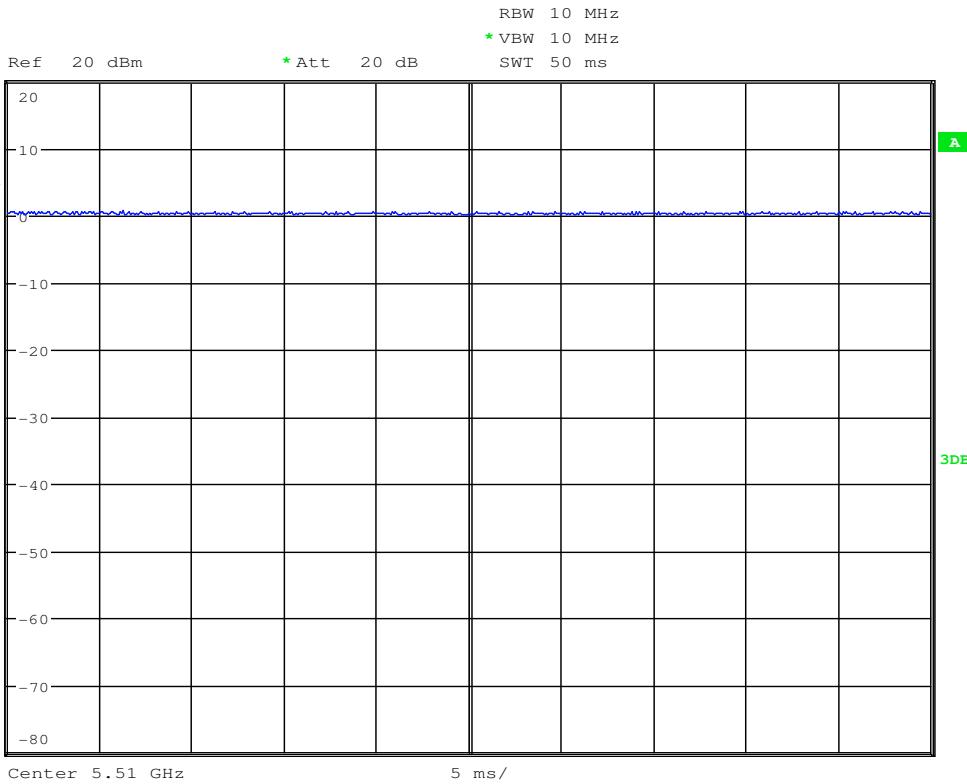
Duty Cycle_11AC80_5775_Ant1**Duty Cycle_11AC80_5775_Ant2**

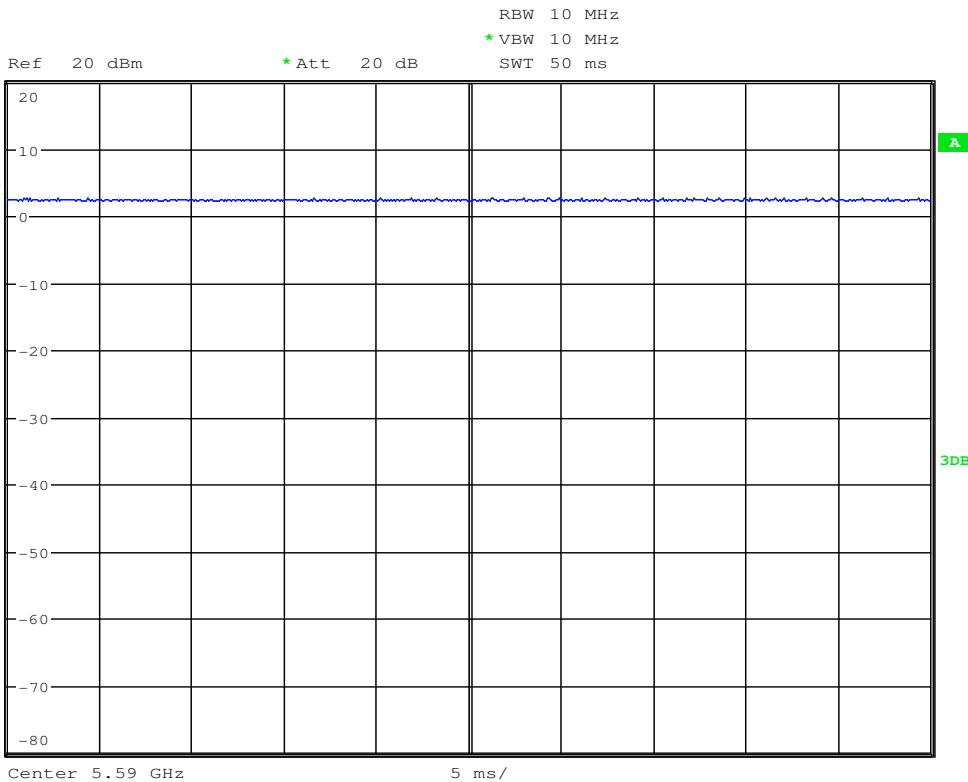
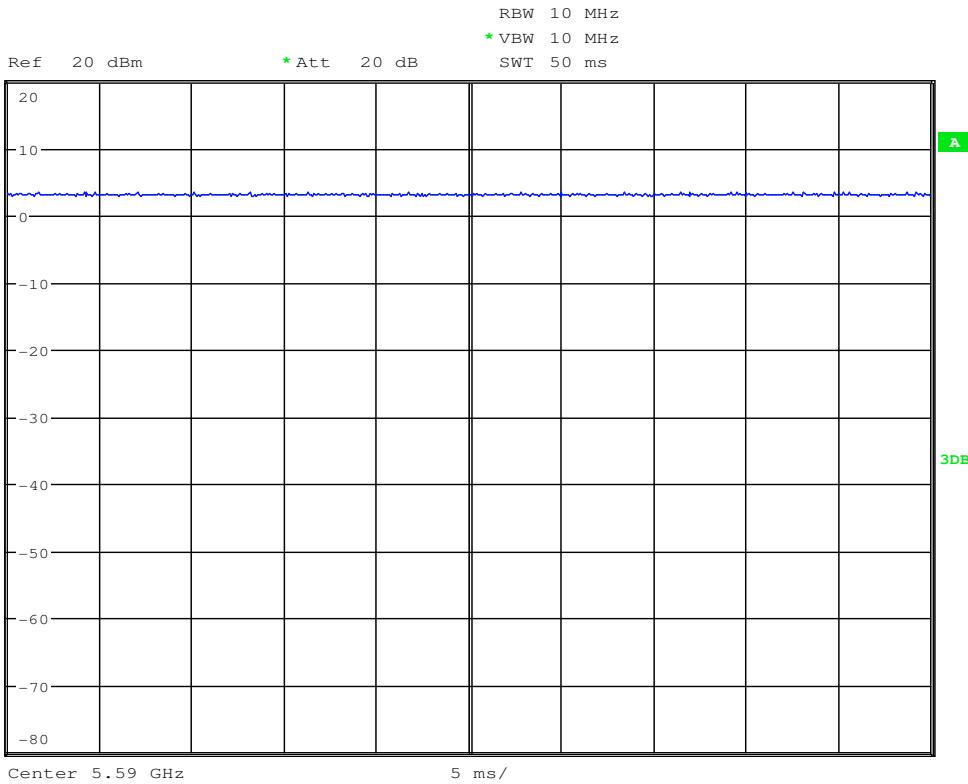
Duty Cycle_11AC40_5190_Ant1**Duty Cycle_11AC40_5190_Ant2**

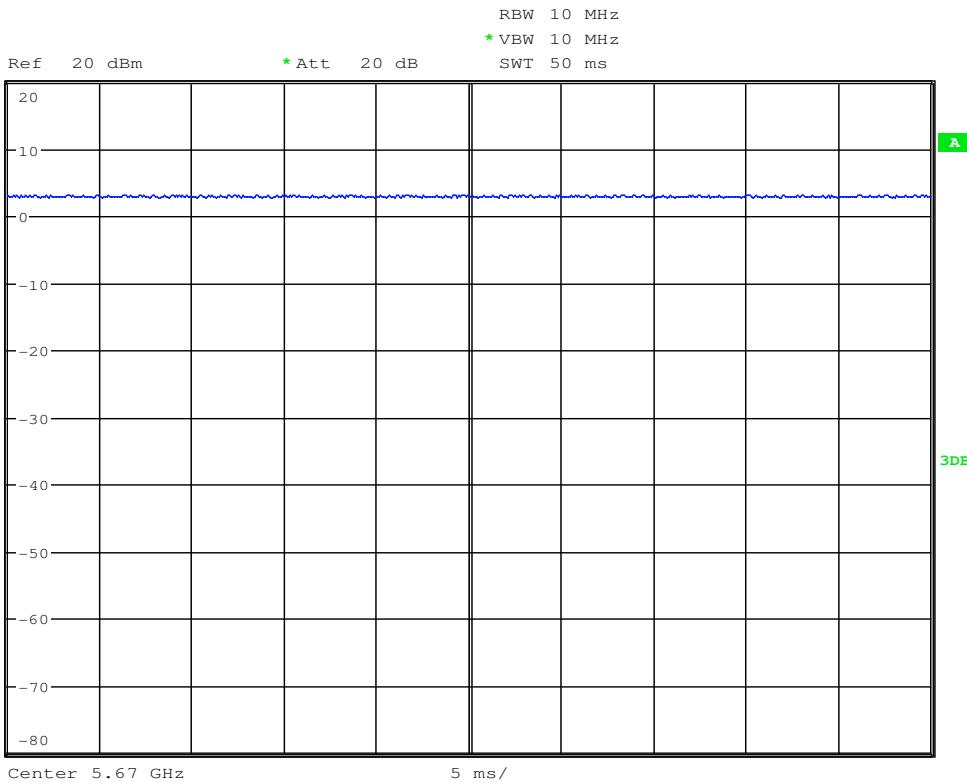
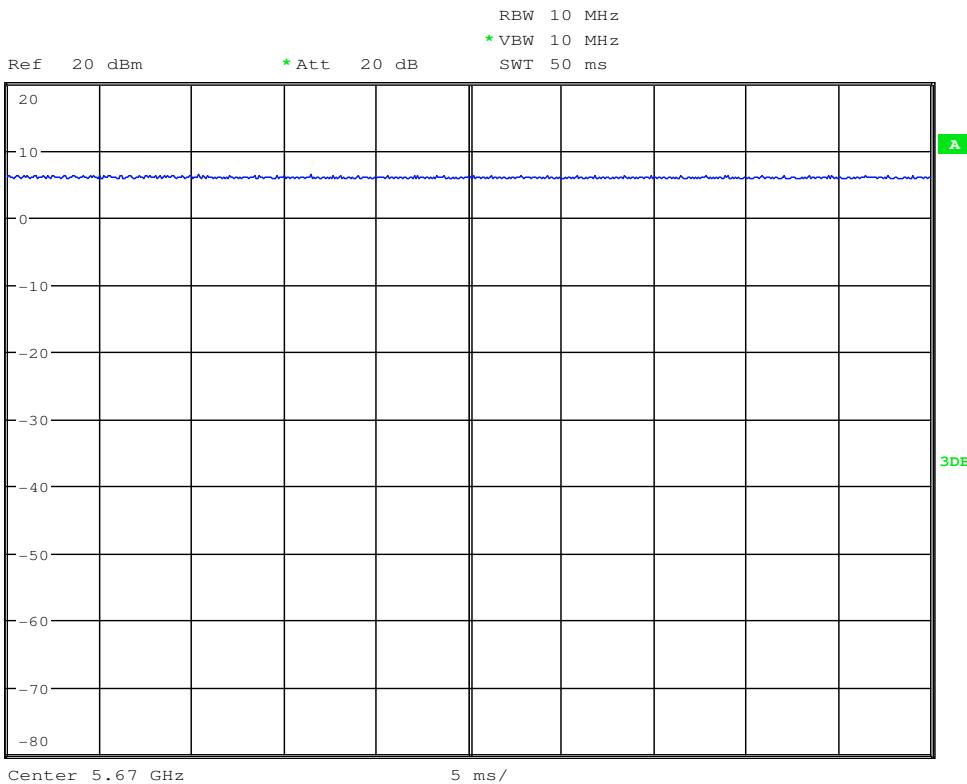
Duty Cycle_11AC40_5230_Ant1**Duty Cycle_11AC40_5230_Ant2**

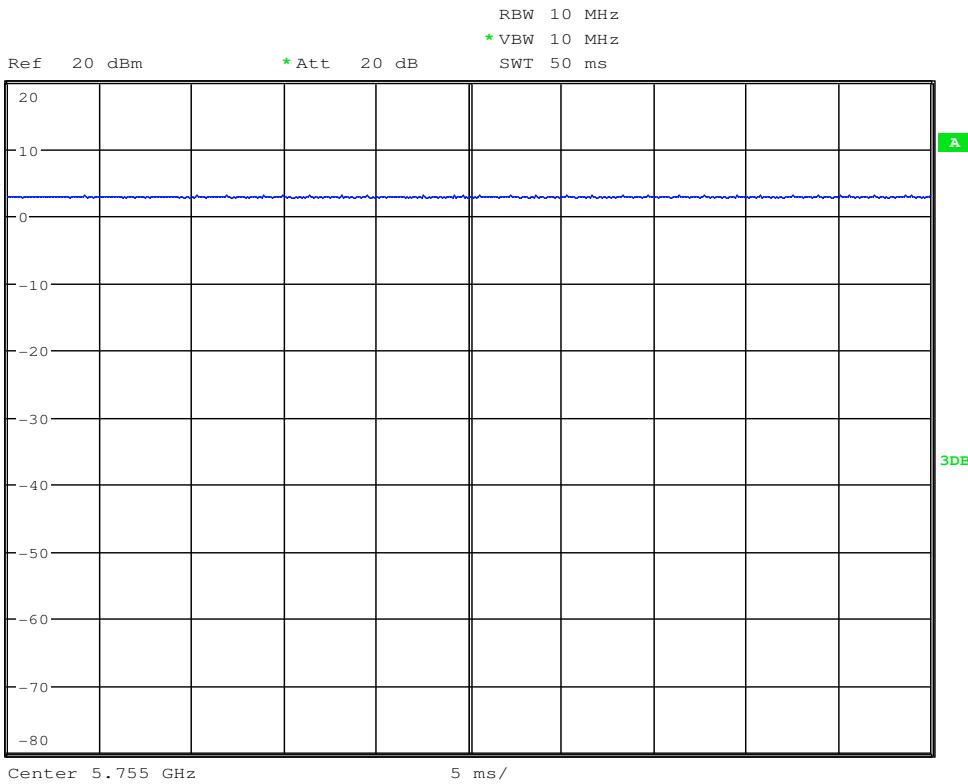
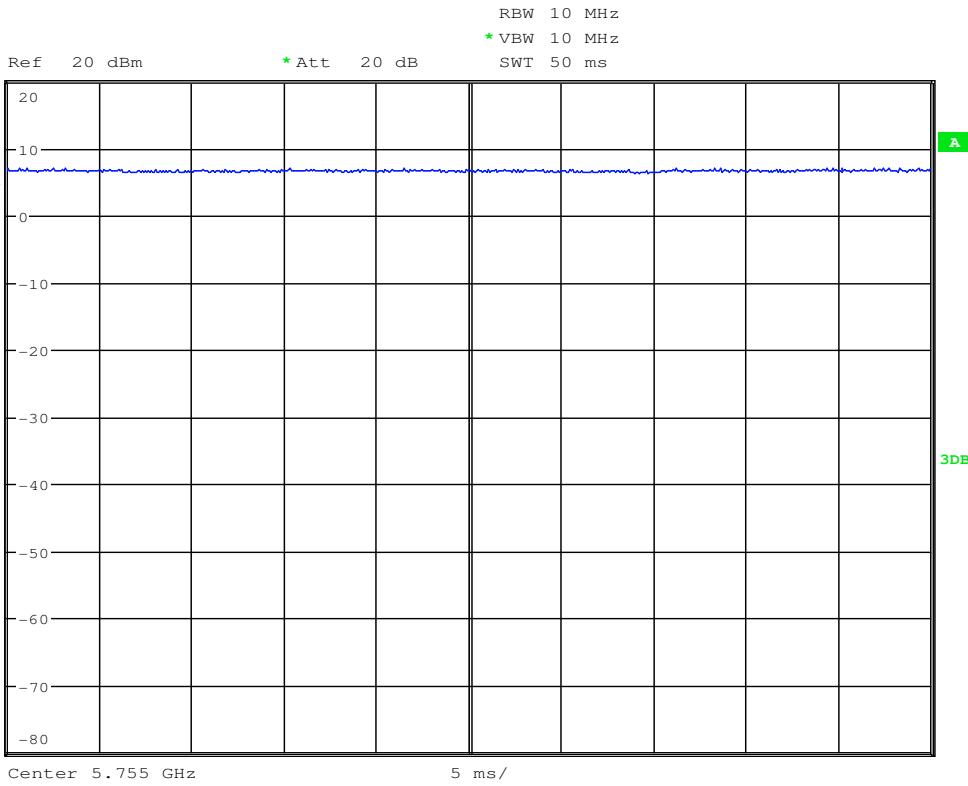
Duty Cycle_11AC40_5270_Ant1**Duty Cycle_11AC40_5270_Ant2**

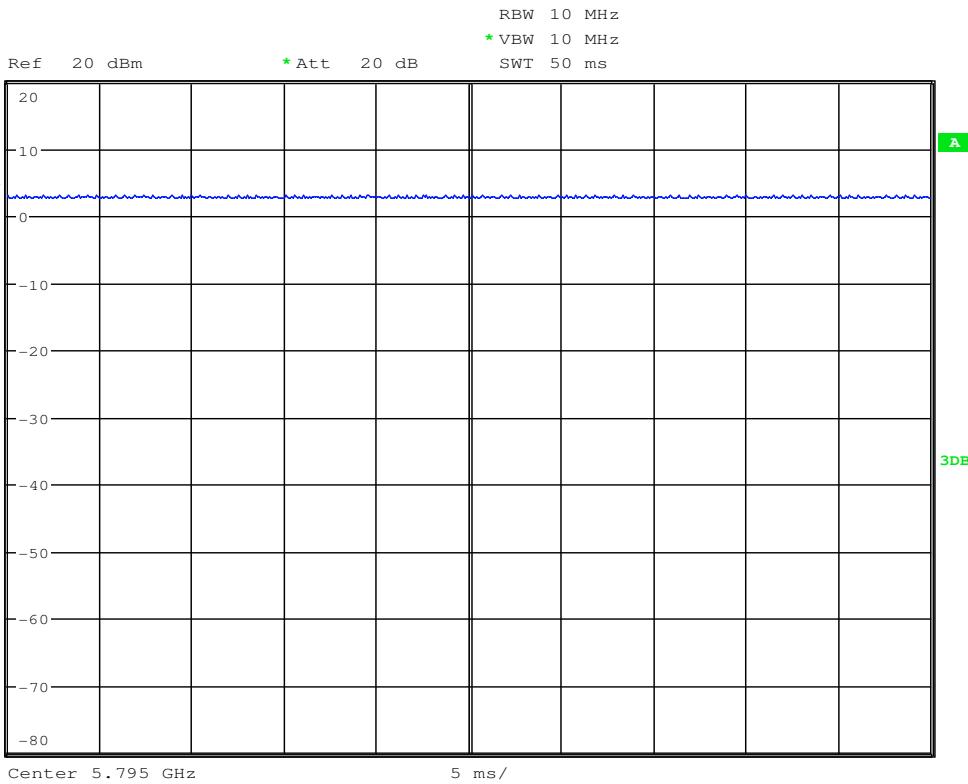
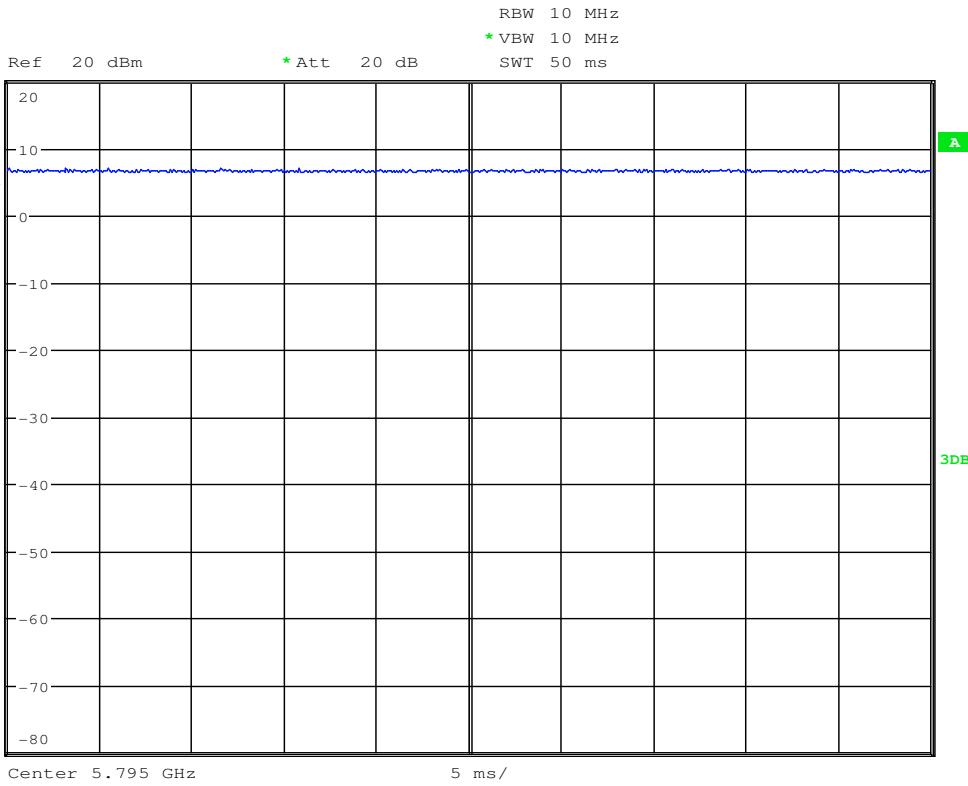
Duty Cycle_11AC40_5310_Ant1**Duty Cycle_11AC40_5310_Ant2**

Duty Cycle_11AC40_5510_Ant1**Duty Cycle_11AC40_5510_Ant2**

Duty Cycle_11AC40_5590_Ant1**Duty Cycle_11AC40_5590_Ant2**

Duty Cycle_11AC40_5670_Ant1**Duty Cycle_11AC40_5670_Ant2**

Duty Cycle_11AC40_5755_Ant1**Duty Cycle_11AC40_5755_Ant2**

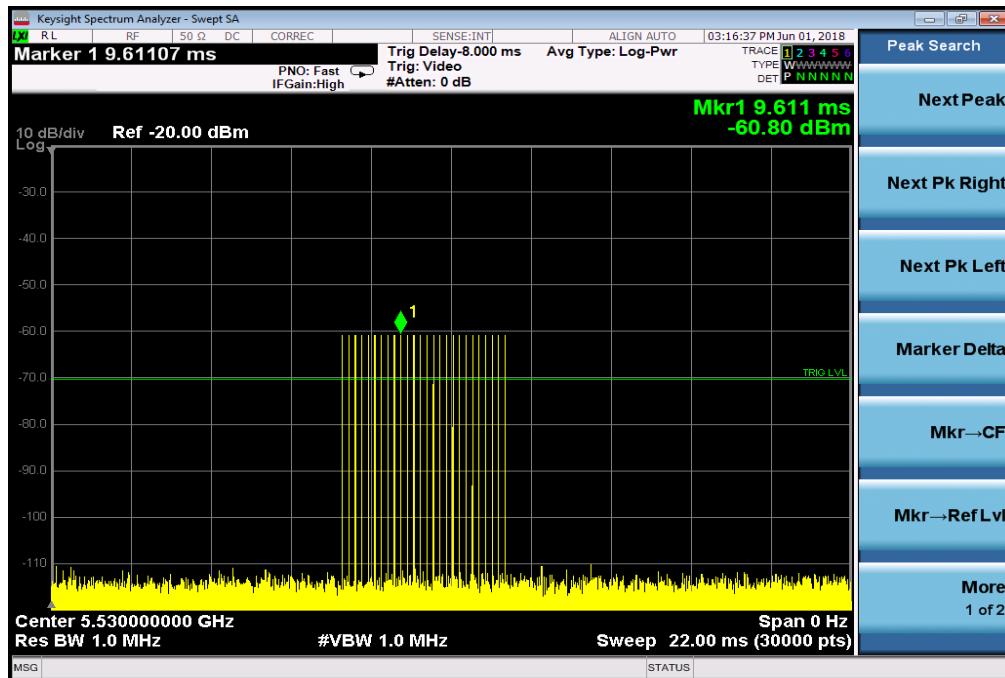
Duty Cycle_11AC40_5795_Ant1**Duty Cycle_11AC40_5795_Ant2**

6. (DFS: Non-occupancy period; DFS: Channel Move Time; DFS: Channel Closing Transmission Time)

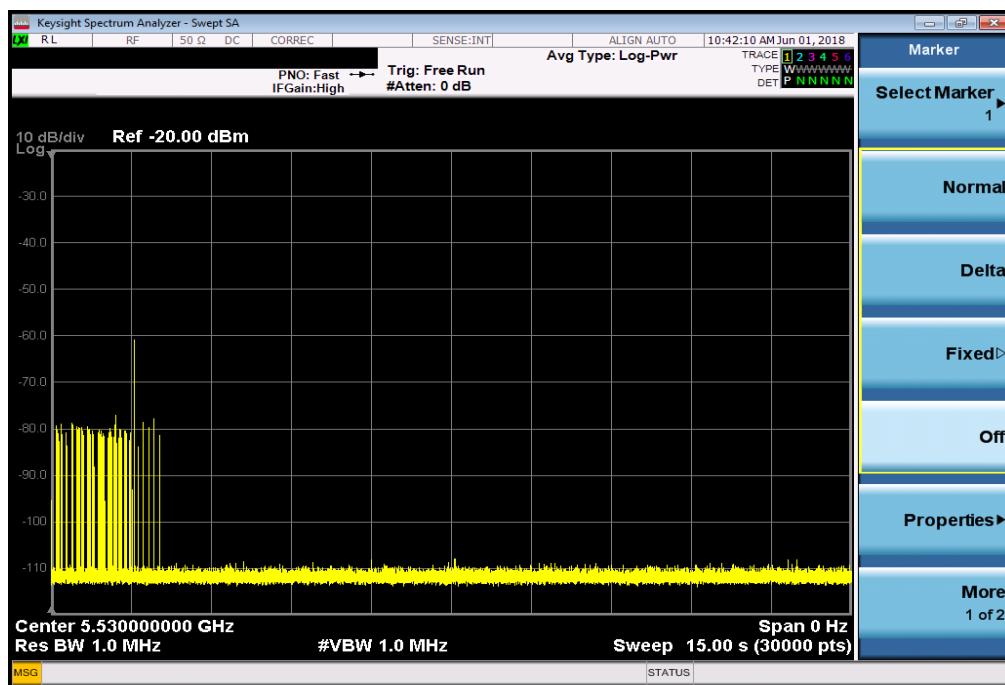
Test plots as follows:

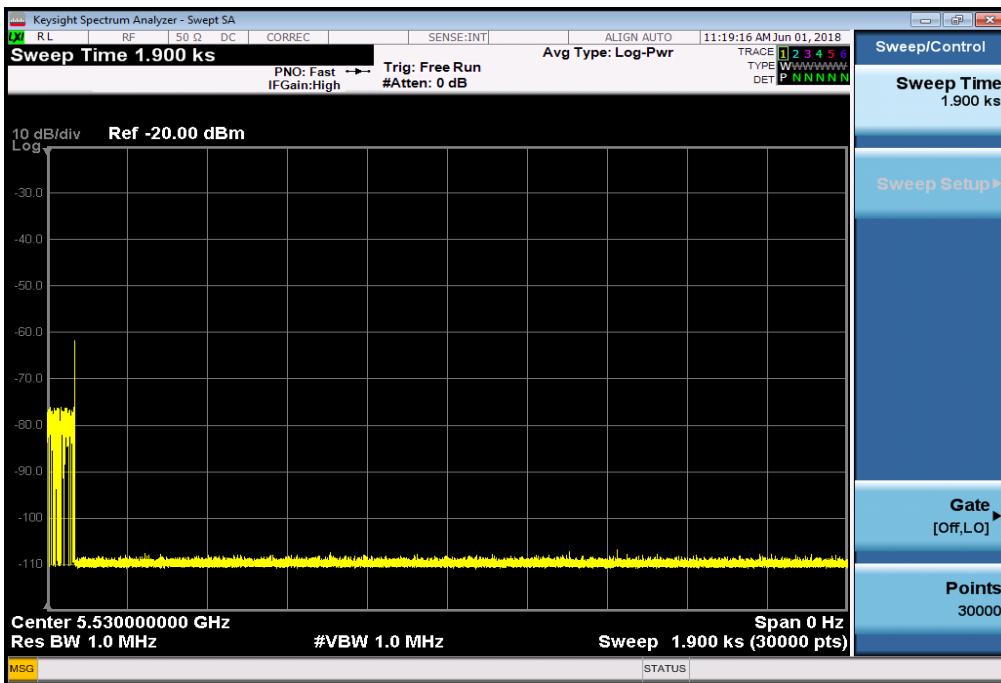
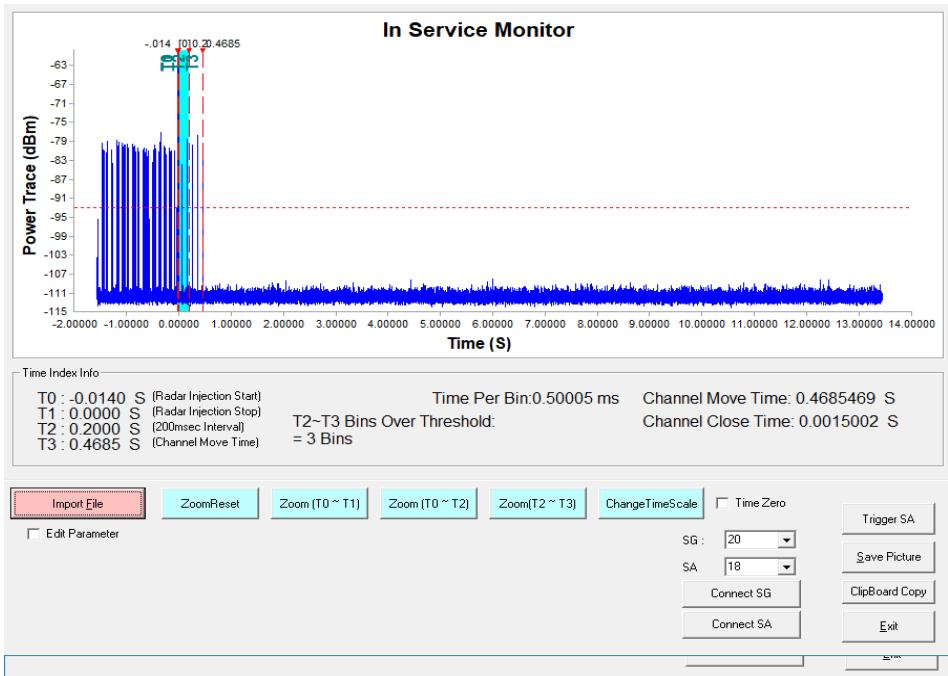
Radar Waveform Calibration Result

Radar Type 0 (80MHz / 5530MHz)



80MHz/5530MHz





- End of the Report -