

Project #234110820
Report Date: 11/8/19

Report # 31963638.001

Test Setup Photos



Figure 1: Test Setup for Conducted Emission – Front (AC Mains)



Figure 2: Test Setup for Conducted Emissions – Back (AC Mains)

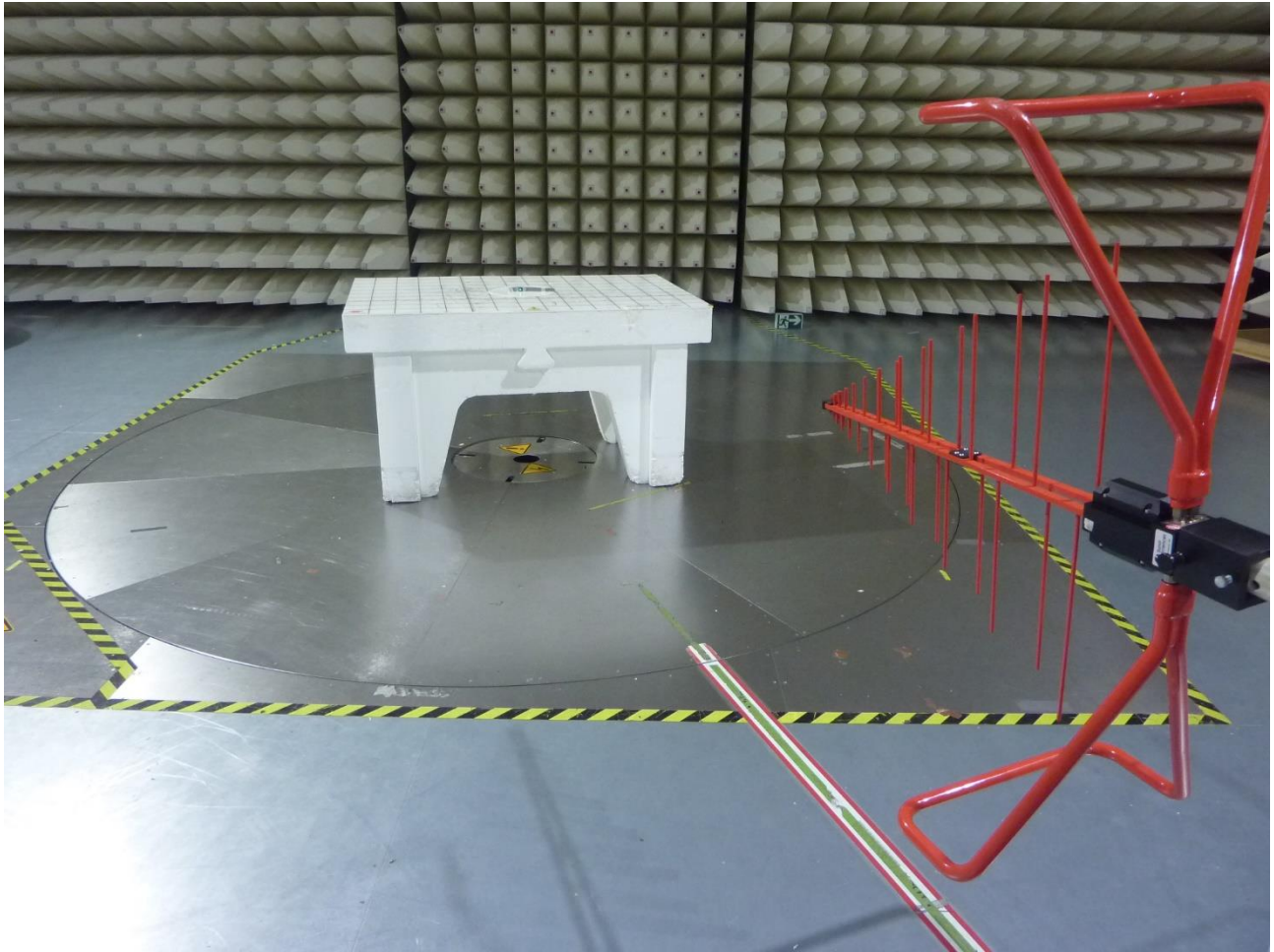


Figure 3: Test Setup for Radiated Emissions 30MHz to 1000MHz, Unintentional

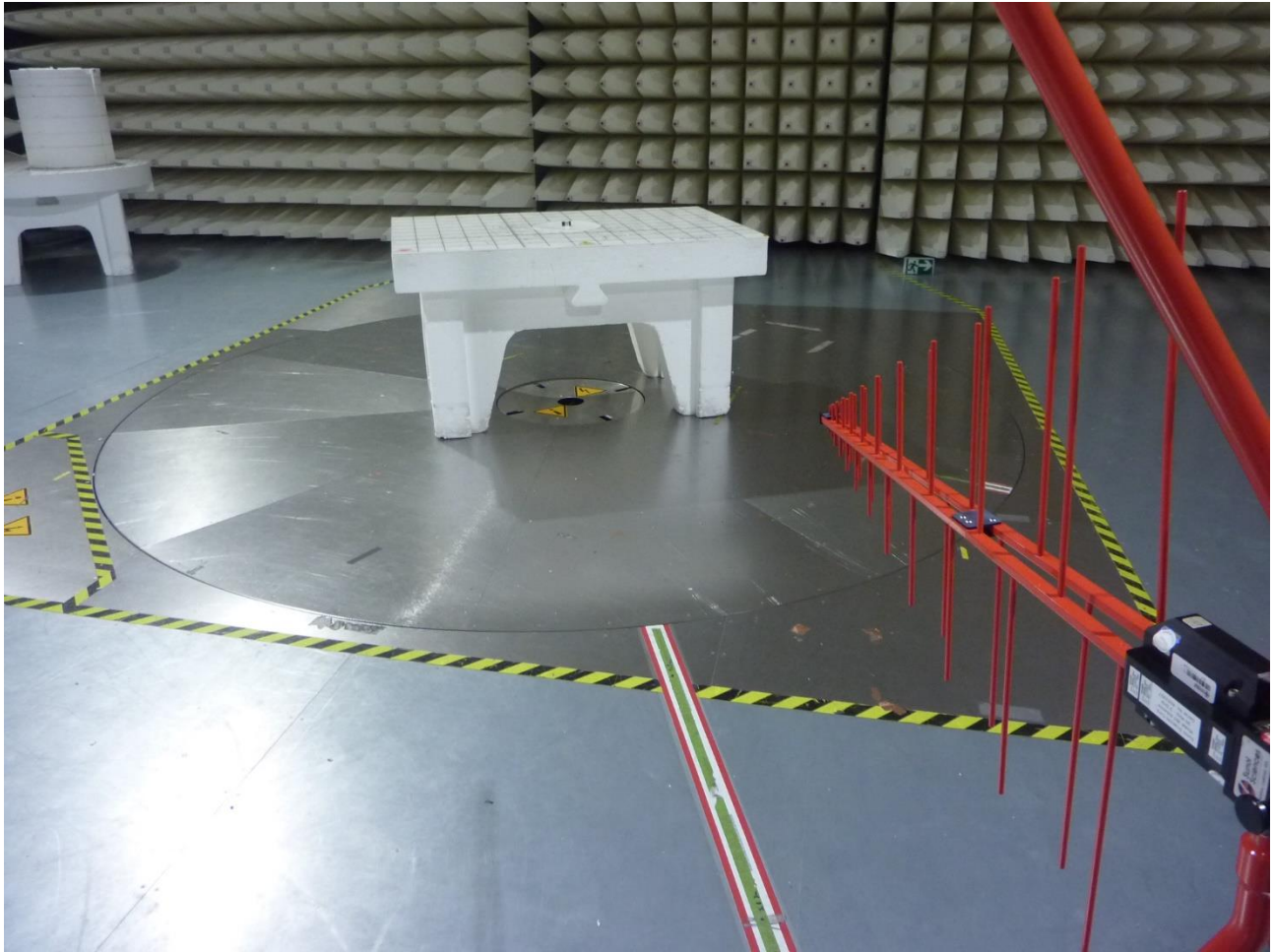


Figure 4: Test Setup for Radiated Emissions 30MHz to 1000MHz, Intentional

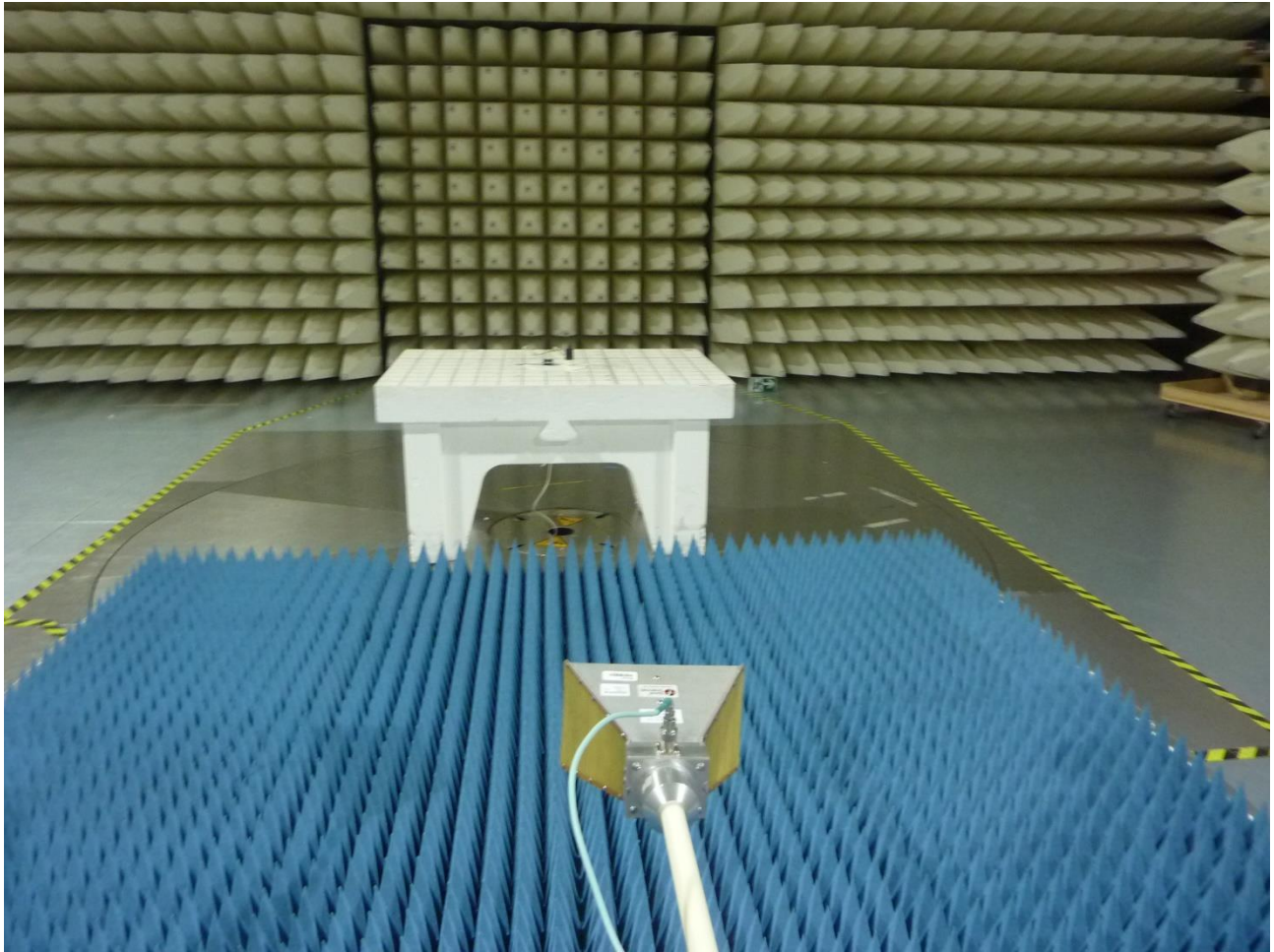


Figure 5: Test Setup for Radiated Emissions 1GHz to 18GHz, 15B Unintentional

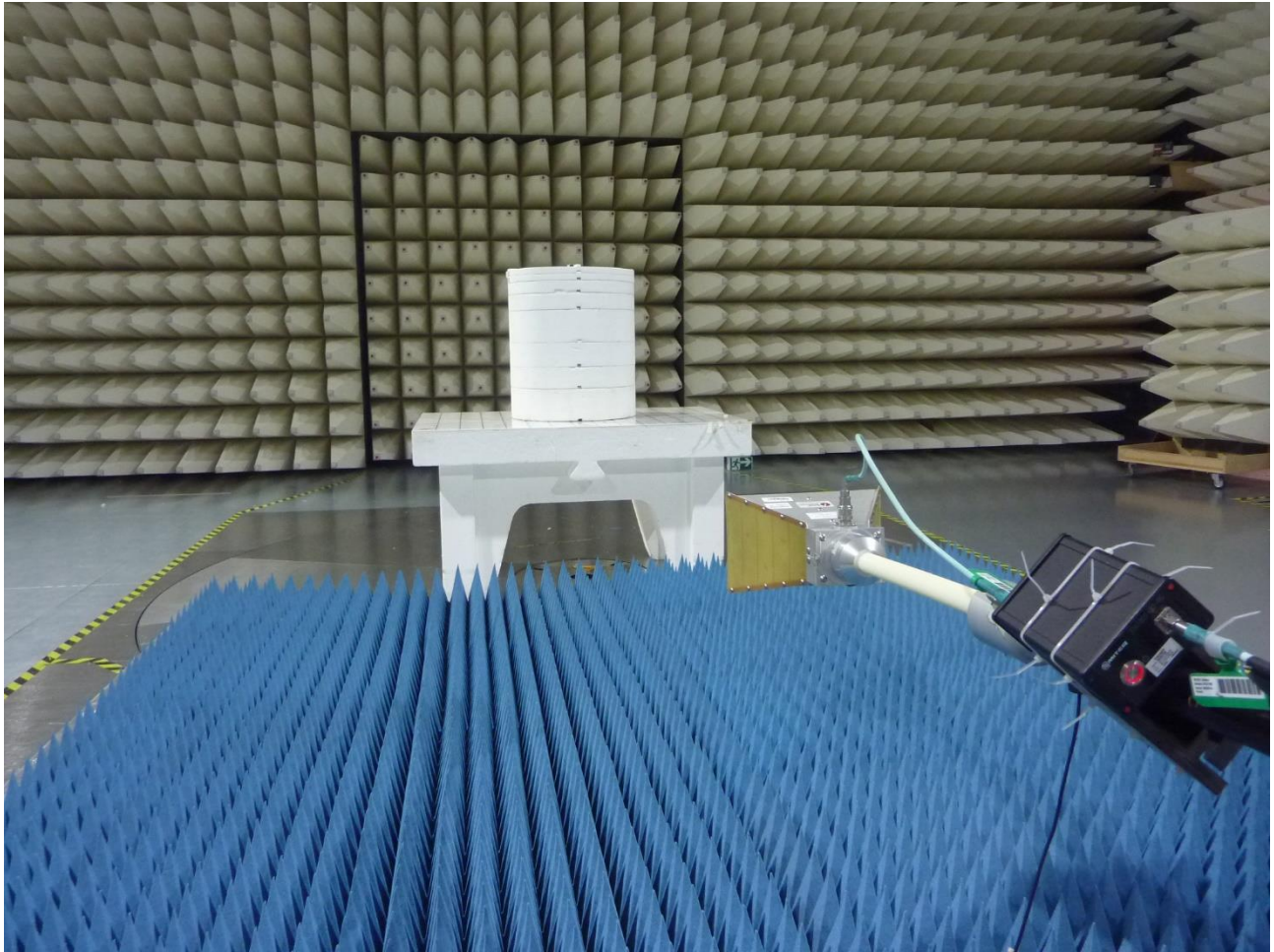


Figure 6: Test Setup for Radiated Emissions 1GHz to 18GHz, 15C Intentional

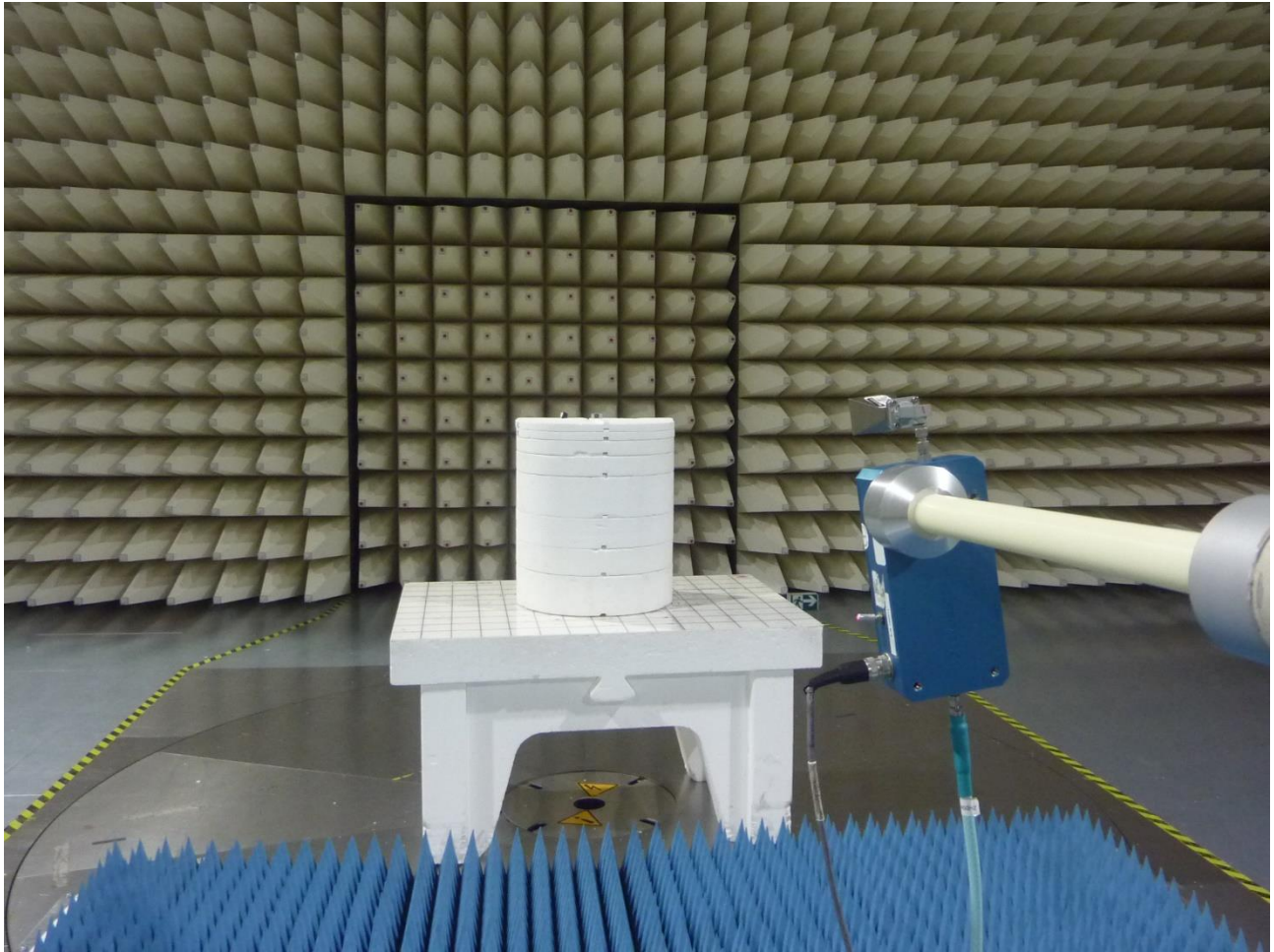


Figure 7: Test Setup for Radiated Emissions 18GHz to 26 GHz

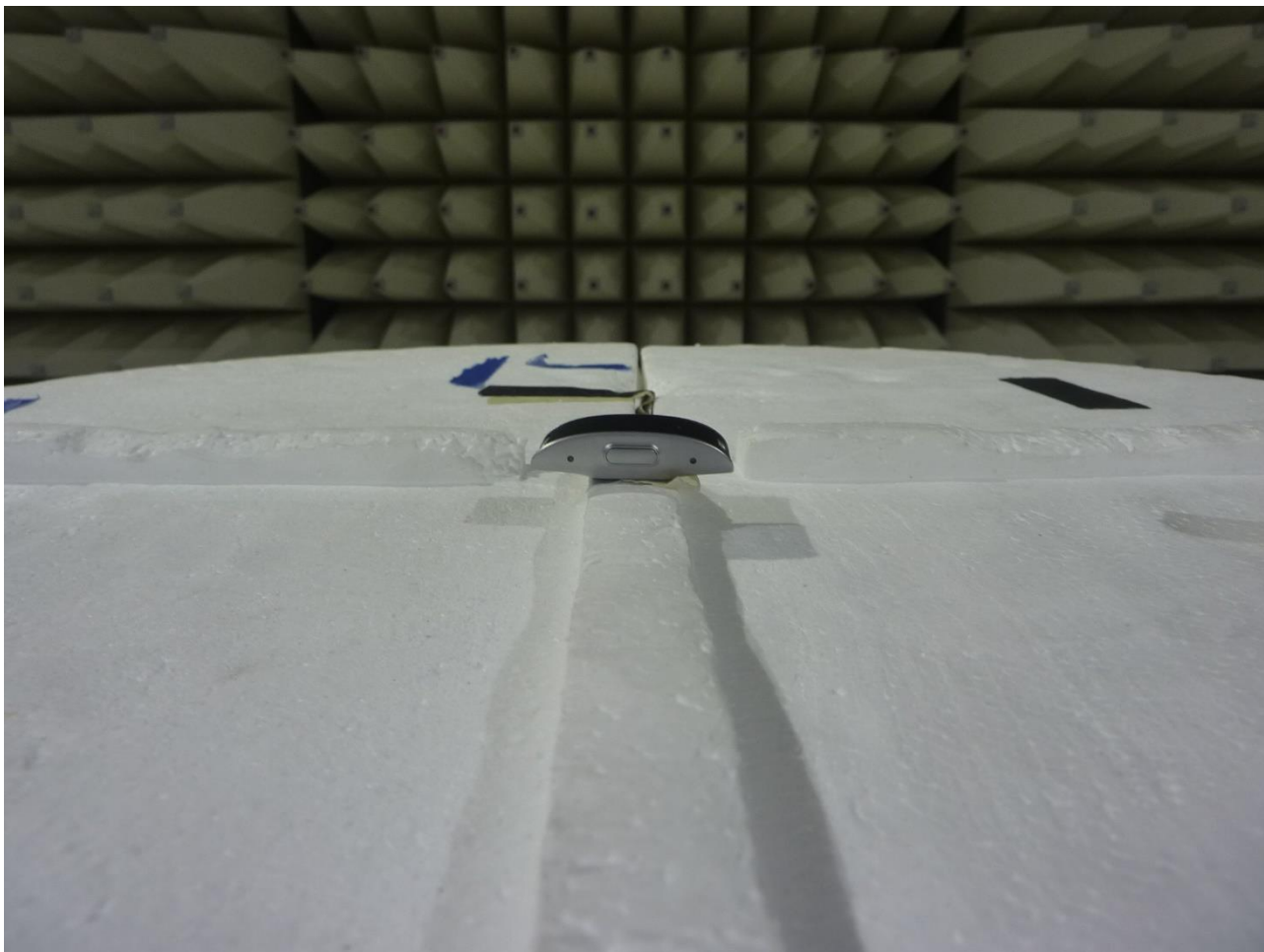


Figure 8: EUT X-Axis

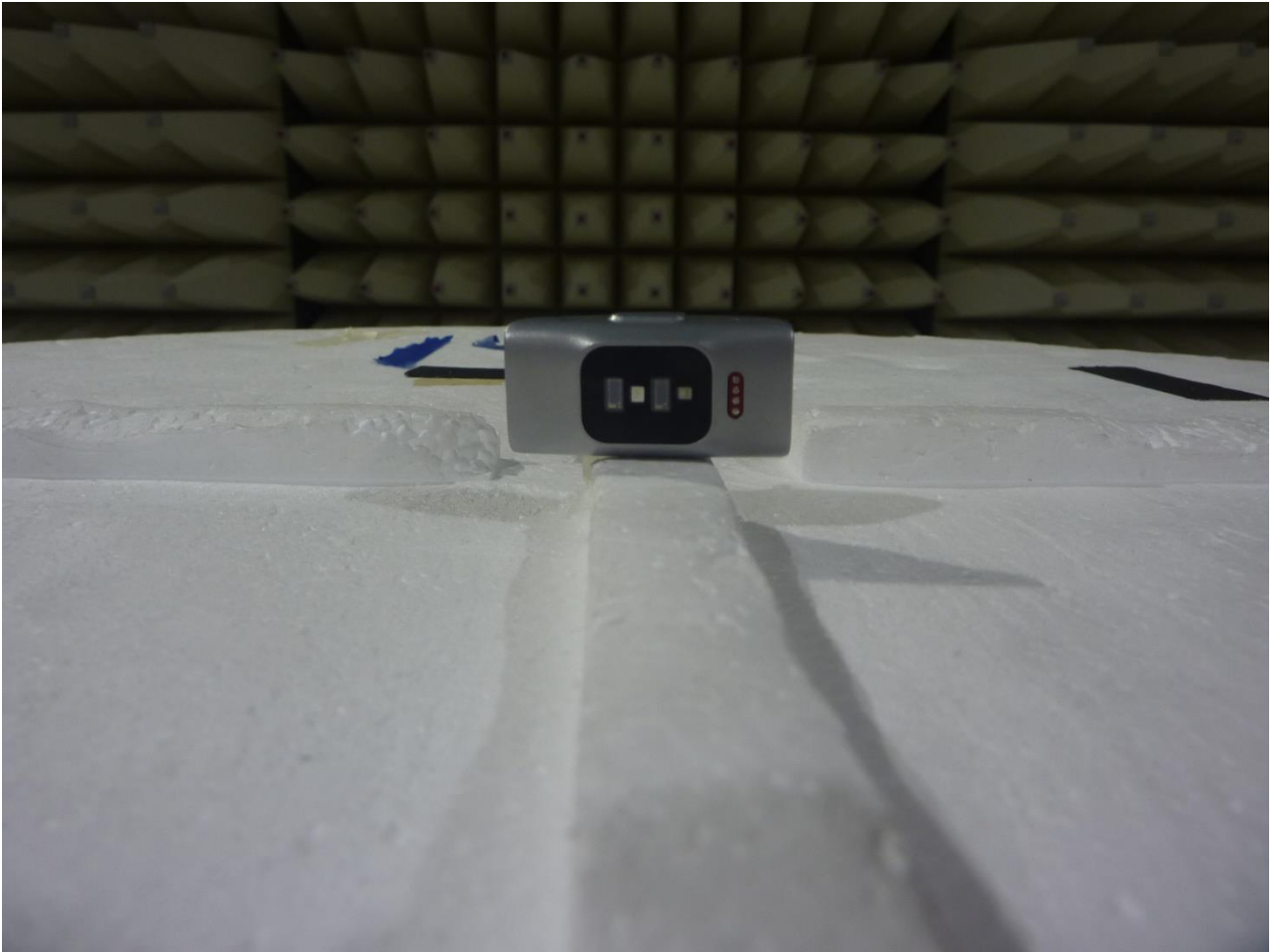


Figure 9: EUT Y-Axis (Worse Case)

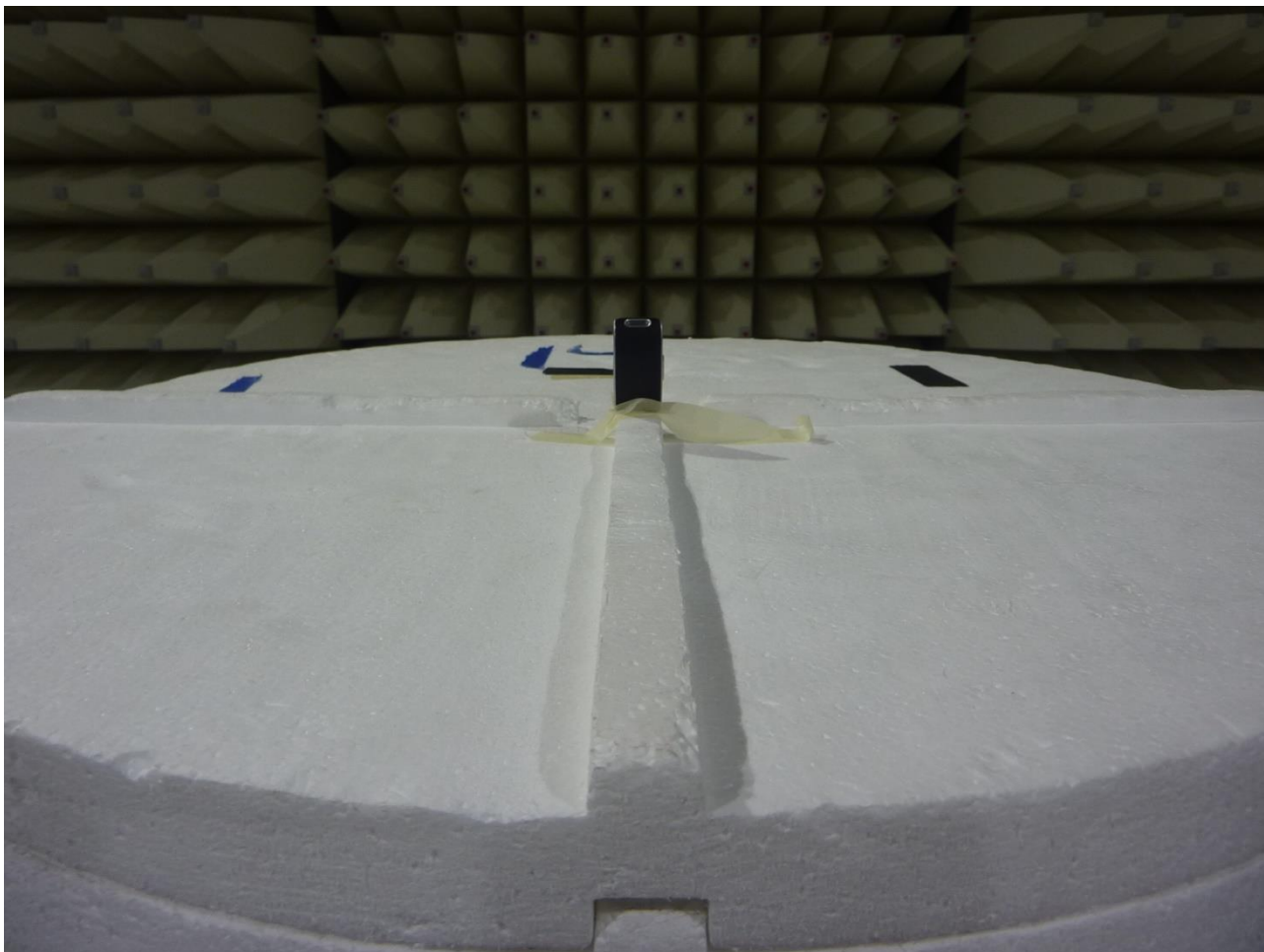


Figure 10: EUT Z-Axis