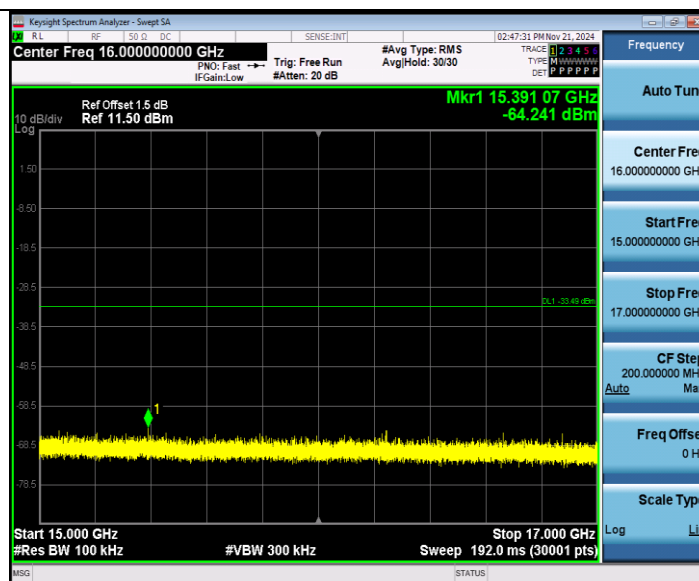
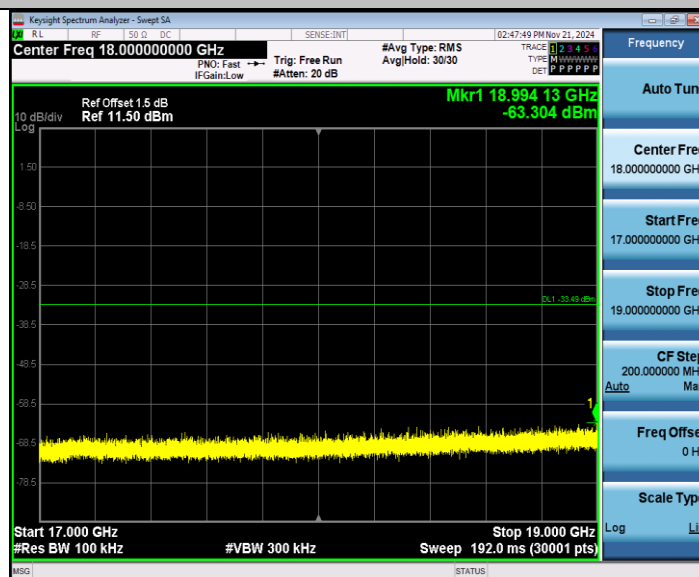
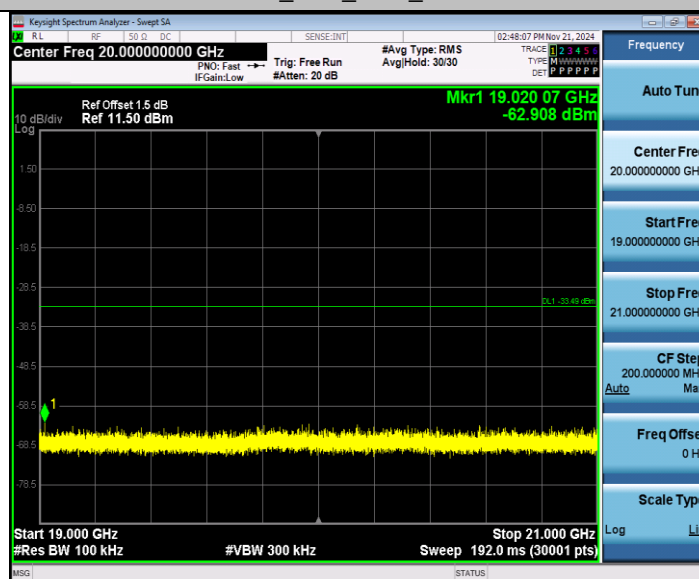




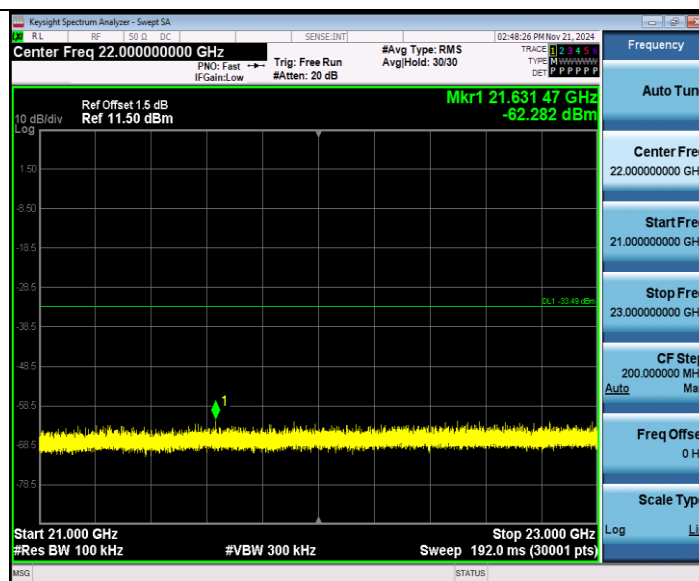
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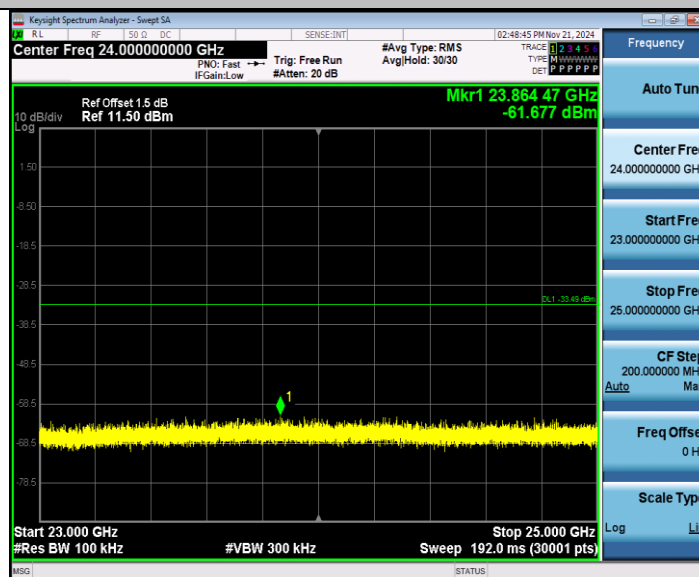
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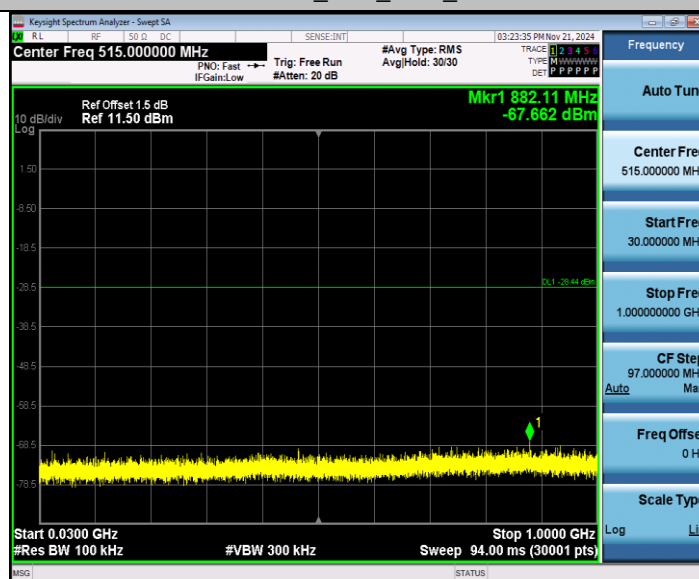
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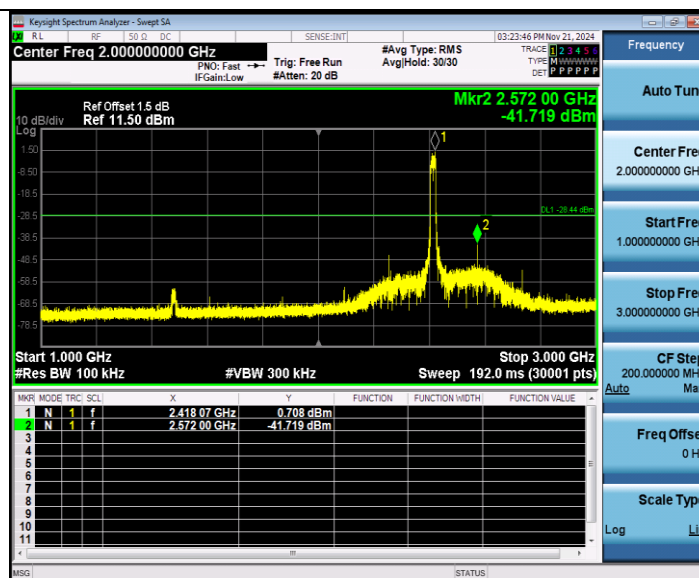
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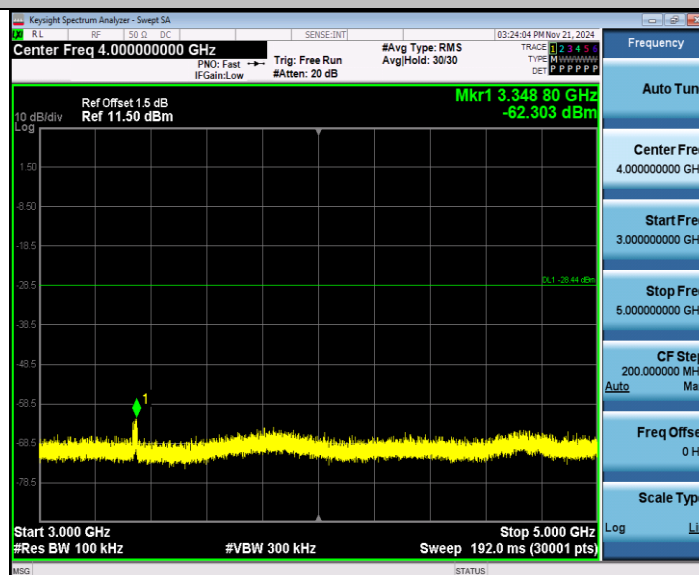
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11AX20SISO_Ant1_2412_1000~3000



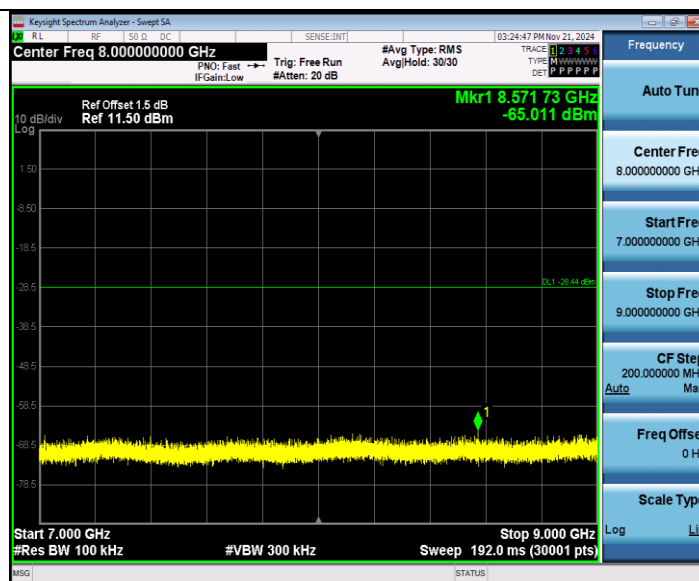
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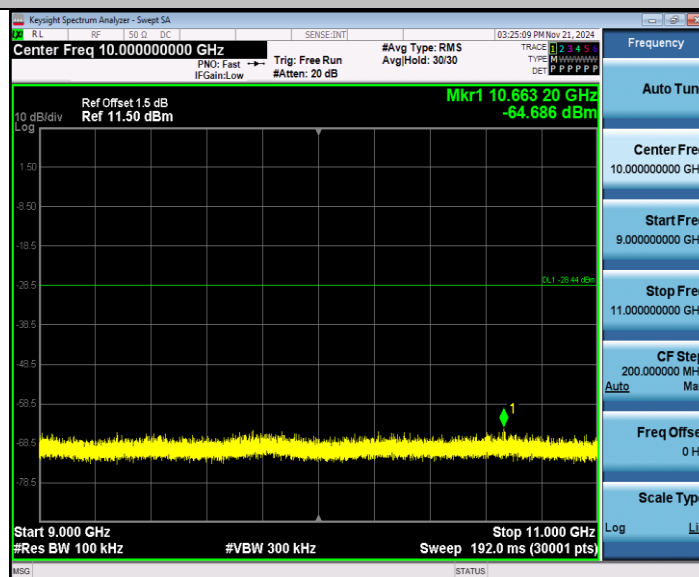
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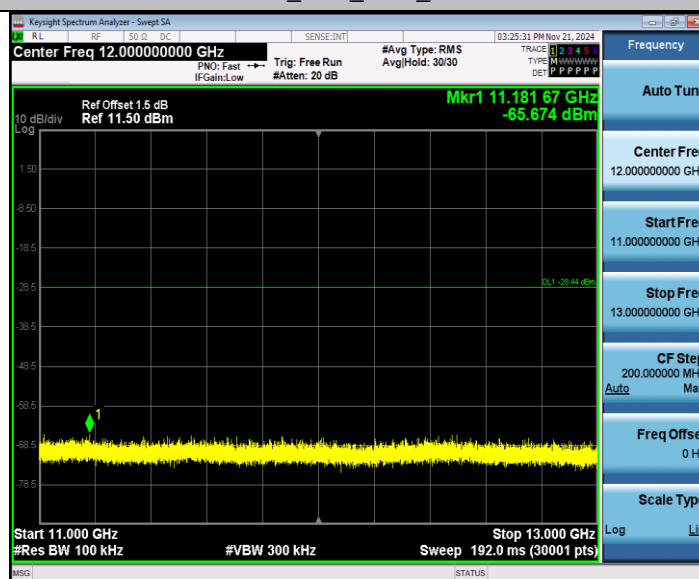
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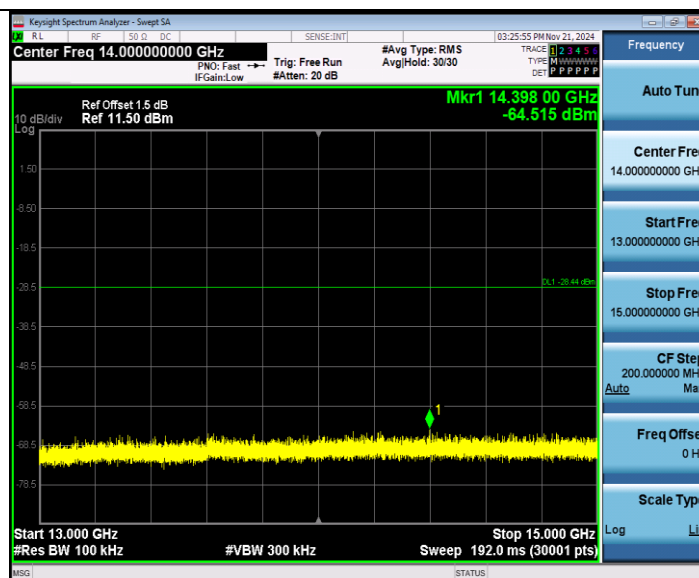
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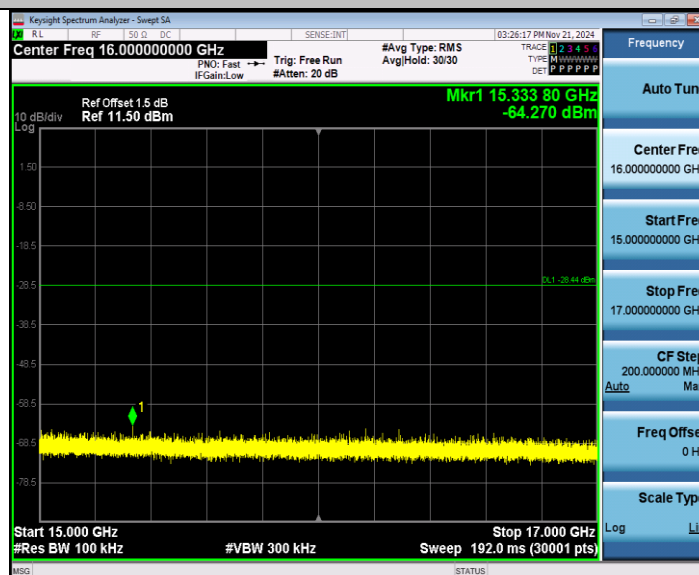
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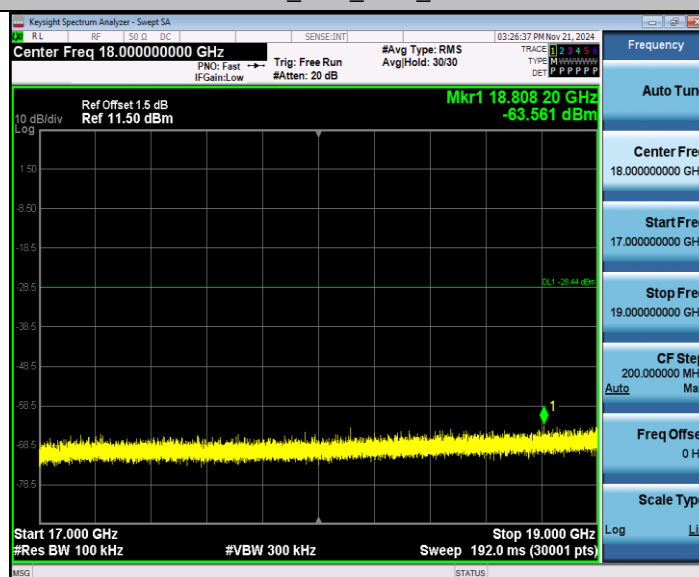
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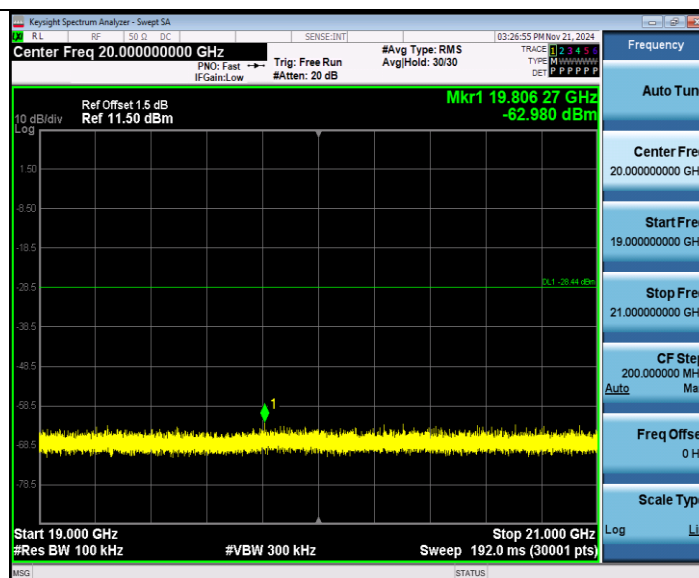
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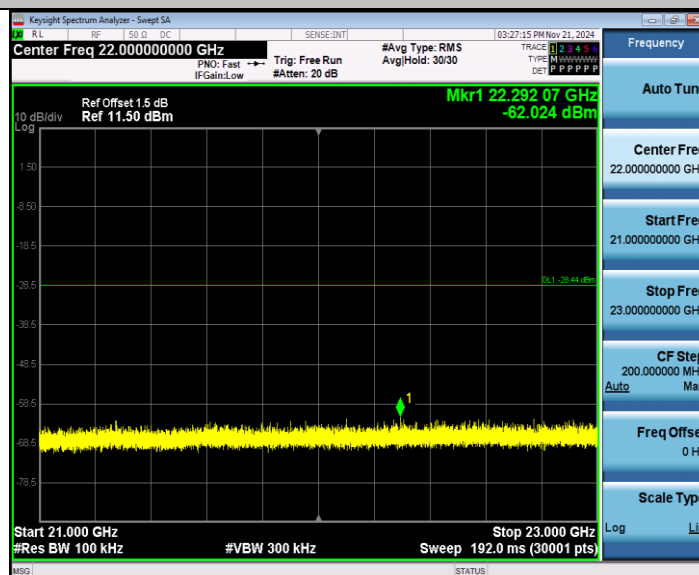
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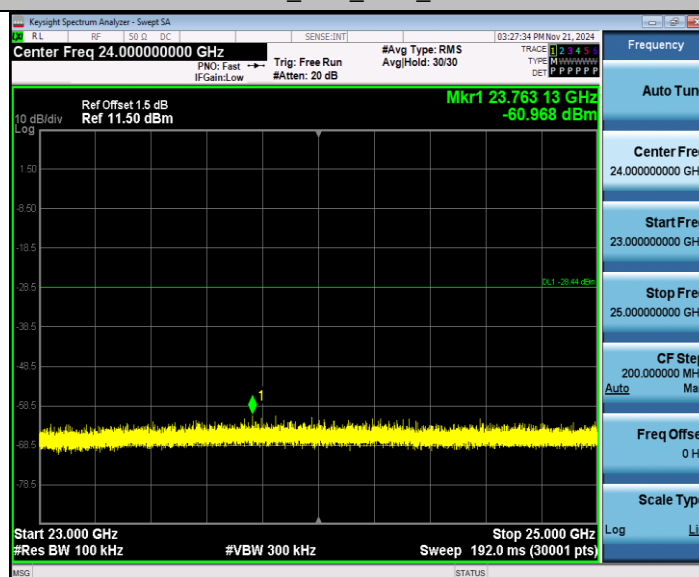
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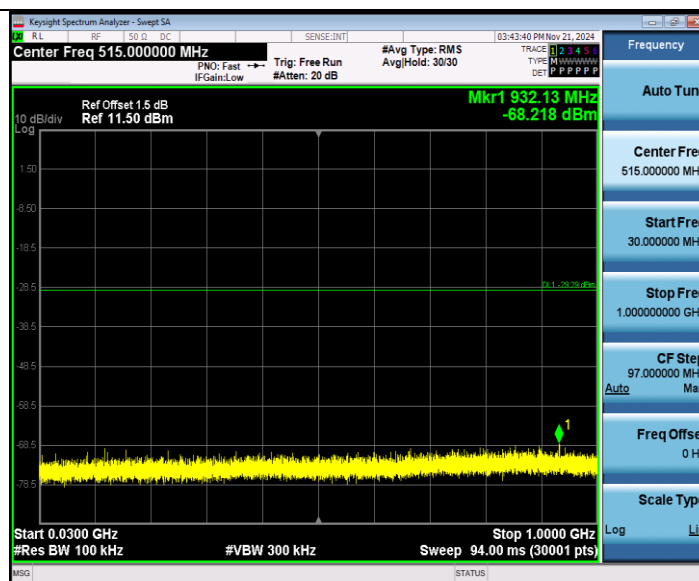
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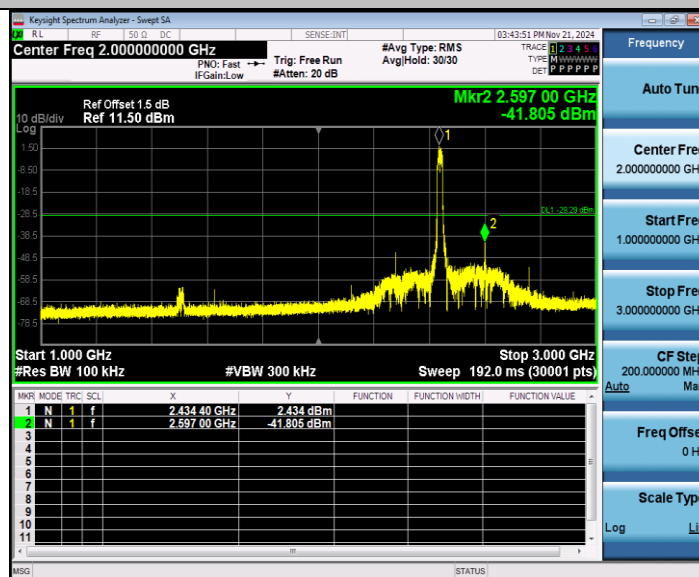
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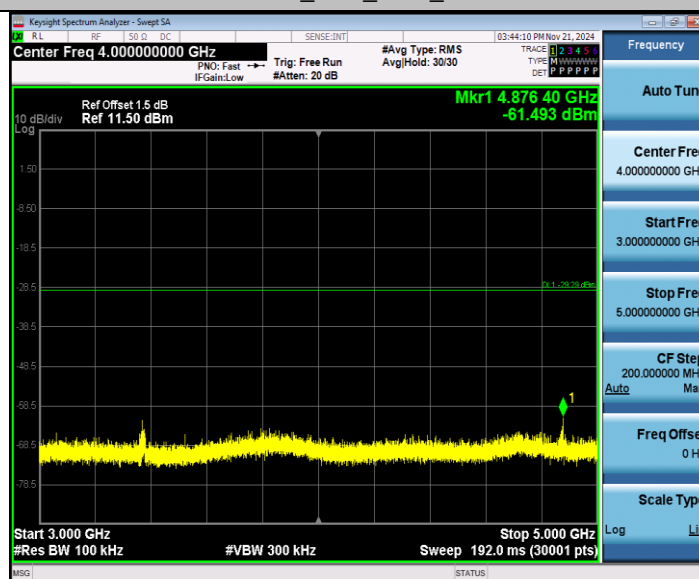
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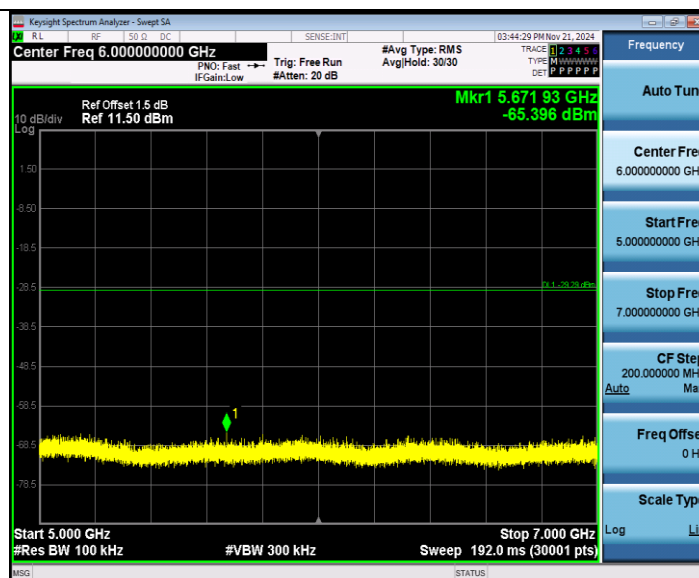
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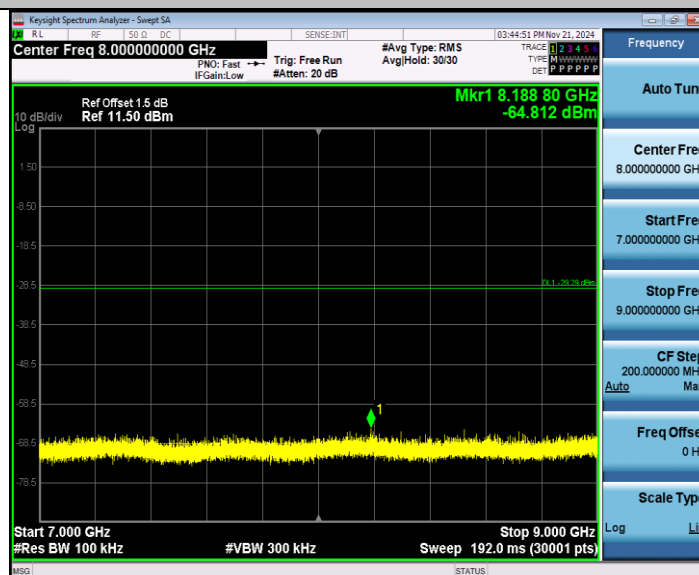
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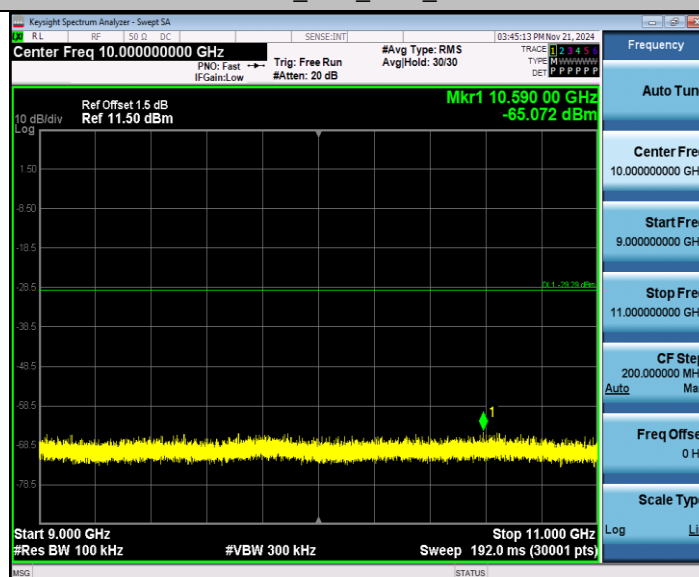
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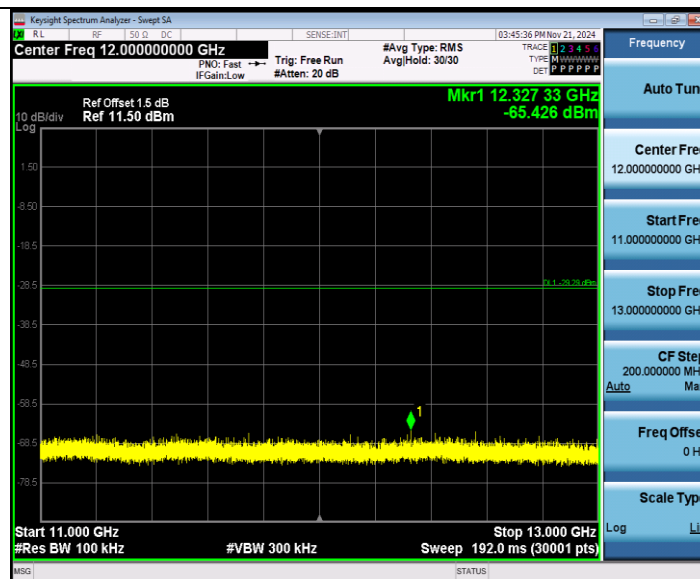
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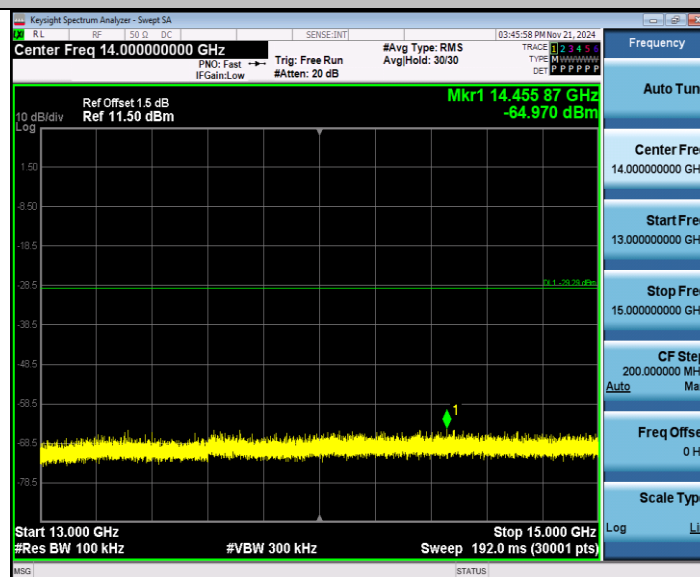
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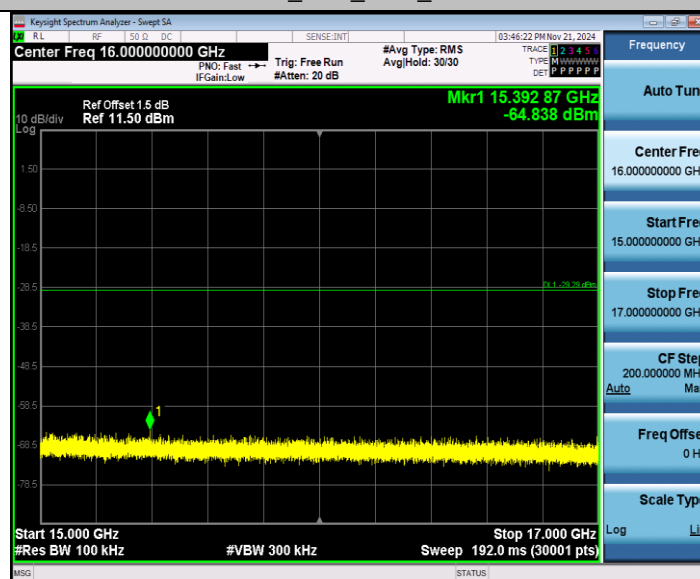
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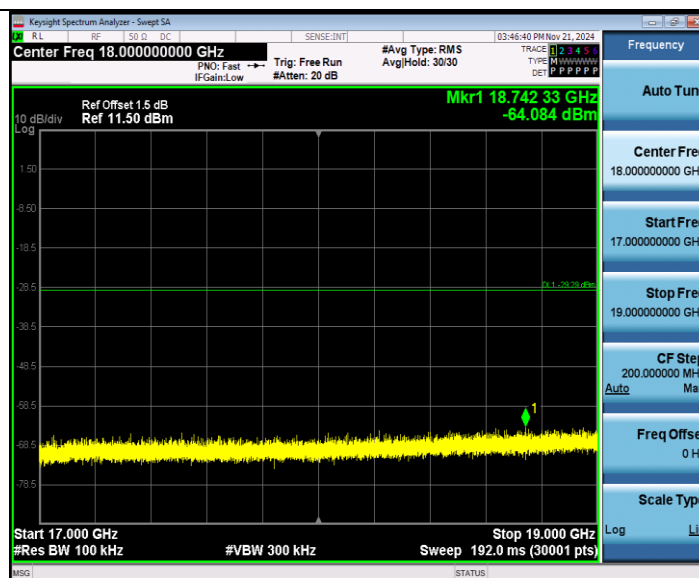
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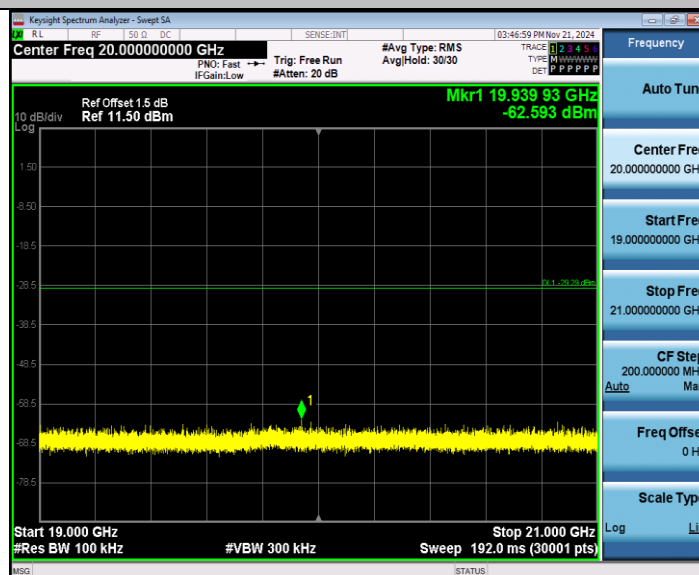
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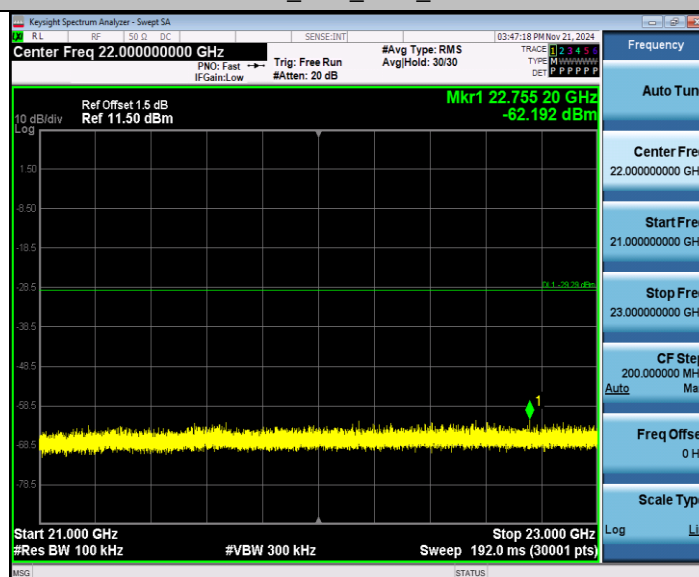
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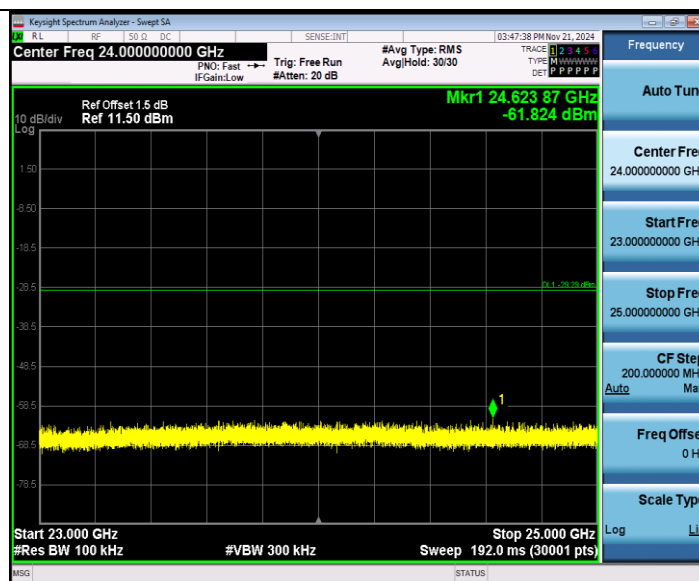
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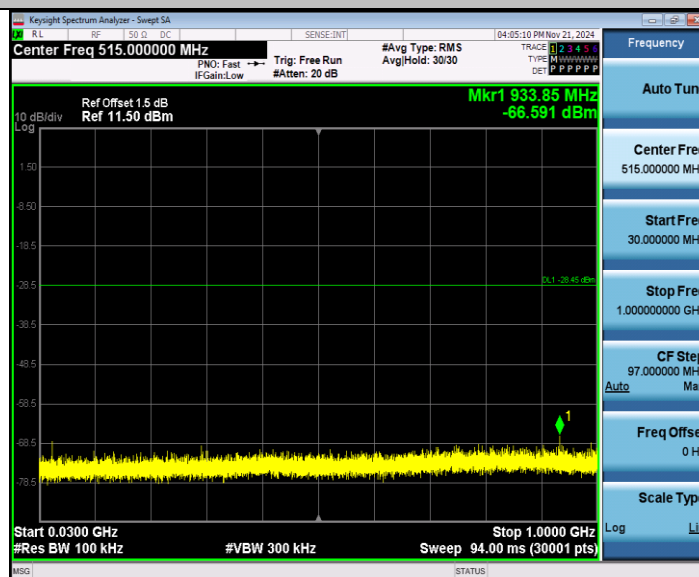
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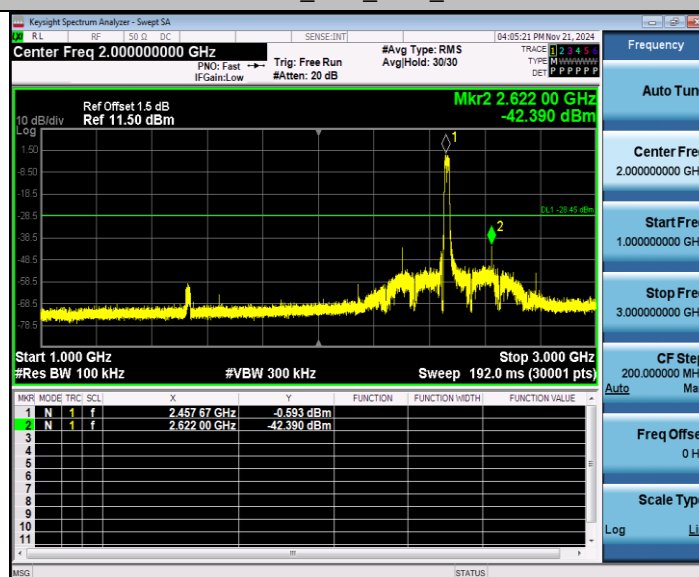
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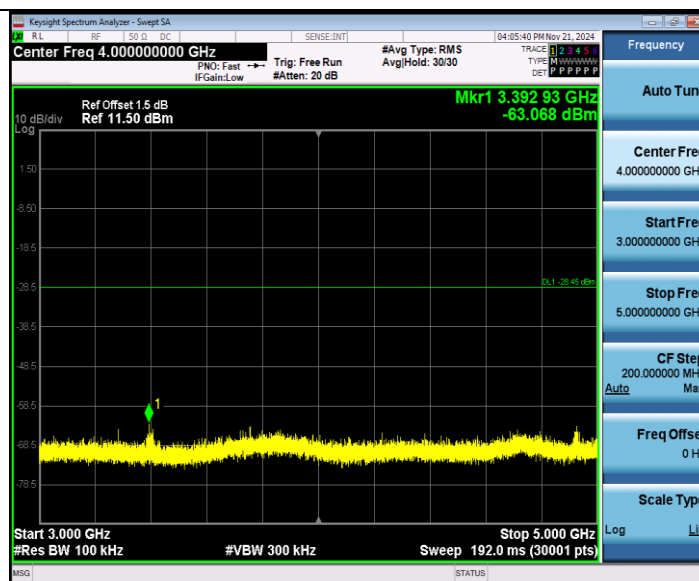
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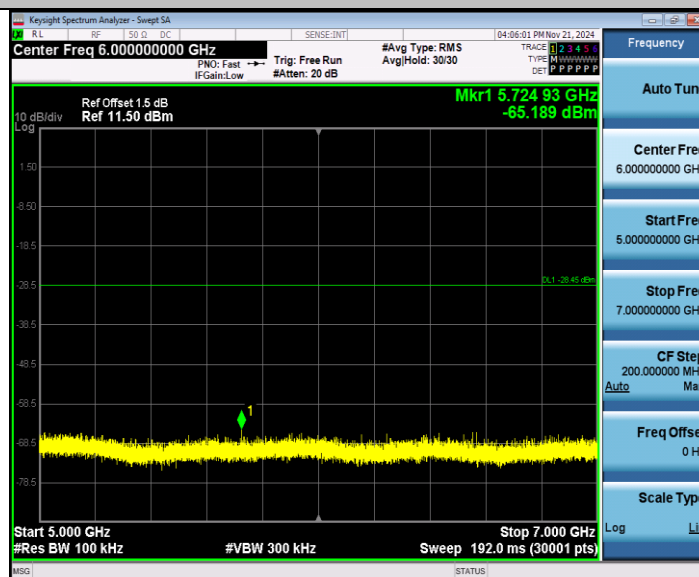
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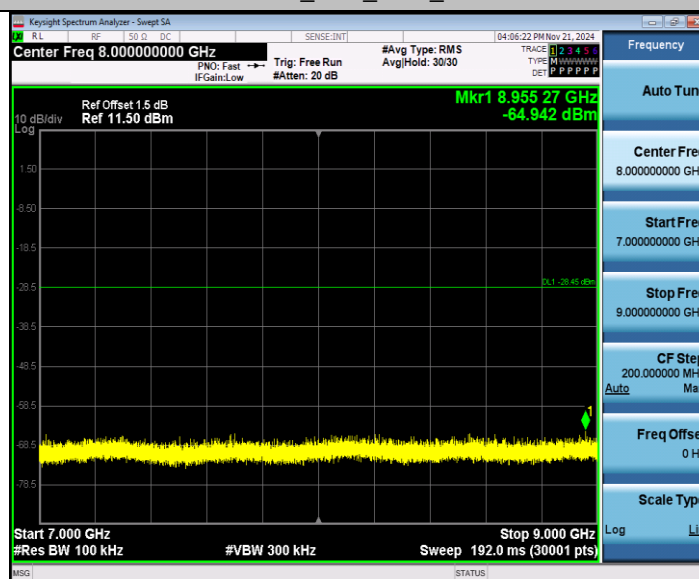
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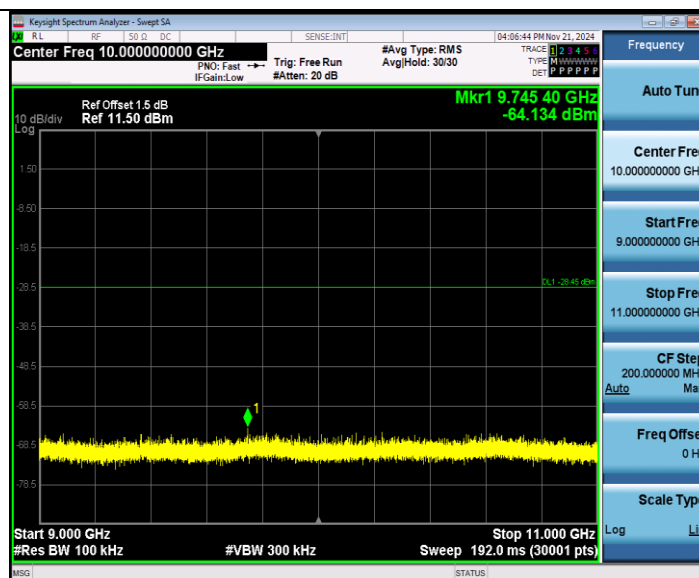
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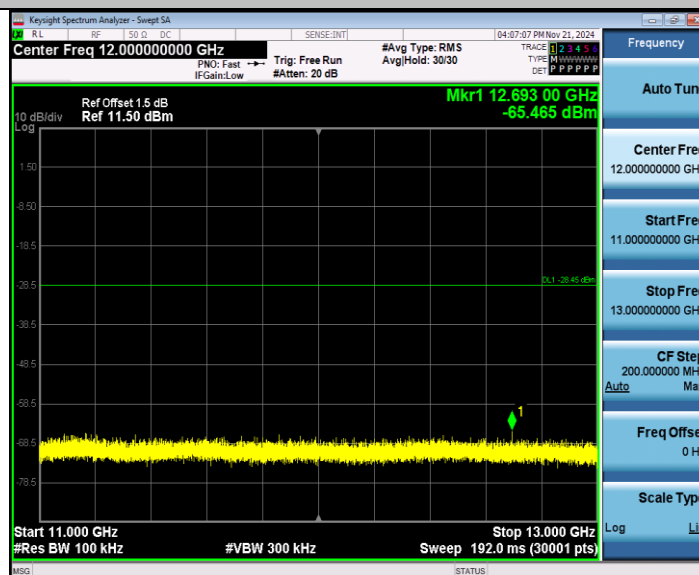
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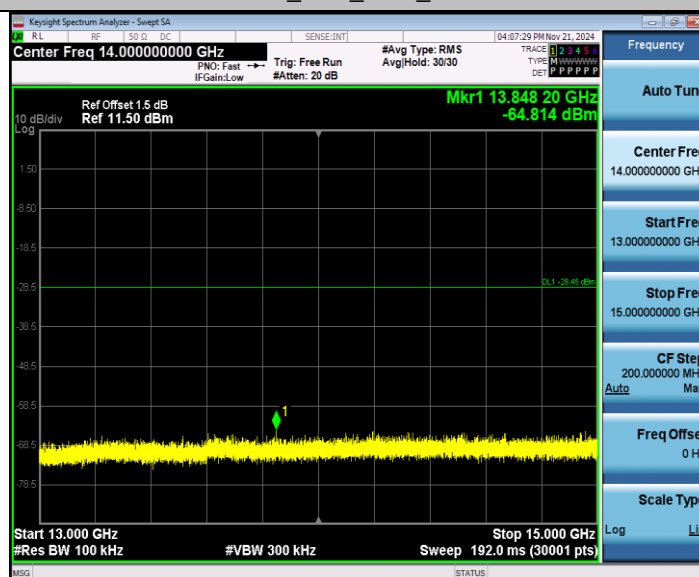
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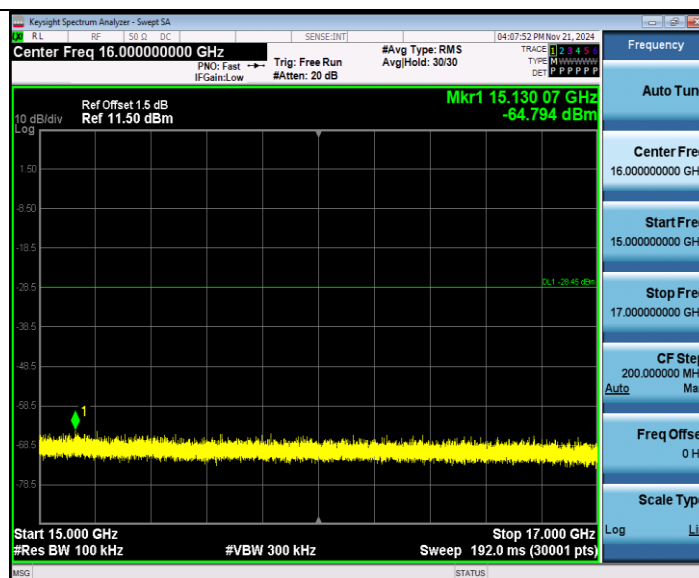
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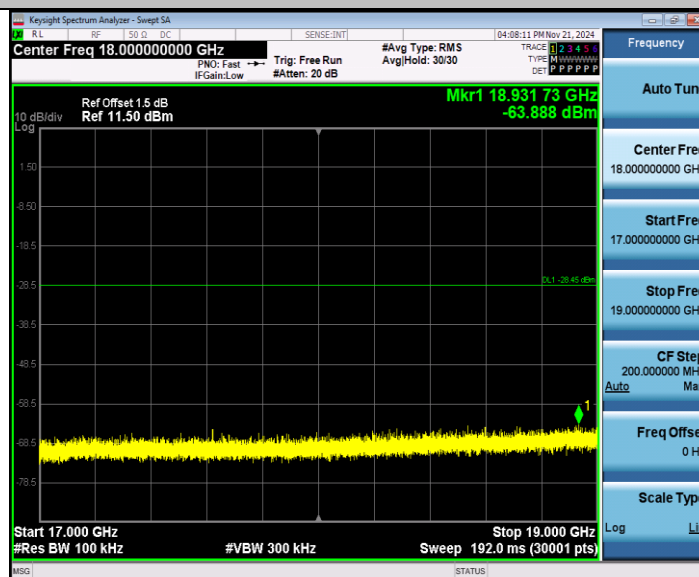
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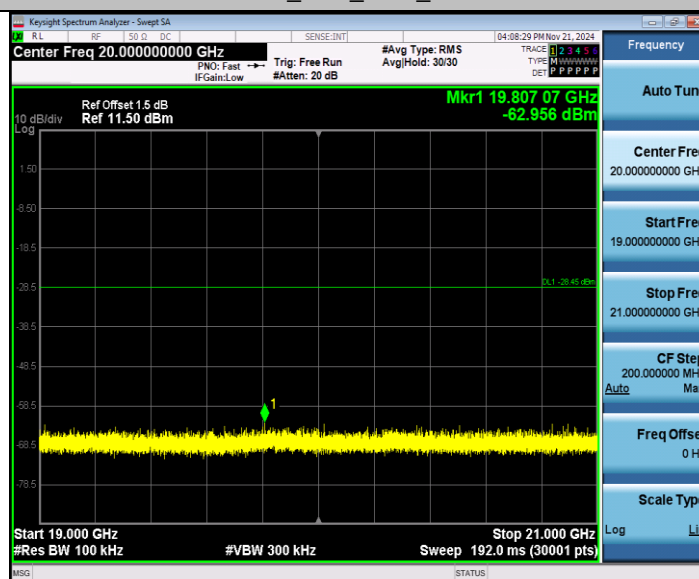
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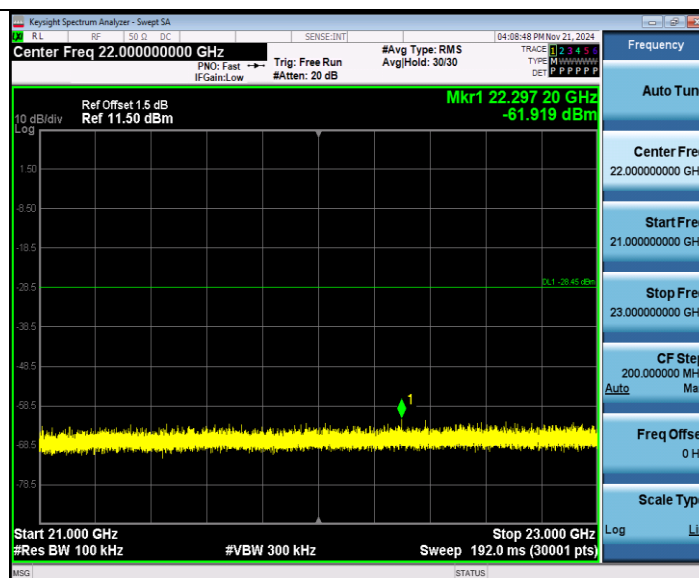
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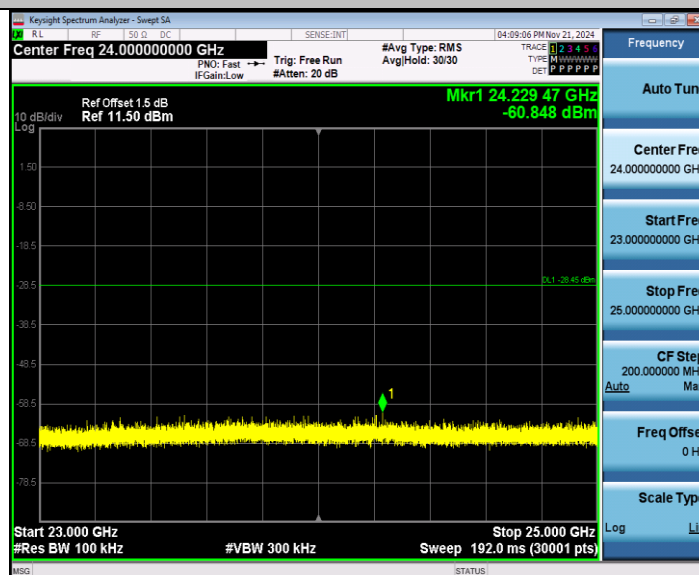
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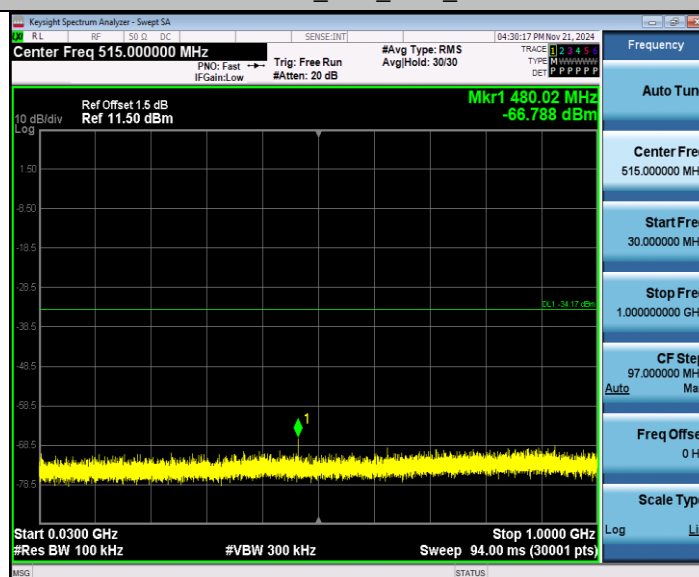
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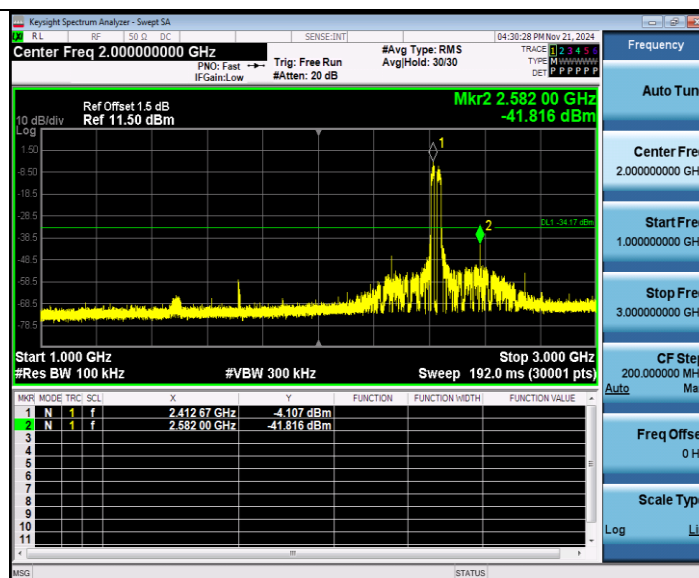
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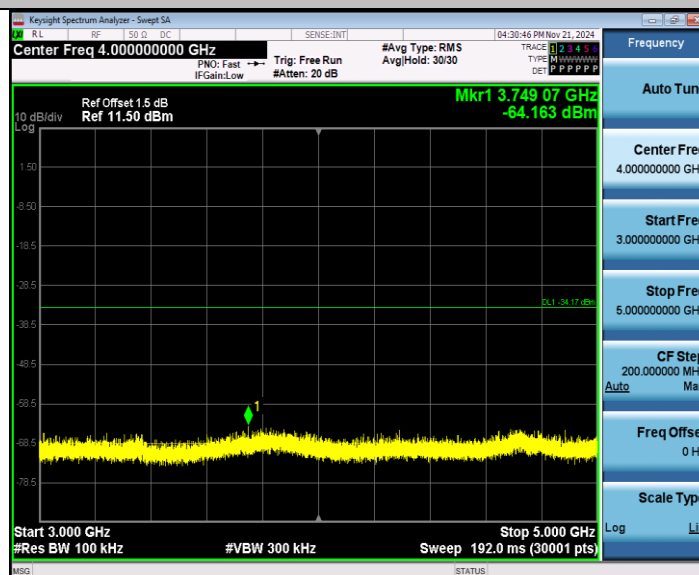
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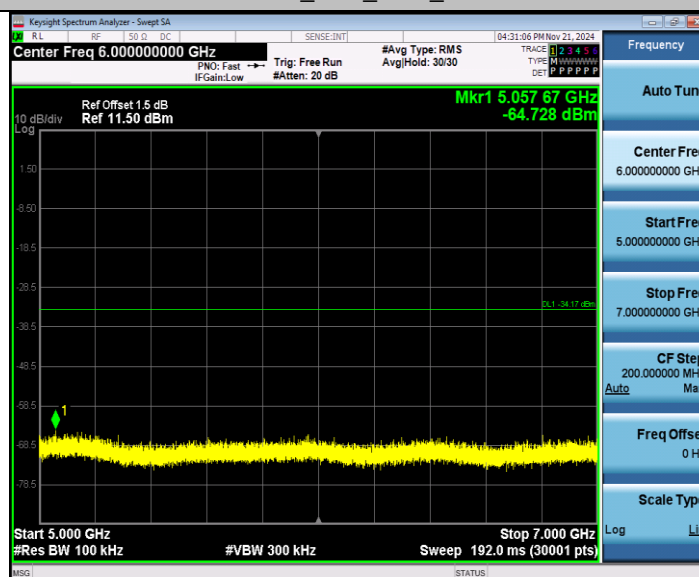
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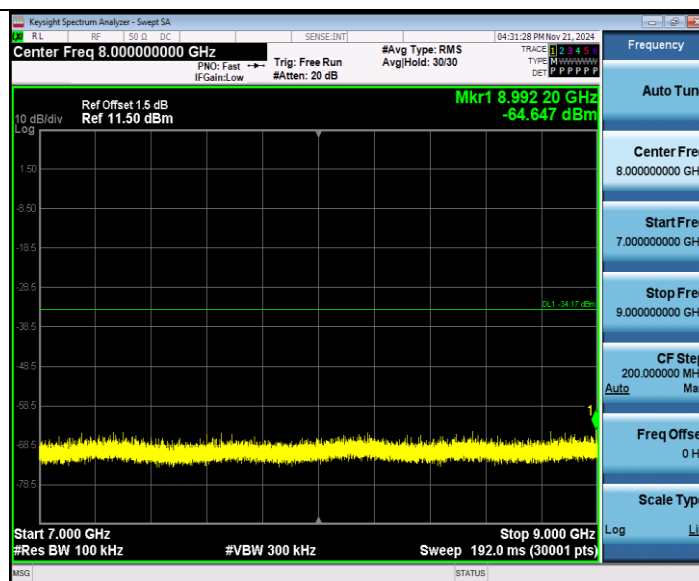
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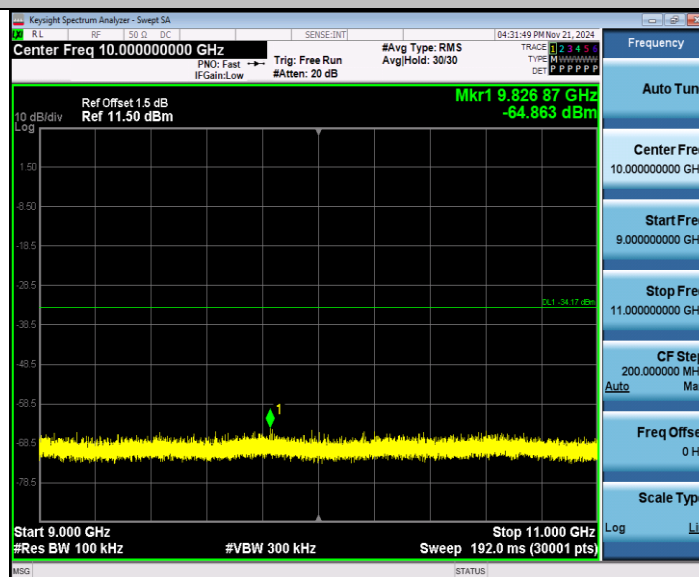
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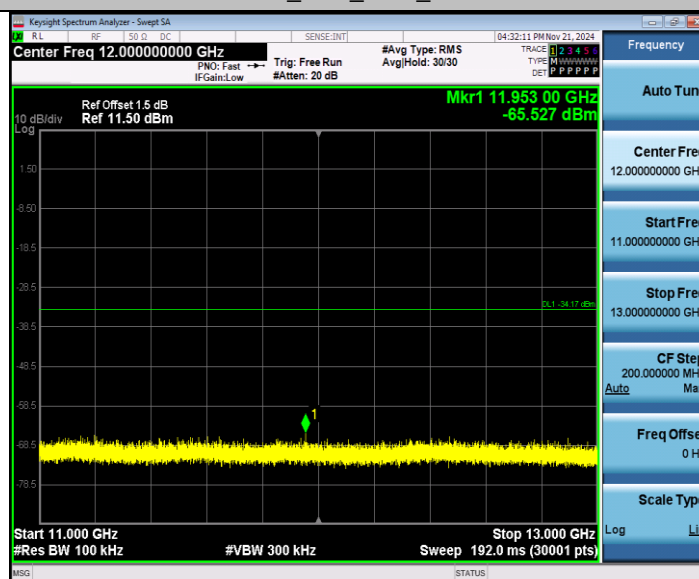
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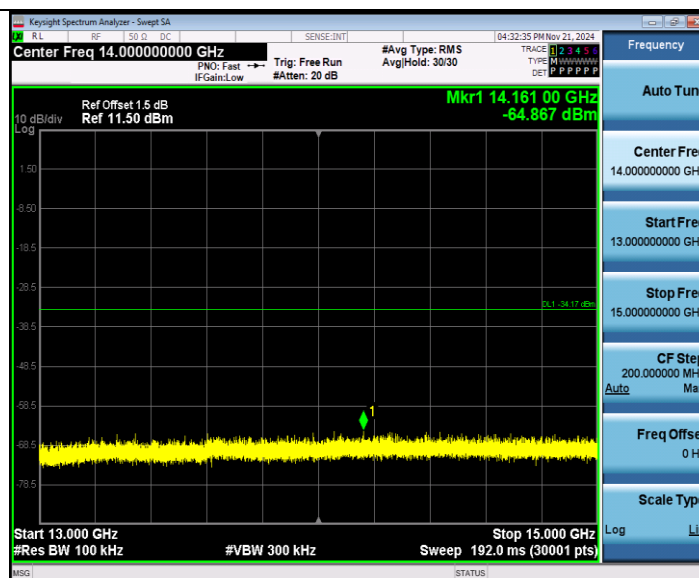
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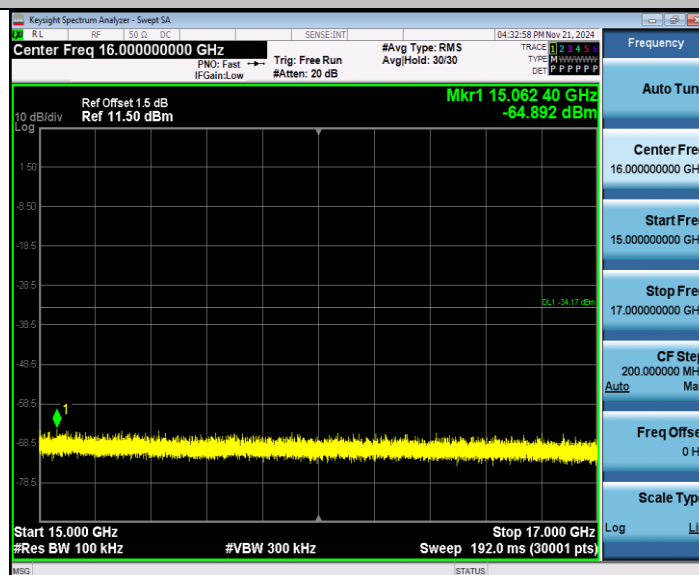
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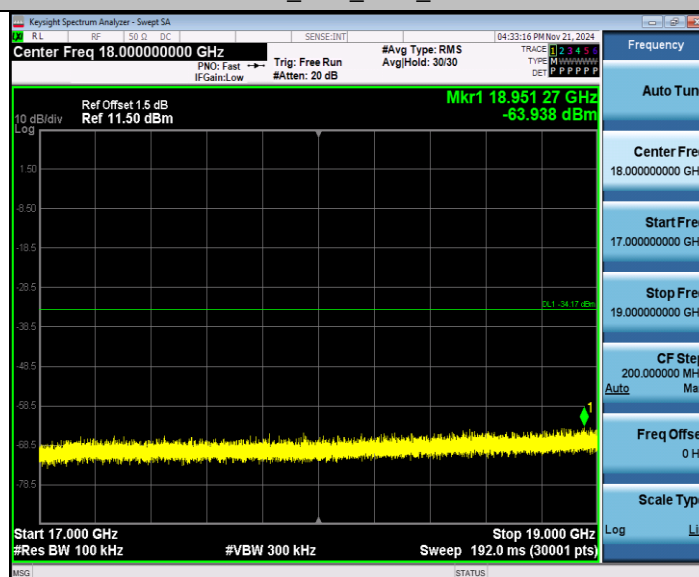
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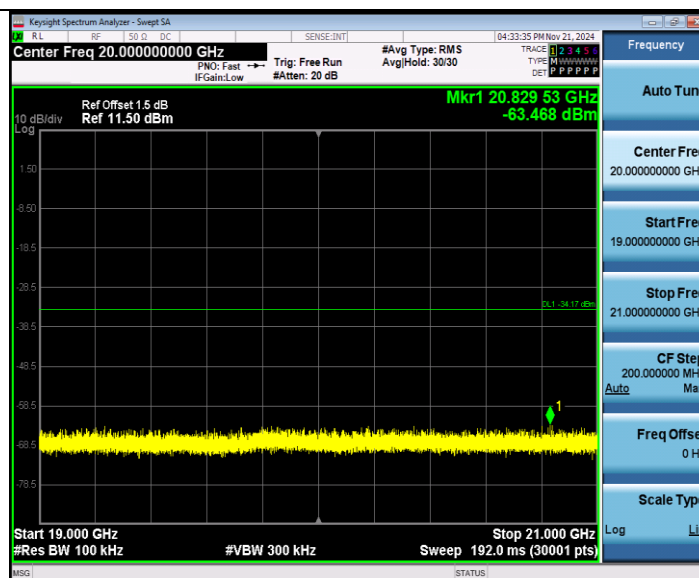
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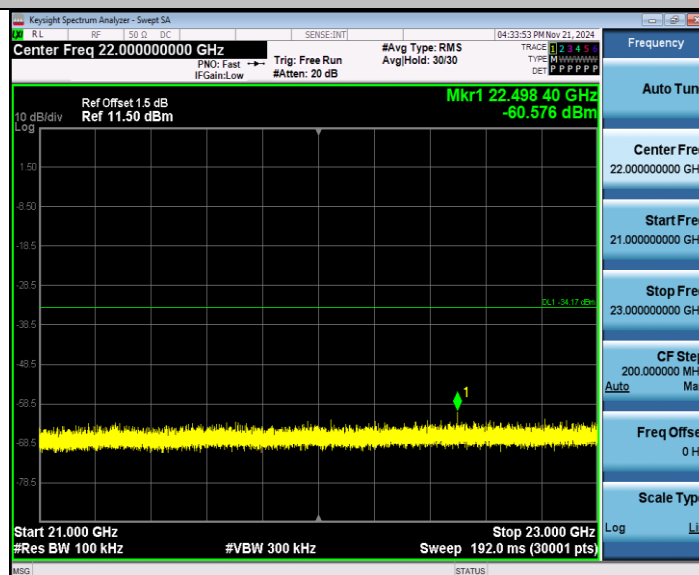
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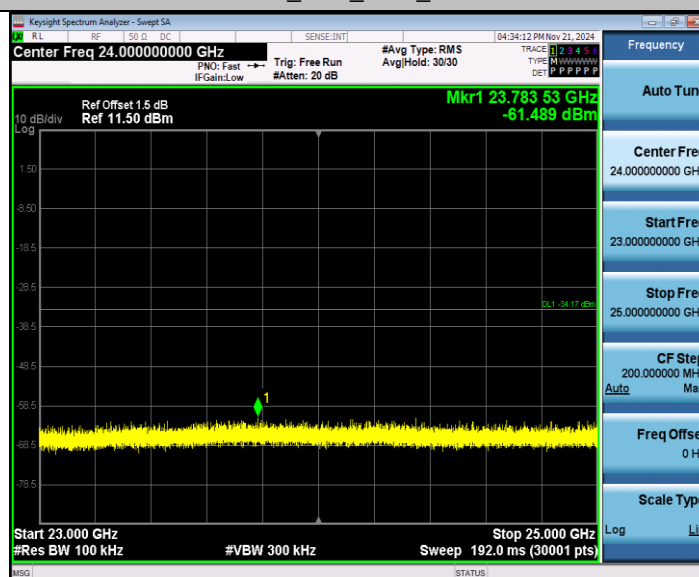
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11AX40SISO_Ant1_2422_21000~23000



11AX40SISO_Ant1_2422_23000~25000



11AX40SISO_Ant1_2437_30~1000