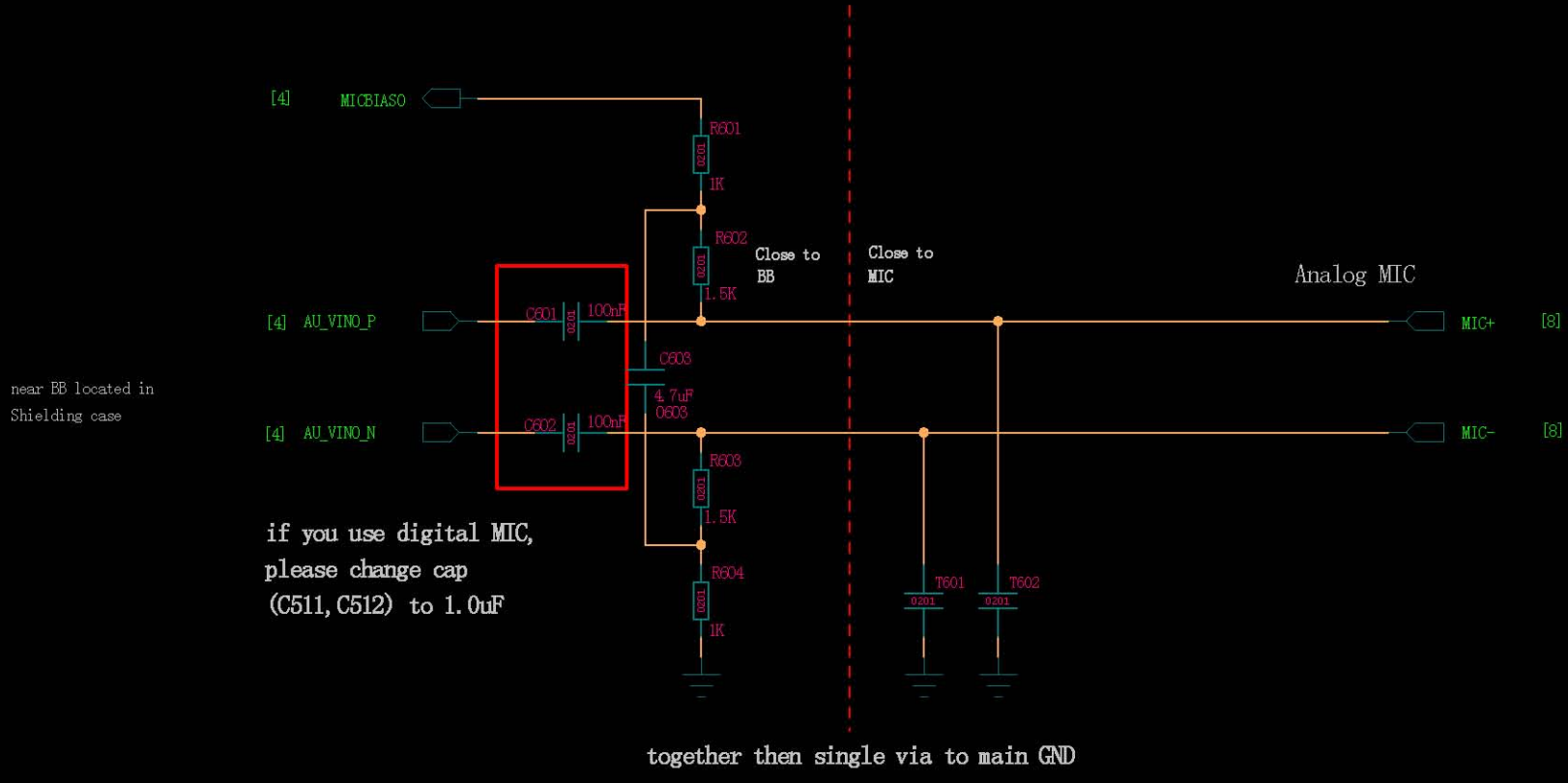
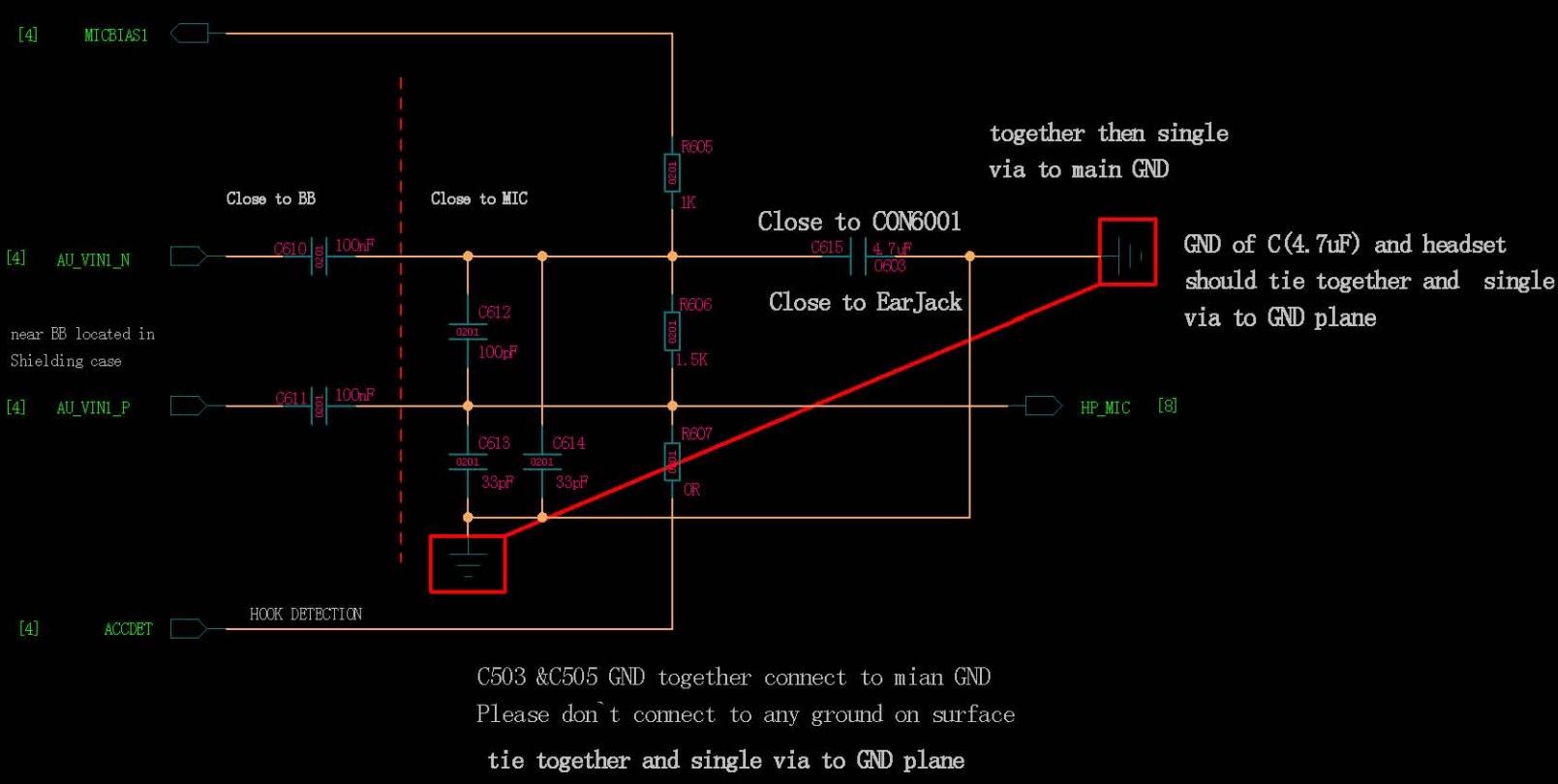




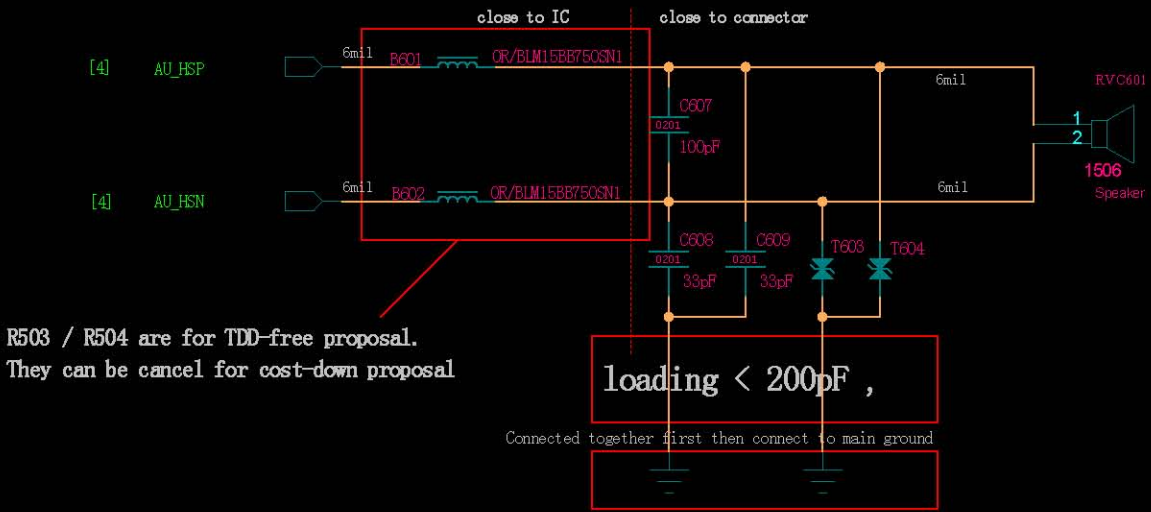
Noma1 MIC



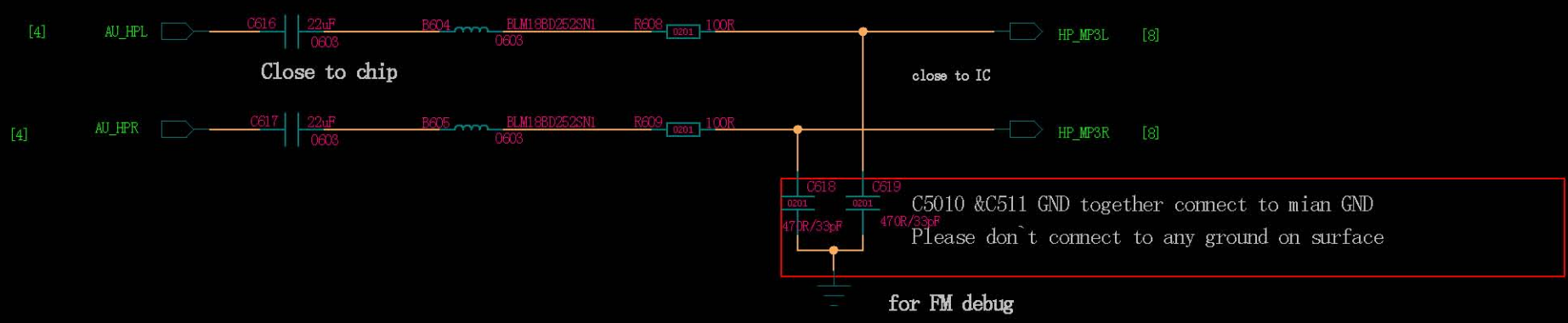
Earphone MICPHONE



Receiver



Earphone RVC

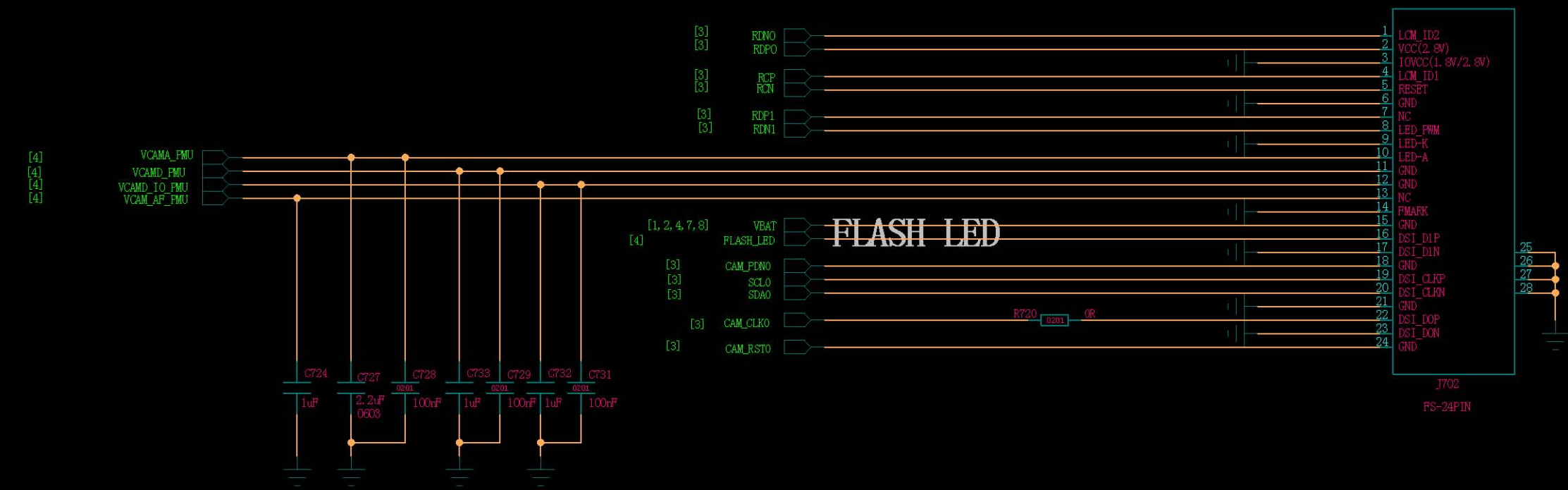


Based on your system level design, if better desense performance is needed on your system , please refer to desense performance enhance proposal

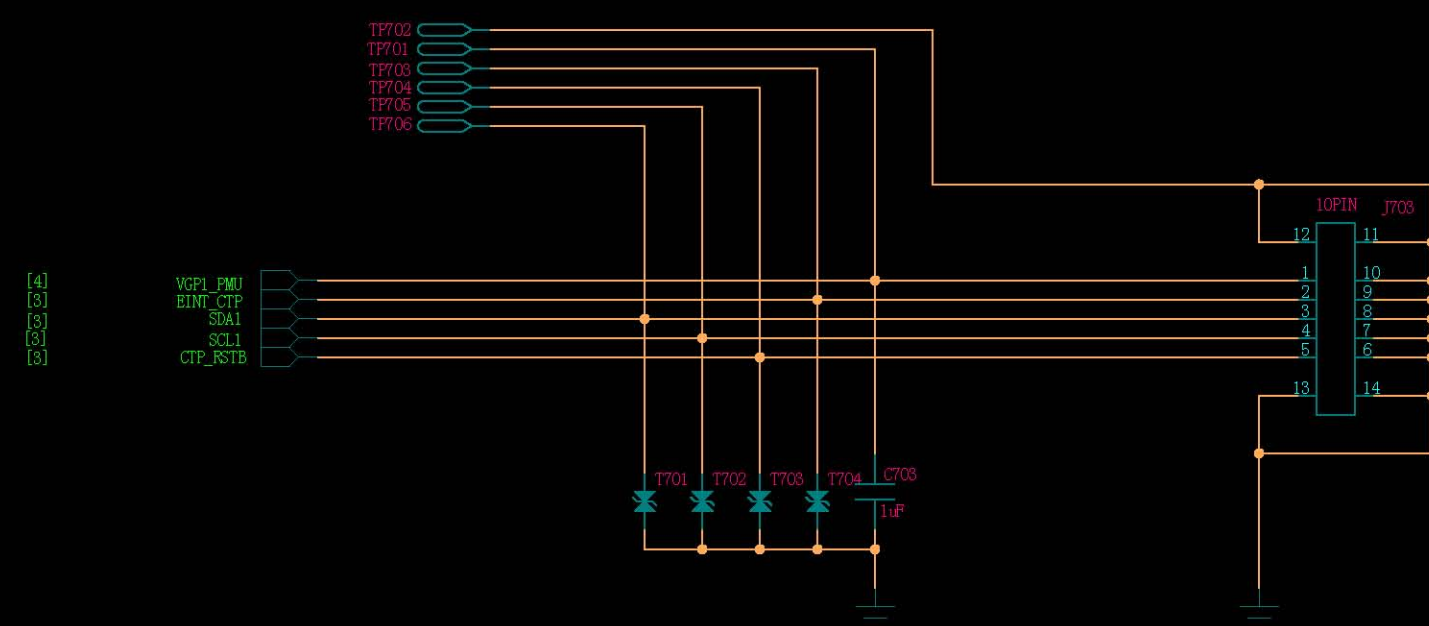
DRAWN:	WSR	DATED:	20160304	COMPANY:	ALONG			
CHECKED:	<Checked By>	DATED:	<Checked Date>	TITLE:	T15			
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>	TJ CODE:	<Code>	REV:	V10	
RELEASED:	<Released By>	DATED:	<Release Date>	SIZE:	A1	SHEET:	60	9

LCM

REAR CAMERA

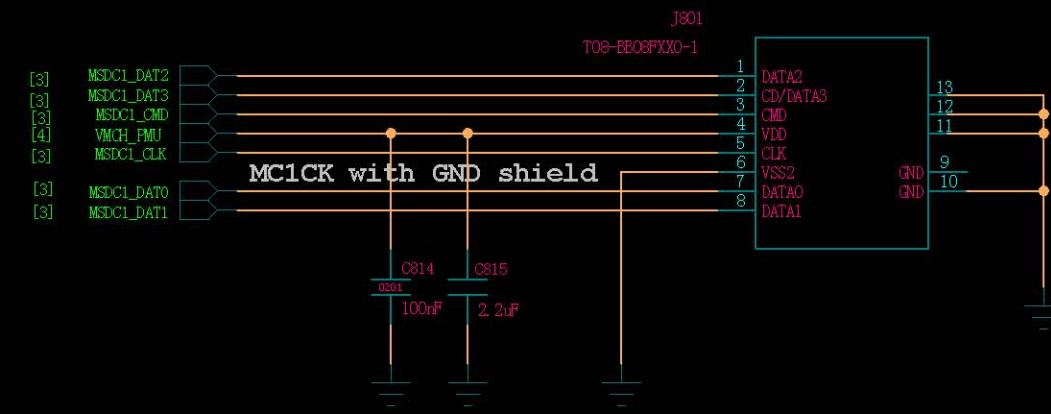


CTP



NANO SIM CARD

SD CARD

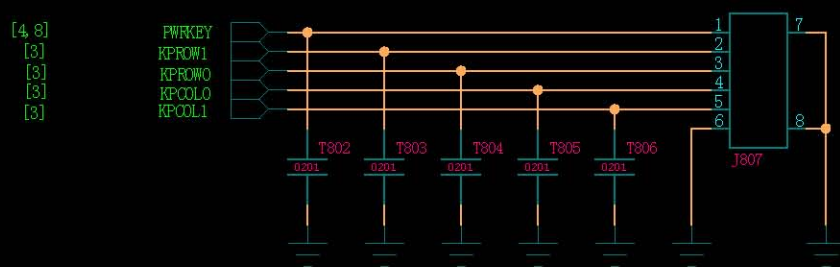


Accelerometer

MC3410 / Accelerometer I2C address: 0x4C (Write:0x98, Read:0x99)

Reserve for open drain type : R6312

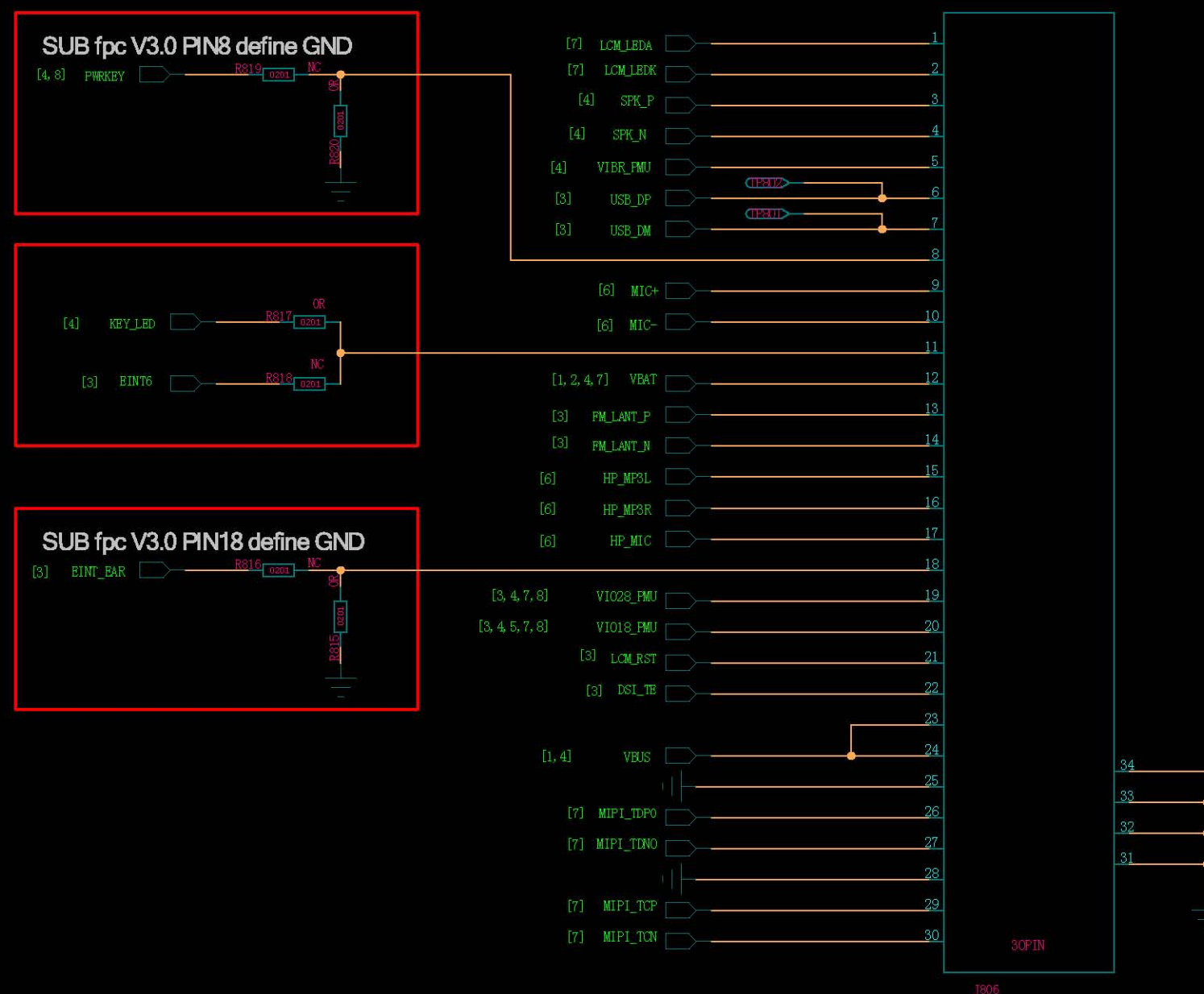
KeyPad

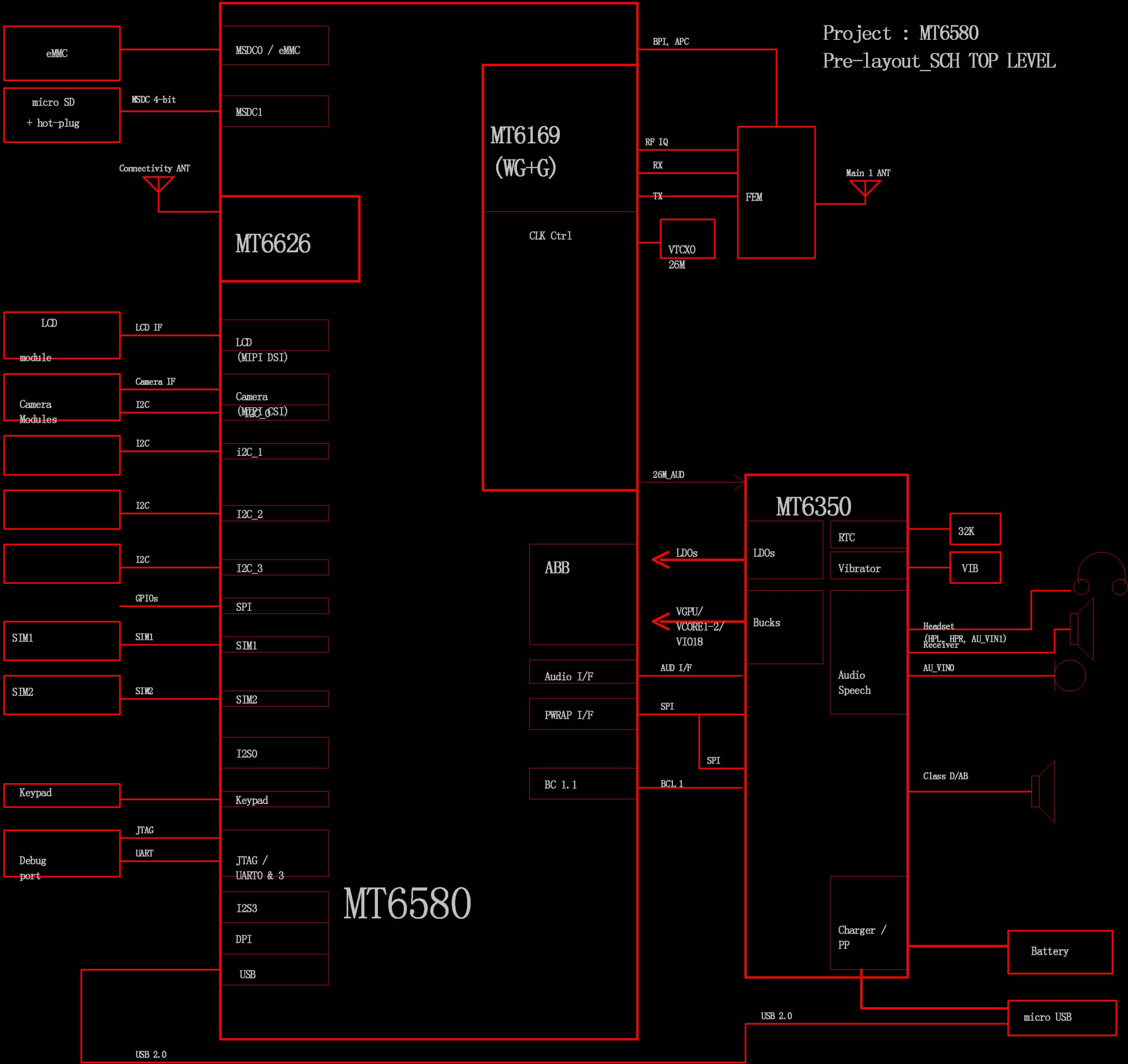


Thermistor / To sense board level temperature

NTC6301 close to BB , and located in the same layer
(can refer to thermal design notice)
NTC6302 close to PA , and located in the same layer

SUB B2B





- 20150326 修改记录
1. 修改and2连接策略
2. 修改and5连接策略

20140401

1. Reseve R126,R123,R124, del R407
2. GPS Co-TMS trapping remove (P.3)
3. Change C306 byapss cap. of VMC to 4.7uF
4. Reserve C335,C336 for decreasing harmonic level. The essentiality is depend on design quality.

20140402

1. Reseve R719, R720

I2C	Function	I2C Spec.	Budgeted Timing	I2C Slave Address (7-bit mode)
I2C-0	Rear Camera	400 Kbps		Rear camera (IMX179) I2C address: 0X10 (Write:0x20, Read:0x21) AF driver (FSN03C) I2C address: 0x0C (Write:0x18 , Read:0x19)
	Front Camera	400 Kbps		Front camera (OV5670) I2C address: 0X36 (Write:0x6C, Read:0x6D)
I2C-1	CTP	400 Kbps	Yes.	GT1151 / CTP I2C address: 0X5D (Write:0xBA, Read:0xBB) or 0x14 (Write:0x28, Read:0x29)
I2C-2	Flash LED 5V boost	400 Kbps		LM3642 / Flashlight Driver I2C address: 0X63 (Write:0xC6, Read:0xC7)
	Accelerometer	400 Kbps	Yes.	MC3410 / Accelerometer I2C address: 0x4C (Write:0x98, Read:0x99)
	Gyro	400 Kbps	Yes.	Gyro I2C Address: 0x68 (Write:0xD0, Read:0xD1)
	M sensor	2.5 Mbps	Yes.	M-Sensor I2C Address: 0x0D (Write:0x1A, Read:0x1B)
	Light&PS sensor	400 Kbps	Yes.	CM36652 / RGB + PS I2C address: 0X60 (Write:0xC0, Read:0xC1)

Note : I2C Spec. : Standard mode (100 kbps) and Fast mode (400 kbps), Fast mode Plus (1 Mbps) and High-speed mode (3.4 Mbps)

DRAWN: WSR	DATE: 20160304	COMPANY: ALONG		
CHECKED: <Checked By>	DATE: <Checked Date>	TITLE: T15		
QUALITY CONTROL: <QC By>	DATE: <QC Date>	T/J CODE: <Code>	REV: V10	
RELEASED: <Released By>	DATE: <Release Date>	SIZE: A1	SHEET: 9 of 9	