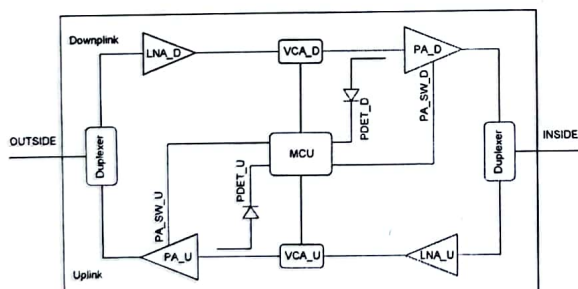
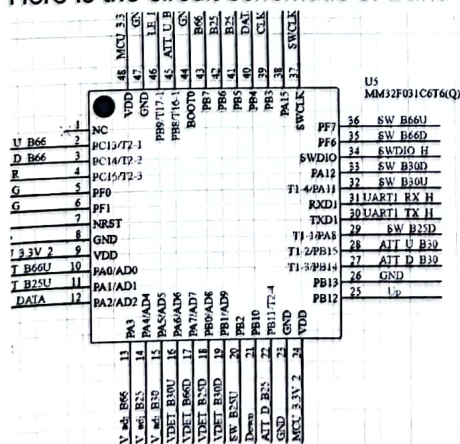


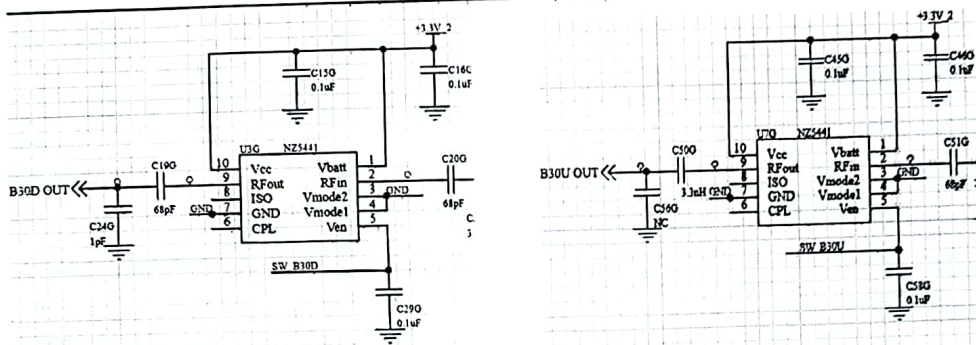
Date: Jul. 13, 23

- Here is the working diagram of our product model of AN-USF7:



Here is the circuit schematic of Band 30:





Pin 33 of component U5 controls the Downlink Power Amplifier (PA) for Band 30, while pin 32 of component U5 controls the Uplink Power Amplifier (PA) for Band 30. These pins enable independent control of the amplification or deactivation of signals in the Downlink and Uplink channels of Band 30.

The circuit design for each band in the product remains identical, enabling independent control of the Power Amplifiers (PAs) for Downlink and Uplink through the Microcontroller Unit (MCU). The MCU governs the operation of the PAs by manipulating control pins associated with each band. Specifically, when the MCU control pin is set to a high level, the PA is activated, amplifying the signal in the corresponding link. Conversely, setting the MCU control pin to a low level deactivates the PA, causing the link to cease operation.

To address the specific requirements of Band 30 and B71, modifications were made to the MCU program. These modifications ensure that the control pins responsible for the PAs of Band 30 and B71 consistently maintain a low-level output through our software. As a result, these two bands remain in an off status, as they cannot be triggered into a high-level state.

This software-based alteration guarantees that the PAs associated with Band 30 and B71 remain deactivated, in alignment with the intended design and functionality of the product. This allows for precise control and operation of the remaining bands while ensuring that Band 30 and B71 are effectively kept in an off state.

Sincerely,

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