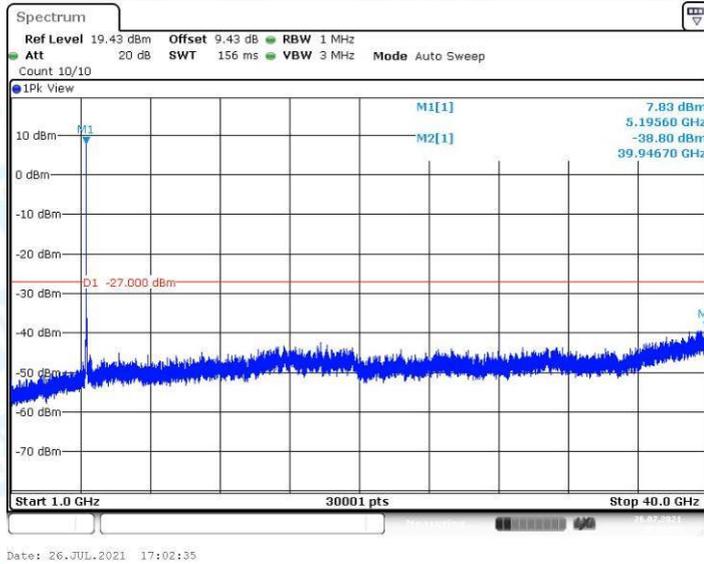
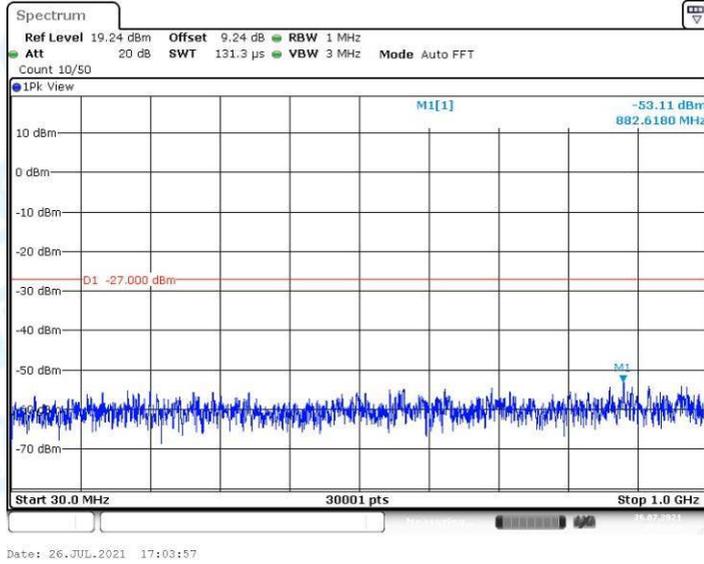


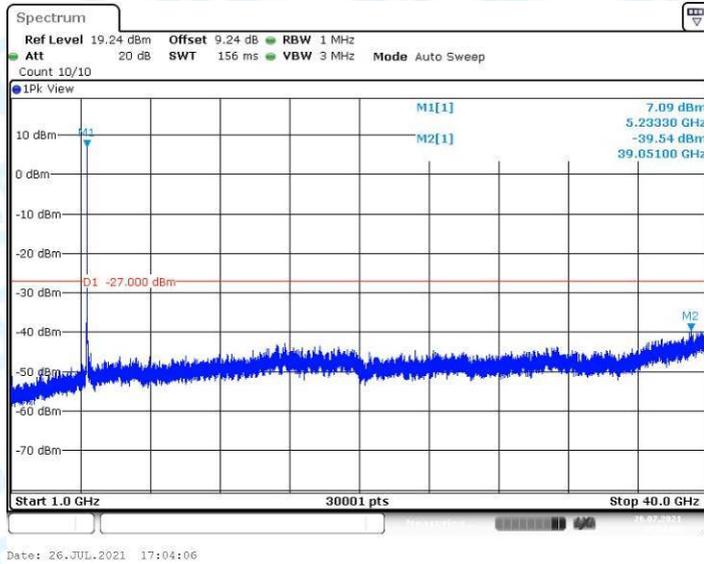
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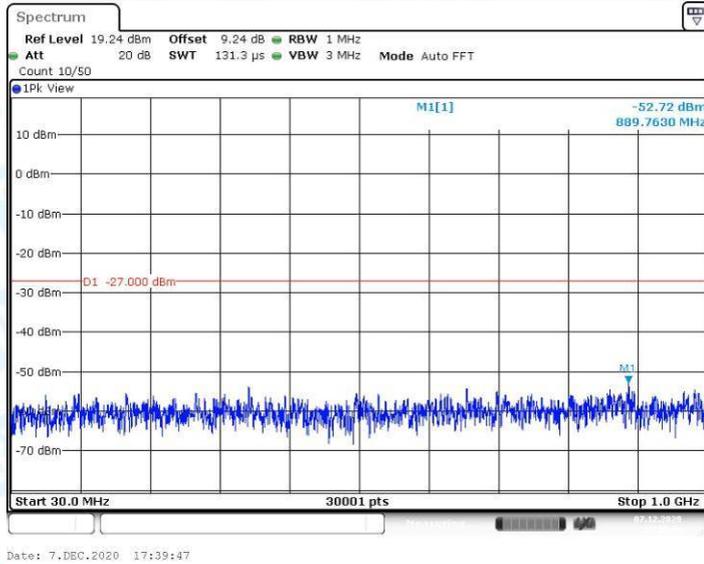
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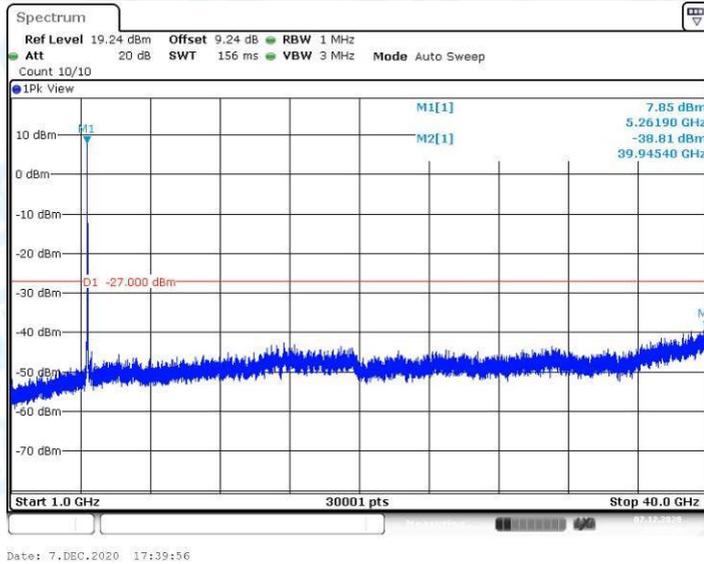
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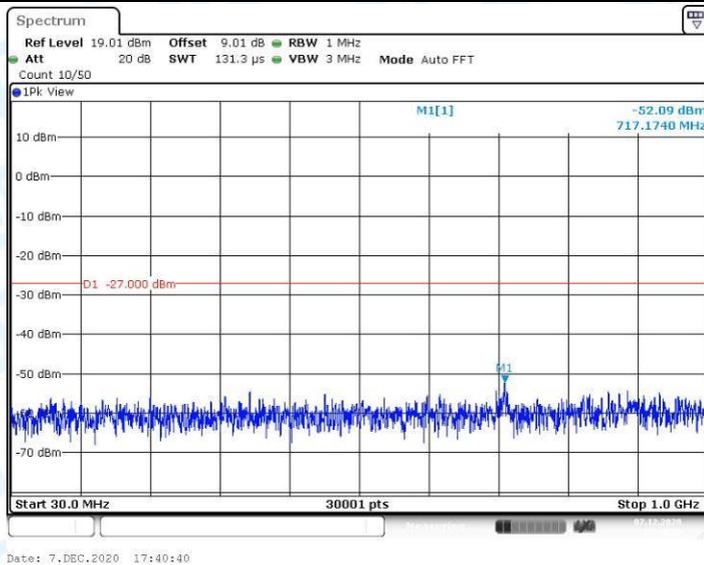
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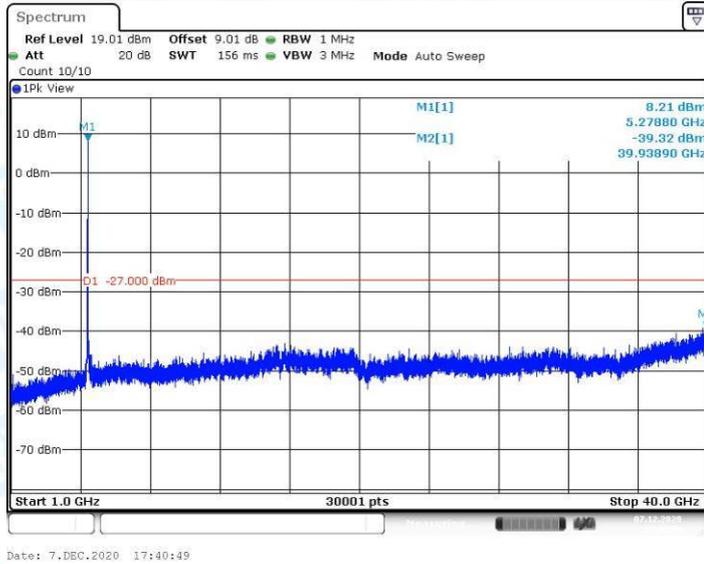
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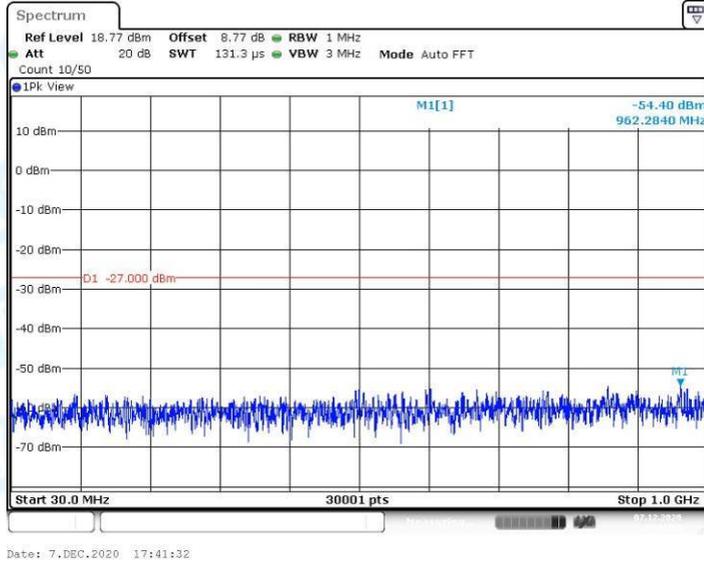
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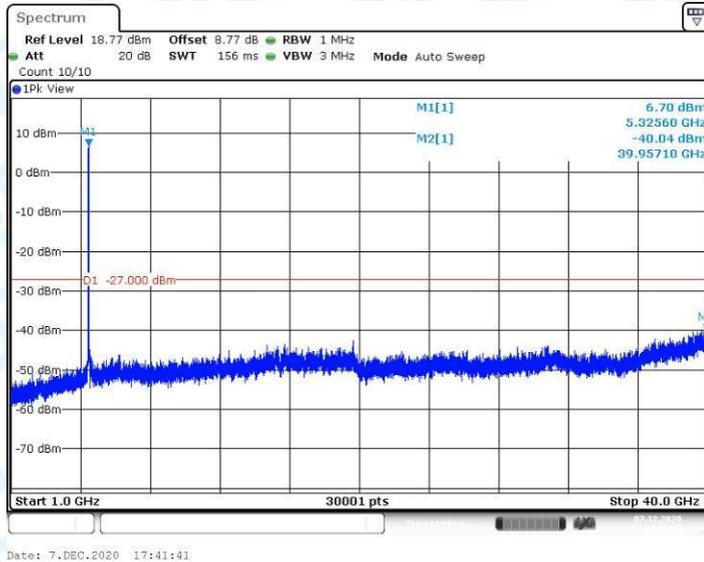
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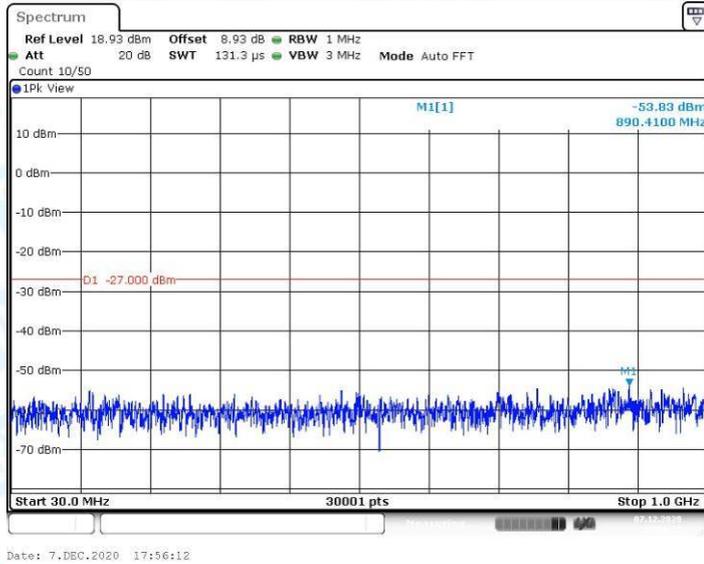
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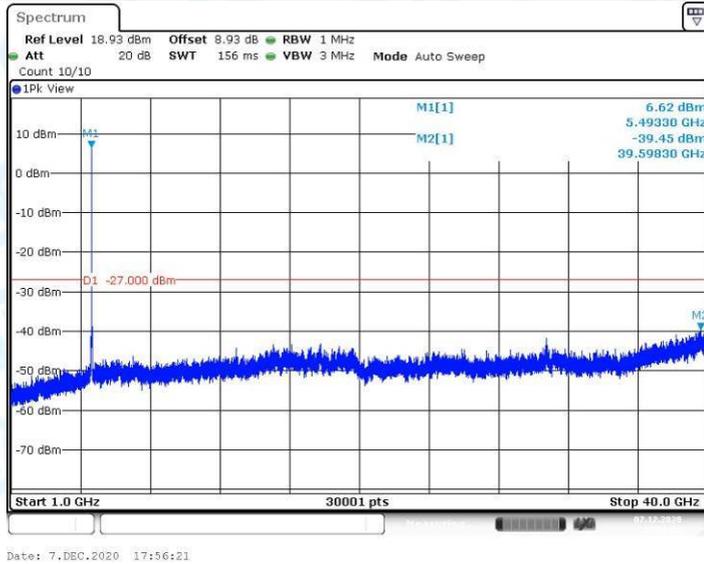
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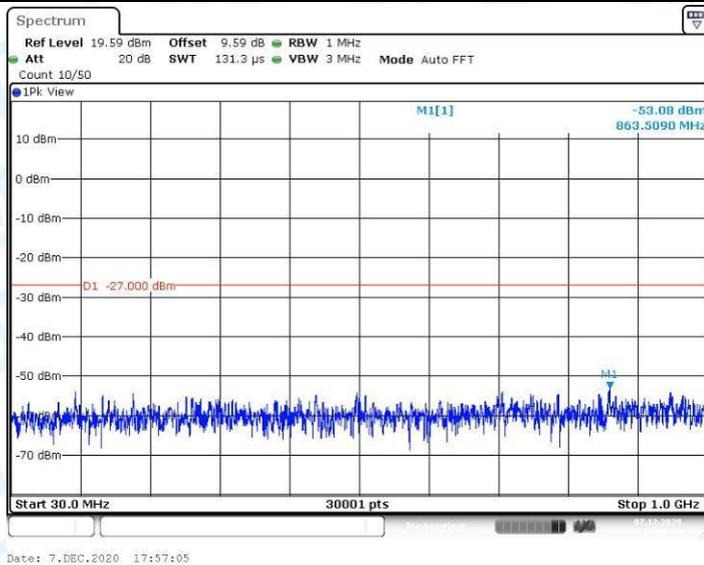
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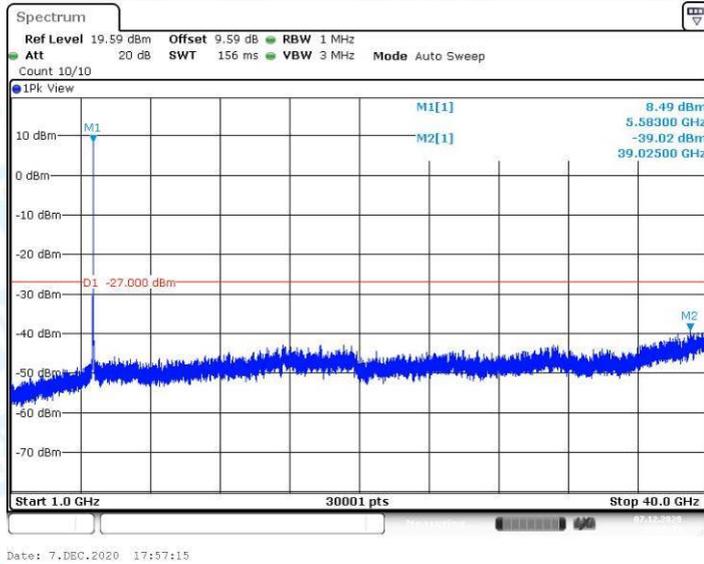
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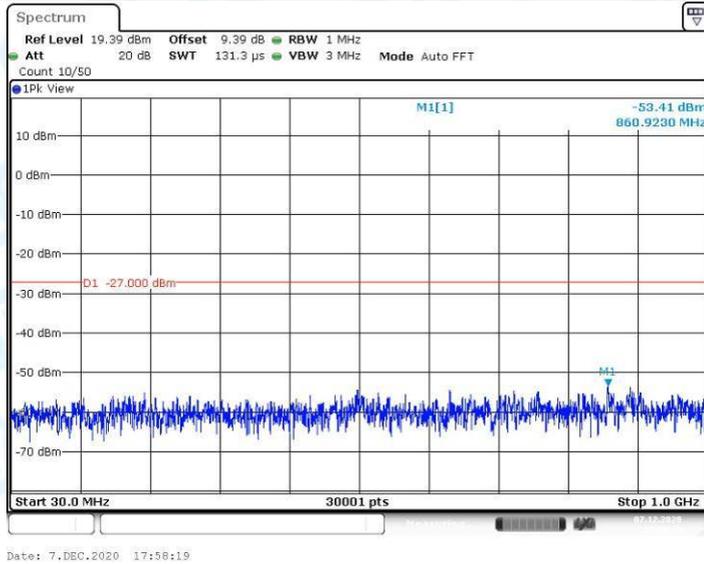
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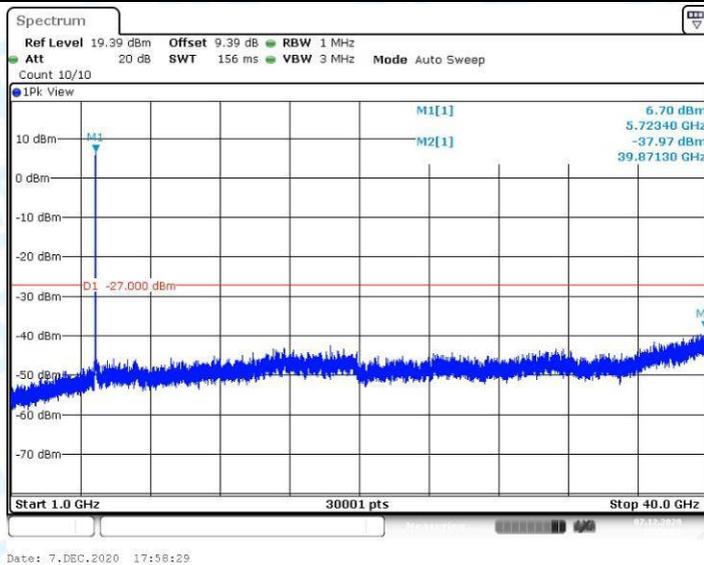
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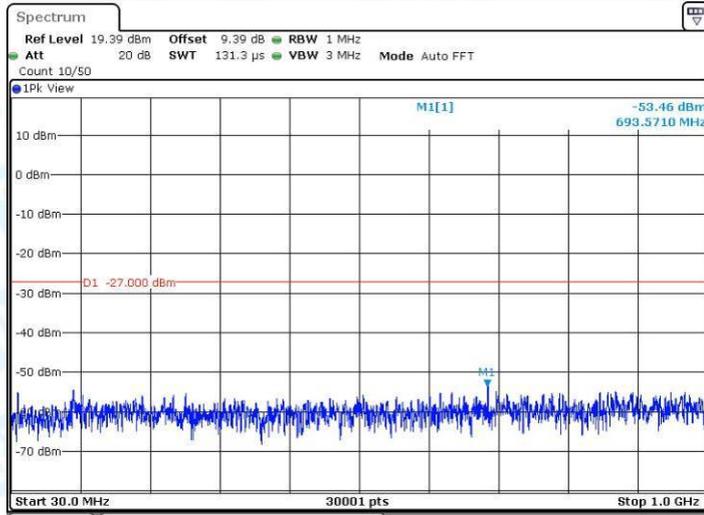
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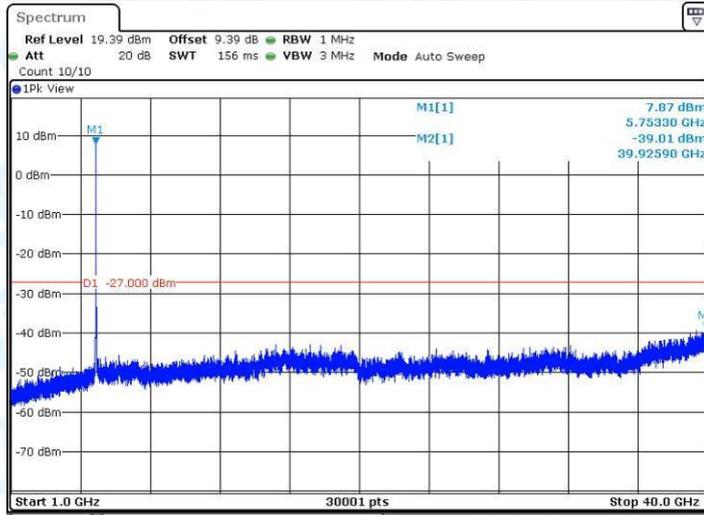


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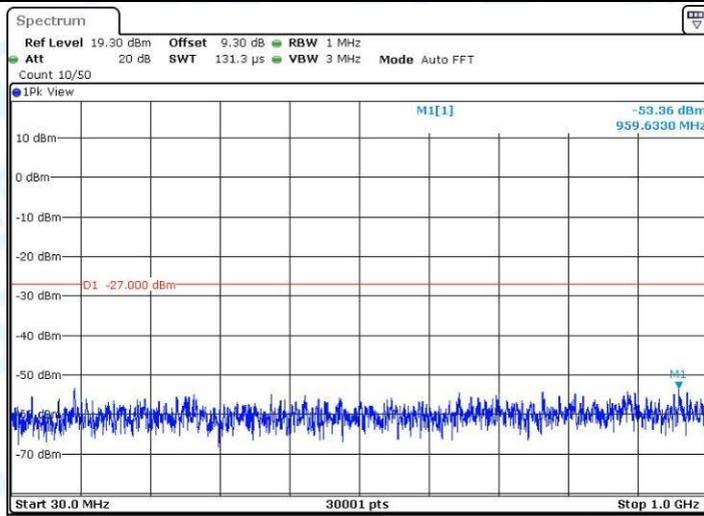
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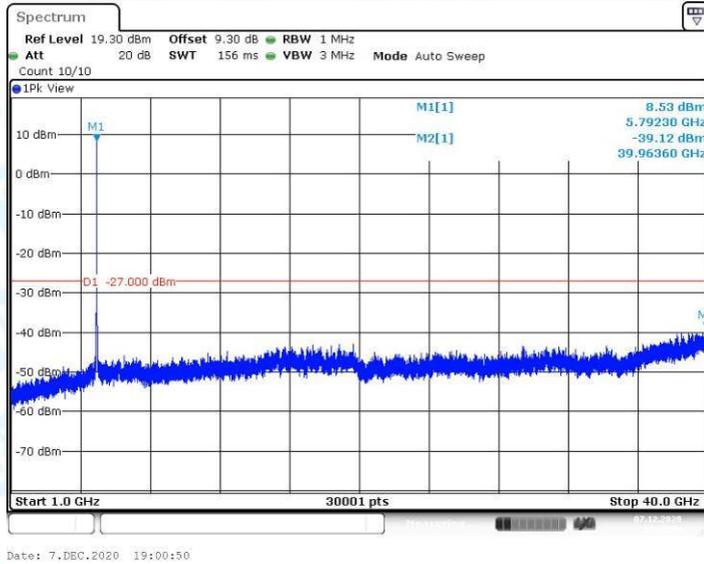
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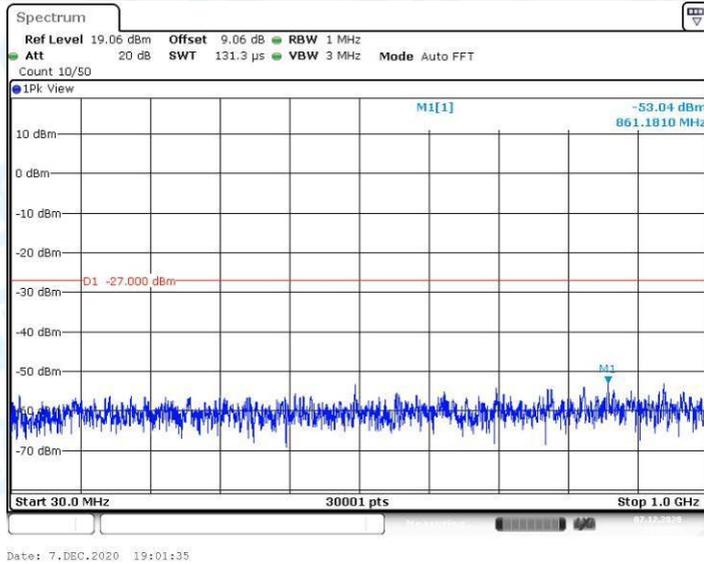


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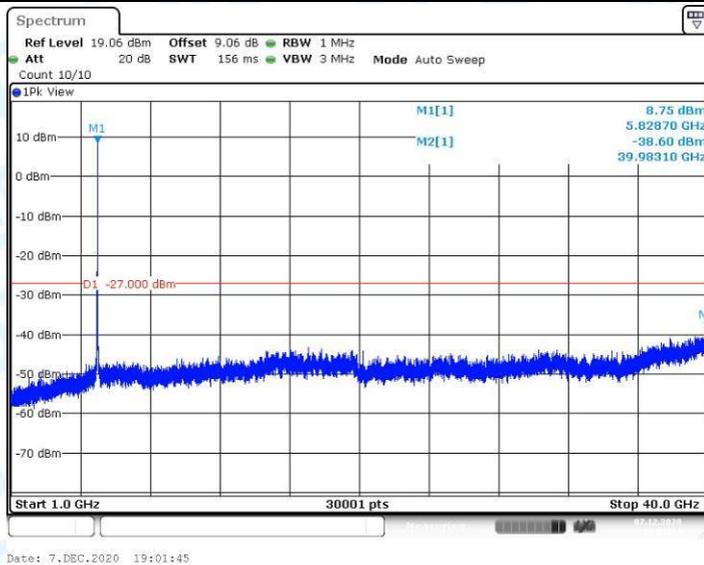
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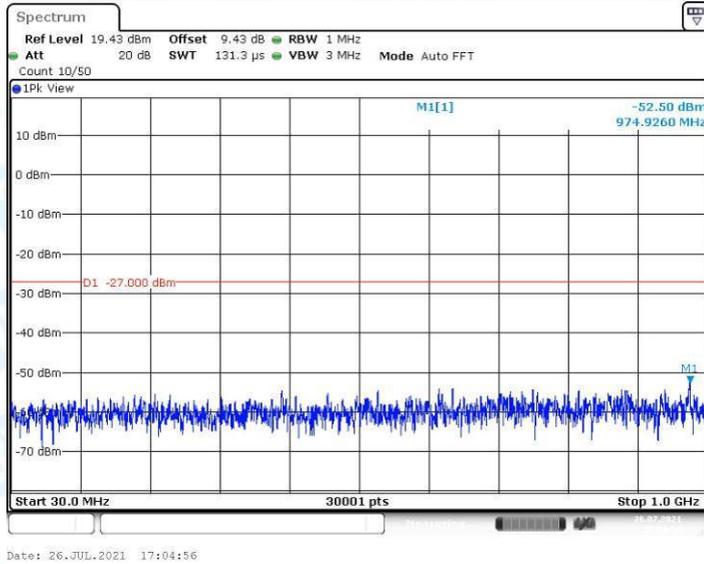
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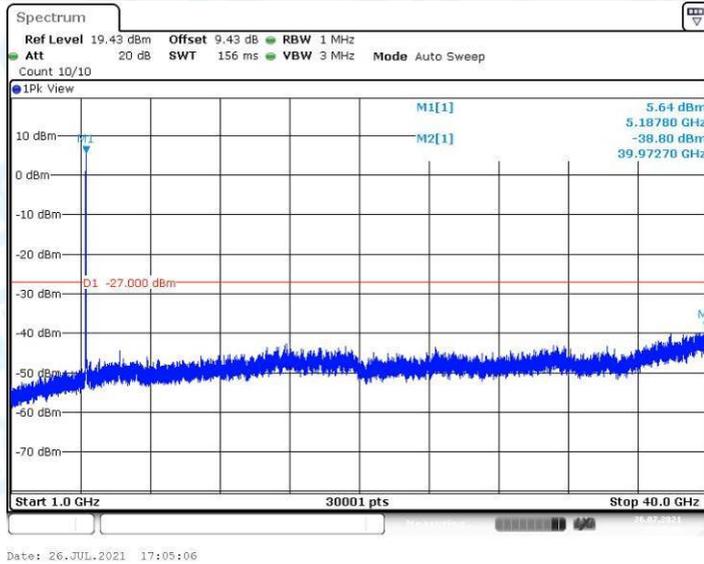
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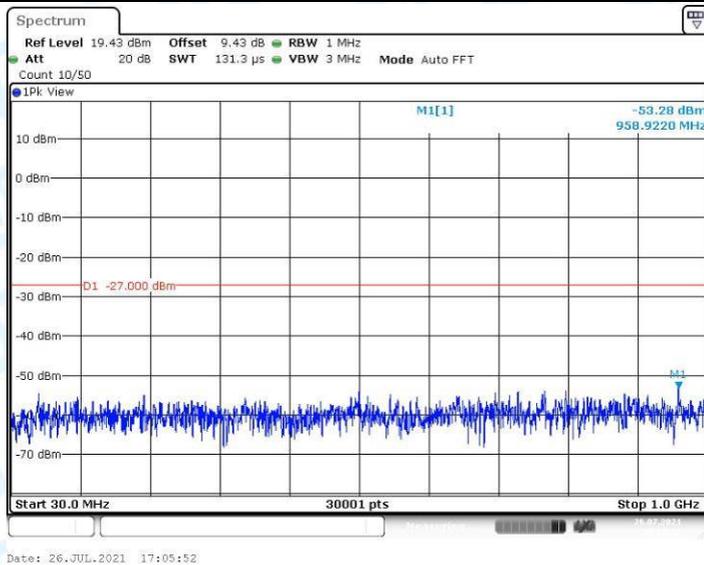
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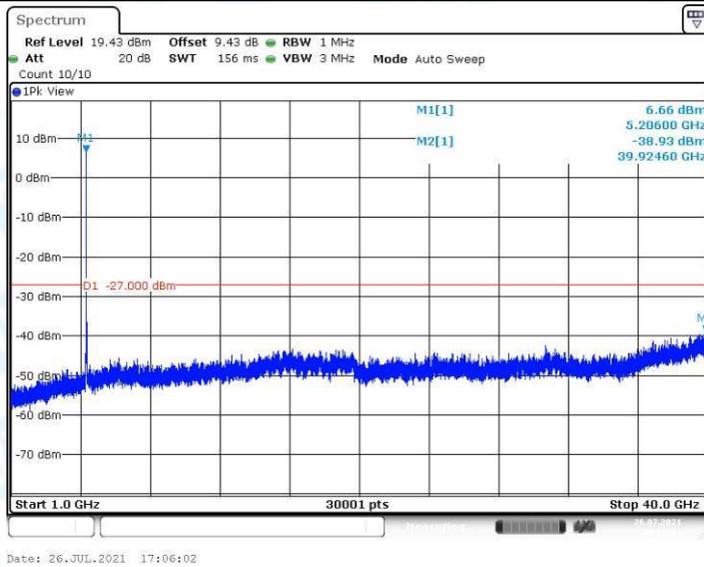
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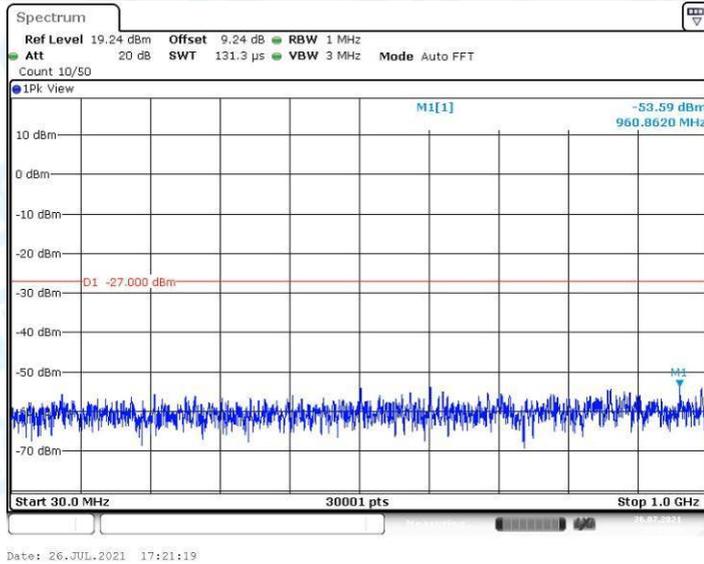
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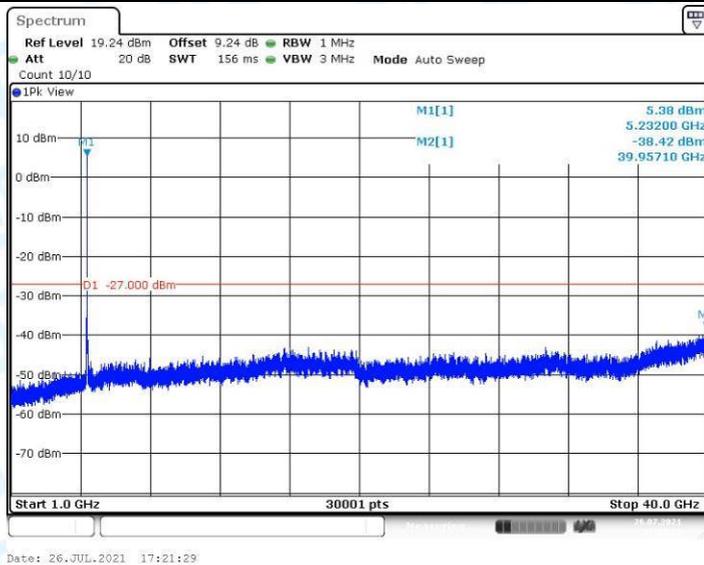
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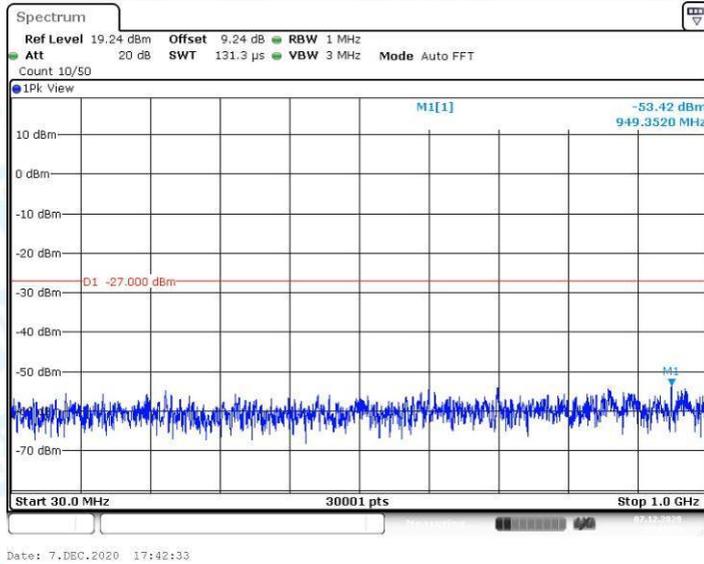
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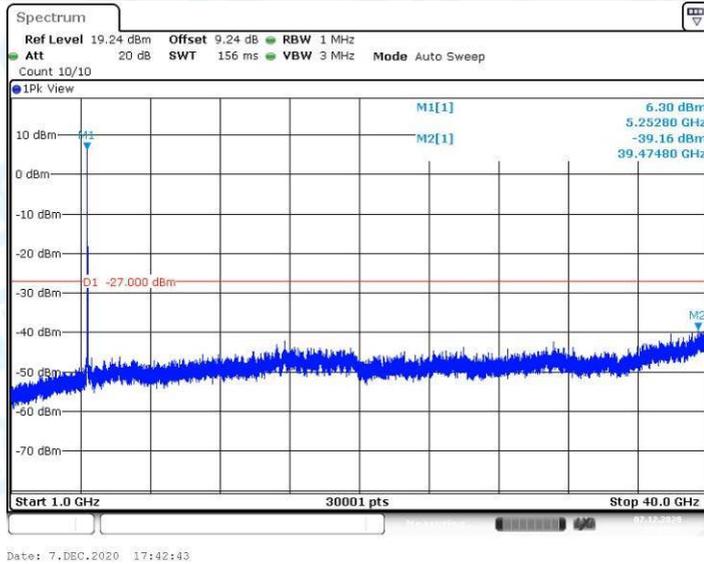
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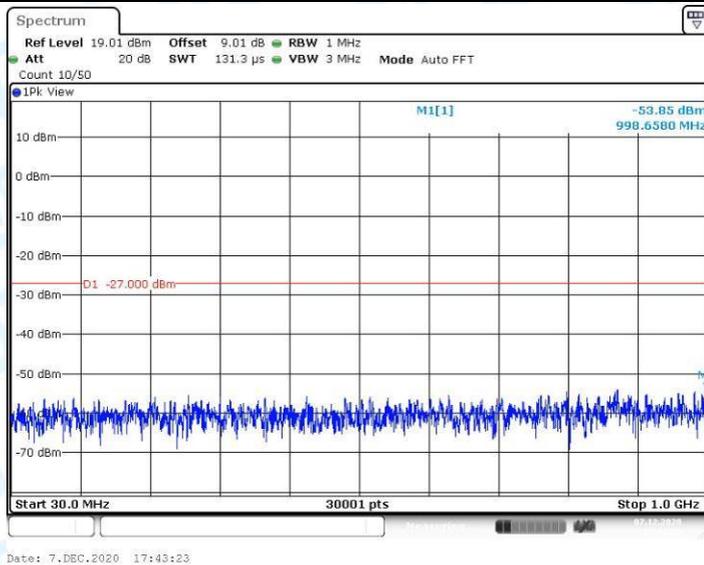
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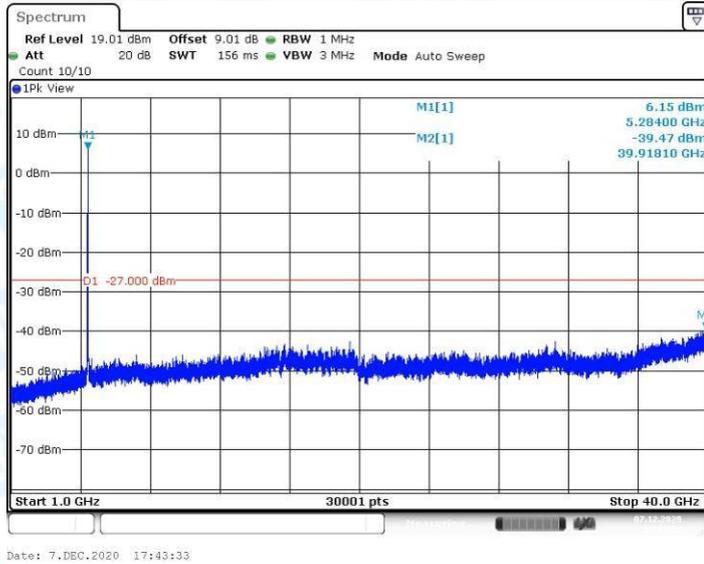
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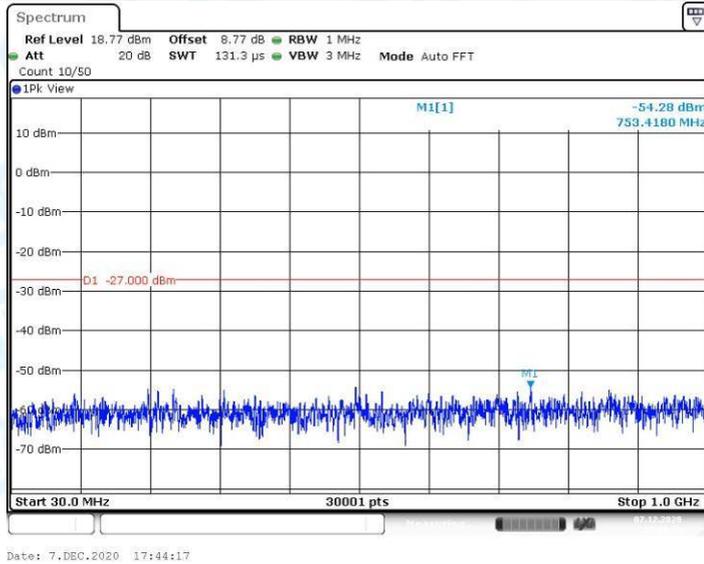
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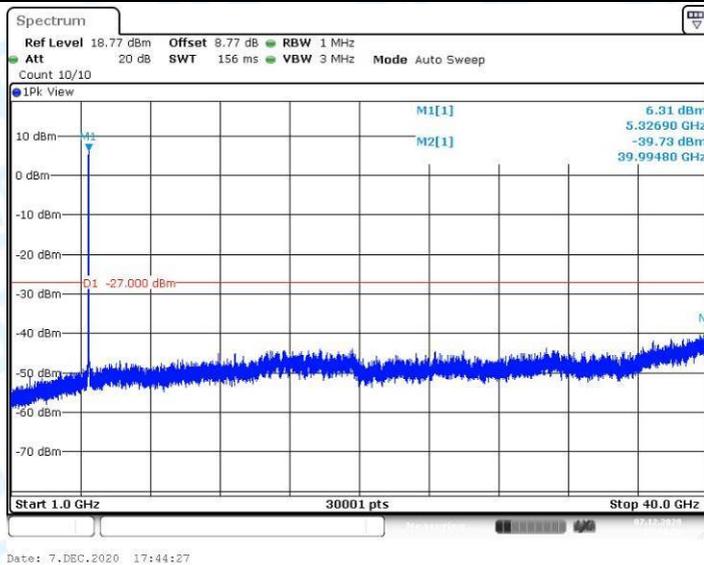
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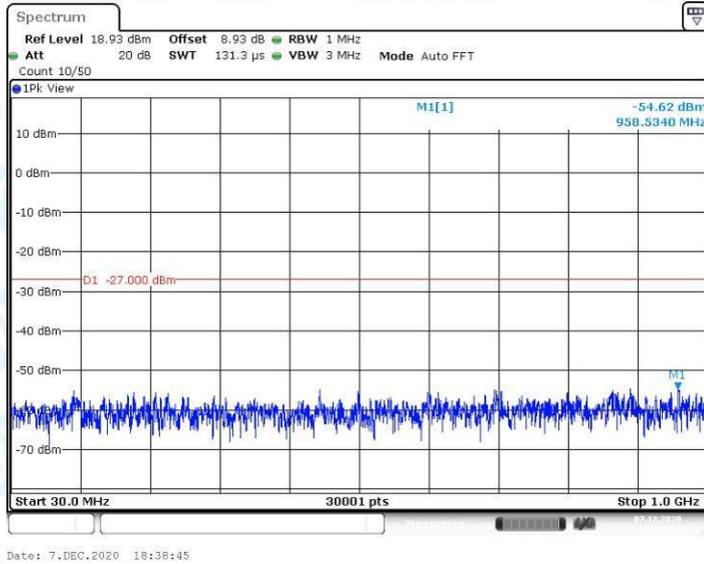
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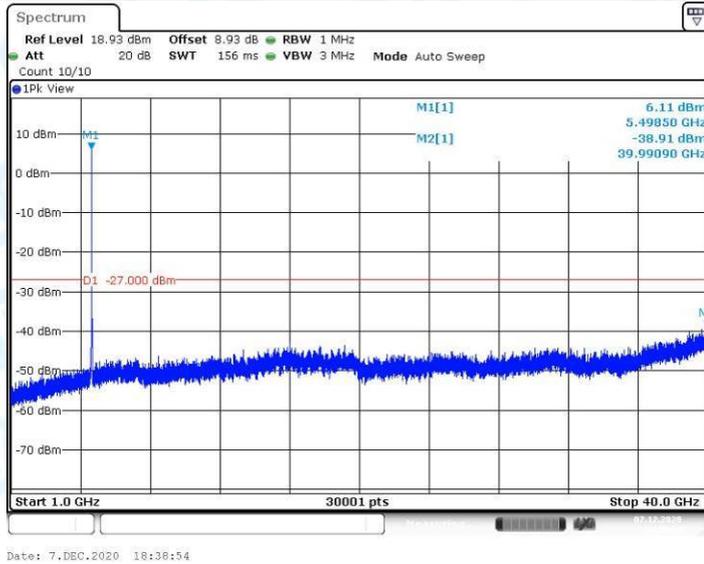
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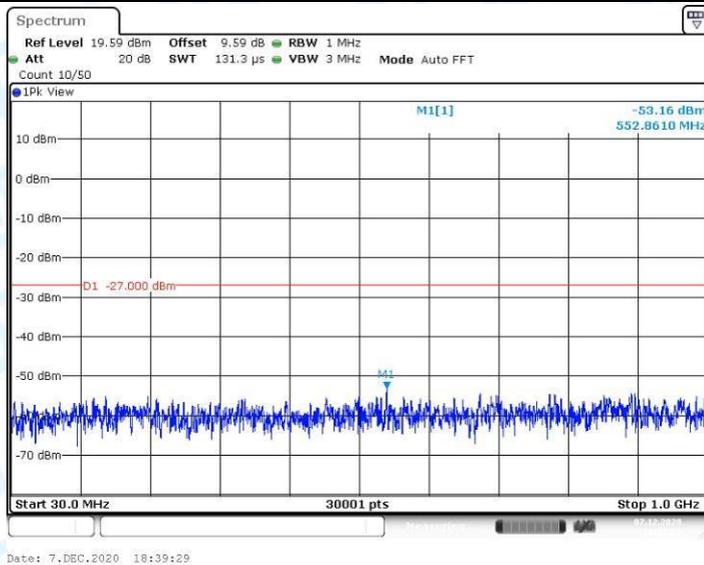
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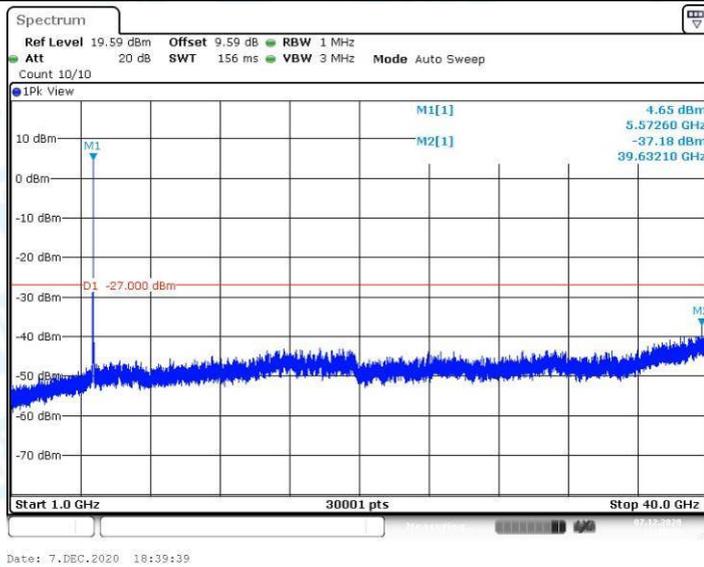
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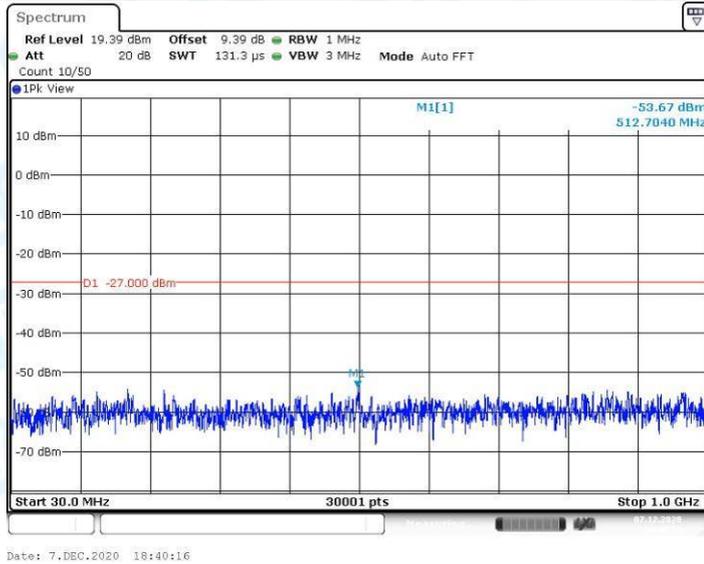
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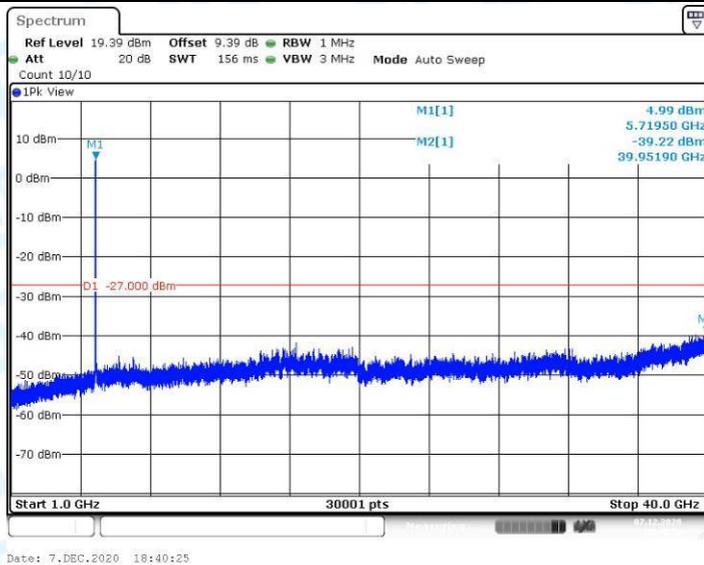
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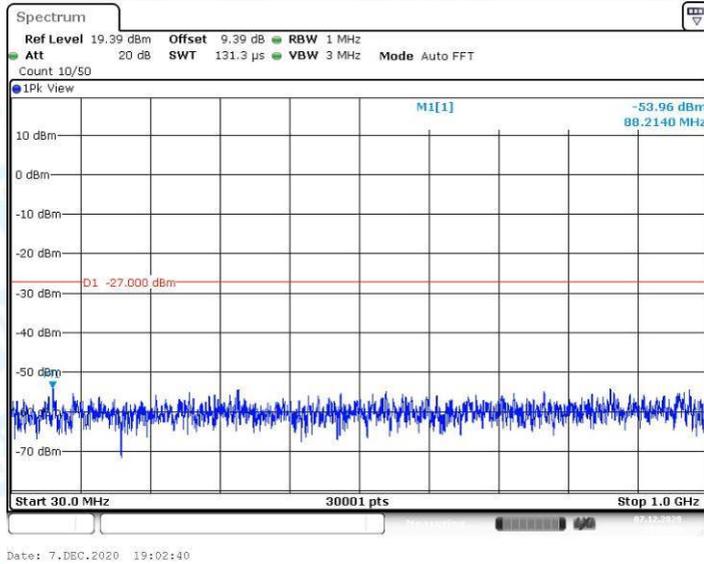
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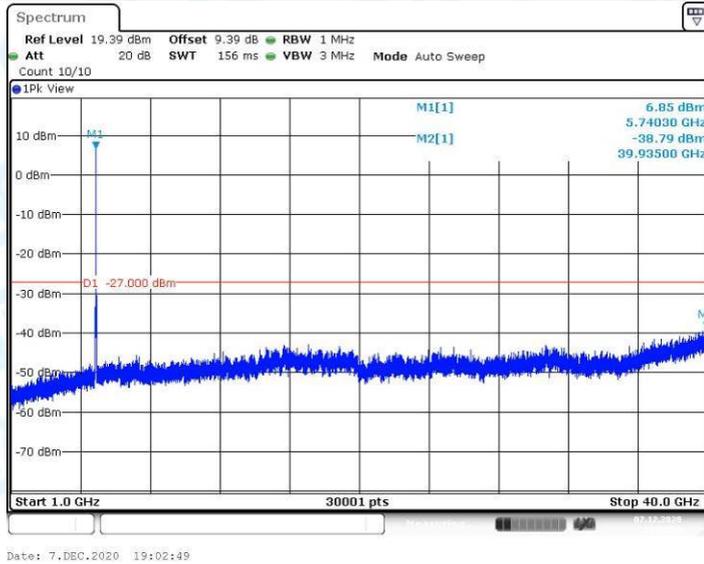
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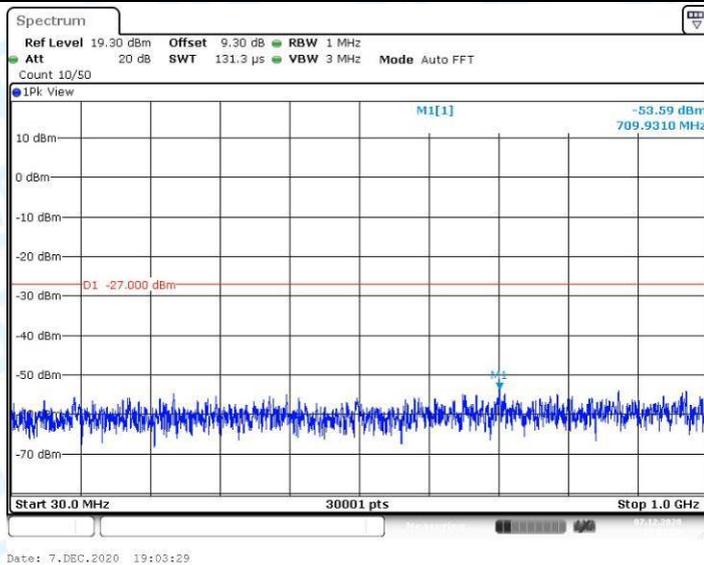
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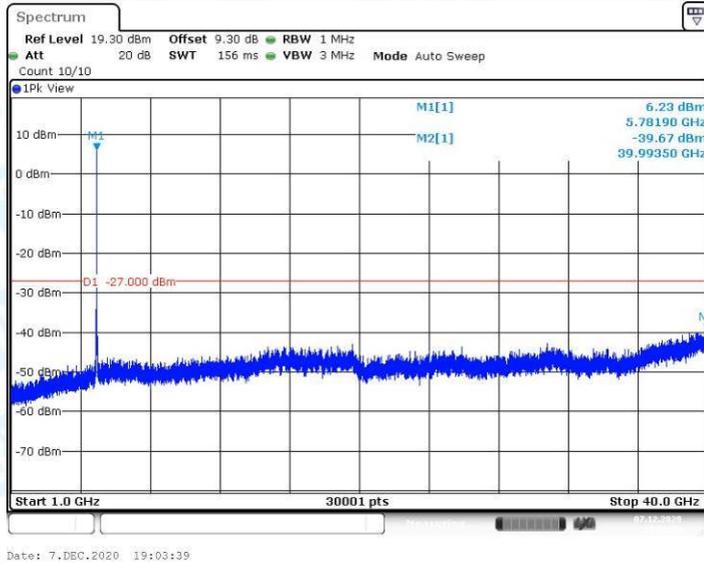
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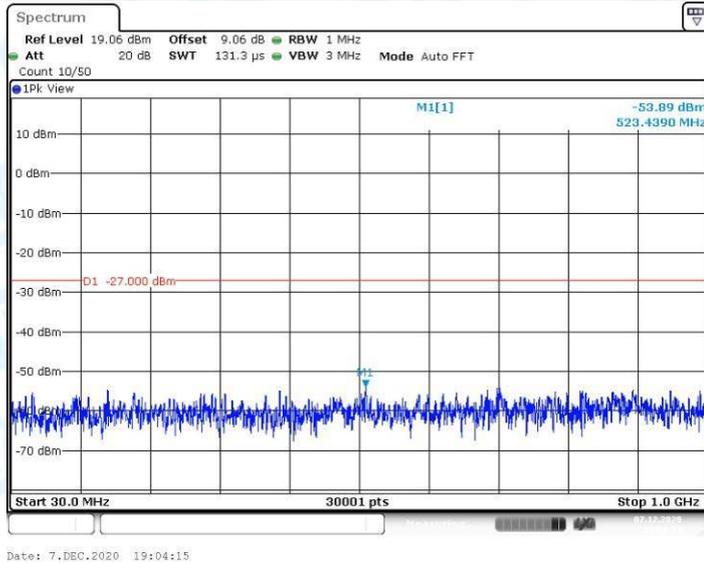
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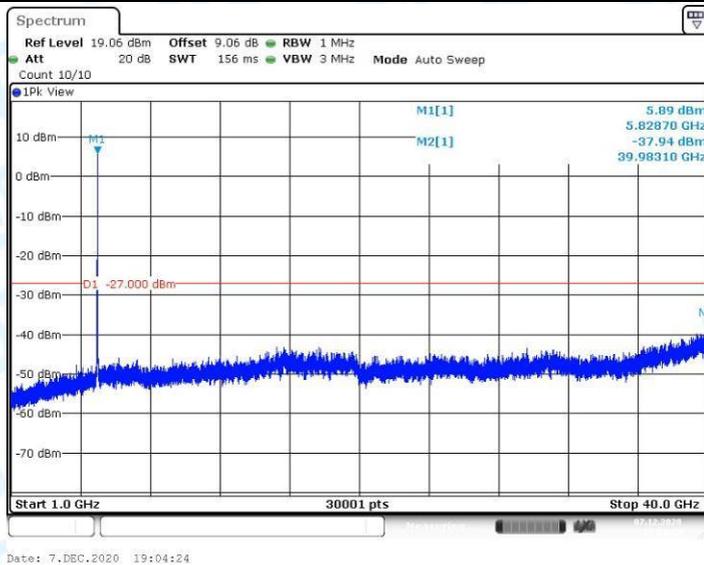
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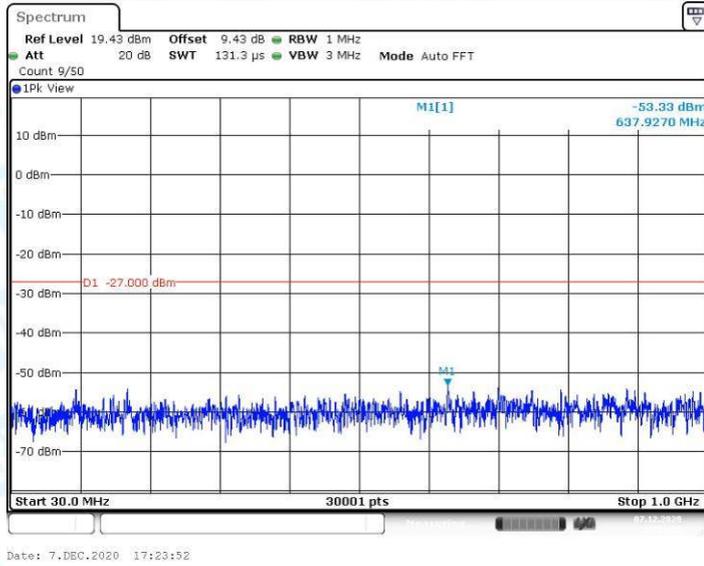
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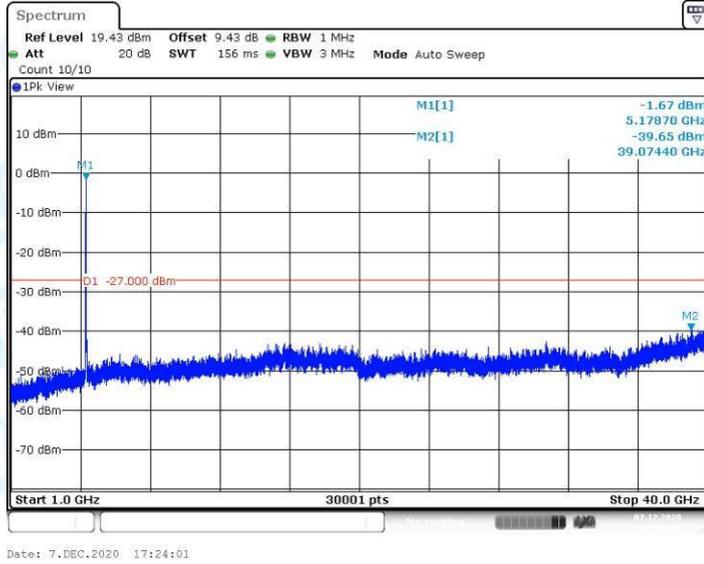
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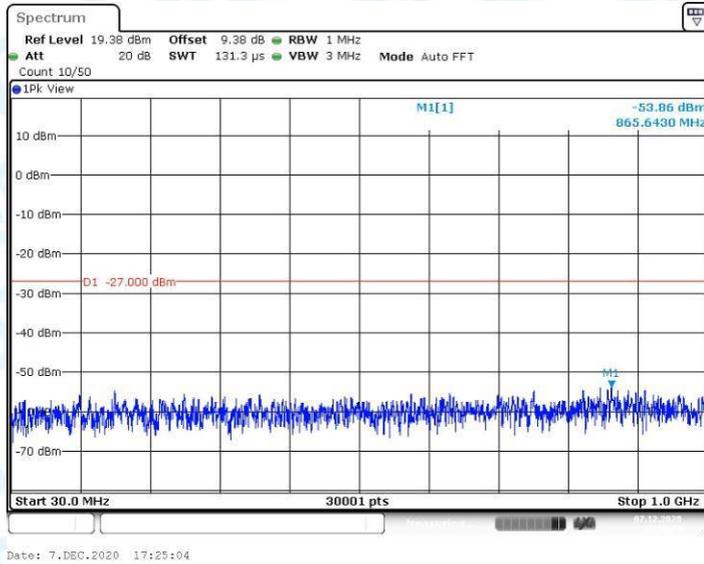
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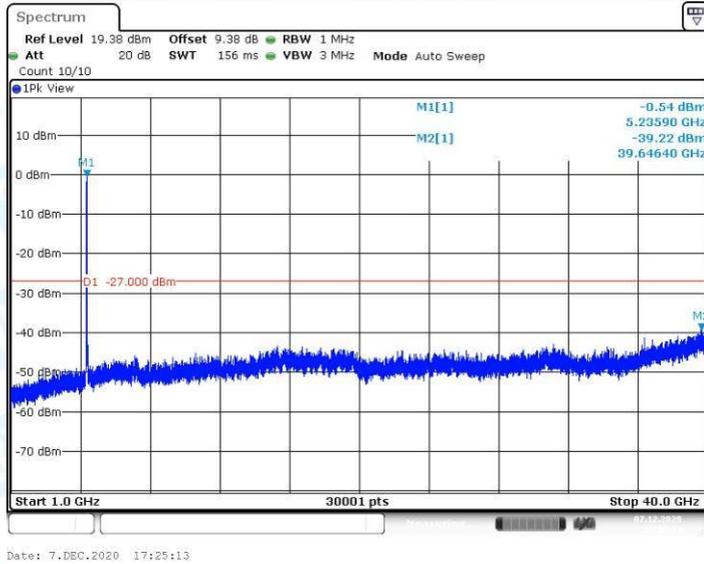
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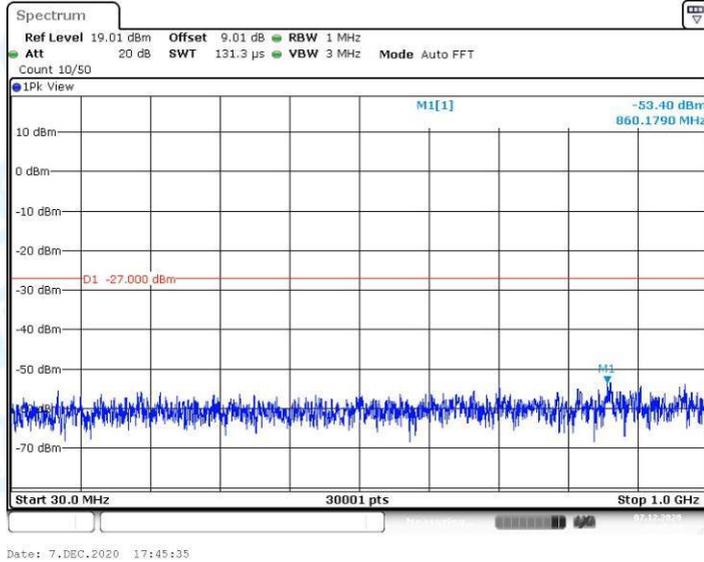
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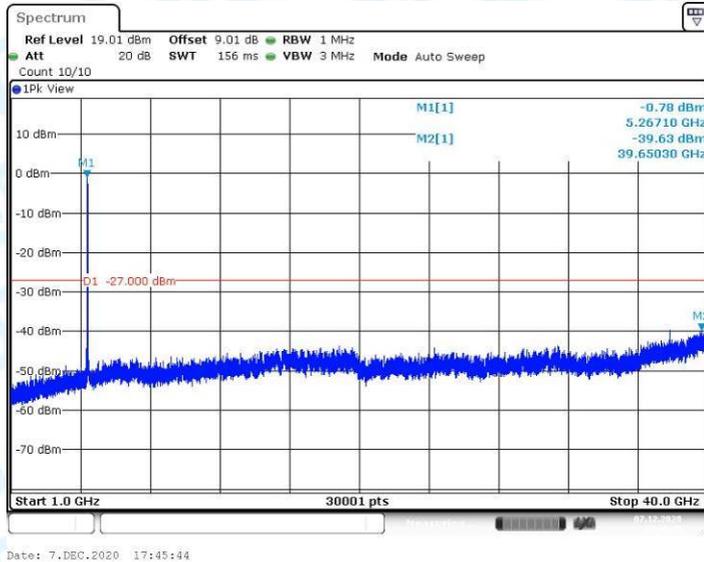
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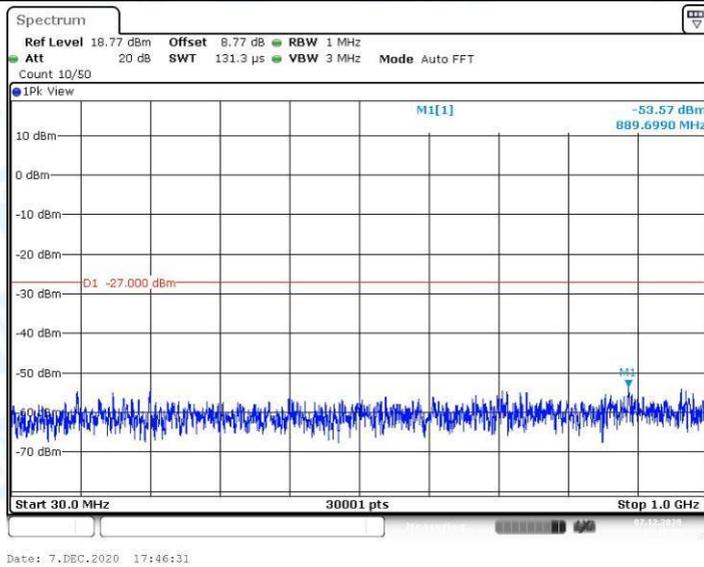
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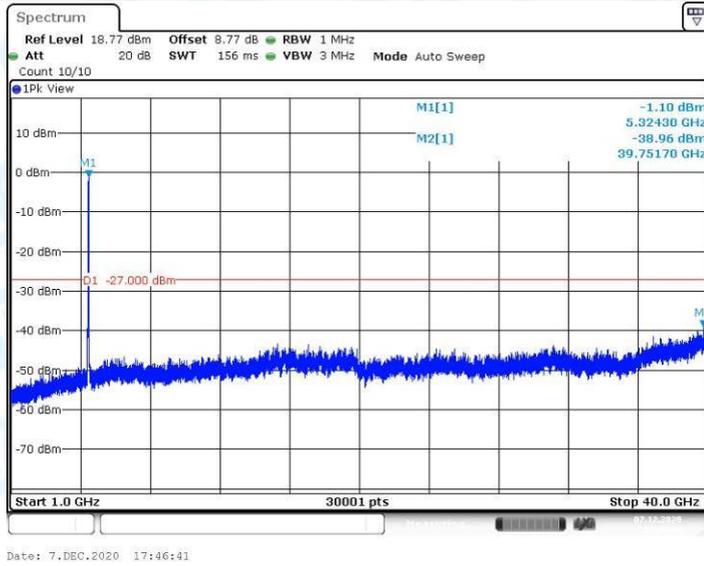
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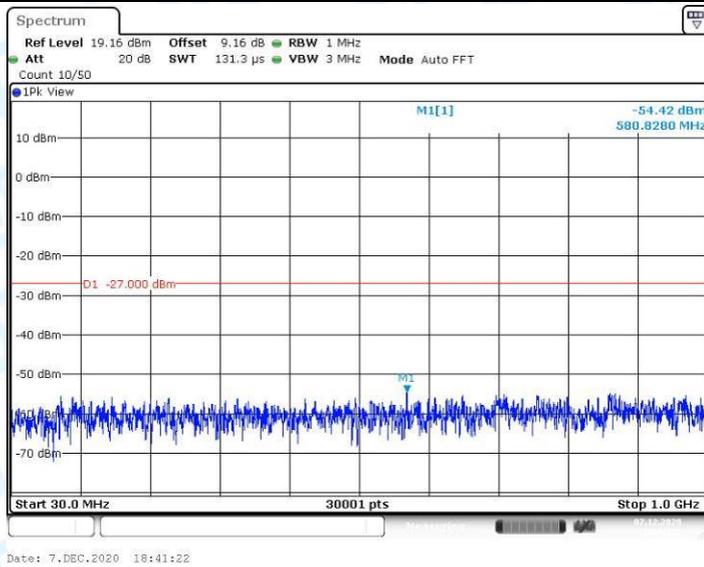
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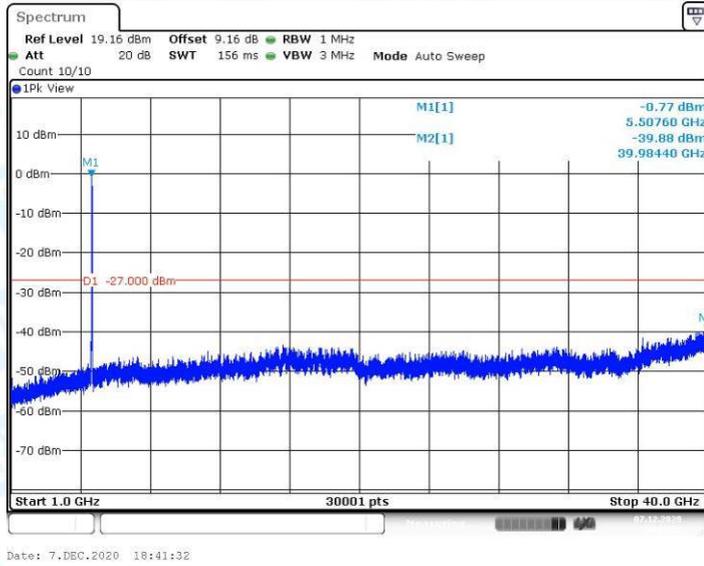
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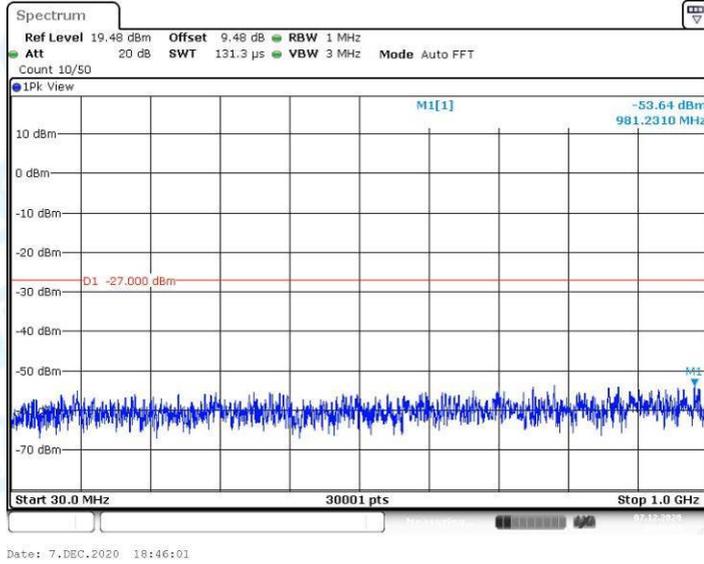
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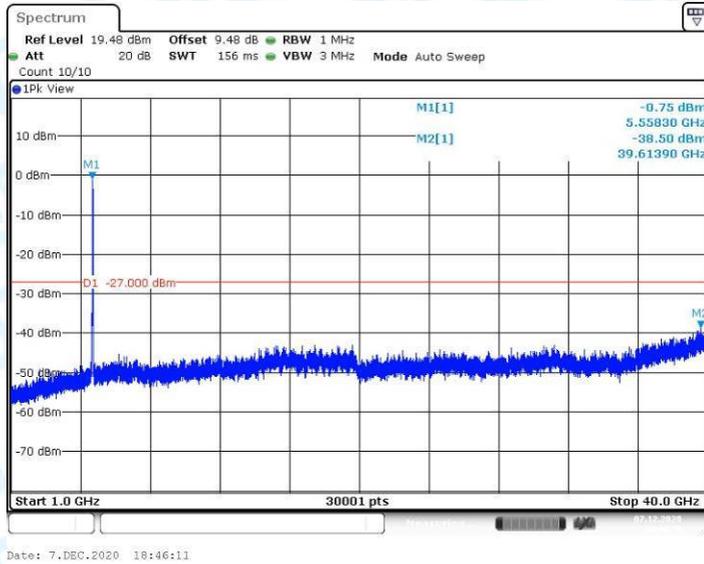
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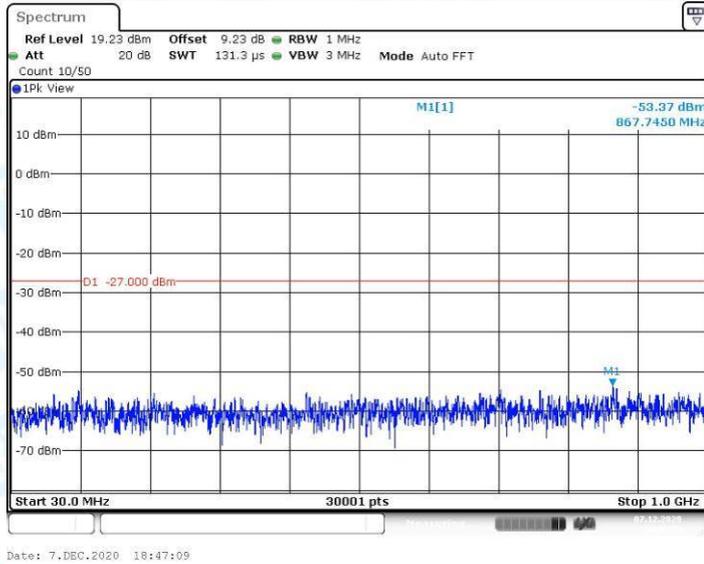
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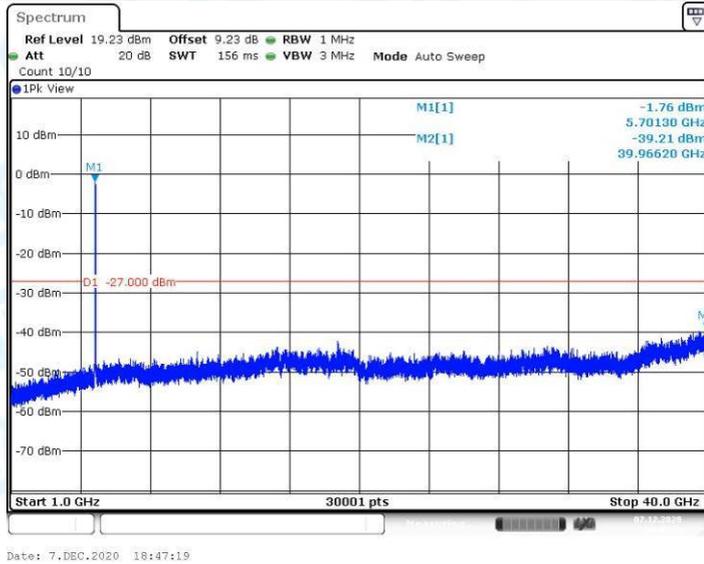
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11N40SISO_Ant1_5710_30~1000



11N40SISO_Ant1_5710_1000~40000



11N40SISO_Ant1_5755_30~1000

