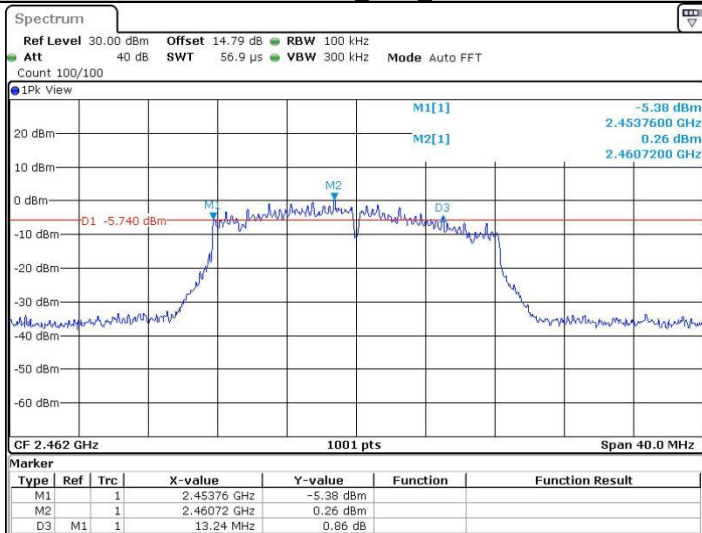
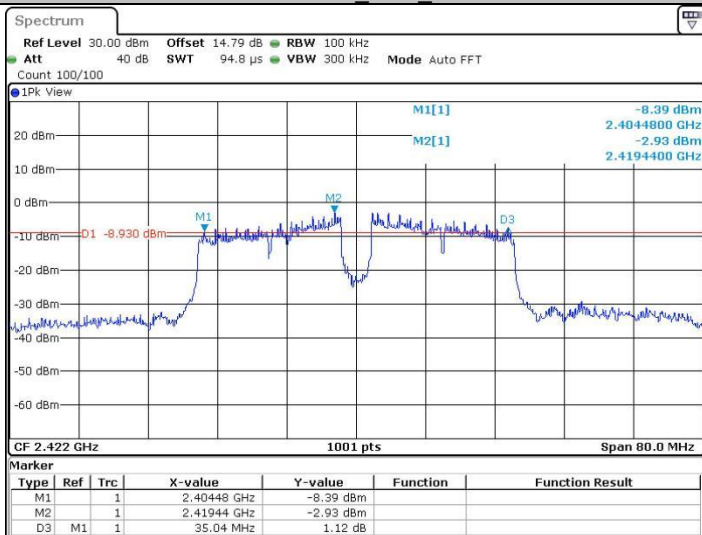


11N20SISO_Ant3_2462



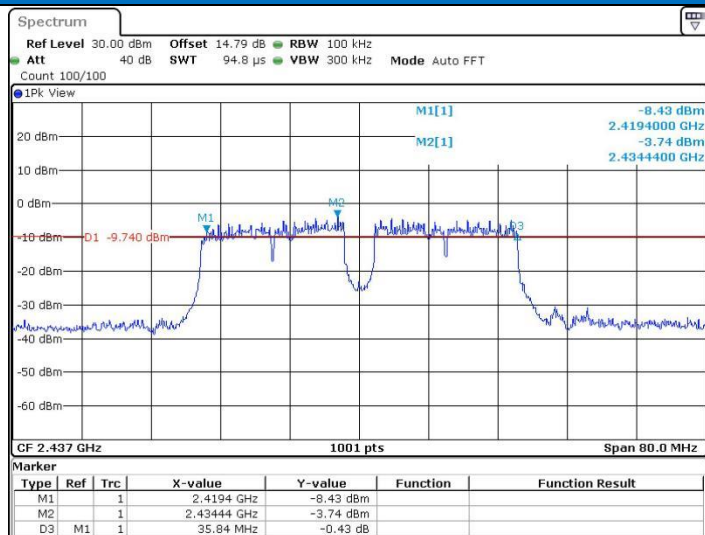
Date: 26 AUG 2024 18:20:23

11N40SISO_Ant3_2422



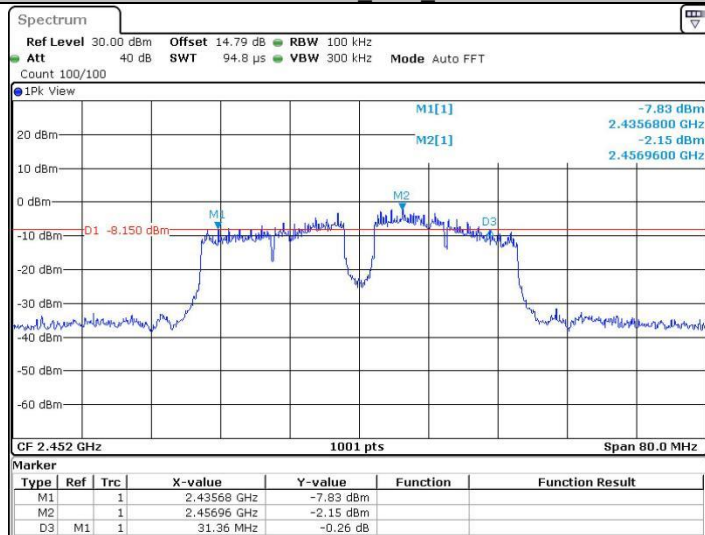
Date: 26 AUG 2024 18:23:21

11N40SISO_Ant3_2437



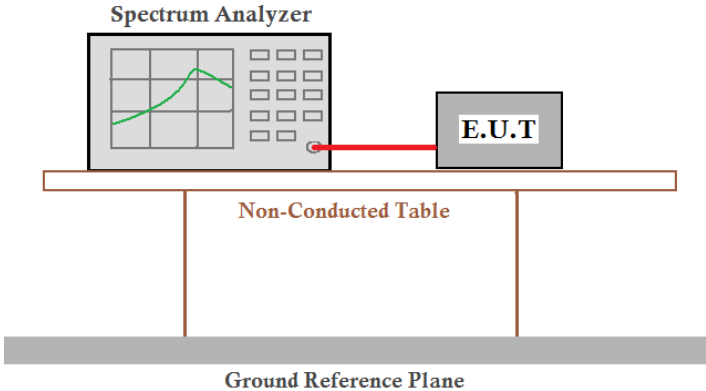
Date: 26 AUG.2024 18:25:23

11N40SISO_Ant3_2452



Date: 26 AUG.2024 18:27:08

5.5 Power Spectral Density

Test Requirement:	47 CFR Part 15C Section 15.247 (e)
Test Method:	ANSI C63.10: 2013
Test Setup:	 Offset=cable loss+ attenuation factor
Exploratory Test Mode:	Transmitting with all kind of modulations, data rates
Final Test Mode:	Only the worst case is recorded in the report.
Limit:	≤8.00dBm/3kHz
Test Results:	Pass

Test Result

Module 1 (AIC8800D40L)

Test Mode	Antenna	Frequency [MHz]	PSD [dBm/3kHz]	Limit [dBm/3kHz]	Verdict
11B	Ant1	2412	-16.38	≤8	PASS
		2437	-18.43	≤8	PASS
		2462	-17.62	≤8	PASS
11G	Ant1	2412	-19.89	≤8	PASS
		2437	-20.48	≤8	PASS
		2462	-20.07	≤8	PASS
11N20SISO	Ant1	2412	-20.01	≤8	PASS
		2437	-18.5	≤8	PASS
		2462	-20.36	≤8	PASS
11N40SISO	Ant1	2422	-24.44	≤8	PASS
		2437	-25.25	≤8	PASS
		2452	-22.9	≤8	PASS

Module 2 (RTL8192FS)

Test Mode	Antenna	Frequency [MHz]	PSD [dBm/3kHz]	Limit [dBm/3kHz]	Verdict
11B	Ant2	2412	-22.03	≤8	PASS
		2437	-20.82	≤8	PASS
		2462	-20.97	≤8	PASS
11G	Ant2	2412	-23.25	≤8	PASS
		2437	-23.76	≤8	PASS
		2462	-22.49	≤8	PASS
11N20SISO	Ant2	2412	-20.27	≤8	PASS
		2437	-19.7	≤8	PASS
		2462	-18.77	≤8	PASS
11N40SISO	Ant2	2422	-22.54	≤8	PASS
		2437	-23.04	≤8	PASS
		2452	-21.5	≤8	PASS

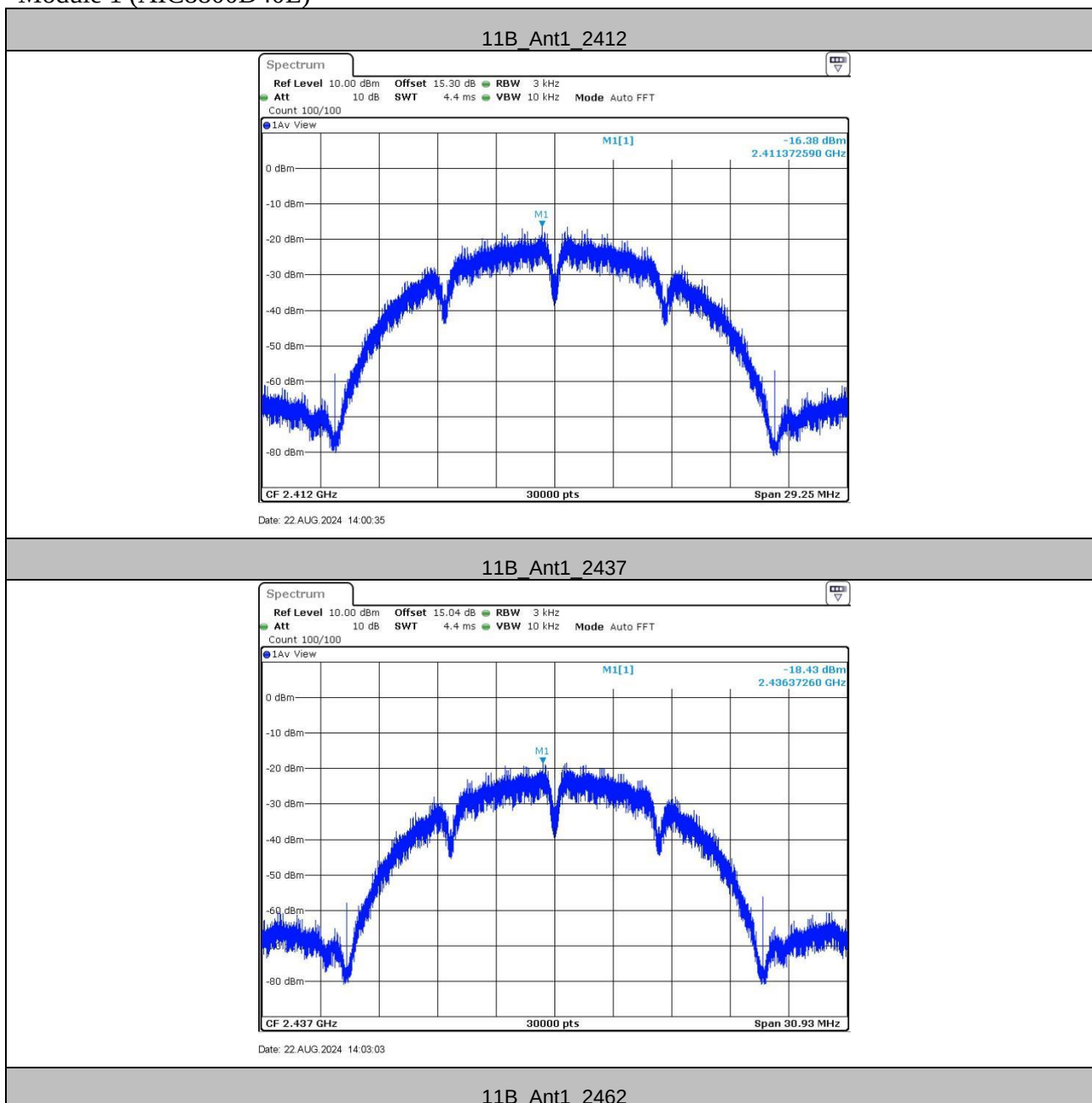
Module 2 (RTL8192FS)

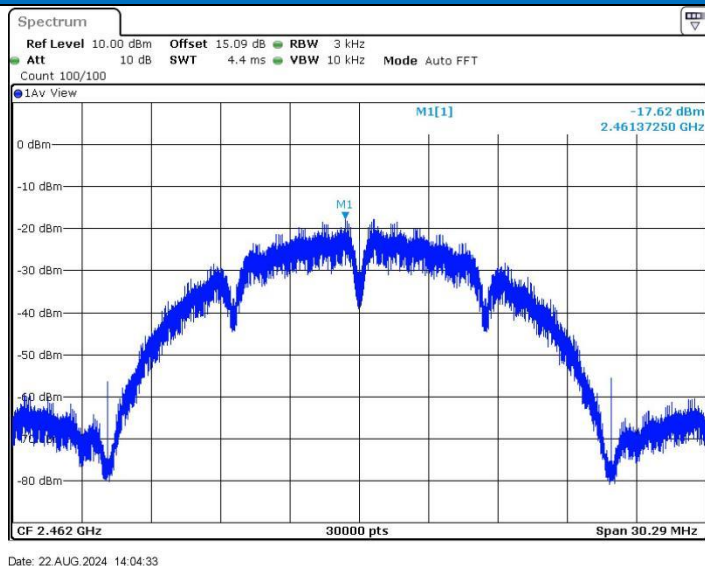
Test Mode	Antenna	Frequency [MHz]	PSD [dBm/3kHz]	Limit [dBm/3kHz]	Verdict
11B	Ant3	2412	-21.85	≤8	PASS
		2437	-21.59	≤8	PASS
		2462	-20.79	≤8	PASS
11G	Ant3	2412	-24.3	≤8	PASS
		2437	-23.75	≤8	PASS
		2462	-22.37	≤8	PASS
11N20SISO	Ant3	2412	-20.22	≤8	PASS
		2437	-19.84	≤8	PASS
		2462	-18.17	≤8	PASS
11N40SISO	Ant3	2422	-22.31	≤8	PASS
		2437	-23.5	≤8	PASS
		2452	-21.66	≤8	PASS

Note: When Duty cycle >98%, D.C.F is not required.

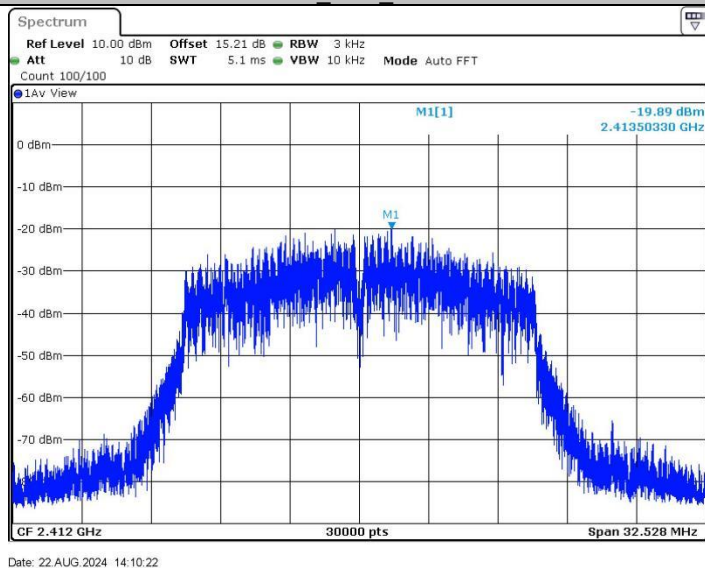
Test Graphs

Module 1 (AIC8800D40L)

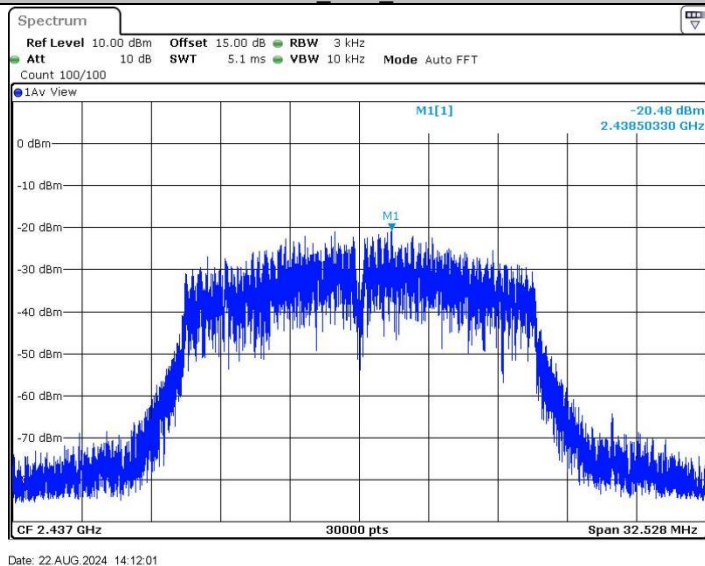




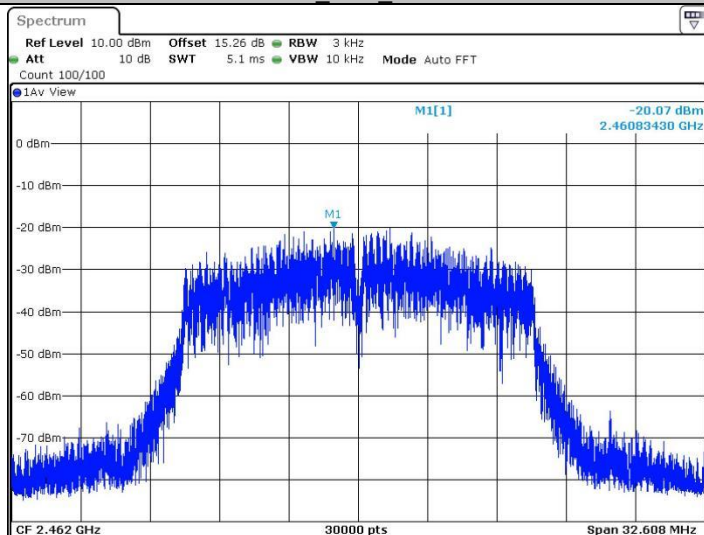
11G_Ant1_2412



11G_Ant1_2437

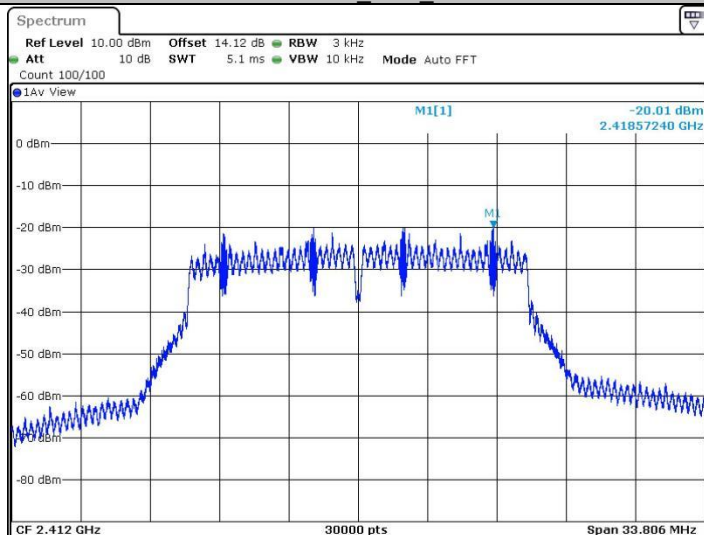


11G_Ant1_2462



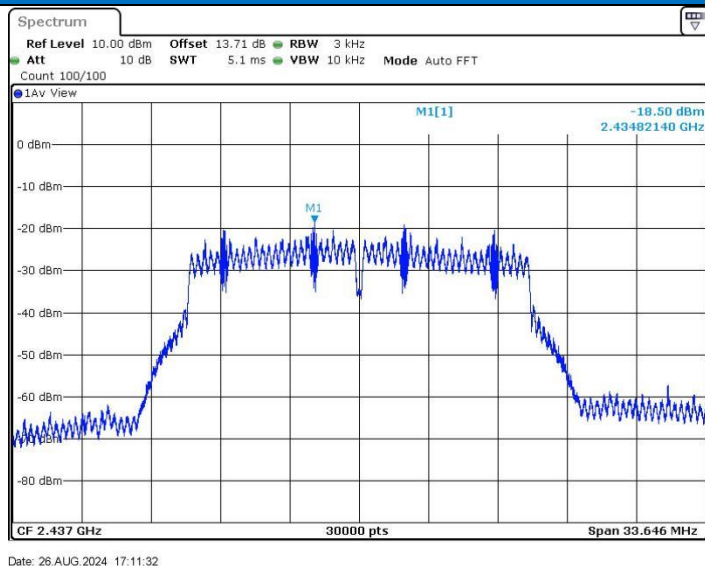
Date: 22 AUG 2024 14:13:49

11N20SISO_Ant1_2412

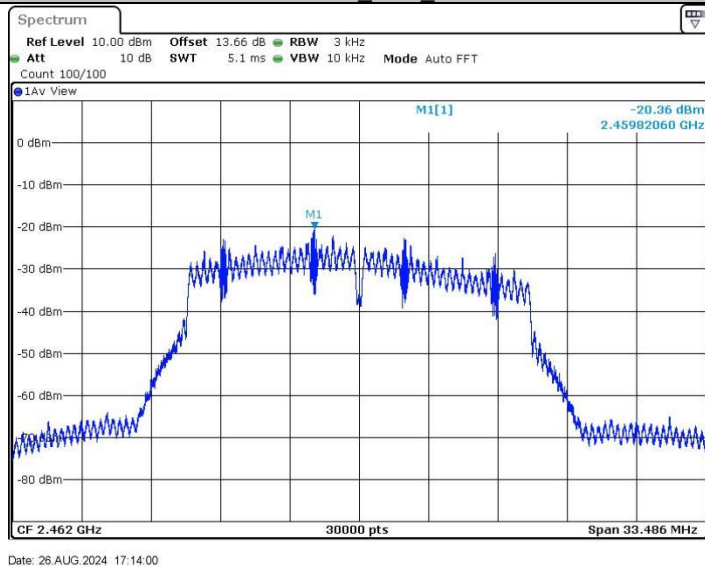


Date: 26 AUG 2024 17:09:13

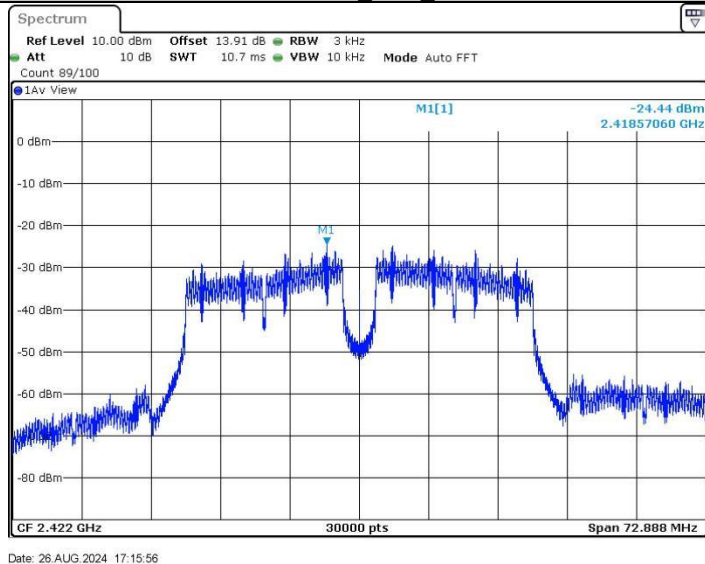
11N20SISO_Ant1_2437



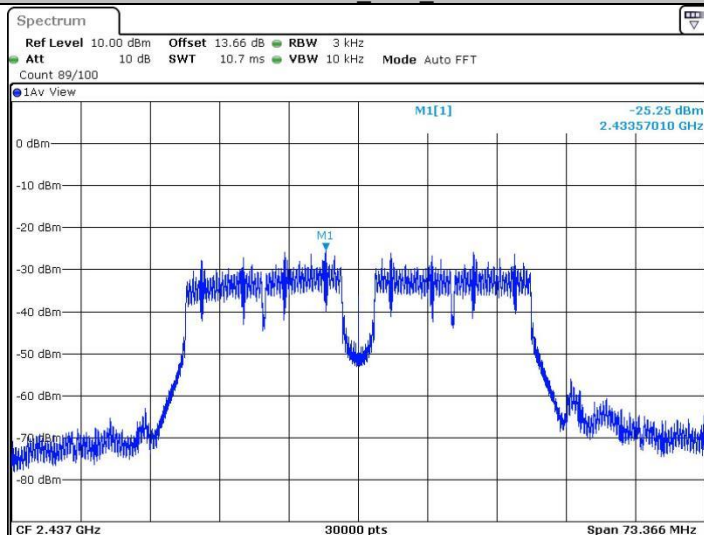
11N20SISO_Ant1_2462



11N40SISO_Ant1_2422

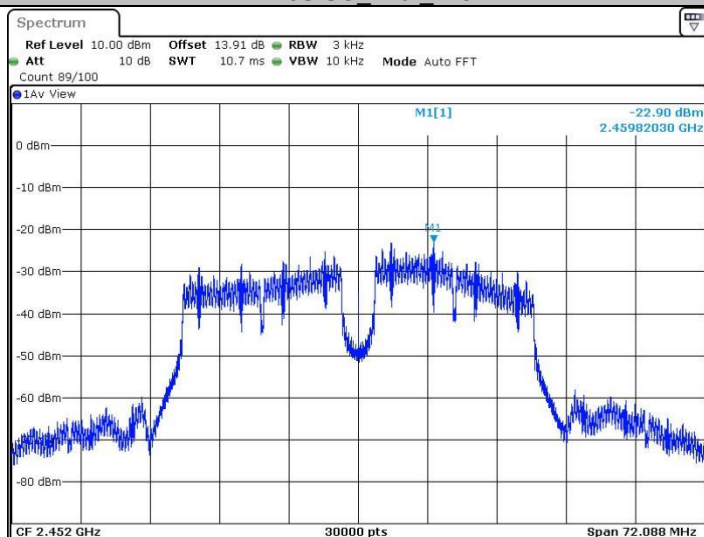


11N40SISO_Ant1_2437



Date: 26 AUG 2024 17:20:46

11N40SISO_Ant1_2452



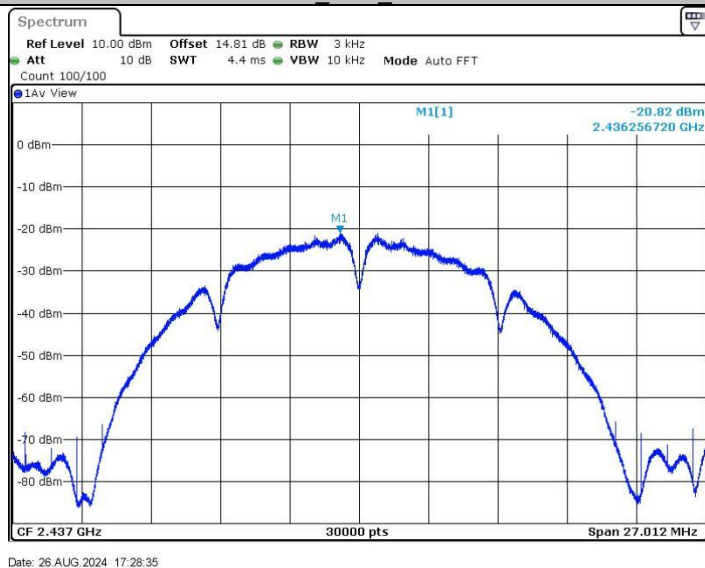
Date: 26 AUG 2024 17:22:21

Module 2 (RTL8192FS)

11B_Ant2_2412



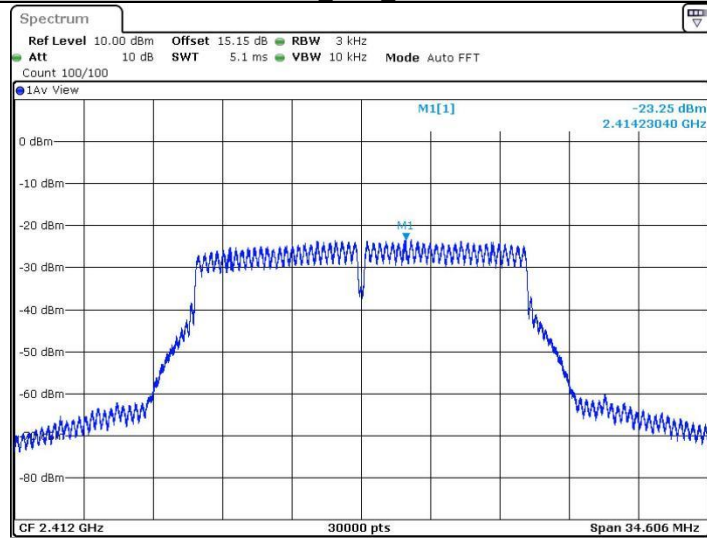
11B_Ant2_2437



11B_Ant2_2462

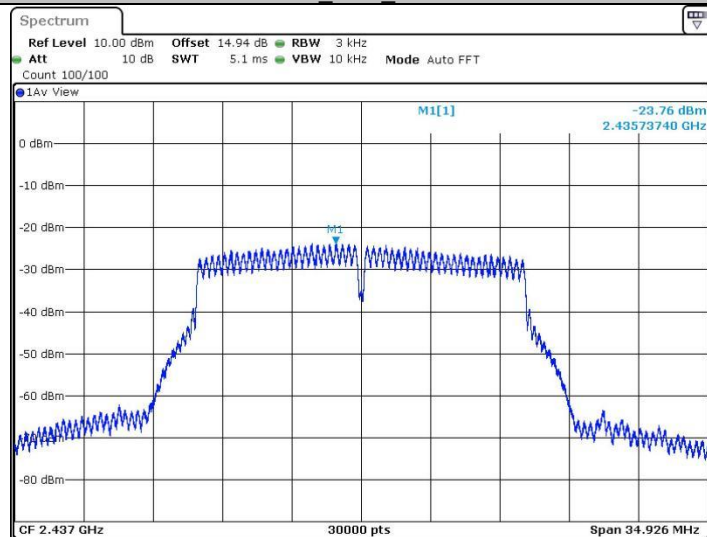


11G_Ant2_2412



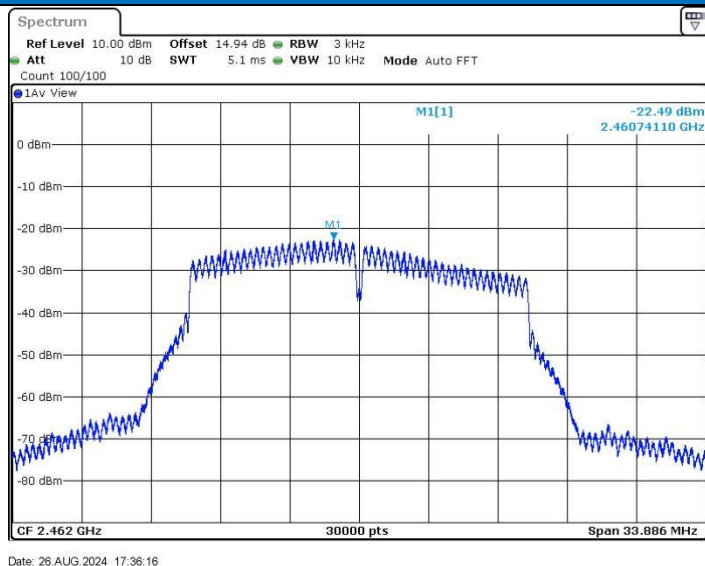
Date: 26 AUG 2024 17:32:16

11G_Ant2_2437

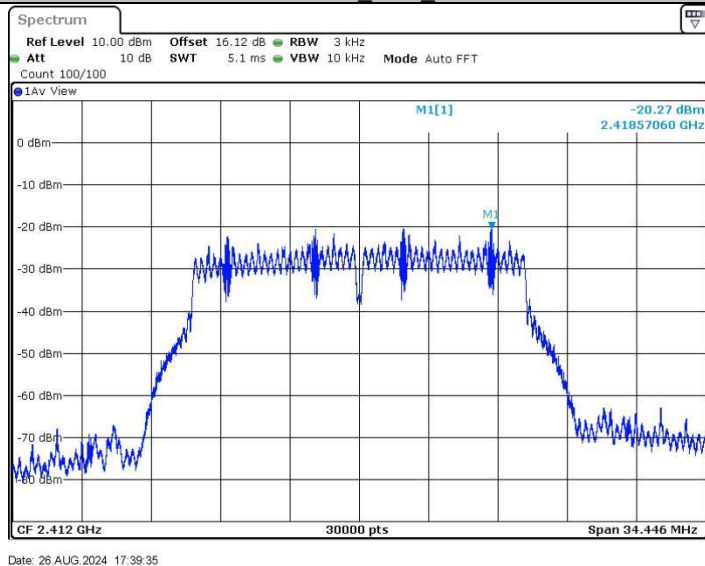


Date: 26 AUG 2024 17:34:45

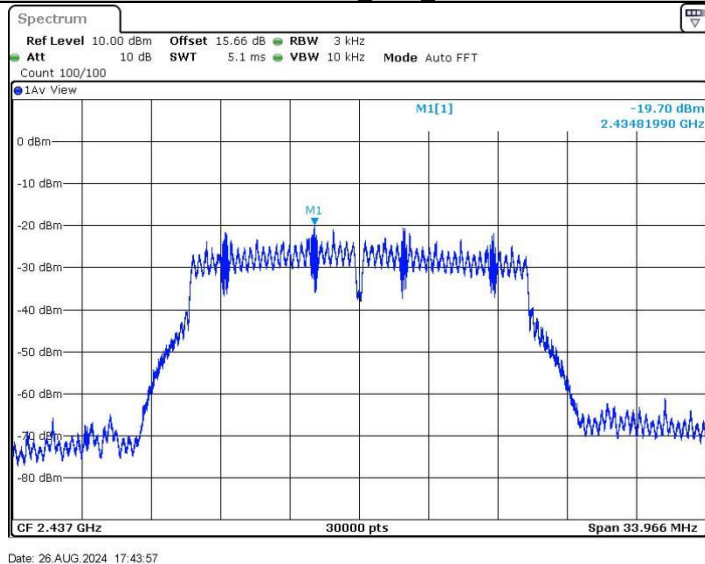
11G_Ant2_2462



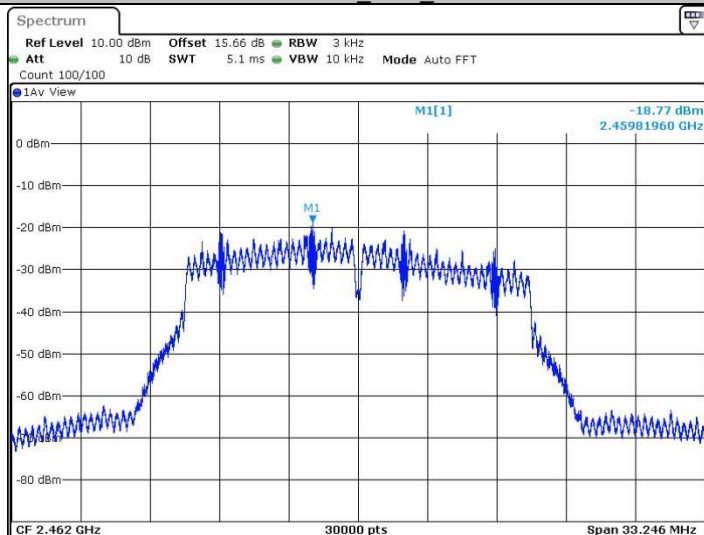
11N20SISO_Ant2_2412



11N20SISO_Ant2_2437

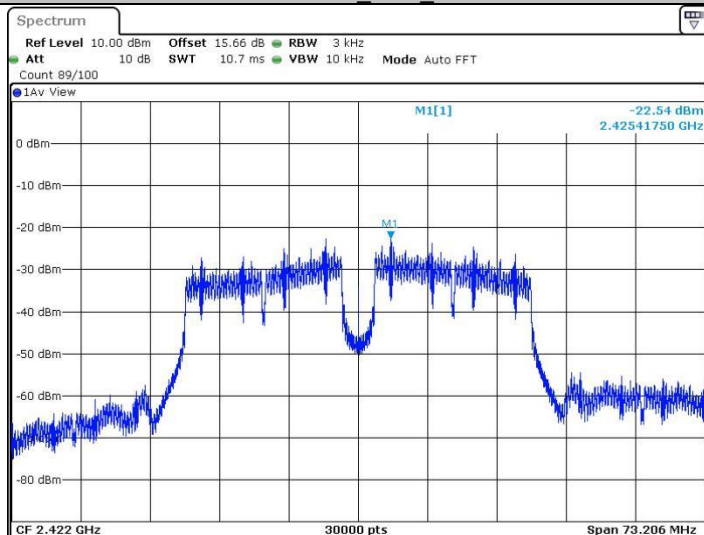


11N20SISO_Ant2_2462



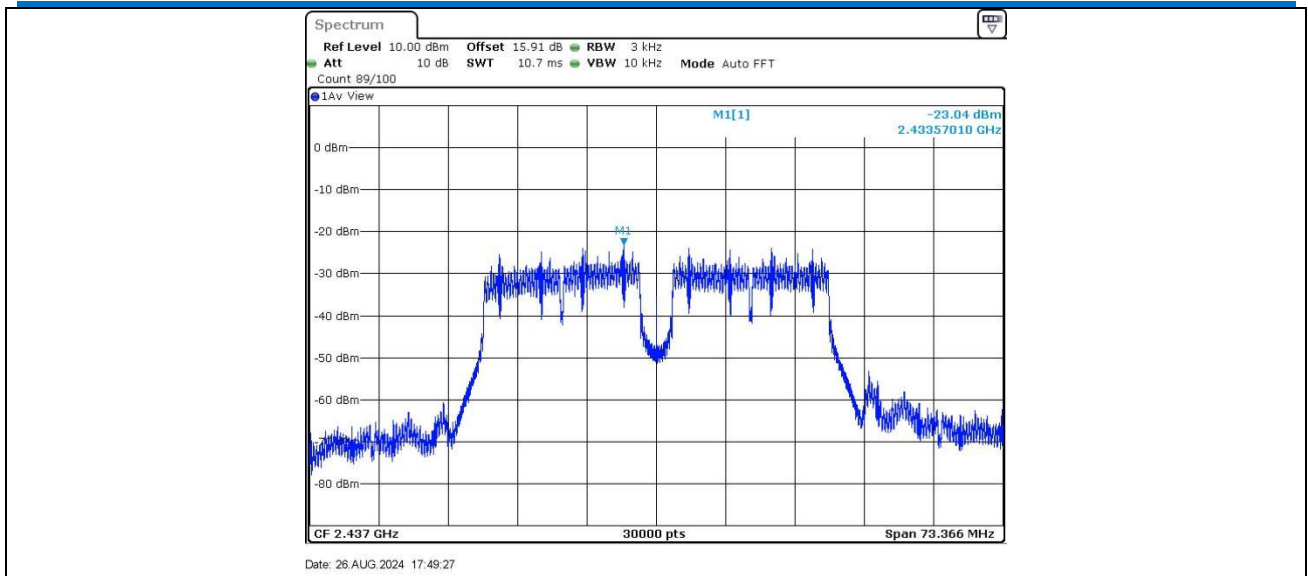
Date: 26 AUG 2024 17:45:27

11N40SISO_Ant2_2422

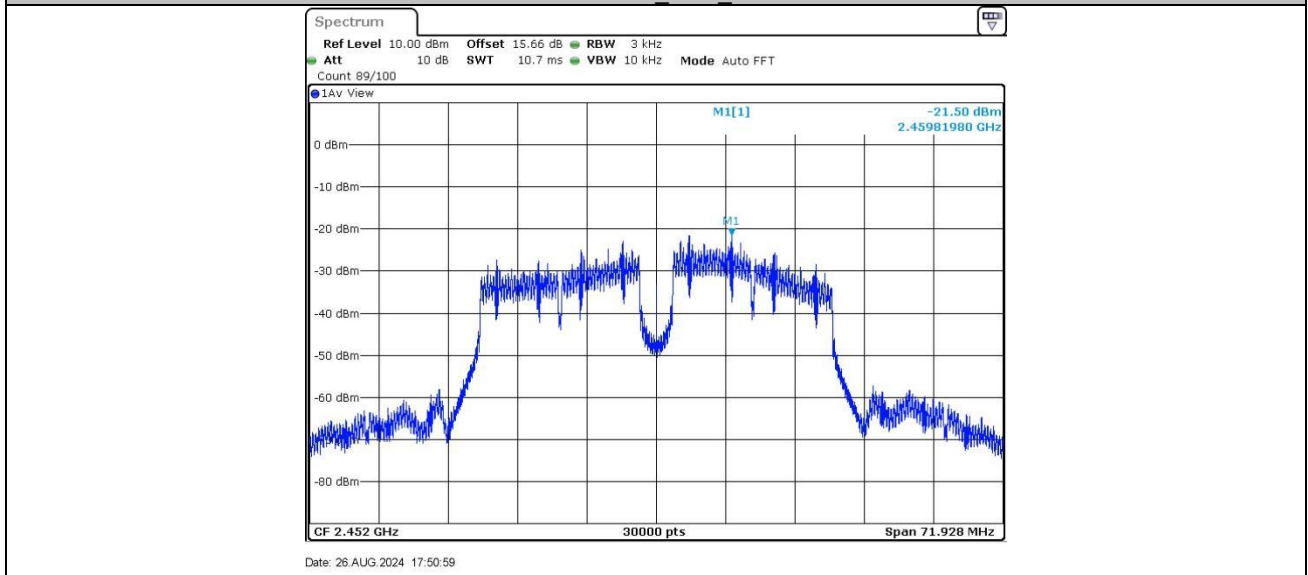


Date: 26 AUG 2024 17:47:20

11N40SISO_Ant2_2437



11N40SISO_Ant2_2452

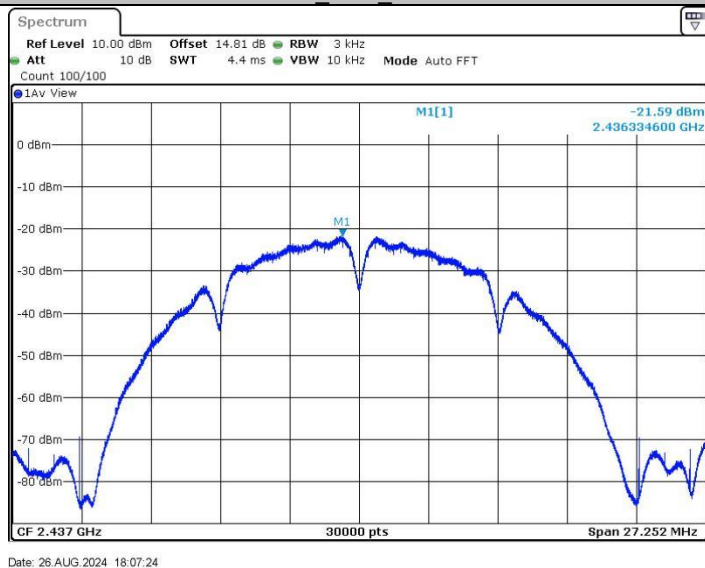


Module 2 (RTL8192FS)

11B_Ant3_2412



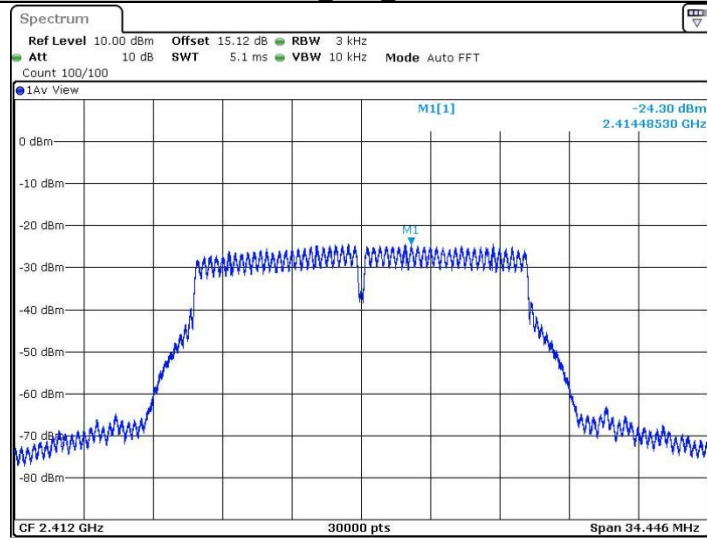
11B_Ant3_2437



11B_Ant3_2462

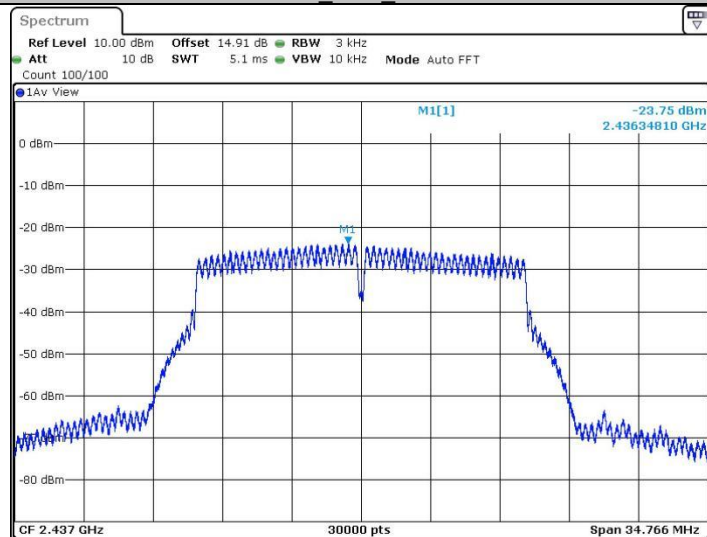


11G_Ant3_2412



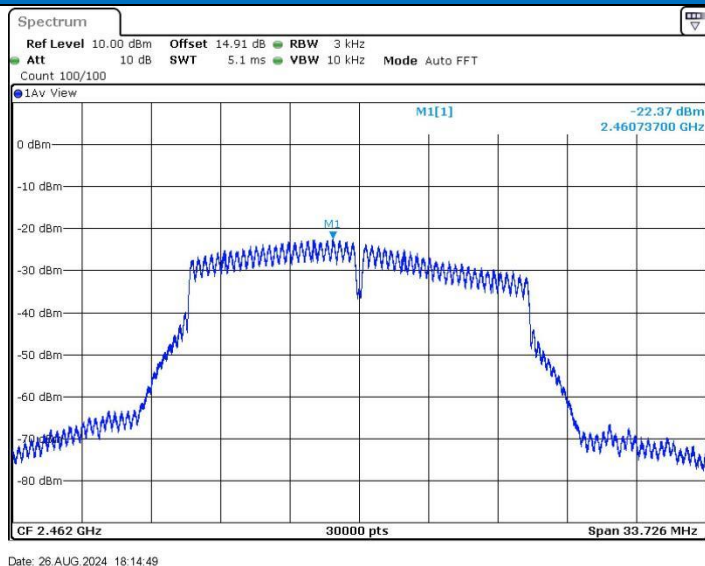
Date: 26 AUG 2024 18:11:14

11G_Ant3_2437

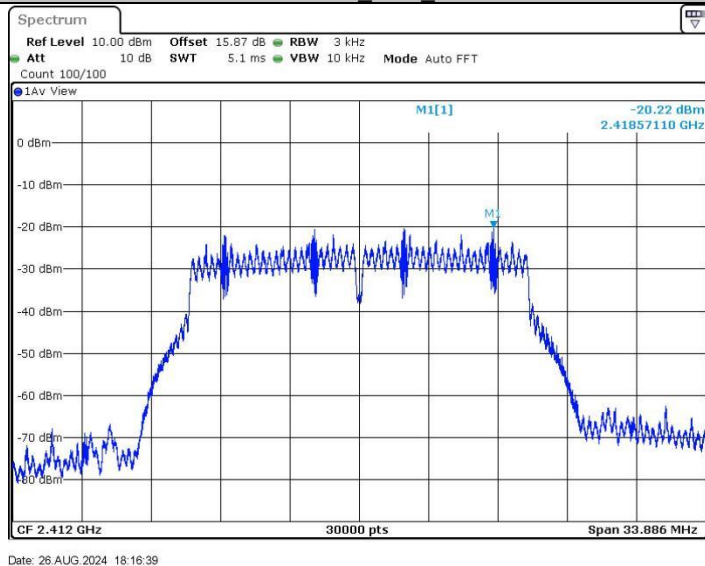


Date: 26 AUG 2024 18:13:10

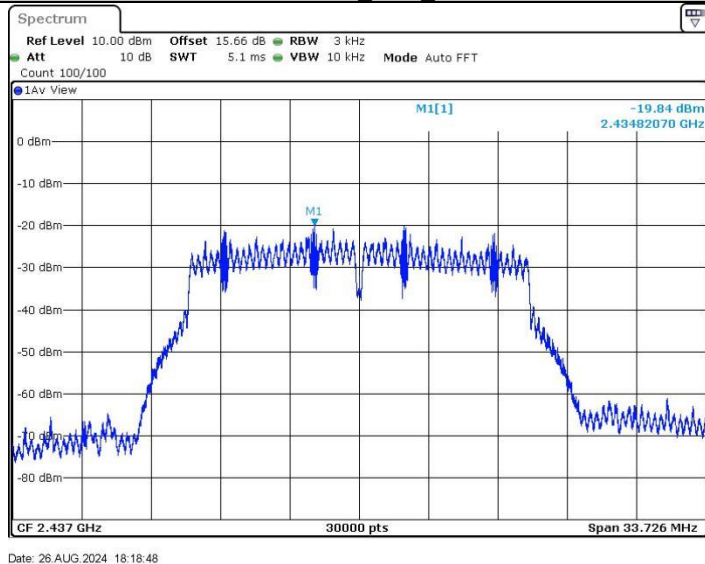
11G_Ant3_2462



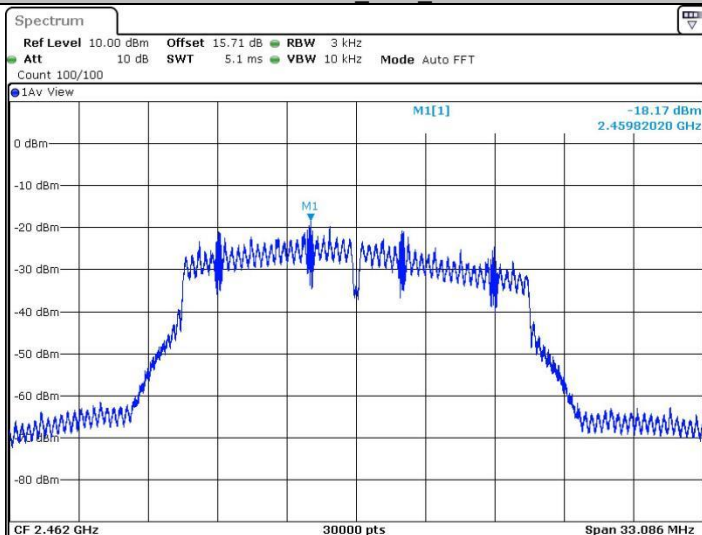
11N20SISO_Ant3_2412



11N20SISO_Ant3_2437

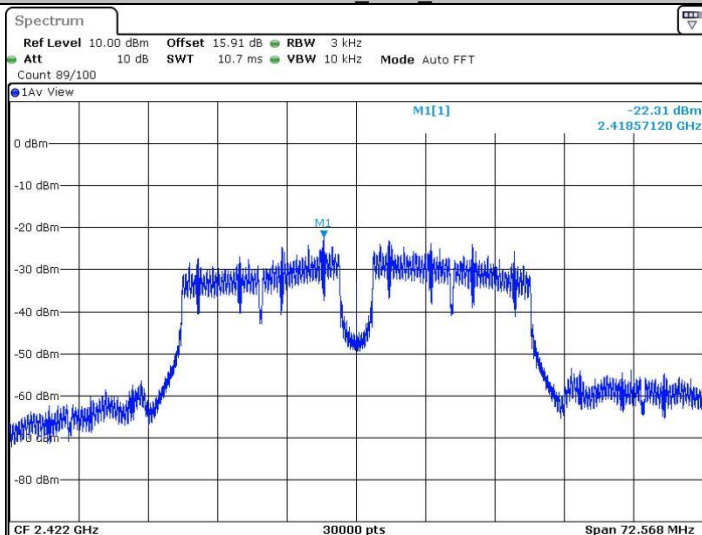


11N20SISO_Ant3_2462



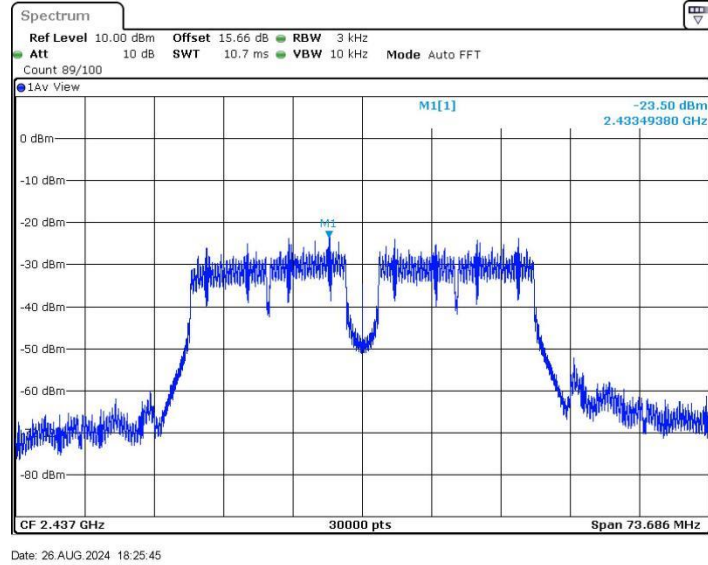
Date: 26 AUG 2024 18:20:44

11N40SISO_Ant3_2422

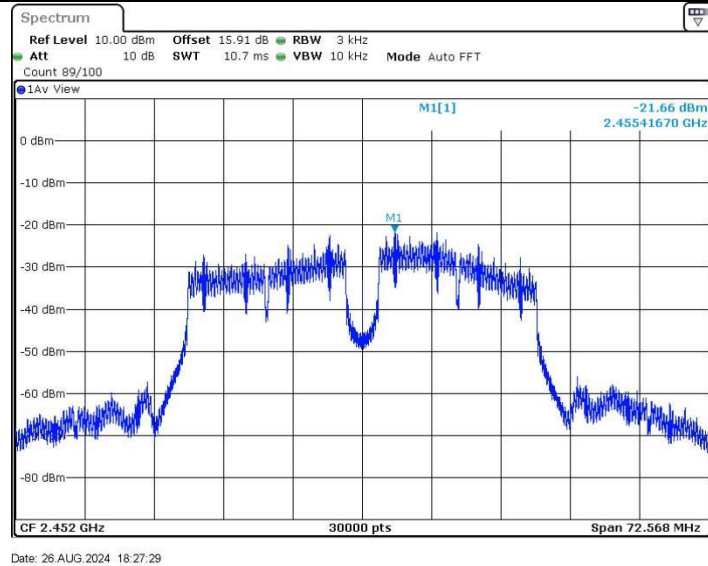


Date: 26 AUG 2024 18:23:42

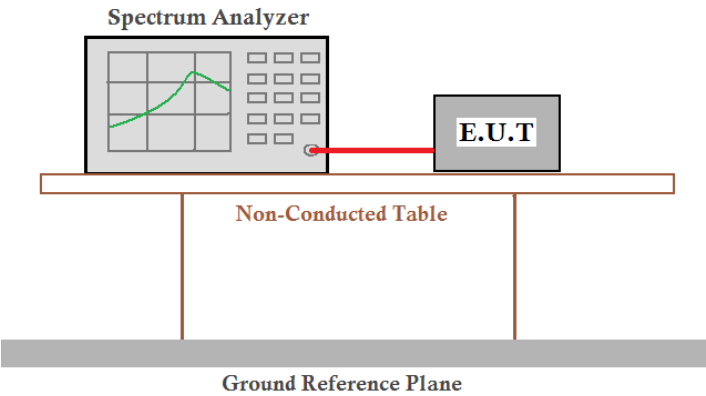
11N40SISO_Ant3_2437



11N40SISO_Ant3_2452



5.6 Band-edge for RF Conducted Emissions

Test Requirement:	47 CFR Part 15C Section 15.247 (d)
Test Method:	ANSI C63.10: 2013
Test Setup:	 Offset=cable loss+ attenuation factor
Exploratory Test Mode:	Transmitting with all kind of modulations, data rates
Final Test Mode:	Only the worst case is recorded in the report.
Limit:	In any 100 kHz bandwidth outside the frequency band in which the spread spectrum intentional radiator is operating, the radio frequency power that is produced by the intentional radiator shall be at least 30 dB below that in the 100 kHz bandwidth within the band that contains the highest level of the desired power, based on either an RF conducted or a radiated measurement.
Test Results:	Pass

Test Result

Module 1 (AIC8800D40L)

TestMode	ChName	Frequency [MHz]	RefLevel[dBm]	Result[dBm]	Limit[dBm]	Verdict
11B	Low	2412	1.12	-41.13	≤-28.88	PASS
	High	2462	0.03	-42.29	≤-29.97	PASS
11G	Low	2412	-3.43	-43.69	≤-33.43	PASS
	High	2462	-3.64	-42.25	≤-33.64	PASS
11N20SISO	Low	2412	-1.34	-35.53	≤-31.34	PASS
	High	2462	-1.67	-44.35	≤-31.67	PASS
11N40SISO	Low	2422	-4.95	-36.76	≤-34.95	PASS
	High	2452	-3.90	-41.7	≤-33.9	PASS

Module 2 (RTL8192FS)

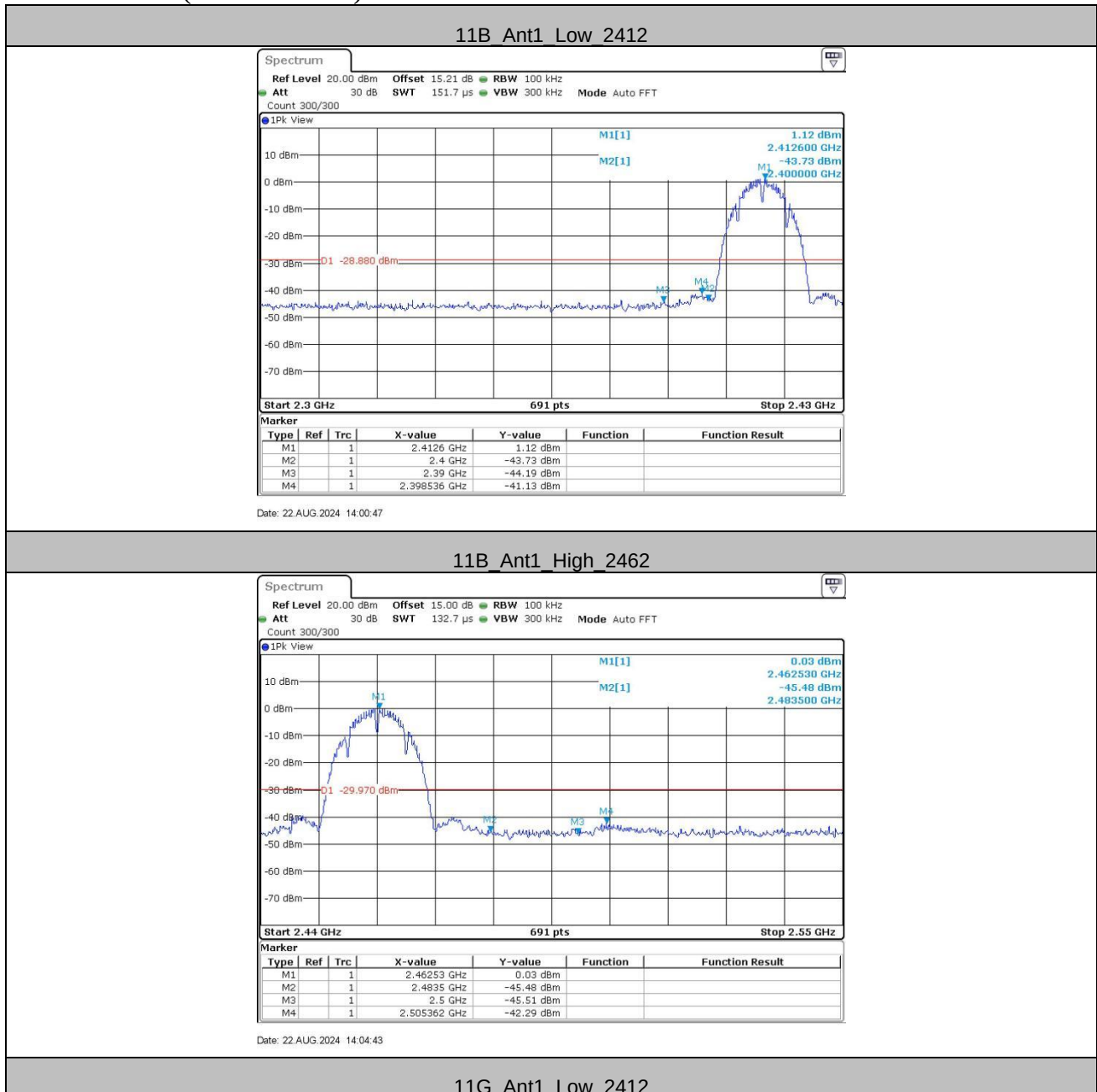
TestMode	ChName	Frequency [MHz]	RefLevel[dBm]	Result[dBm]	Limit[dBm]	Verdict
11B	Low	2412	1.07	-43.65	≤-28.93	PASS
	High	2462	1.83	-42.72	≤-28.17	PASS
11G	Low	2412	-0.64	-37.55	≤-30.64	PASS
	High	2462	0.11	-41.85	≤-29.89	PASS
11N20SISO	Low	2412	-1.71	-43.09	≤-31.71	PASS
	High	2462	0.03	-43.13	≤-29.97	PASS
11N40SISO	Low	2422	-3.24	-34.89	≤-33.24	PASS
	High	2452	-2.08	-39.94	≤-32.08	PASS

Module 2 (RTL8192FS)

TestMode	ChName	Frequency [MHz]	RefLevel[dBm]	Result[dBm]	Limit[dBm]	Verdict
11B	Low	2412	0.92	-43.13	≤-29.08	PASS
	High	2462	1.85	-43.17	≤-28.15	PASS
11G	Low	2412	-1.74	-39.88	≤-31.74	PASS
	High	2462	0.23	-43.08	≤-29.77	PASS
11N20SISO	Low	2412	-1.16	-42.44	≤-31.16	PASS
	High	2462	0.26	-42.98	≤-29.74	PASS
11N40SISO	Low	2422	-2.94	-33.48	≤-32.94	PASS
	High	2452	-1.81	-39.66	≤-31.81	PASS

Test Graphs

Module 1 (AIC8800D40L)





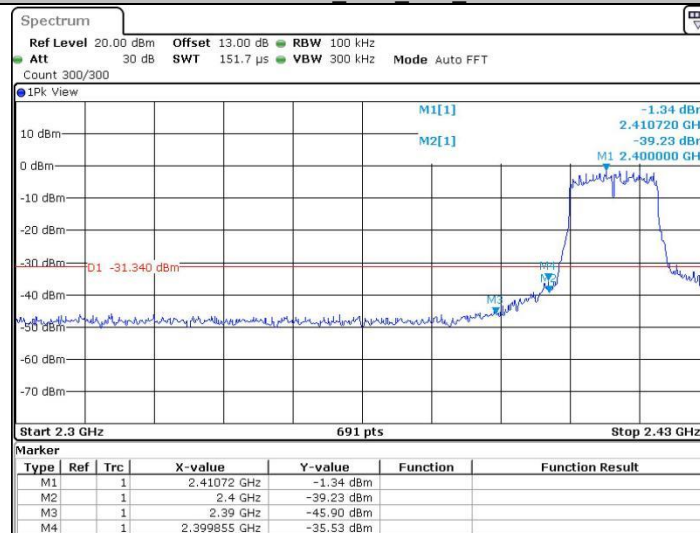
Date: 22 AUG. 2024 14:10:32

11G Ant1 High 2462



Date: 22 AUG. 2024 14:14:00

11N20SISO Ant1 Low 2412



Date: 26 AUG. 2024 17:09:23

11N20SISO_Ant1_High_2462



Date: 26 AUG 2024 17:14:10

11N40SISO_Ant1_Low_2422

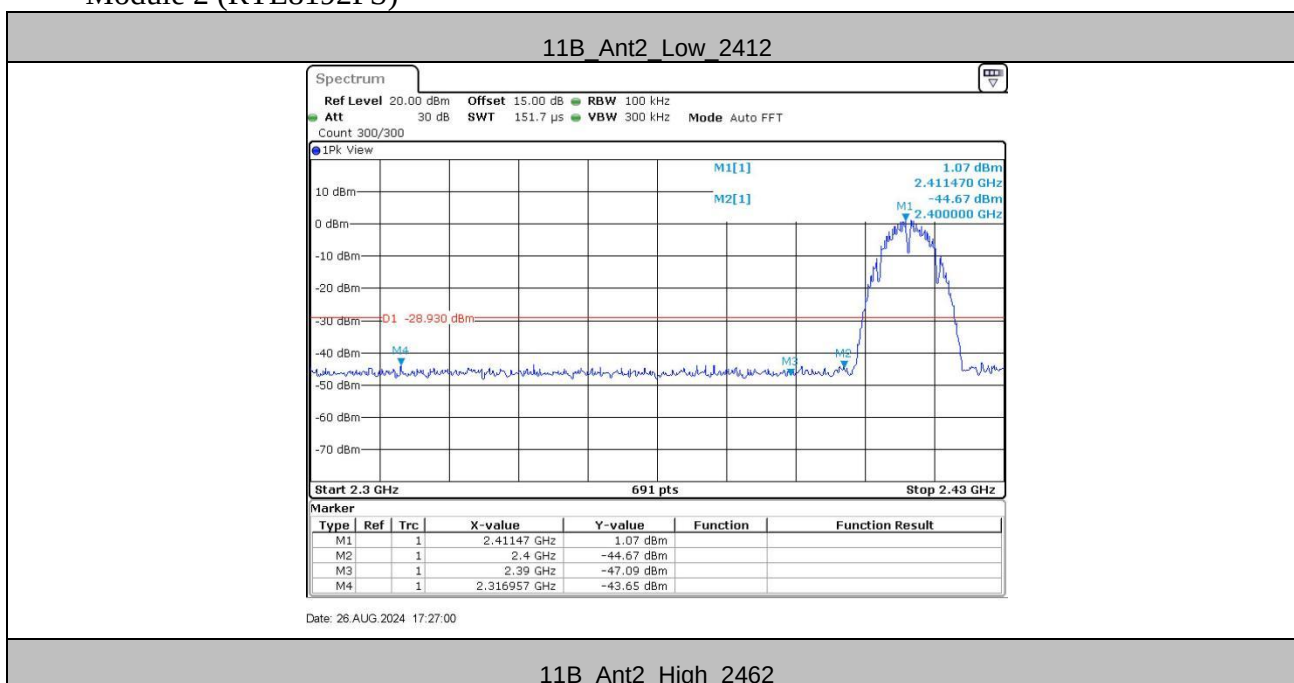


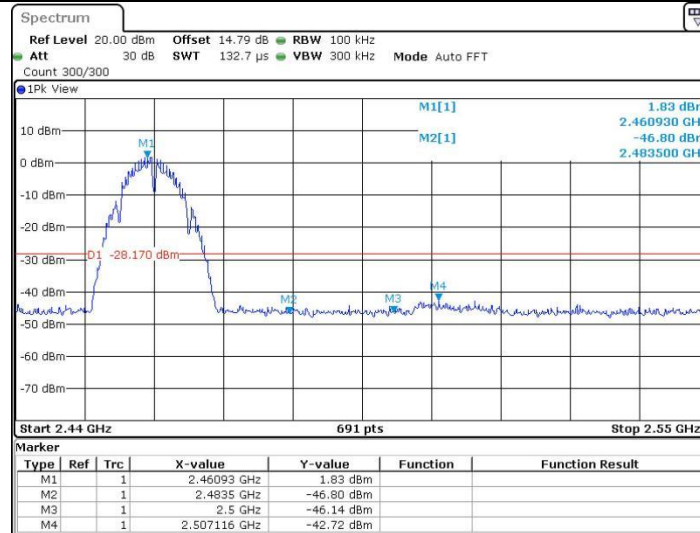
Date: 26 AUG 2024 17:16:06

11N40SISO_Ant1_High_2452



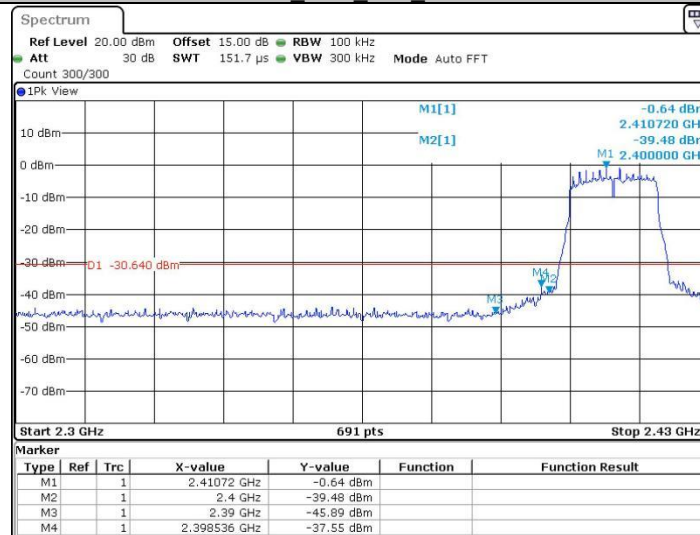
Module 2 (RTL8192FS)





Date: 26 AUG.2024 17:30:20

11G_Ant2_Low_2412



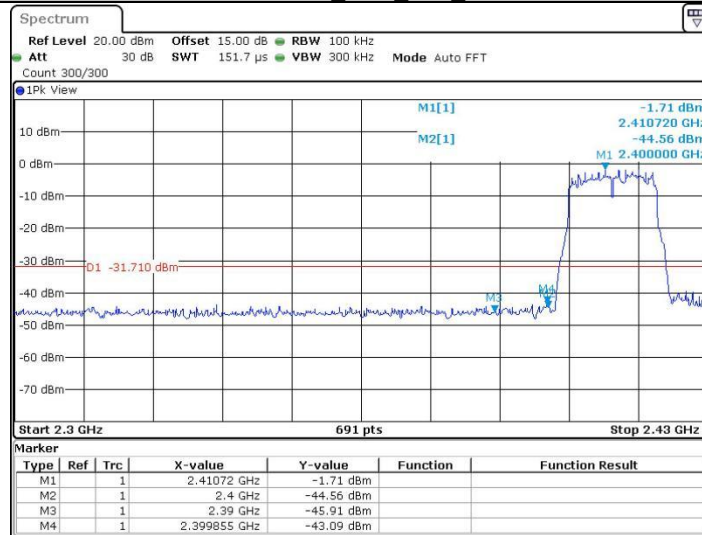
Date: 26 AUG.2024 17:32:27

11G_Ant2_High_2462



Date: 26 AUG.2024 17:36:26

11N20SISO_Ant2_Low_2412



Date: 26 AUG 2024 17:39:45

11N20SISO_Ant2_High_2462



Date: 26 AUG 2024 17:45:37

11N40SISO_Ant2_Low_2422



Date: 26 AUG.2024 17:47:30

11N40SISO_Ant2_High_2452



Date: 26 AUG.2024 17:51:10

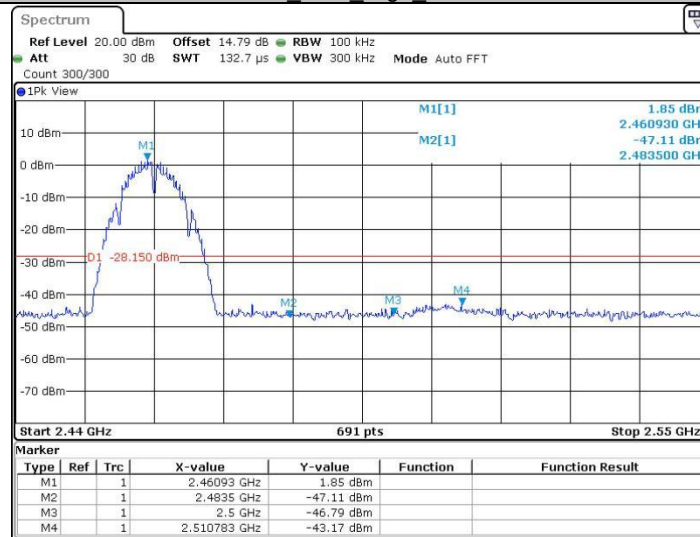
Module 2 (RTL8192FS)

11B_Ant3_Low_2412



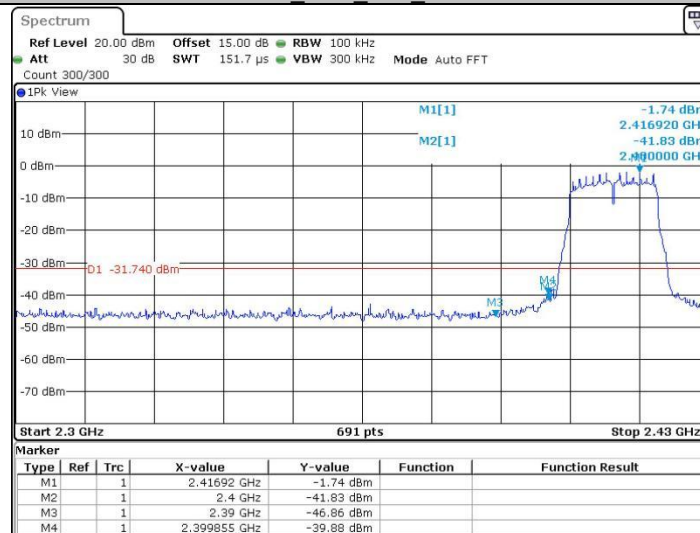
Date: 26 AUG. 2024 17:54:34

11B Ant3 High 2462



Date: 26 AUG. 2024 18:09:10

11G Ant3 Low 2412



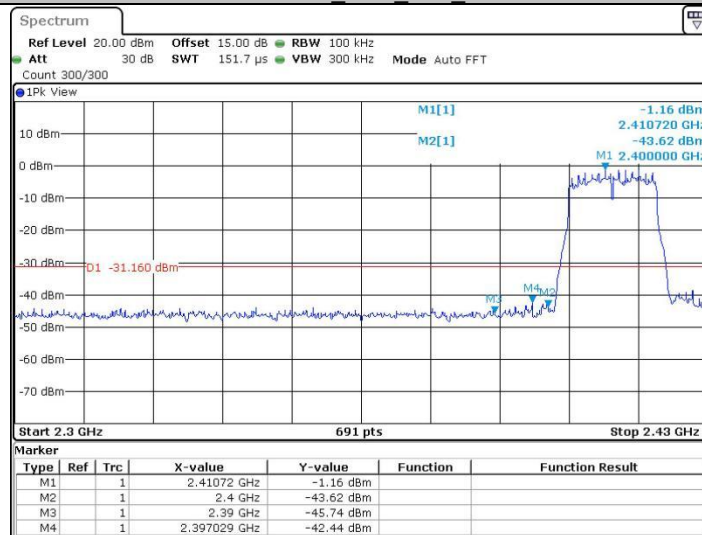
Date: 26 AUG. 2024 18:11:24

11G_Ant3_High_2462



Date: 26 AUG 2024 18:14:59

11N20SISO_Ant3_Low_2412



Date: 26 AUG 2024 18:16:49

11N20SISO_Ant3_High_2462