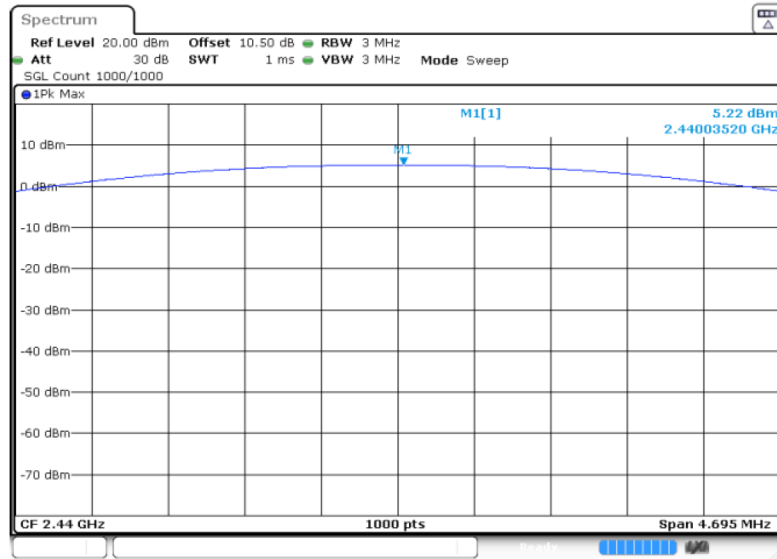
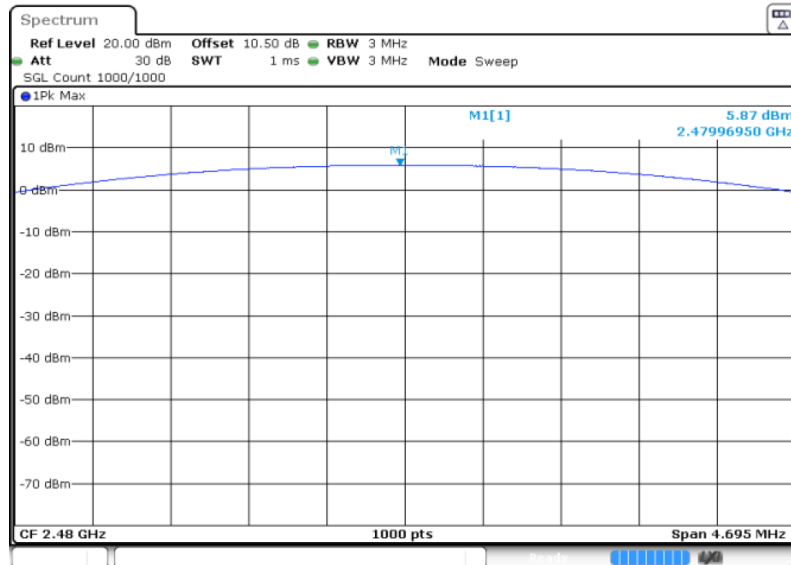


BDR (GFSK): 2441MHz



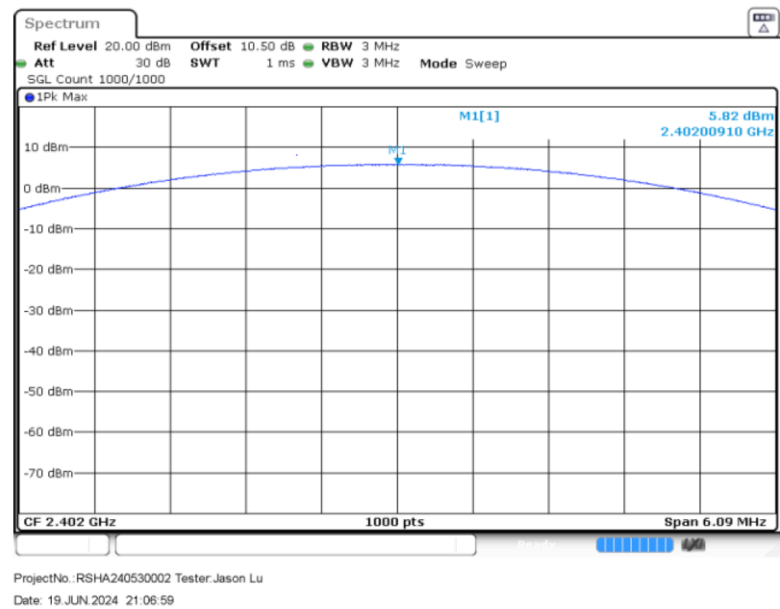
ProjectNo.: RSHA240530002 Tester: Jason Lu
 Date: 19 JUN 2024 19:57:07

BDR (GFSK): 2480MHz

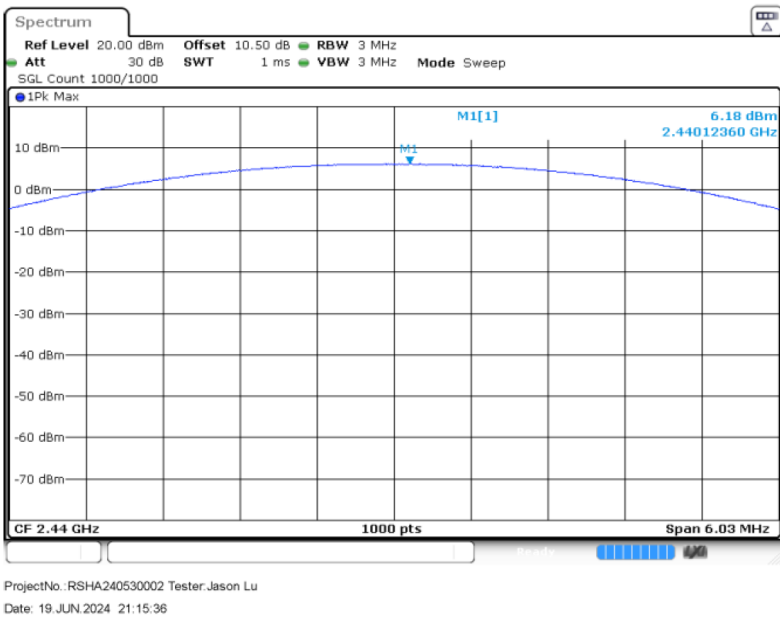


ProjectNo.: RSHA240530002 Tester: Jason Lu
 Date: 19 JUN 2024 20:05:30

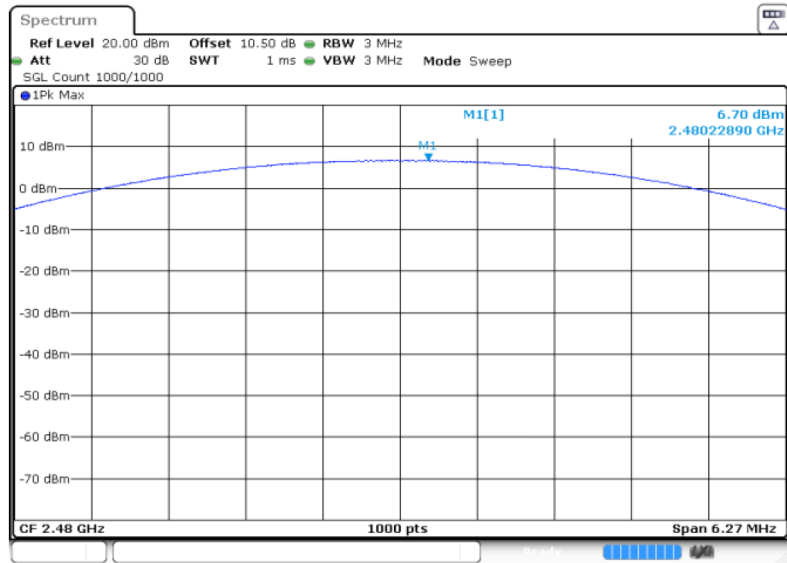
EDR($\pi/4$ -DQPSK): 2402MHz



EDR($\pi/4$ -DQPSK): 2441MHz

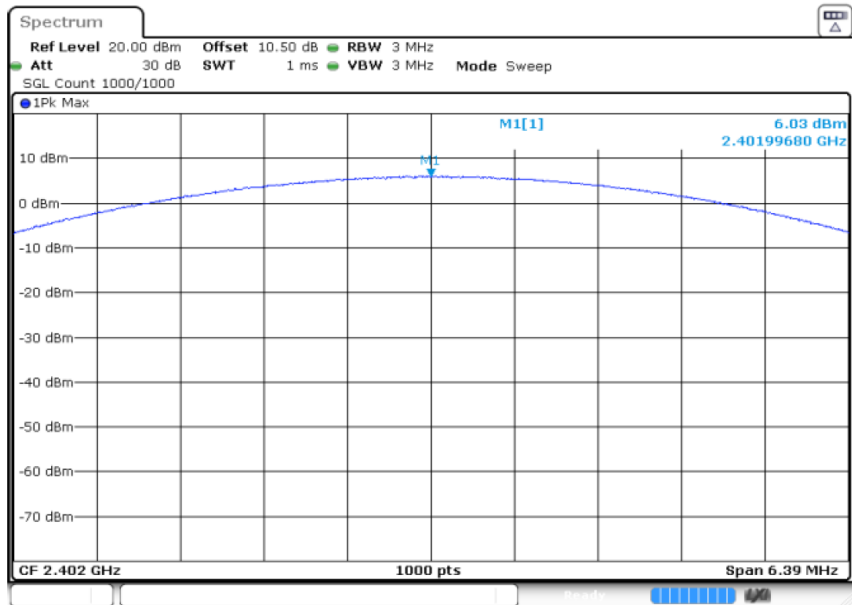


EDR($\pi/4$ -DQPSK): 2480MHz



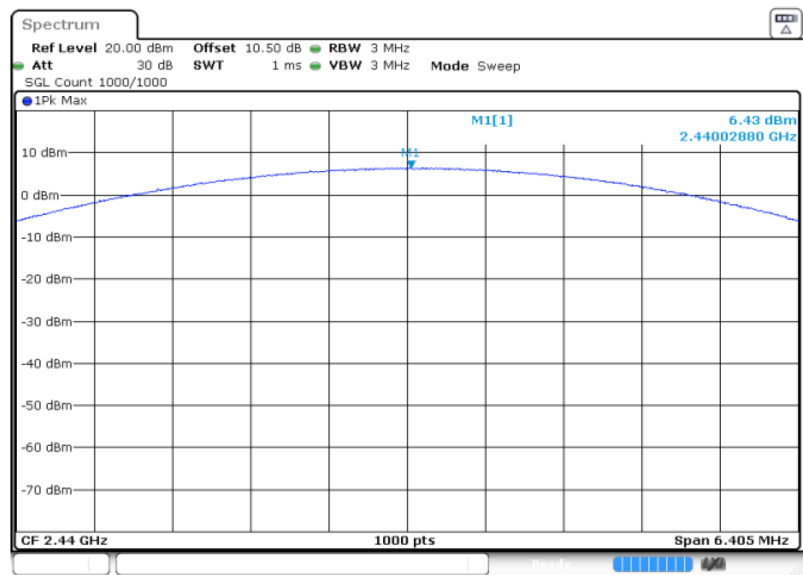
ProjectNo.: RSHA240530002 Tester: Jason Lu
 Date: 19 JUN 2024 21:25:25

EDR(8DPSK): 2402MHz



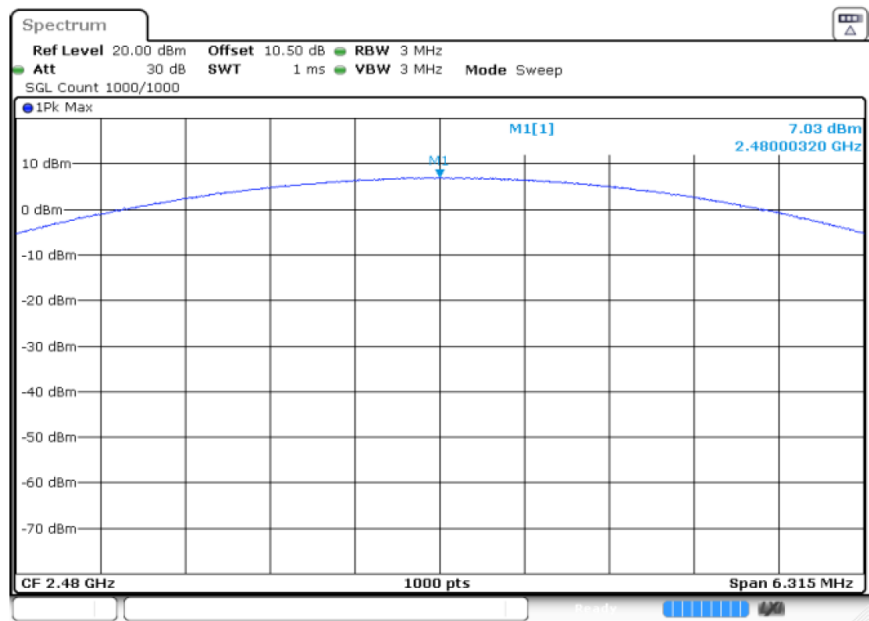
ProjectNo.: RSHA240530002 Tester: Jason Lu
 Date: 19 JUN 2024 21:35:18

EDR(8DPSK): 2441MHz



ProjectNo.:RSHA240530002 Tester:Jason Lu
Date: 19 JUN 2024 21:43:58

EDR(8DPSK): 2480MHz



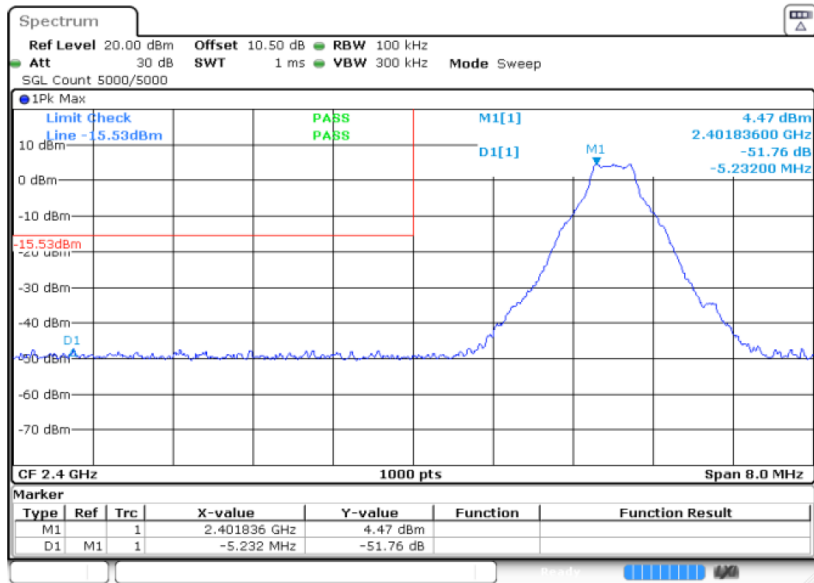
ProjectNo.:RSHA240530002 Tester:Jason Lu
Date: 19 JUN 2024 21:51:37

BAND EDGES*EUT operation mode: Transmitting & Hopping**Test Result: Compliant.*

Mode	Channel	Frequency (MHz)	Result (dBc)	Limit (dBc)
GFSK	Low	2402	51.76	20
	High	2480	52.05	
$\pi/4$ DQPSK	Low	2402	50.19	
	High	2480	51.94	
8DPSK	Low	2402	50.52	
	High	2480	51.09	
GFSK (Hopping)	Low	2402	52.44	
	High	2480	52.84	
$\pi/4$ DQPSK (Hopping)	Low	2402	52.16	
	High	2480	52.56	
8DPSK (Hopping)	Low	2402	50.94	
	High	2480	52.46	

Band Edge

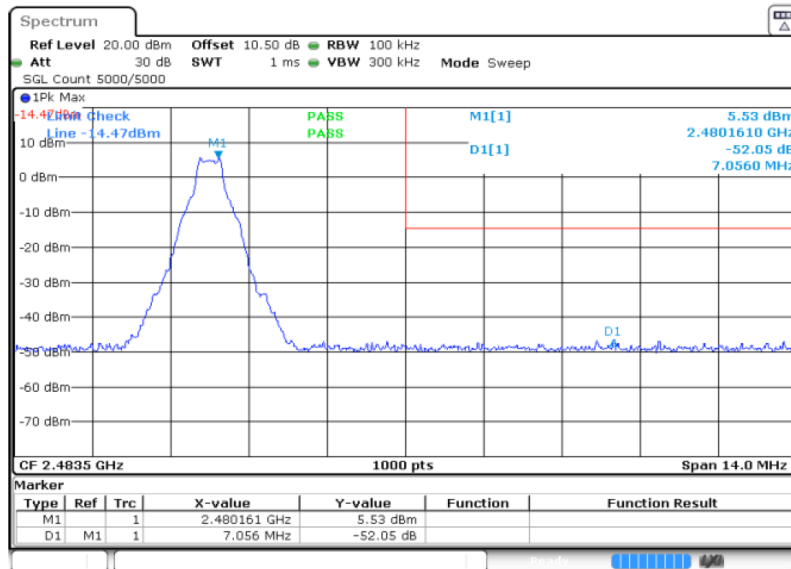
BDR (GFSK): Left Side



ProjectNo.: RSHA240530002 Tester: Jason Lu

Date: 19 JUN 2024 19:33:57

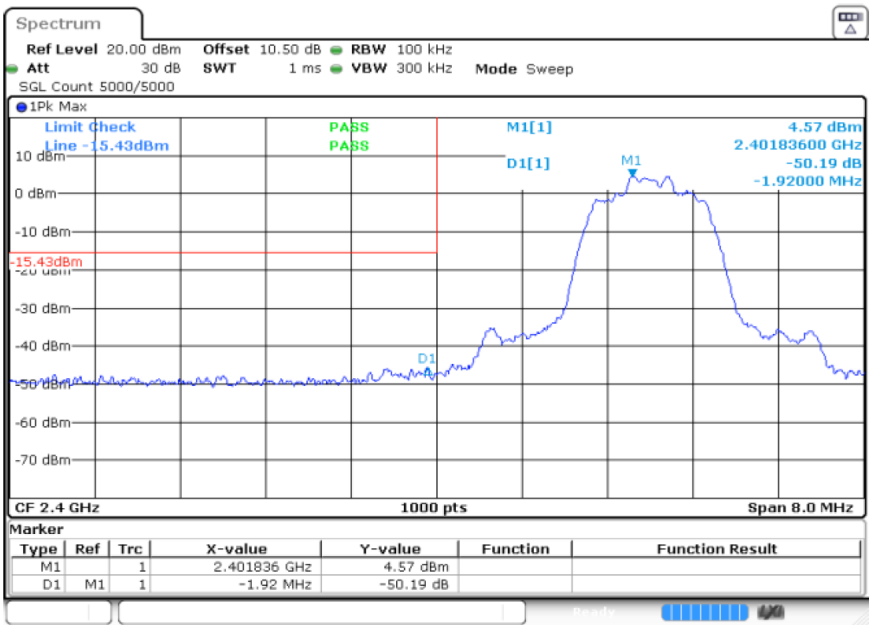
BDR (GFSK): Right Side



ProjectNo.: RSHA240530002 Tester: Jason Lu

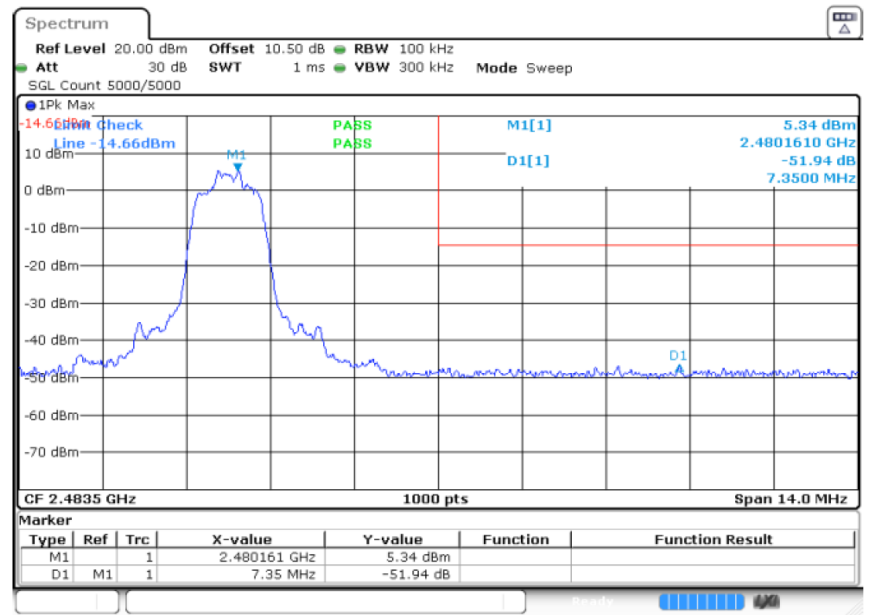
Date: 19 JUN 2024 20:03:16

EDR ($\pi/4$ -DQPSK): Left Side



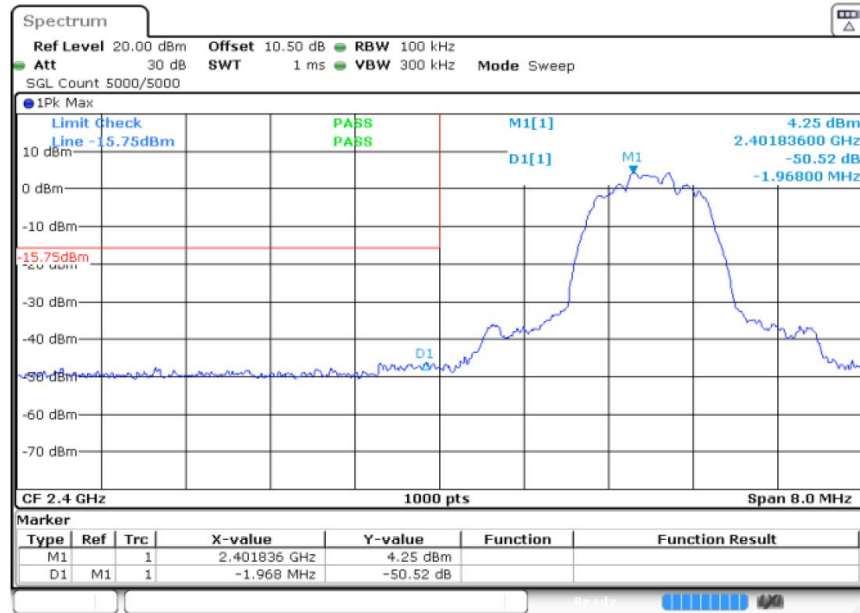
ProjectNo.: RSHA240530002 Tester: Jason Lu
Date: 19 JUN. 2024 21:04:30

EDR ($\pi/4$ -DQPSK): Right Side



ProjectNo.: RSHA240530002 Tester: Jason Lu
Date: 19 JUN. 2024 21:21:38

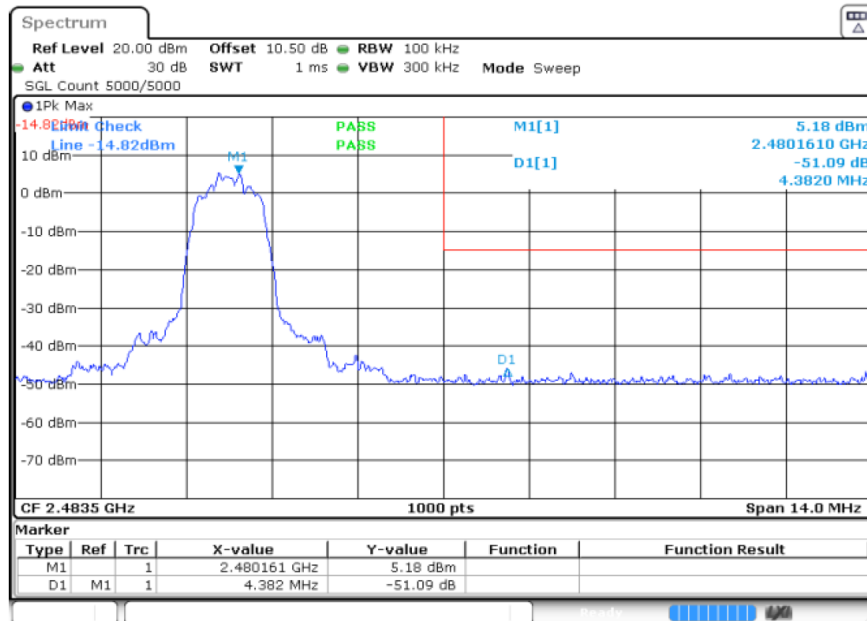
EDR (8DPSK): Left Side



ProjectNo.: RSHA240530002 Tester: Jason Lu

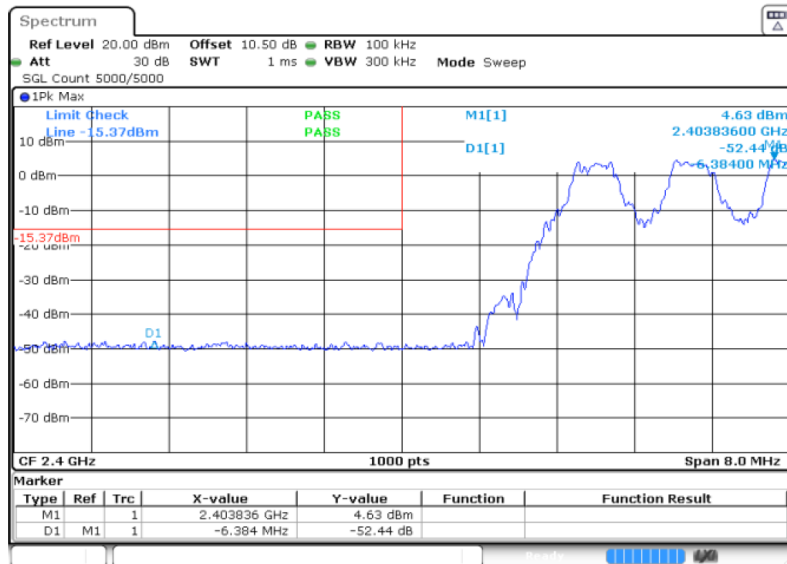
Date: 19 JUN 2024 21:32:43

EDR (8DPSK): Right Side



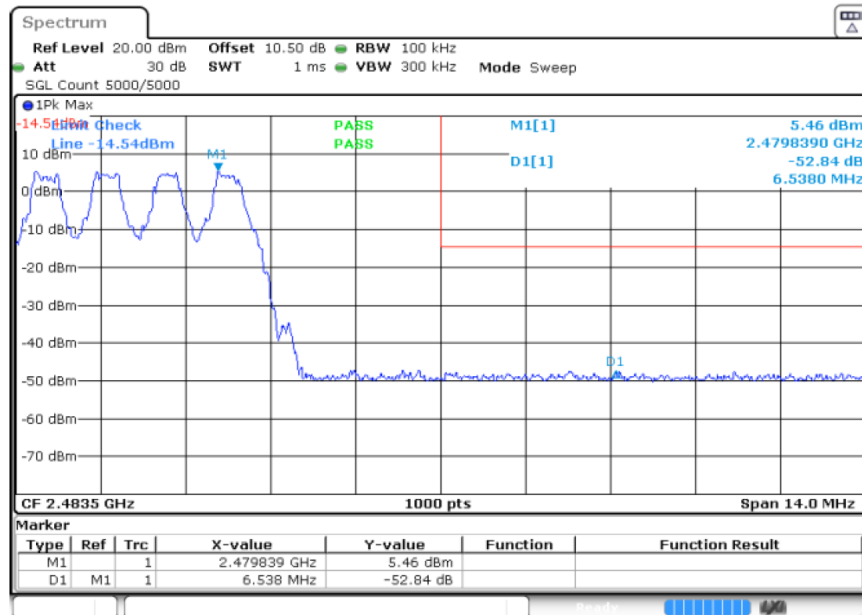
ProjectNo.: RSHA240530002 Tester: Jason Lu

Date: 19 JUN 2024 21:49:20

BDR (GFSK): Left Side - Hopping

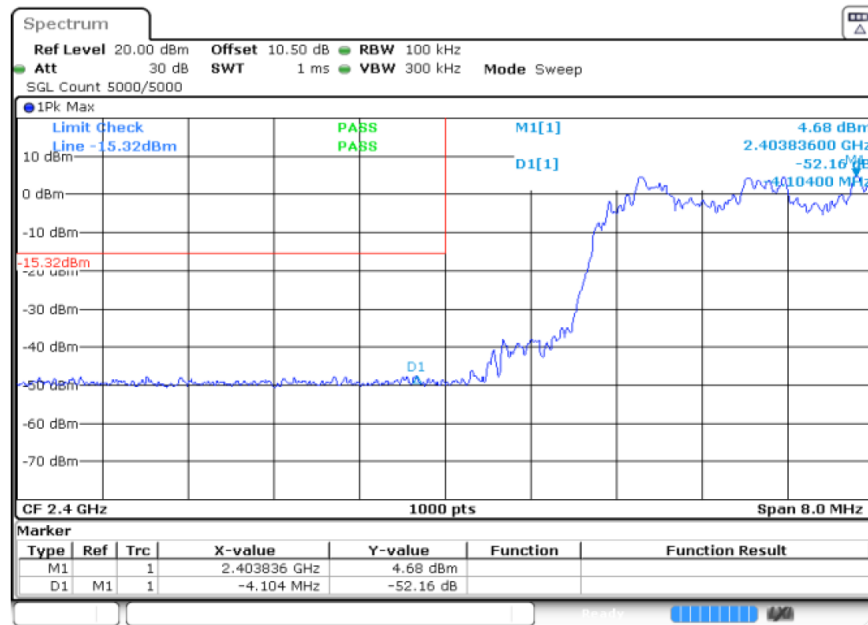
ProjectNo.: RSHA240530002 Tester: Jason Lu

Date: 20 JUN 2024 13:44:45

BDR (GFSK): Right Side- Hopping

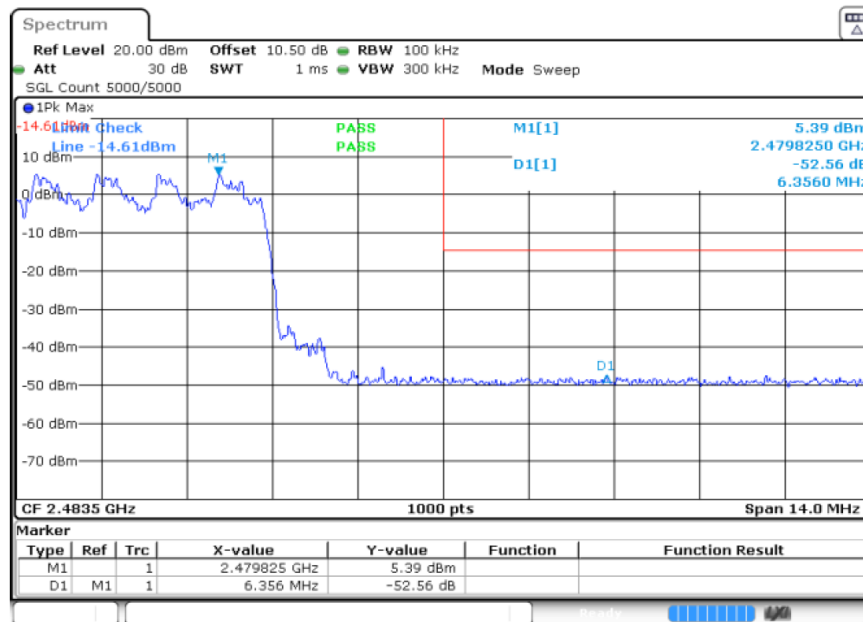
ProjectNo.: RSHA240530002 Tester: Jason Lu

Date: 20 JUN 2024 13:46:11

EDR ($\pi/4$ -DQPSK): Left Side- Hopping

ProjectNo.: RSHA240530002 Tester: Jason Lu

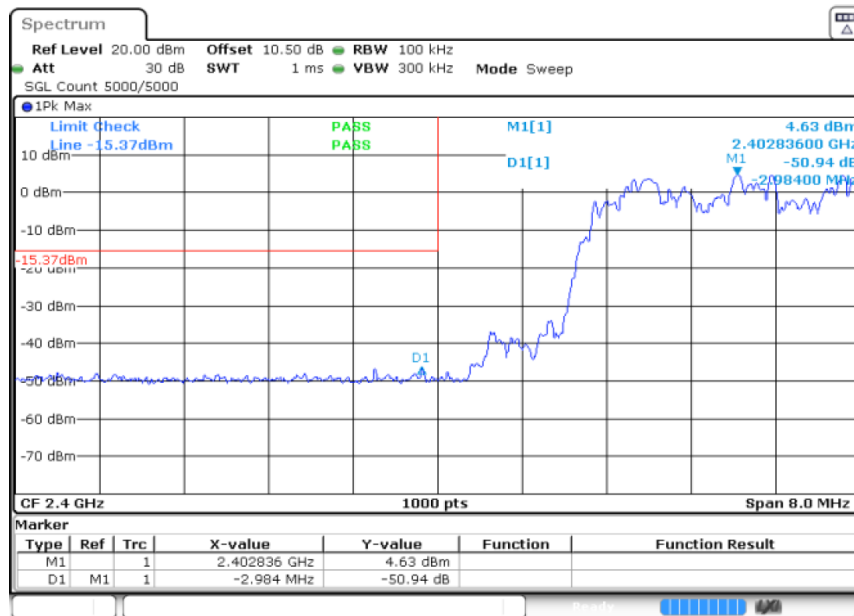
Date: 20 JUN 2024 13:48:17

EDR ($\pi/4$ -DQPSK): Right Side- Hopping

ProjectNo.: RSHA240530002 Tester: Jason Lu

Date: 20 JUN 2024 13:49:30

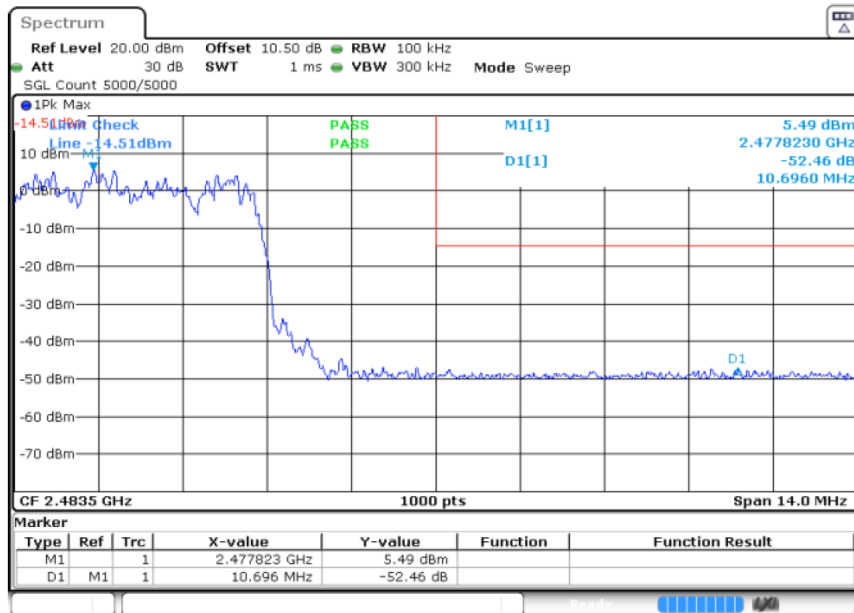
EDR (8DPSK): Left Side- Hopping



ProjectNo.: RSHA240530002 Tester: Jason Lu

Date: 20 JUN 2024 13:52:00

EDR (8DPSK): Right Side- Hopping



ProjectNo.: RSHA240530002 Tester: Jason Lu

Date: 20 JUN 2024 13:53:14

EUT PHOTOGRAPHS

Please refer to the attachment EXHIBIT A-EUT EXTERNAL PHOTOGRAPHS and EXHIBIT B-EUT INTERNAL PHOTOGRAPHS.

TEST SETUP PHOTOGRAPHS

Please refer to the attachment EXHIBIT C-TEST SETUP PHOTOGRAPHS.

Declarations

1. The laboratory is not responsible for the authenticity of any information provided by the applicant. Information from the applicant that may affect test results is marked with “★”.
2. The test data was only valid for the test sample(s).
3. This report is valid only with a valid digital signature. The digital signature may be available only under the Adobe software above version 7.0.
4. Otherwise required by the applicant or Product Regulations, Decision Rule in this report did not consider the uncertainty.
5. The extended uncertainty given in this report is obtained by combining the standard uncertainty times the coverage factor $k=2$ with the 95.45% confidence interval.

******* END OF REPORT *******