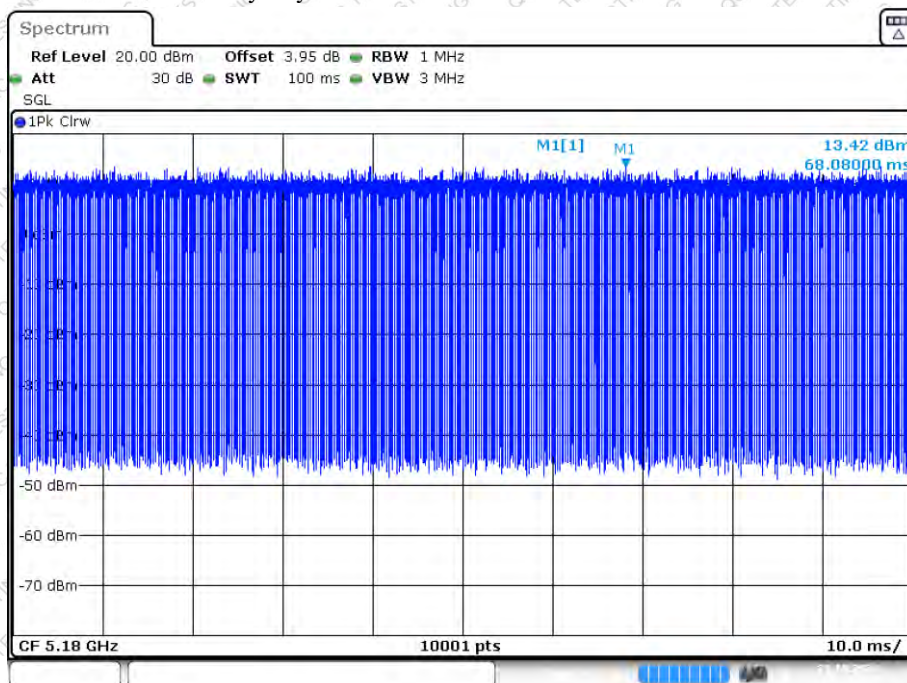


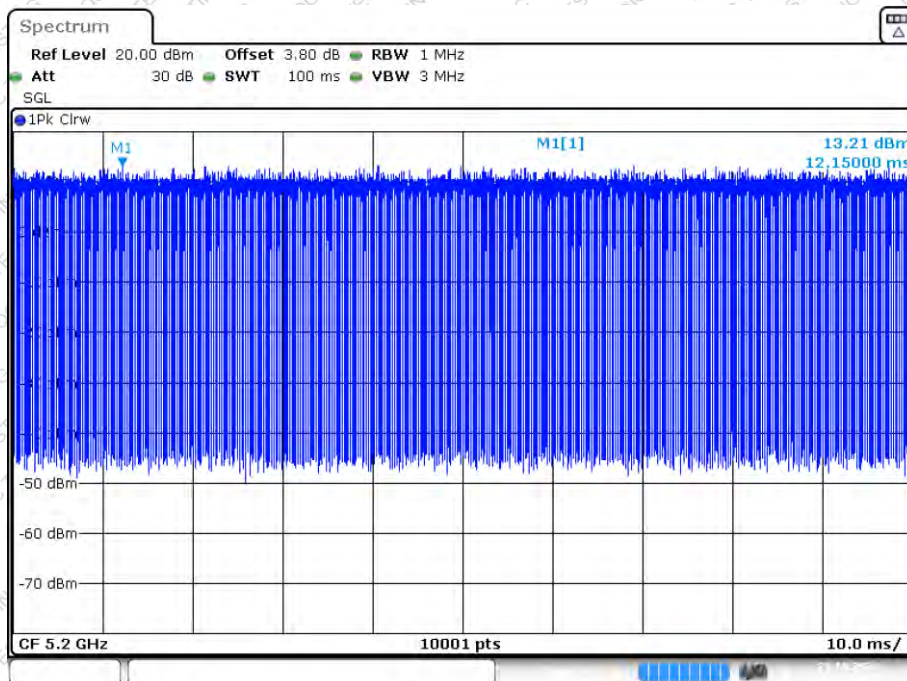


Duty Cycle NVNT ax20 5180MHz Ant1



Date: 22.OCT.2024 11:00:00

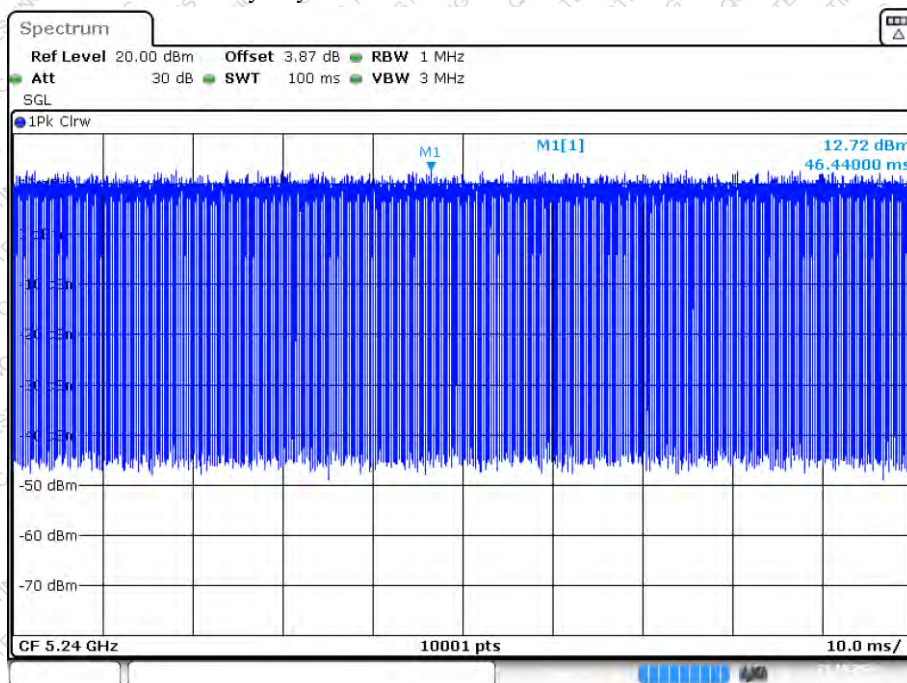
Duty Cycle NVNT ax20 5200MHz Ant1



Date: 23.OCT.2024 16:37:43

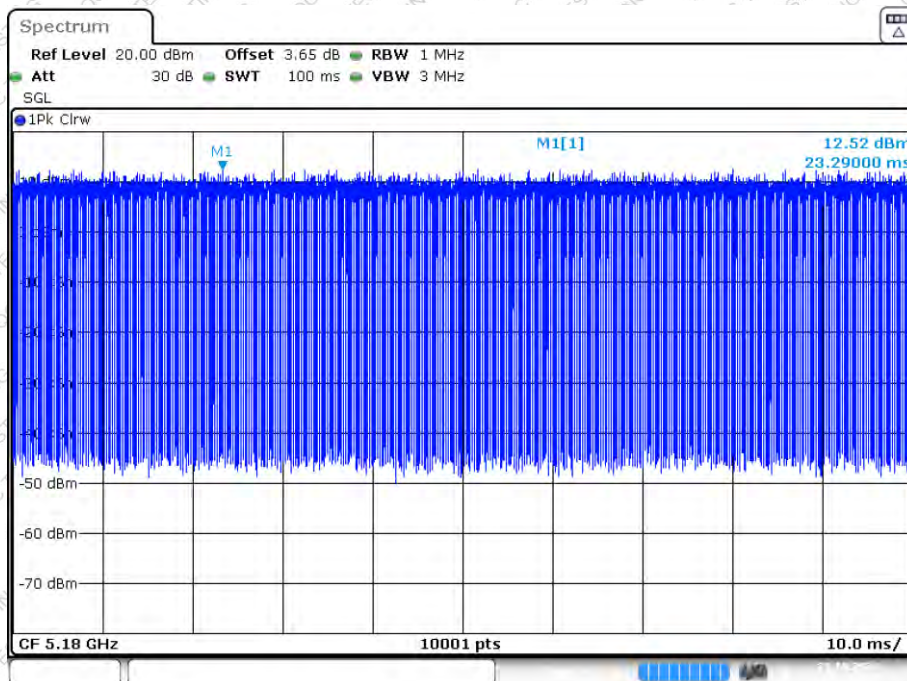


Duty Cycle NVNT ax20 5240MHz Ant1



Date: 22.OCT.2024 11:05:25

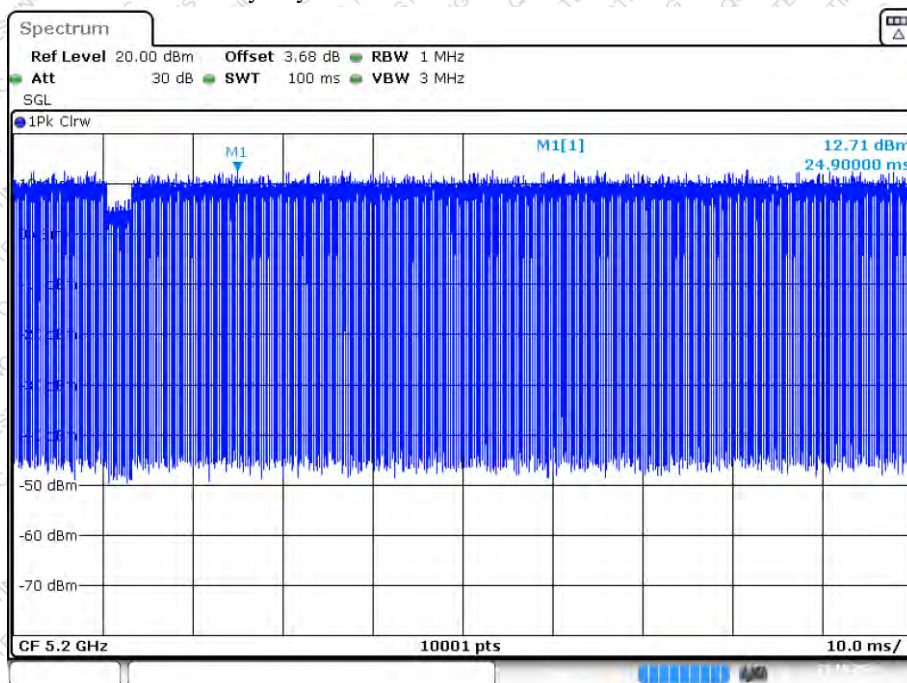
Duty Cycle NVNT ax20 5180MHz Ant2



Date: 22.OCT.2024 18:44:14

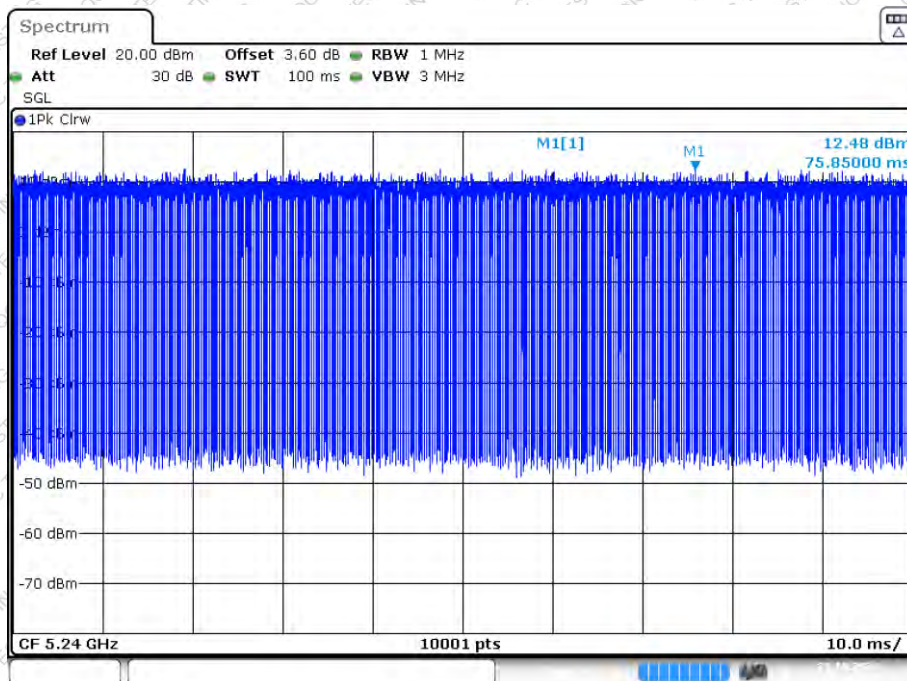


Duty Cycle NVNT ax20 5200MHz Ant2



Date: 23.OCT.2024 17:49:01

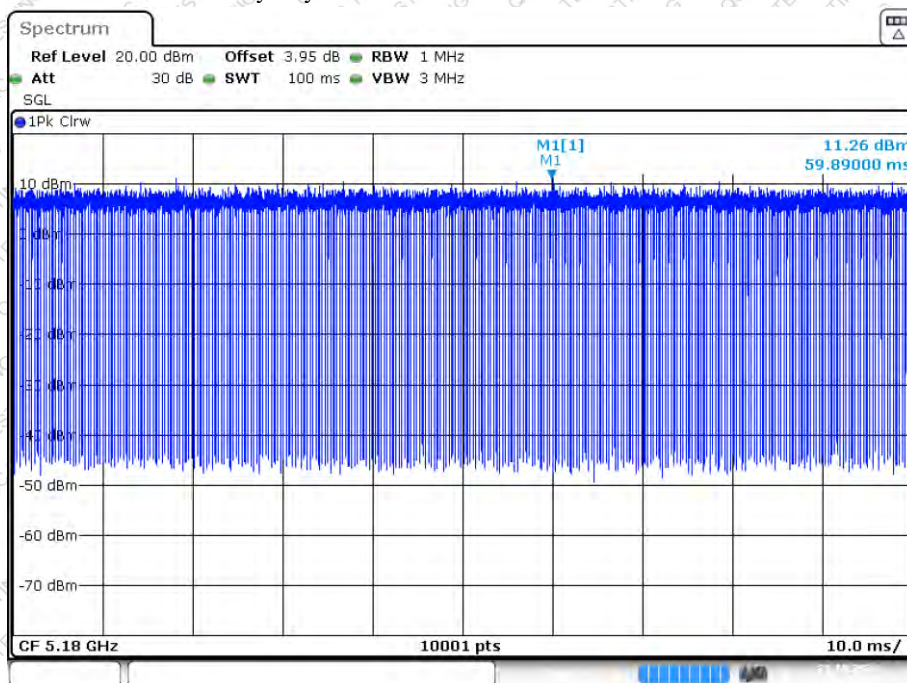
Duty Cycle NVNT ax20 5240MHz Ant2



Date: 22.OCT.2024 18:45:28

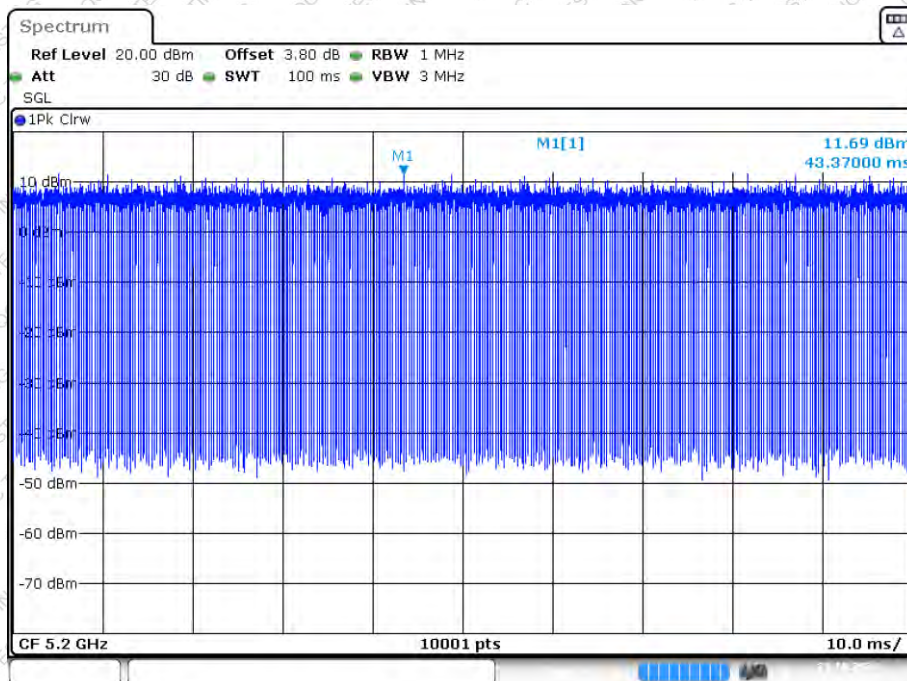


Duty Cycle NVNT ax20 5180MHz Sum



Date: 22.OCT.2024 19:34:32

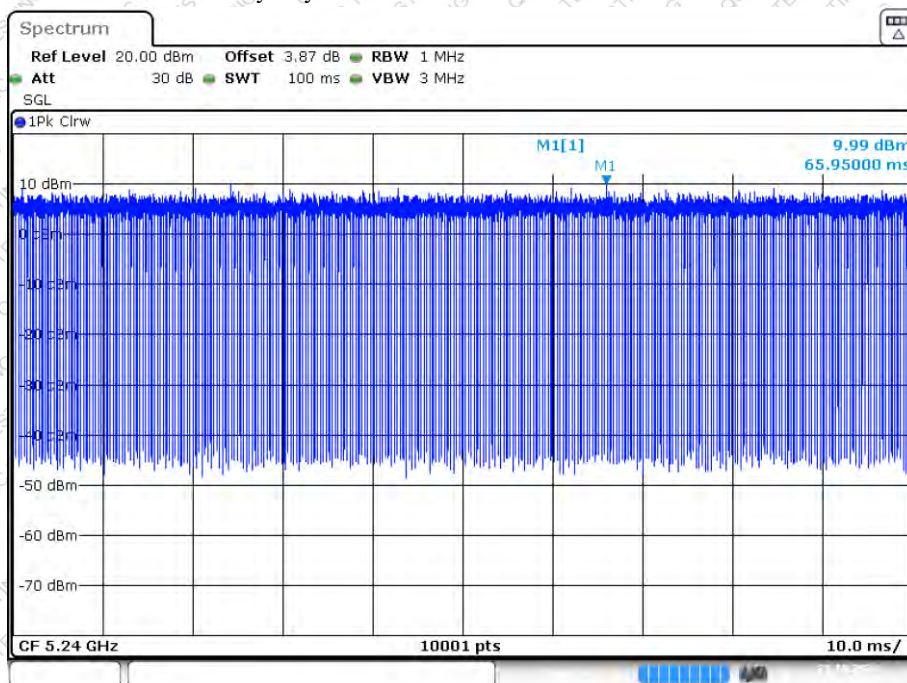
Duty Cycle NVNT ax20 5200MHz Sum



Date: 23.OCT.2024 19:50:59

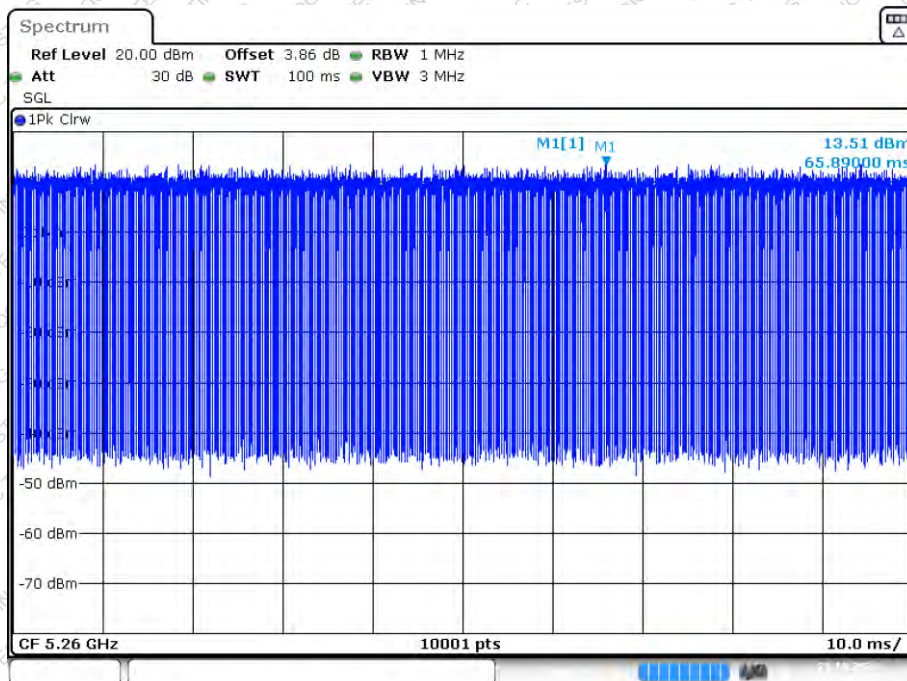


Duty Cycle NVNT ax20 5240MHz Sum



Date: 22.OCT.2024 19:36:33

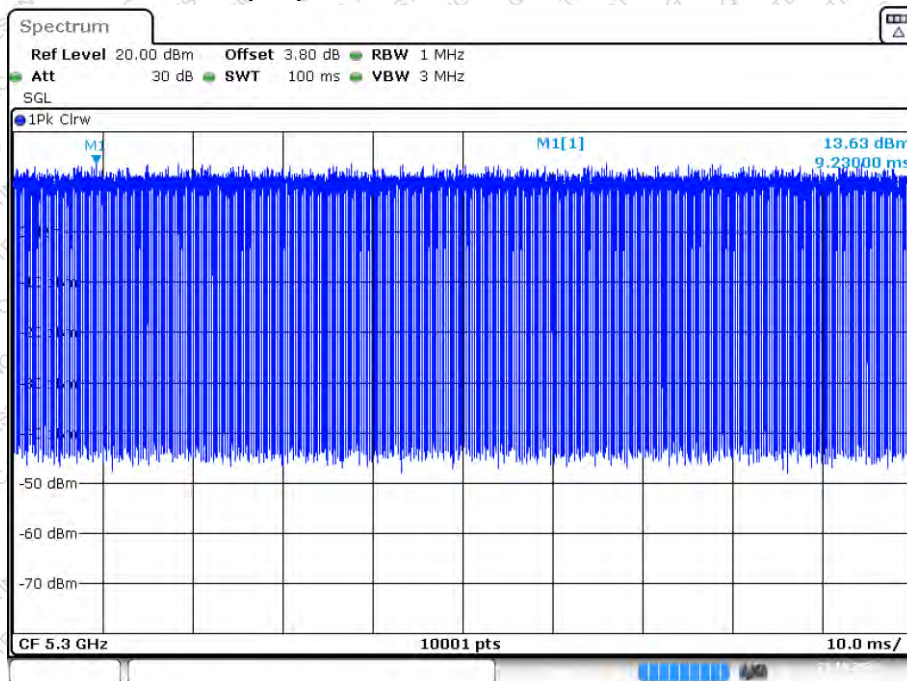
Duty Cycle NVNT ax20 5260MHz Ant1



Date: 23.OCT.2024 16:42:07

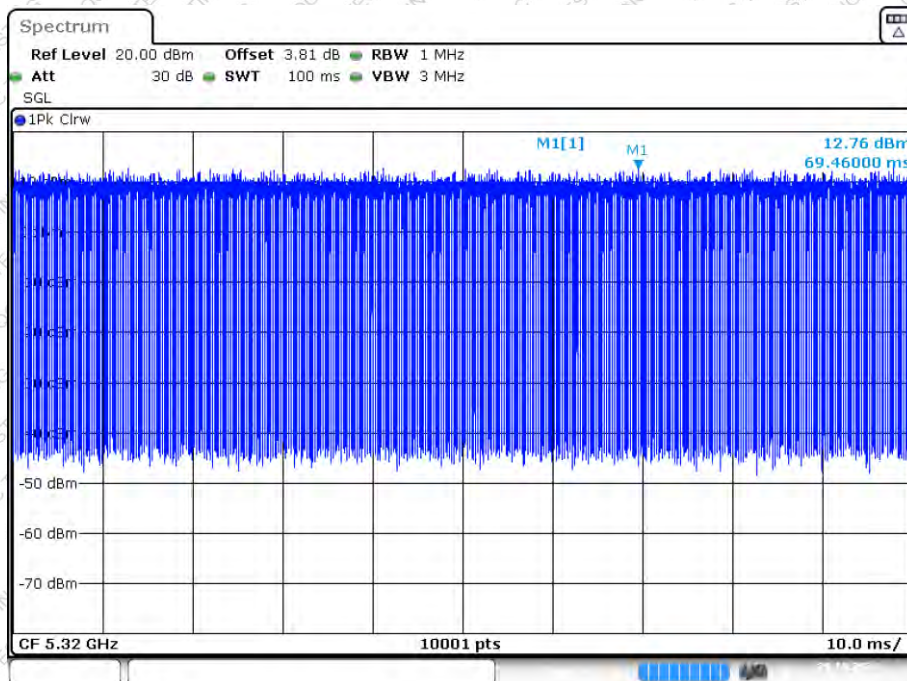


Duty Cycle NVNT ax20 5300MHz Ant1



Date: 23.OCT.2024 16:42:57

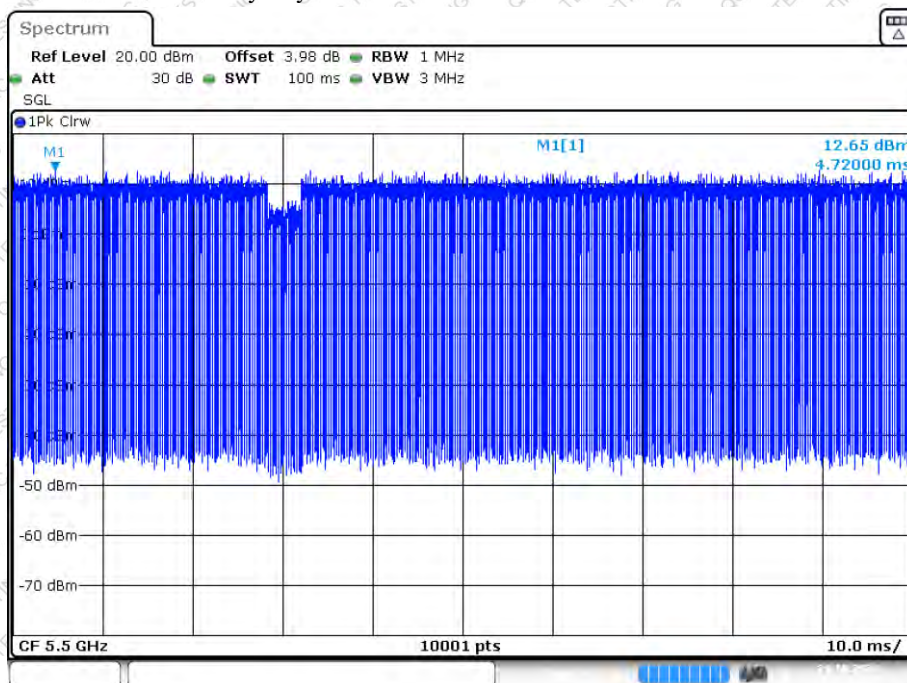
Duty Cycle NVNT ax20 5320MHz Ant1



Date: 28.OCT.2024 08:00:26

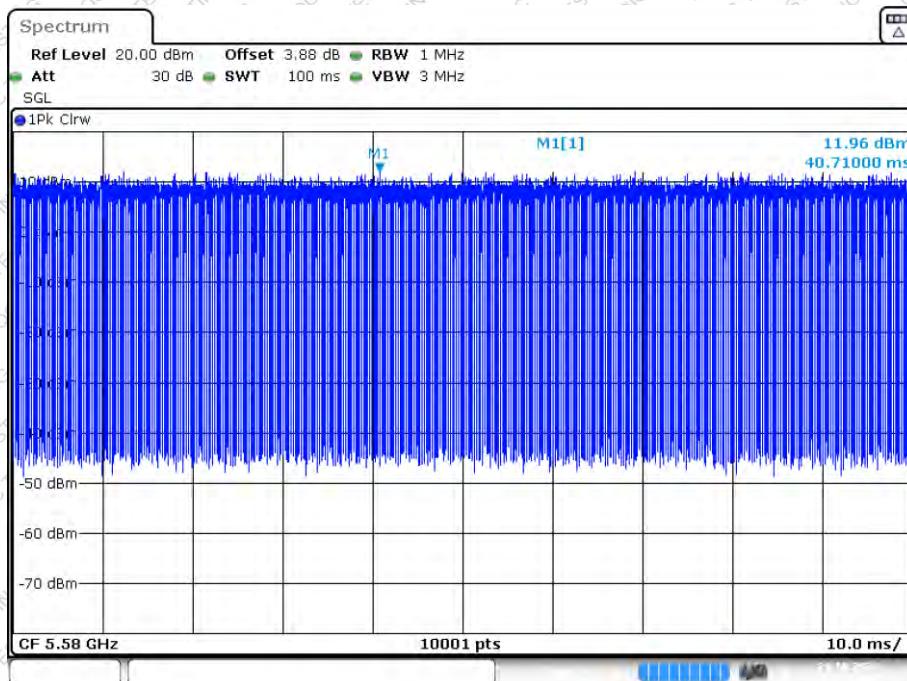


Duty Cycle NVNT ax20 5500MHz Ant1



Date: 24.OCT.2024 17:20:55

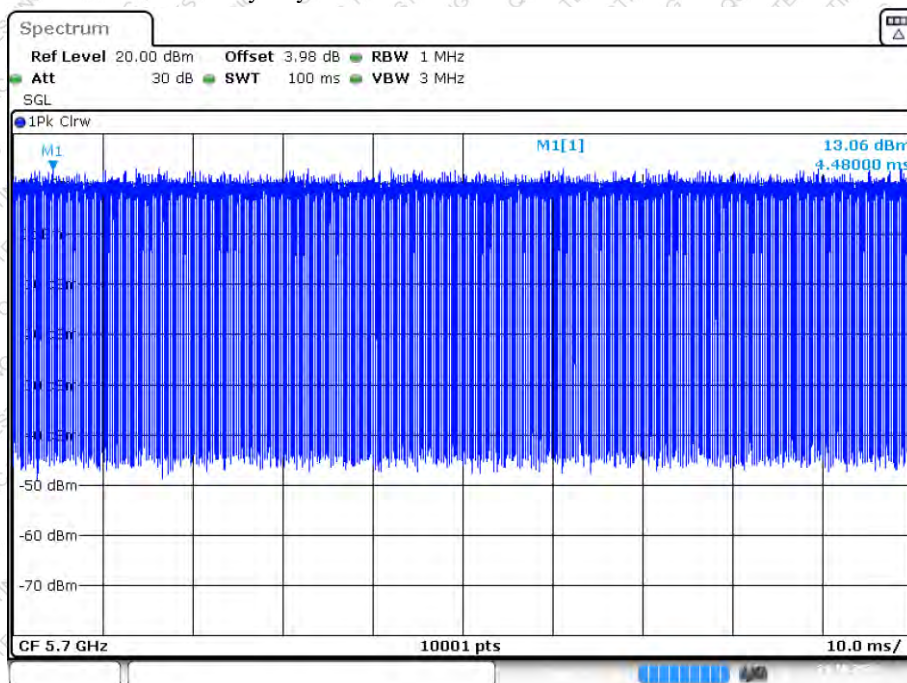
Duty Cycle NVNT ax20 5580MHz Ant1



Date: 24.OCT.2024 17:18:49

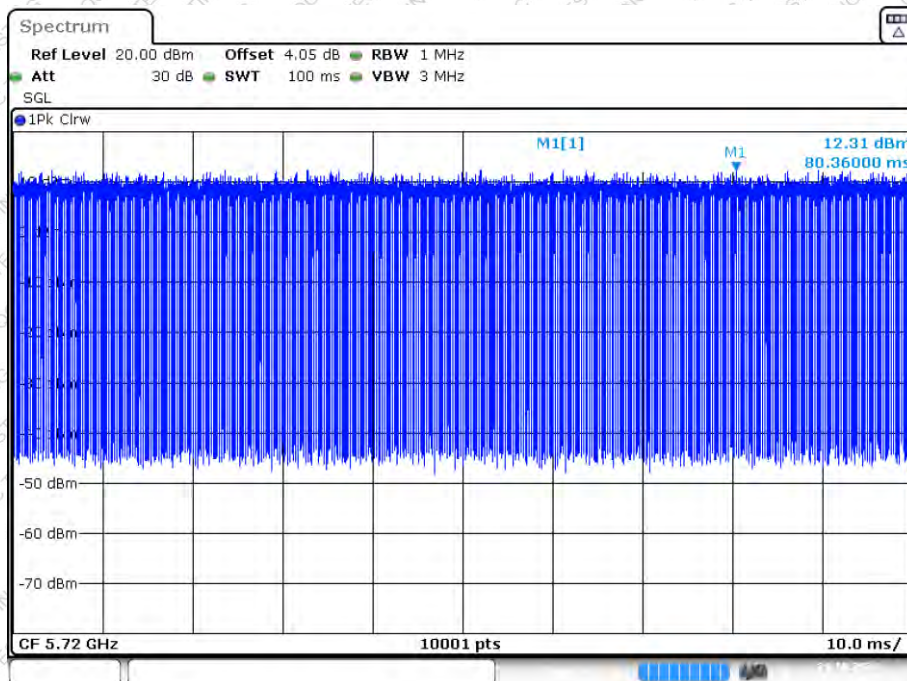


Duty Cycle NVNT ax20 5700MHz Ant1



Date: 24.OCT.2024 17:16:37

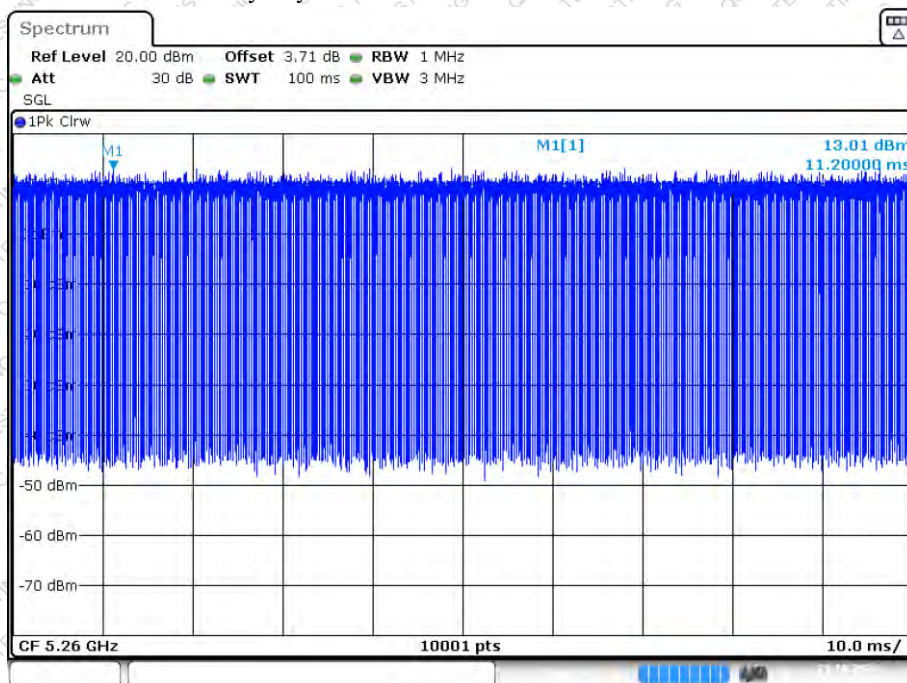
Duty Cycle NVNT ax20 5720MHz Ant1



Date: 24.OCT.2024 17:24:00

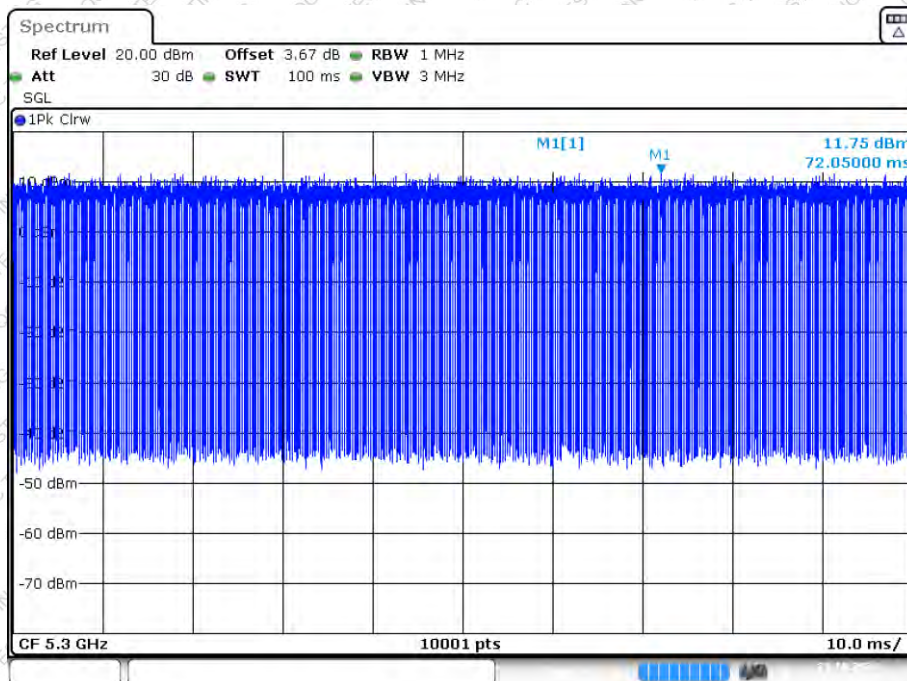


Duty Cycle NVNT ax20 5260MHz Ant2



Date: 23.OCT.2024 17:50:22

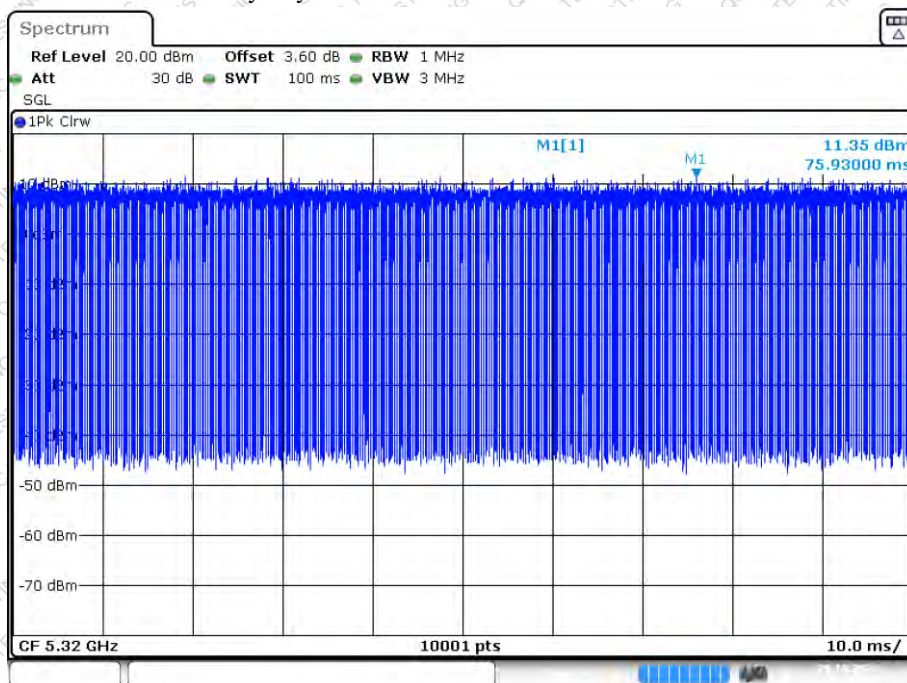
Duty Cycle NVNT ax20 5300MHz Ant2



Date: 23.OCT.2024 17:53:32

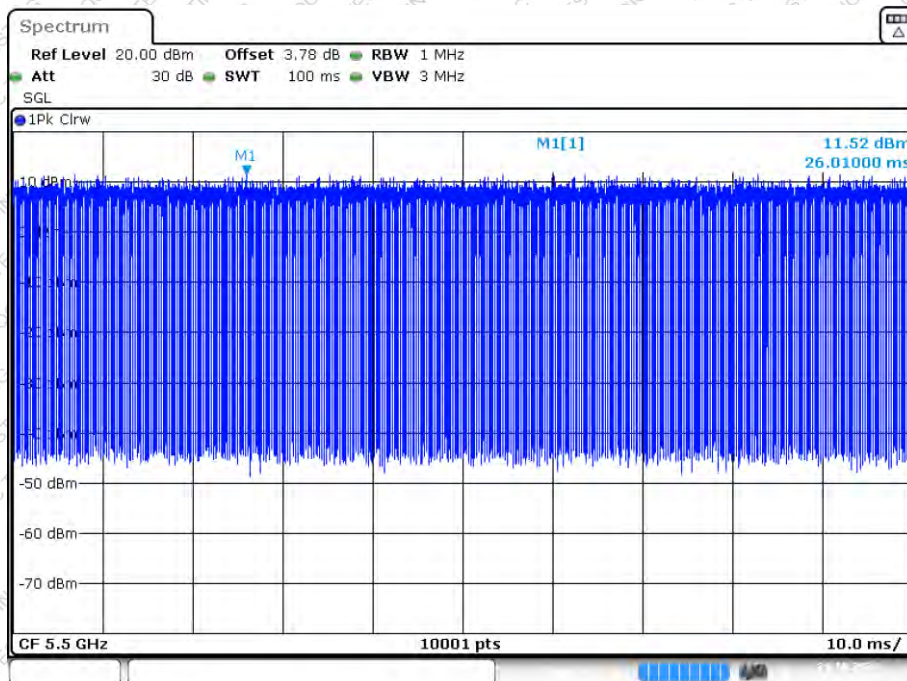


Duty Cycle NVNT ax20 5320MHz Ant2



Date: 28.OCT.2024 08:09:30

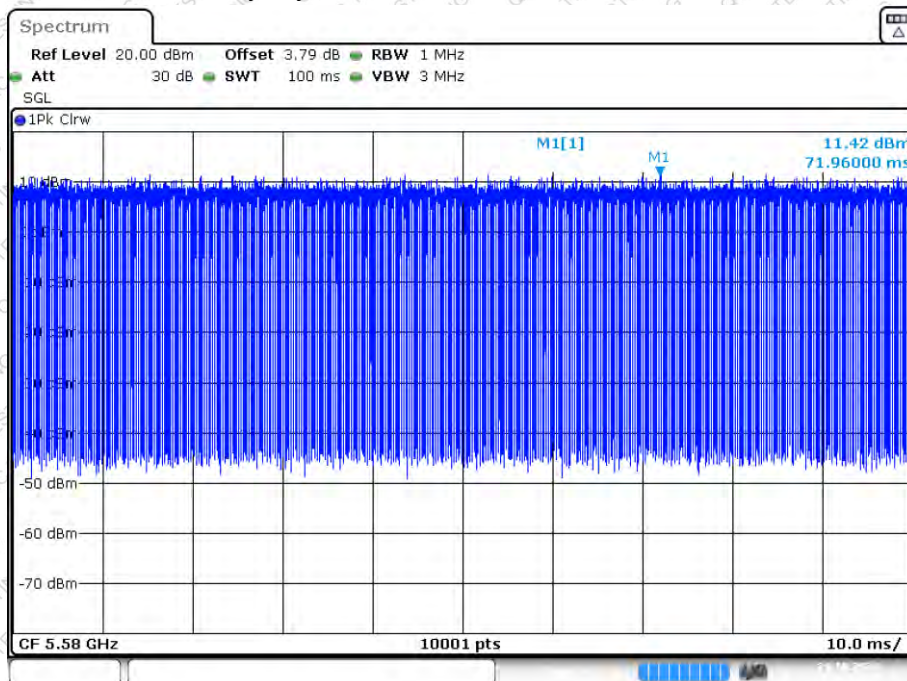
Duty Cycle NVNT ax20 5500MHz Ant2



Date: 24.OCT.2024 19:21:42

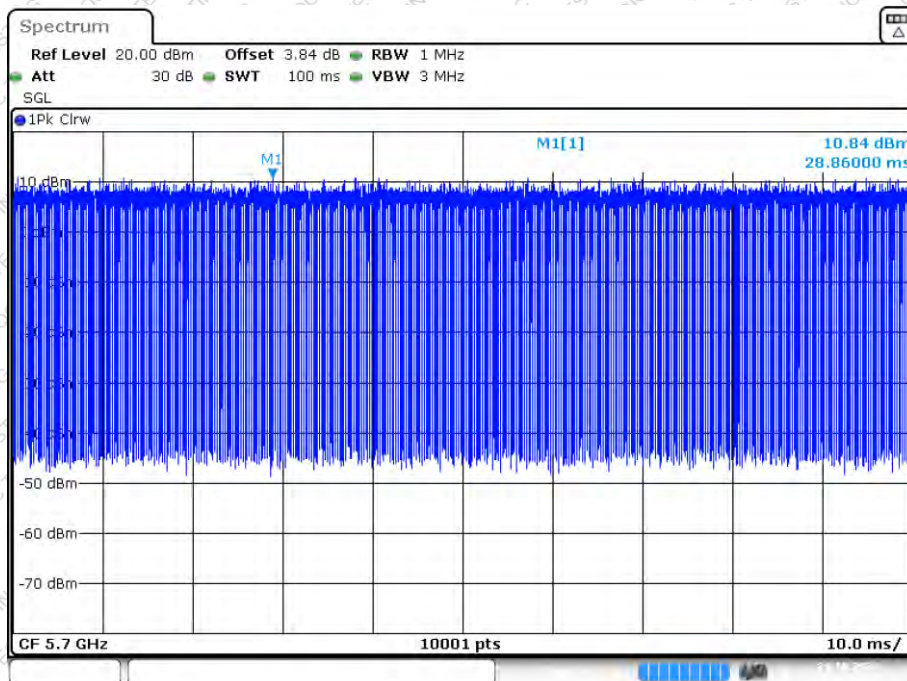


Duty Cycle NVNT ax20 5580MHz Ant2



Date: 24.OCT.2024 19:19:25

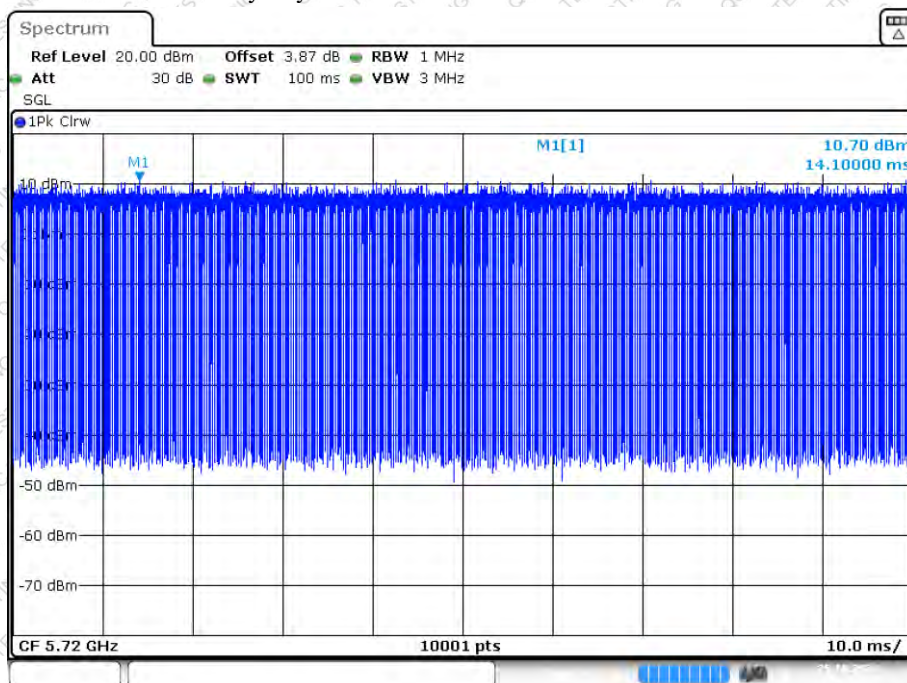
Duty Cycle NVNT ax20 5700MHz Ant2



Date: 24.OCT.2024 19:18:24

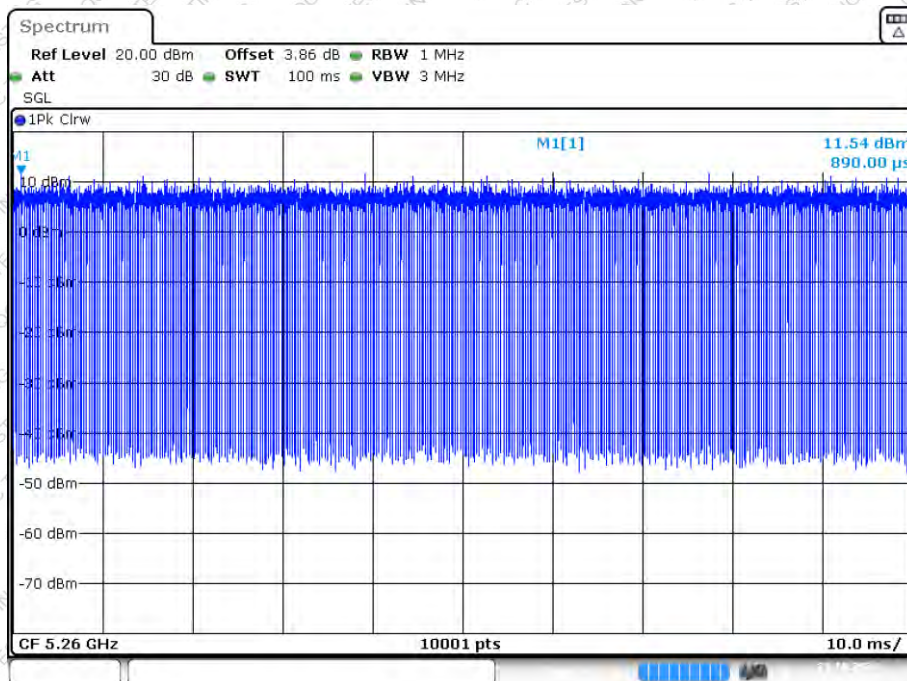


Duty Cycle NVNT ax20 5720MHz Ant2



Date: 25.OCT.2024 08:56:35

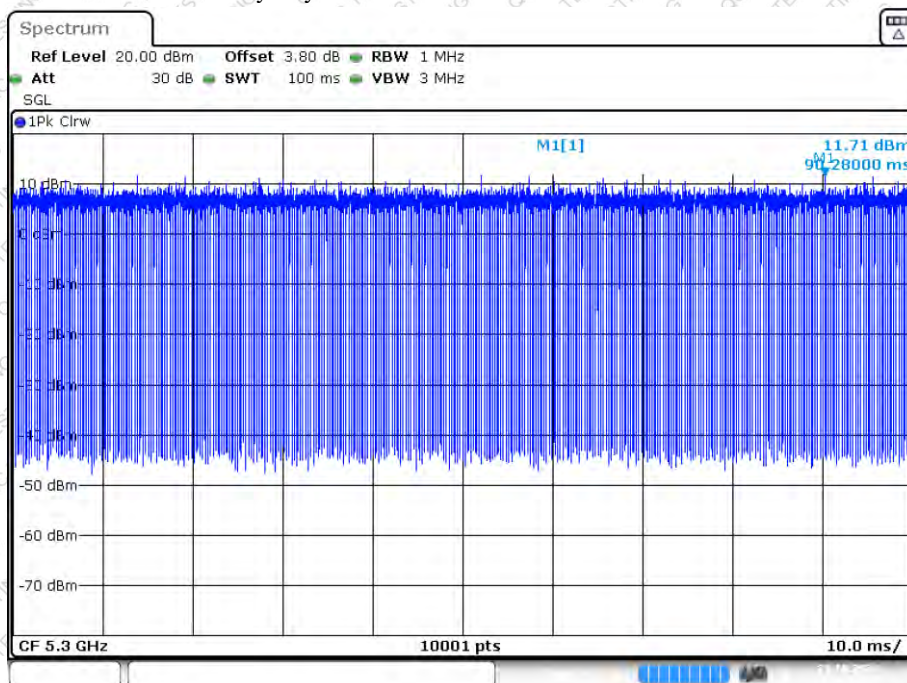
Duty Cycle NVNT ax20 5260MHz Sum



Date: 23.OCT.2024 19:53:12

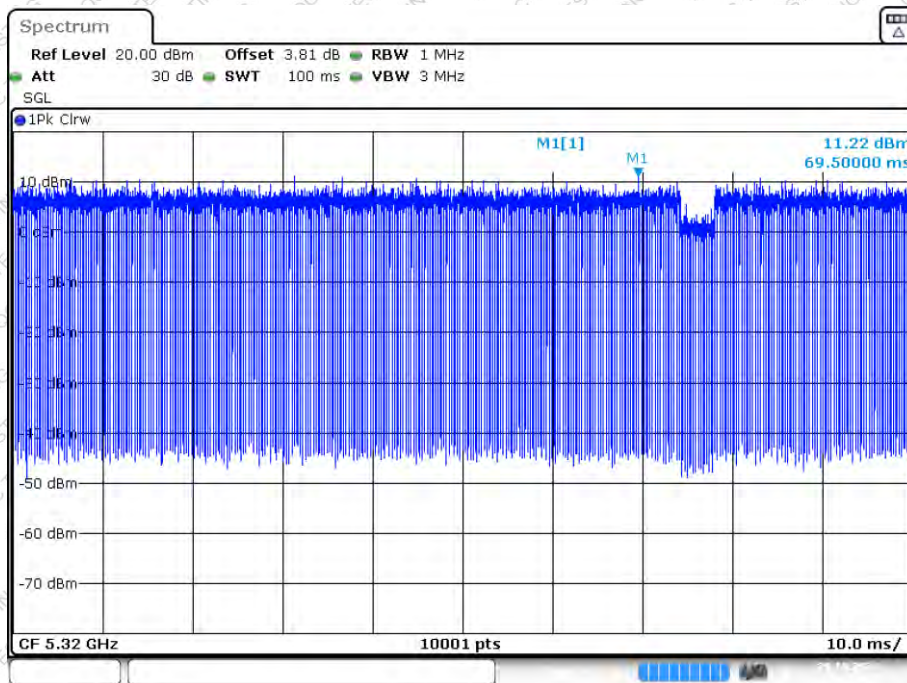


Duty Cycle NVNT ax20 5300MHz Sum



Date: 23.OCT.2024 19:55:18

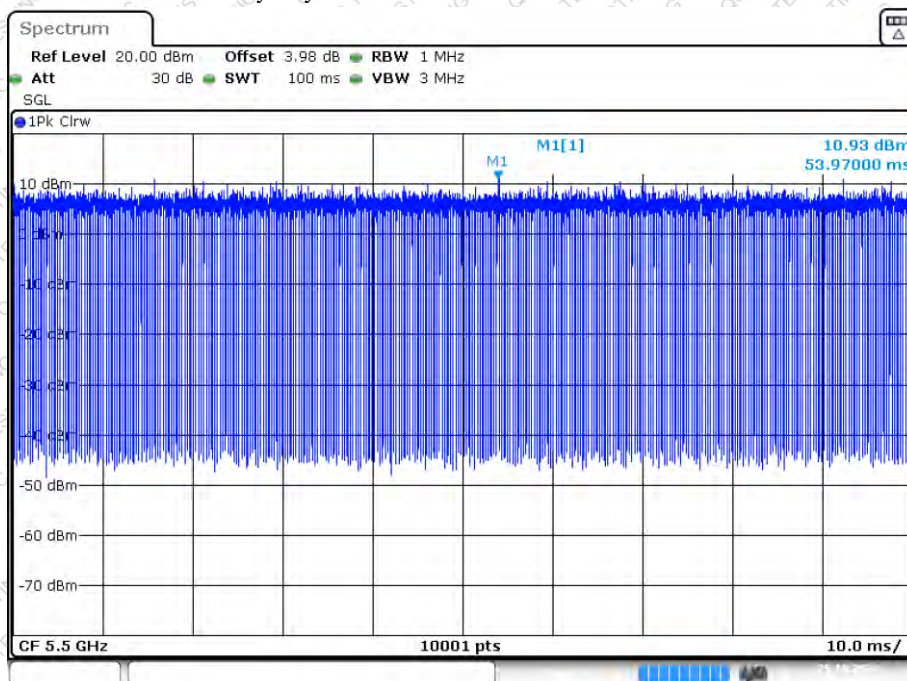
Duty Cycle NVNT ax20 5320MHz Sum



Date: 28.OCT.2024 08:30:43

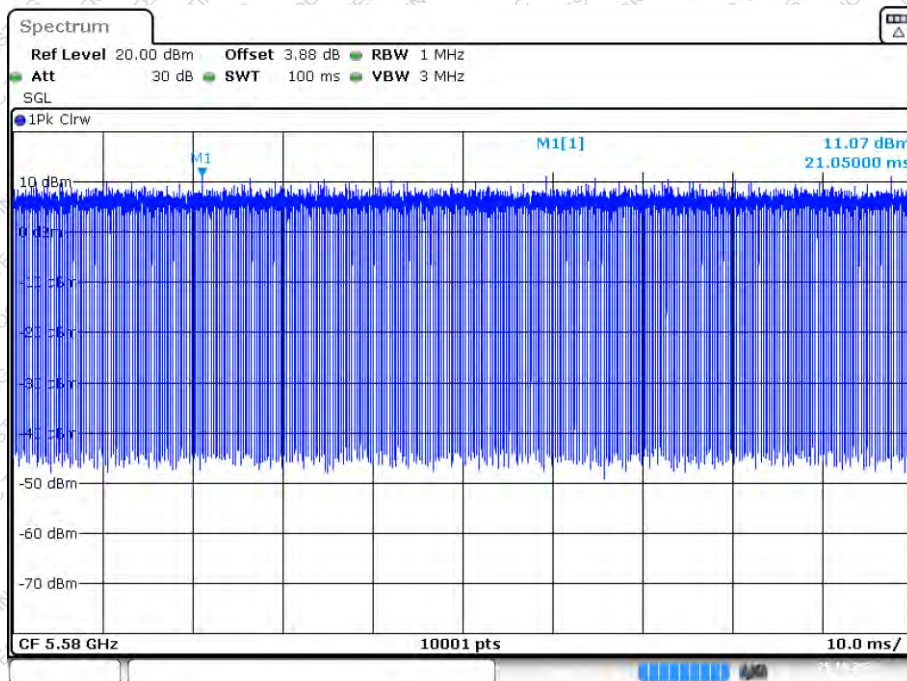


Duty Cycle NVNT ax20 5500MHz Sum



Date: 25.OCT.2024 09:44:54

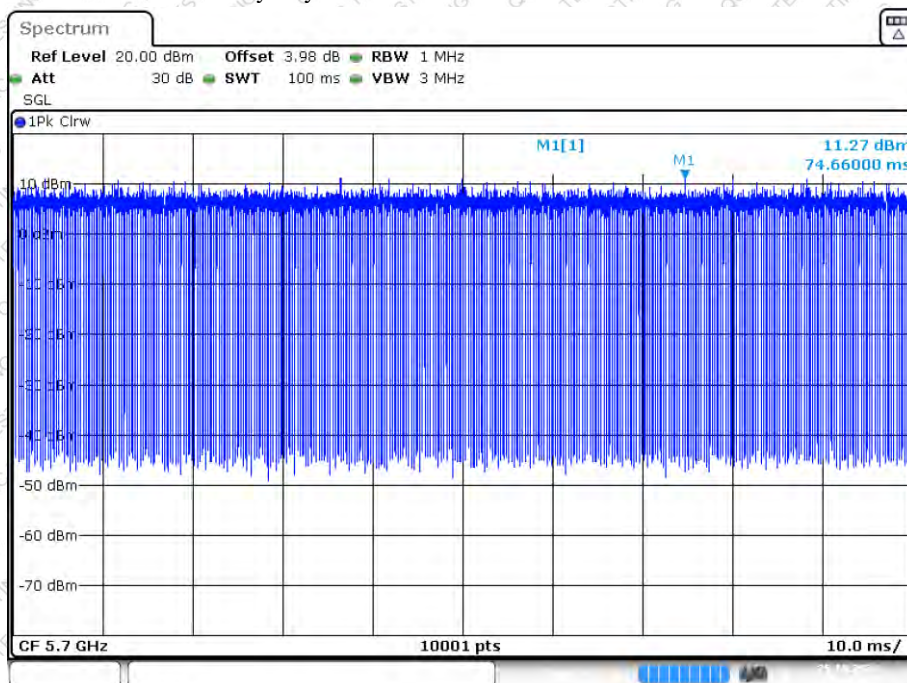
Duty Cycle NVNT ax20 5580MHz Sum



Date: 25.OCT.2024 09:46:20

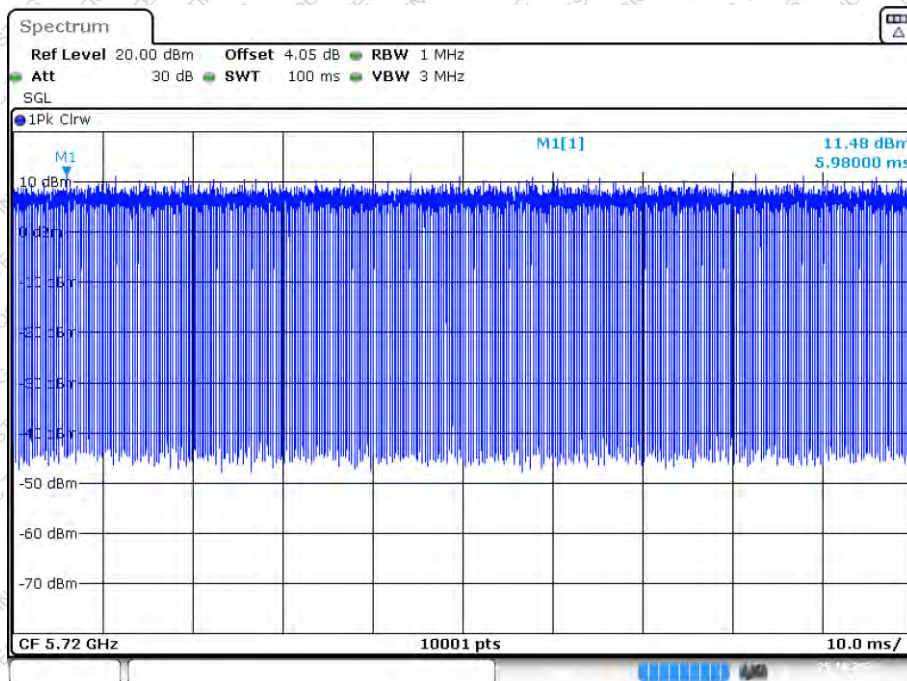


Duty Cycle NVNT ax20 5700MHz Sum



Date: 25.OCT.2024 09:47:42

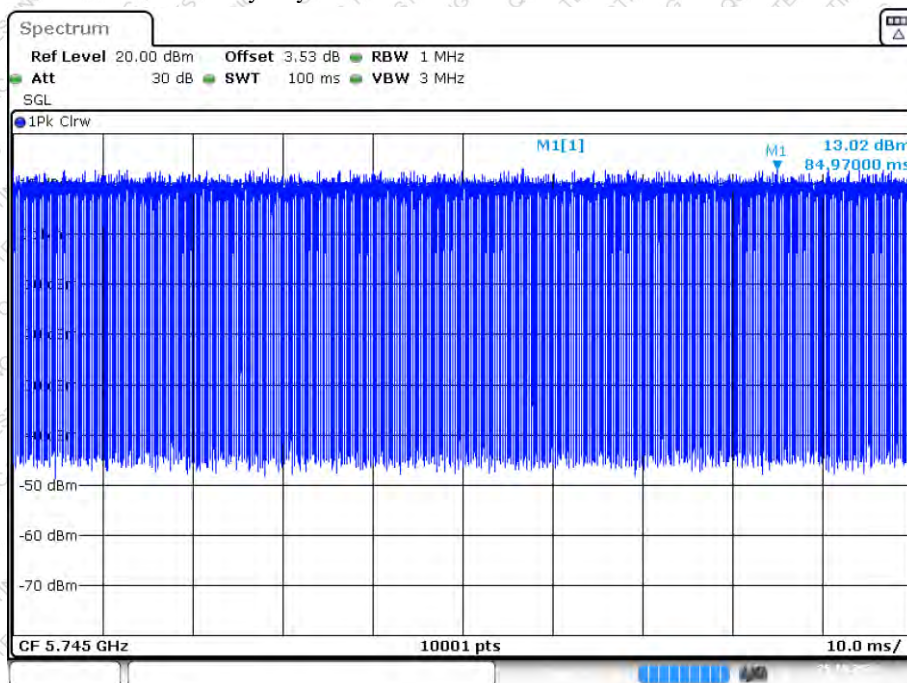
Duty Cycle NVNT ax20 5720MHz Sum



Date: 25.OCT.2024 09:51:19

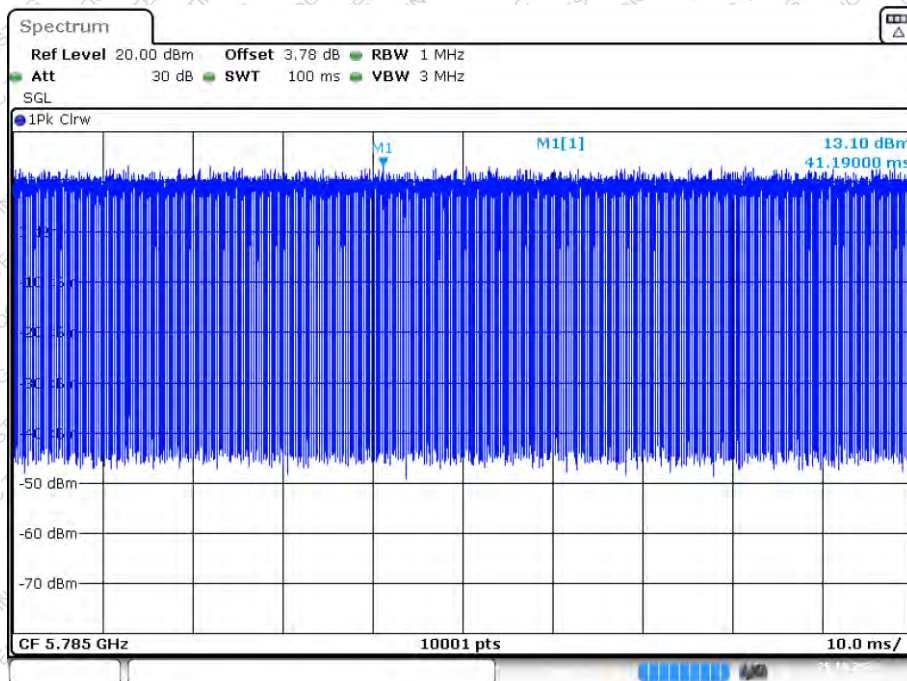


Duty Cycle NVNT ax20 5745MHz Ant1



Date: 25.OCT.2024 11:01:39

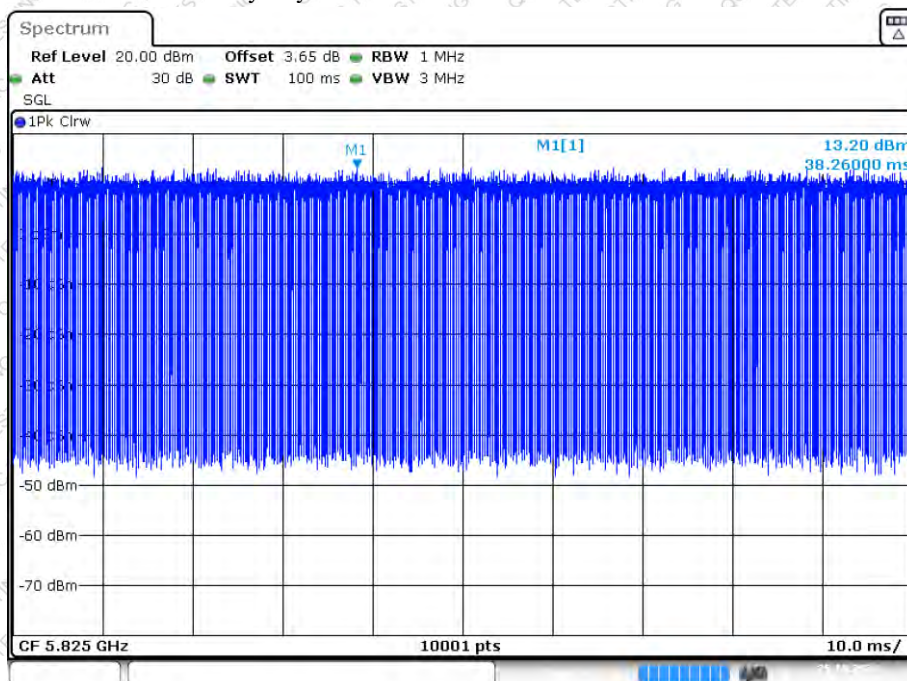
Duty Cycle NVNT ax20 5785MHz Ant1



Date: 25.OCT.2024 11:04:13

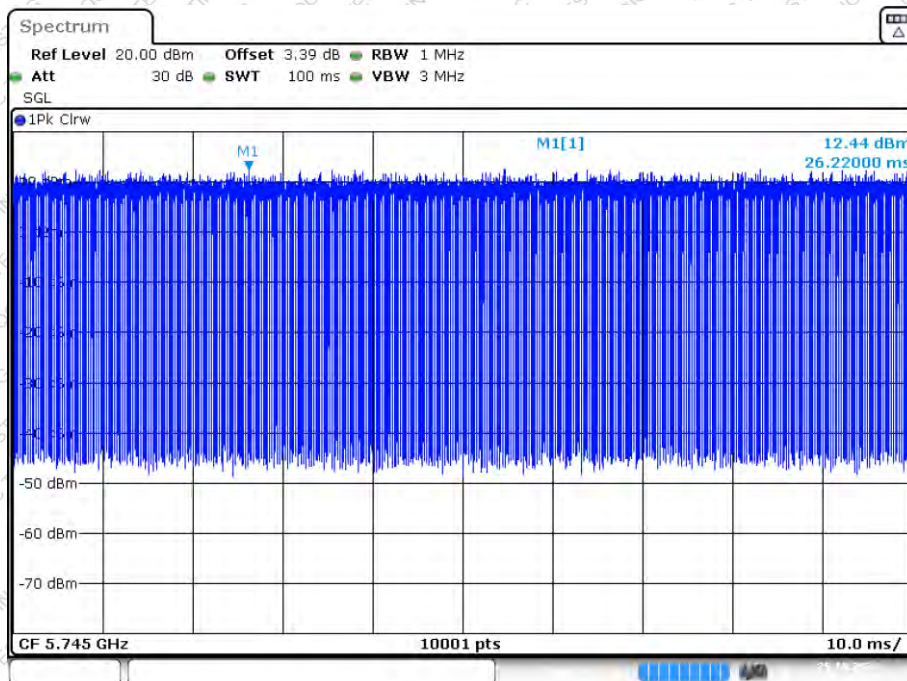


Duty Cycle NVNT ax20 5825MHz Ant1



Date: 25.OCT.2024 11:06:15

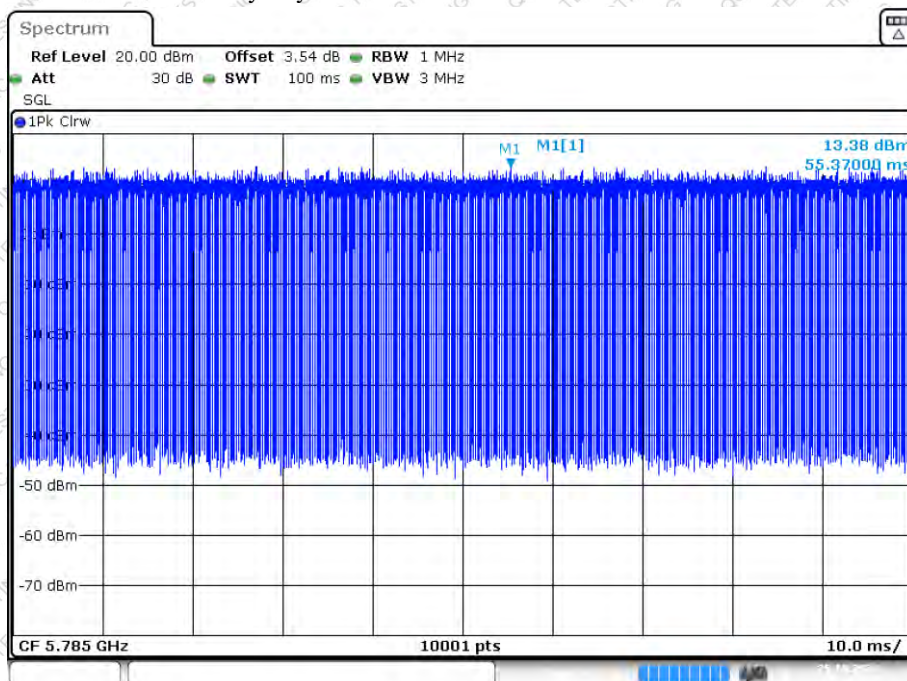
Duty Cycle NVNT ax20 5745MHz Ant2



Date: 25.OCT.2024 13:47:17

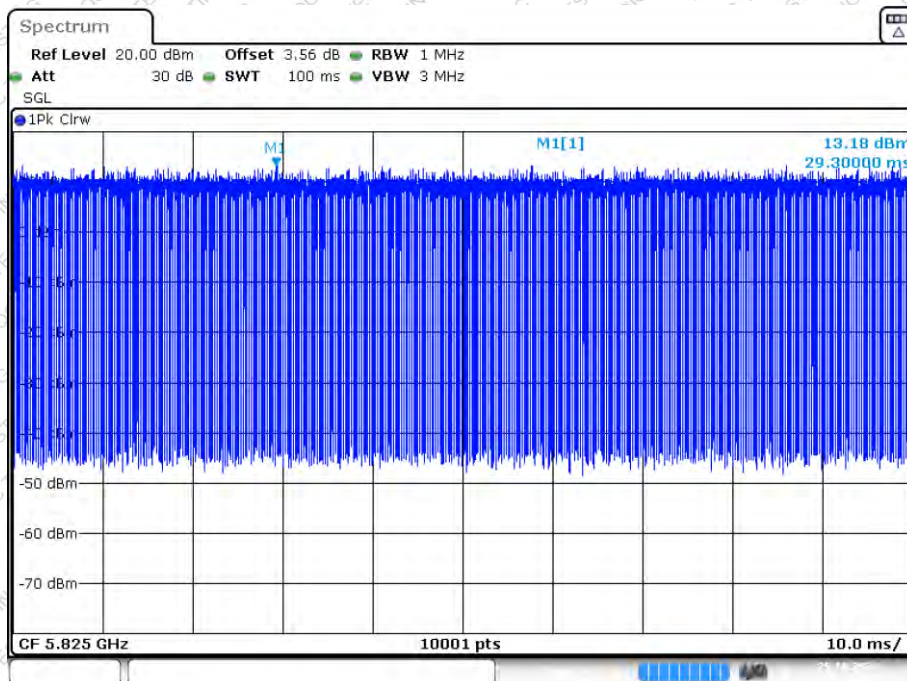


Duty Cycle NVNT ax20 5785MHz Ant2



Date: 25.OCT.2024 13:49:15

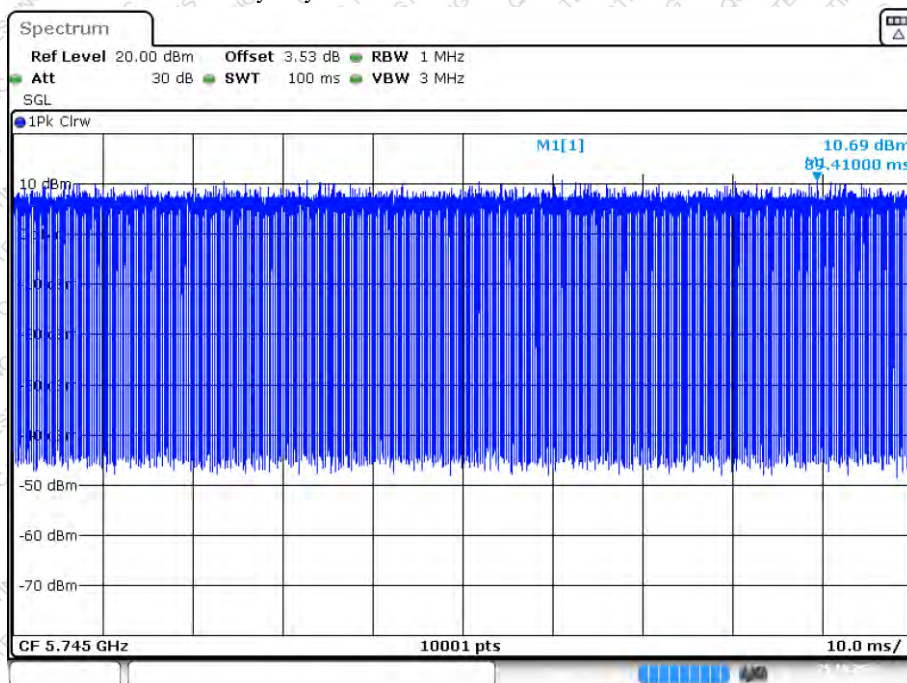
Duty Cycle NVNT ax20 5825MHz Ant2



Date: 25.OCT.2024 13:53:11

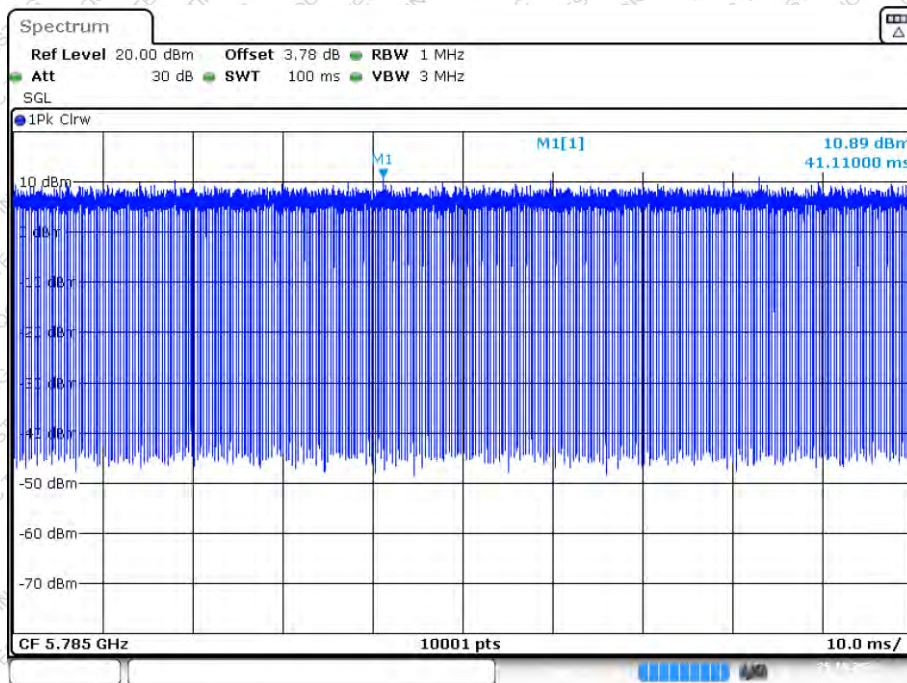


Duty Cycle NVNT ax20 5745MHz Sum



Date: 25.OCT.2024 15:28:15

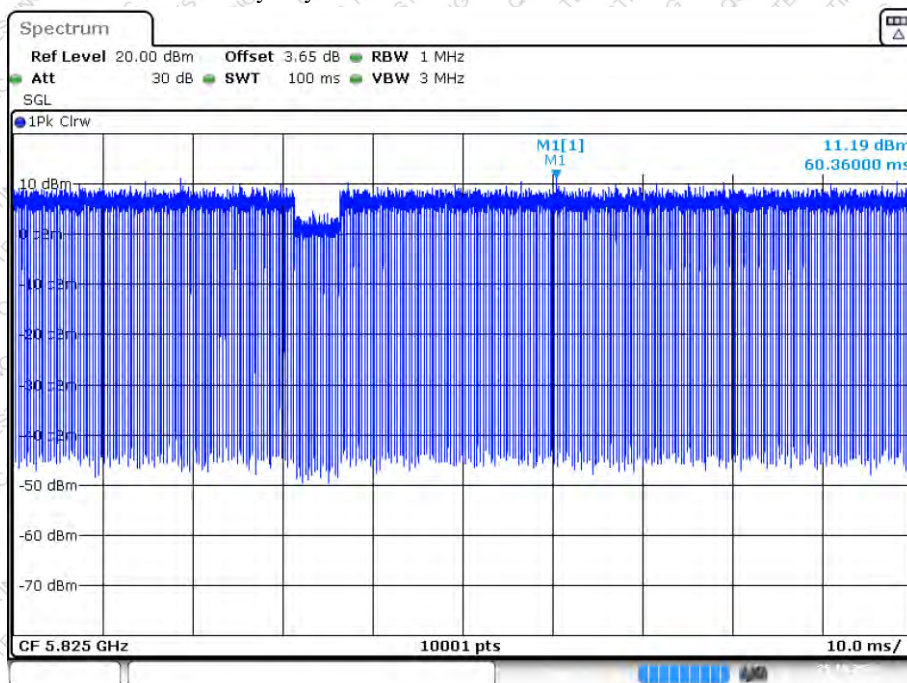
Duty Cycle NVNT ax20 5785MHz Sum



Date: 25.OCT.2024 14:52:55

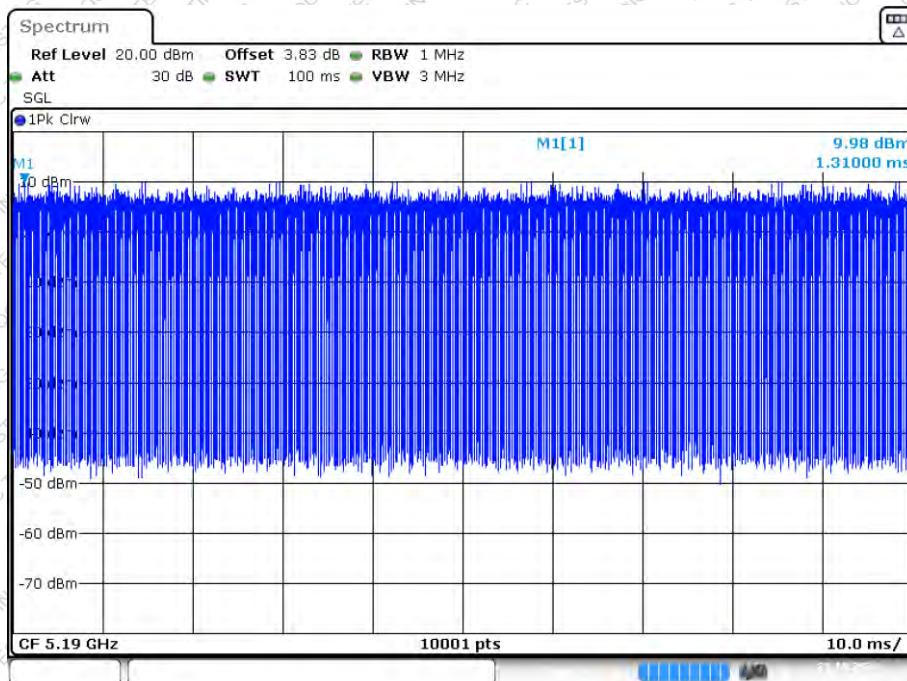


Duty Cycle NVNT ax20 5825MHz Sum



Date: 25.OCT.2024 14:49:36

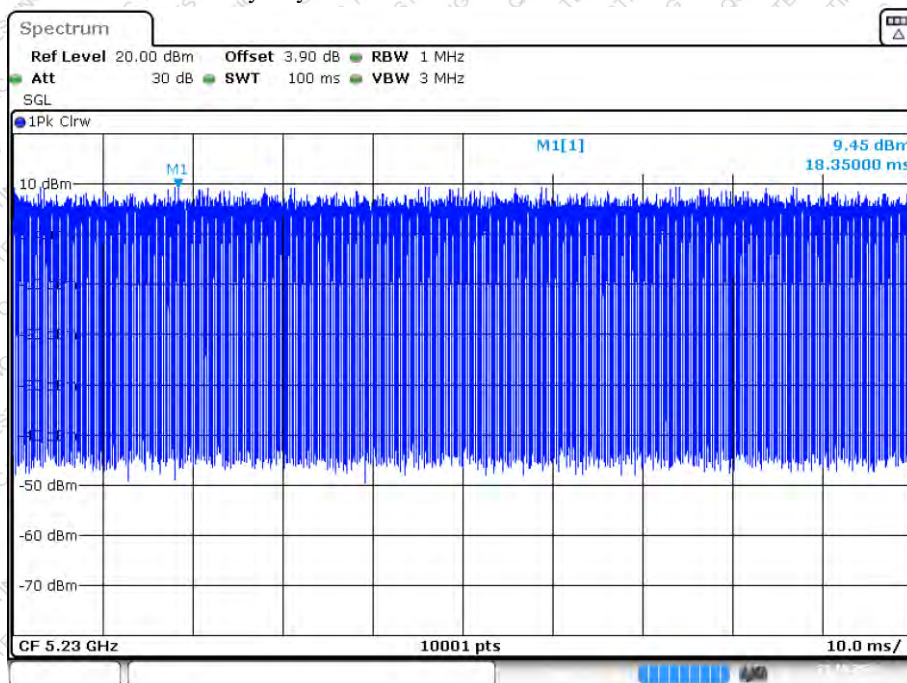
Duty Cycle NVNT ax40 5190MHz Ant1



Date: 22.OCT.2024 11:24:09

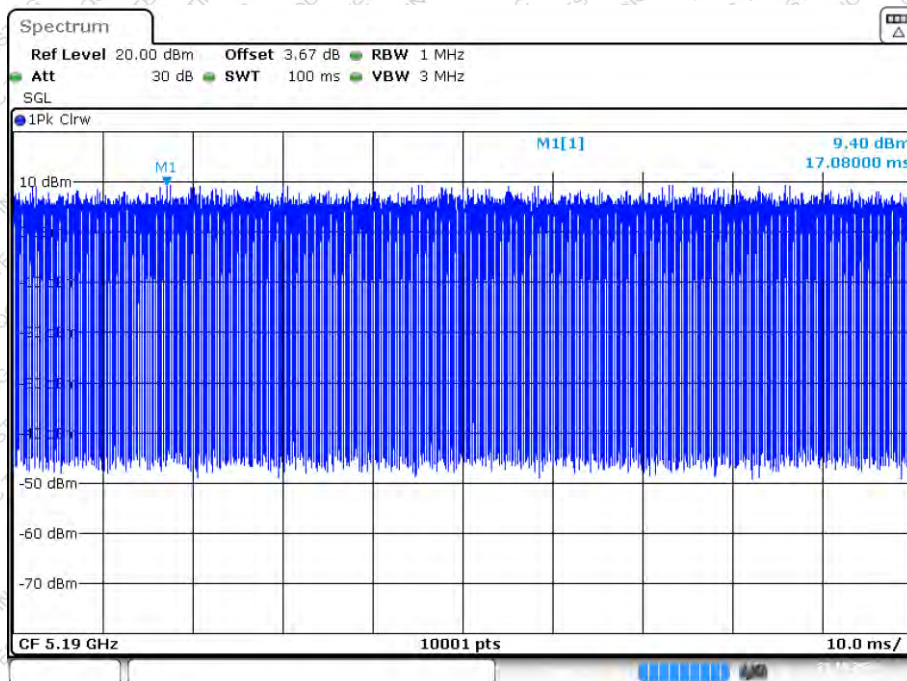


Duty Cycle NVNT ax40 5230MHz Ant1



Date: 22.OCT.2024 11:27:43

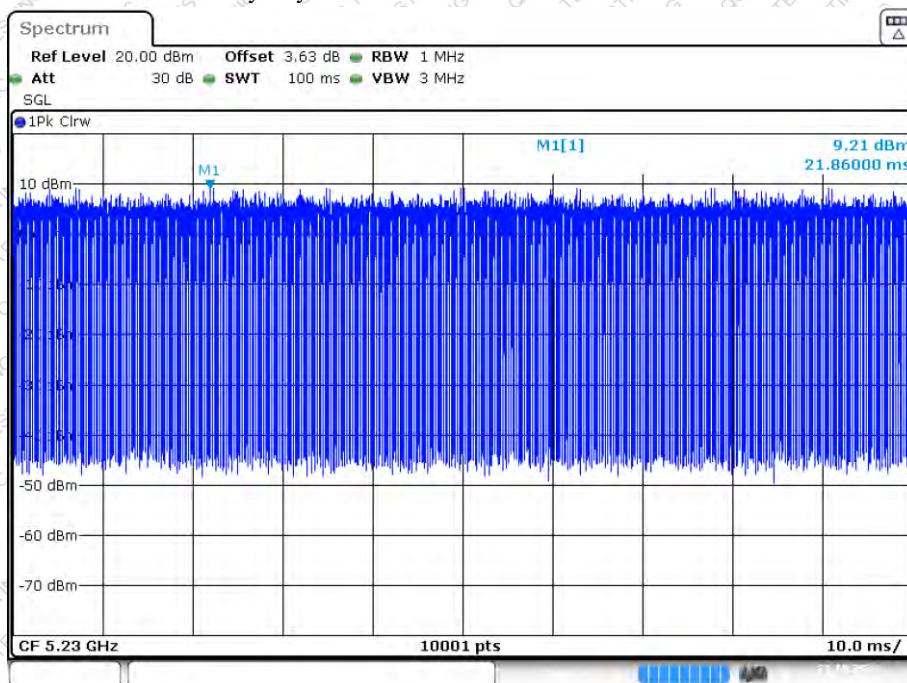
Duty Cycle NVNT ax40 5190MHz Ant2



Date: 22.OCT.2024 19:09:00

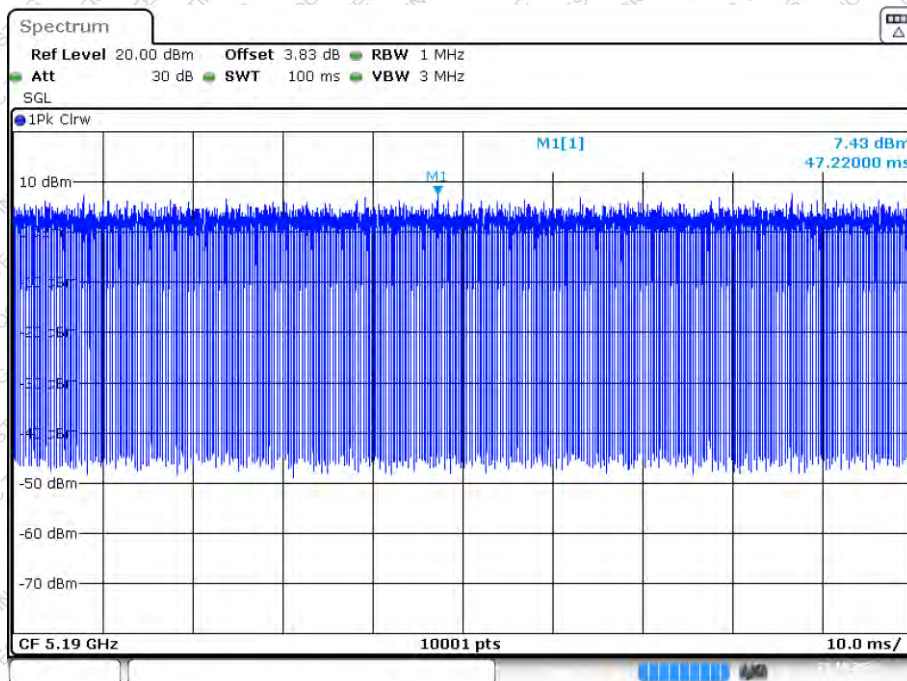


Duty Cycle NVNT ax40 5230MHz Ant2



Date: 22.OCT.2024 19:05:36

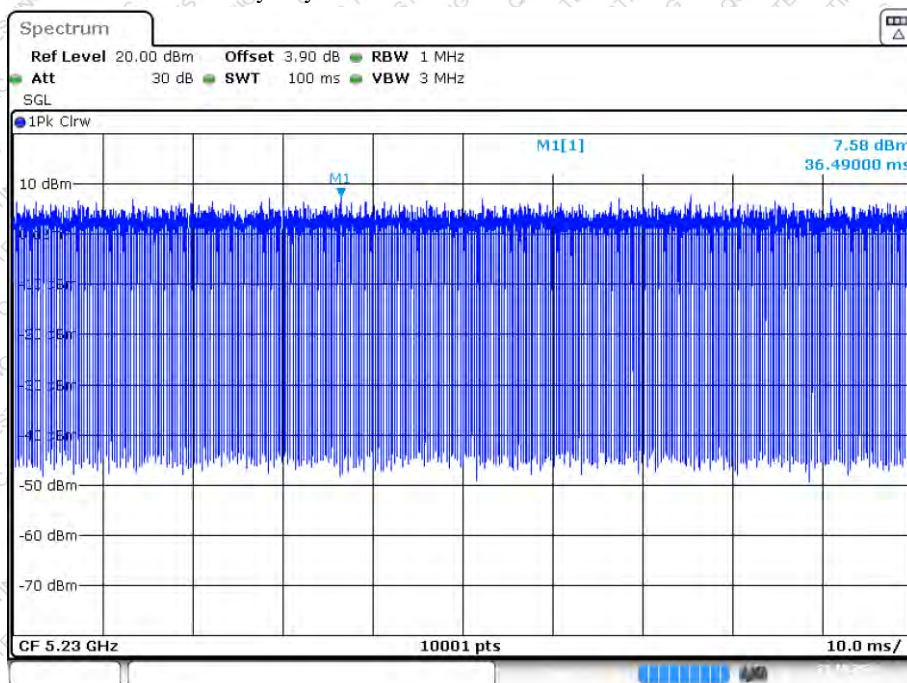
Duty Cycle NVNT ax40 5190MHz Sum



Date: 22.OCT.2024 19:56:31

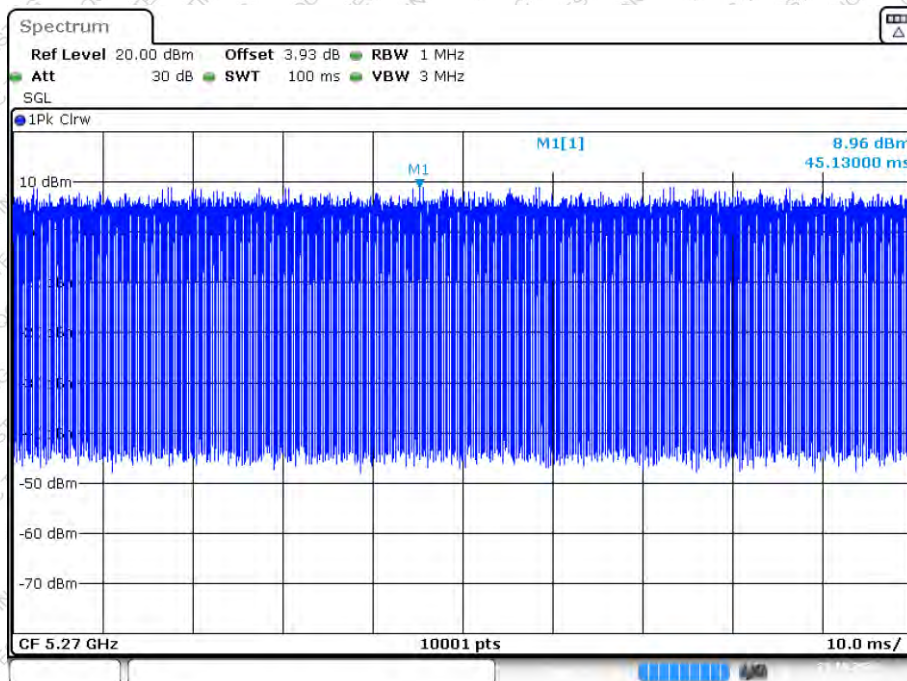


Duty Cycle NVNT ax40 5230MHz Sum



Date: 22.OCT.2024 19:59:20

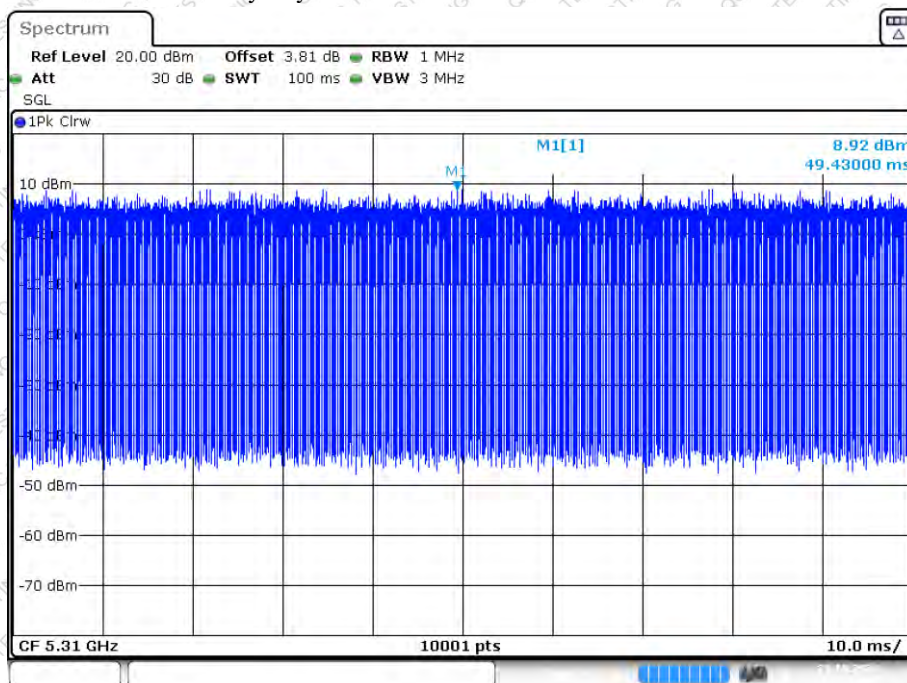
Duty Cycle NVNT ax40 5270MHz Ant1



Date: 23.OCT.2024 17:08:30

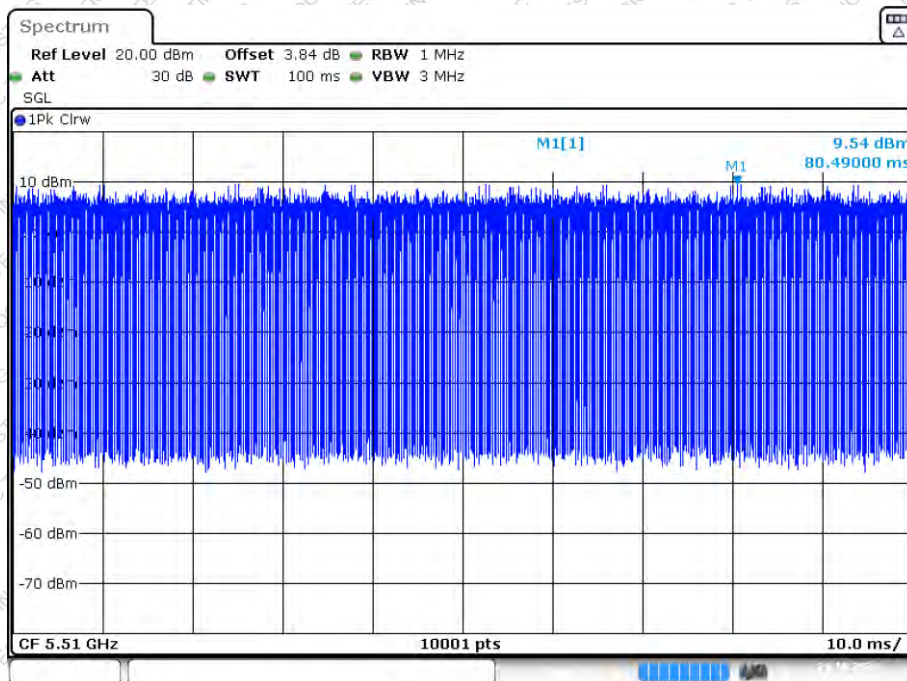


Duty Cycle NVNT ax40 5310MHz Ant1



Date: 23.OCT.2024 17:10:05

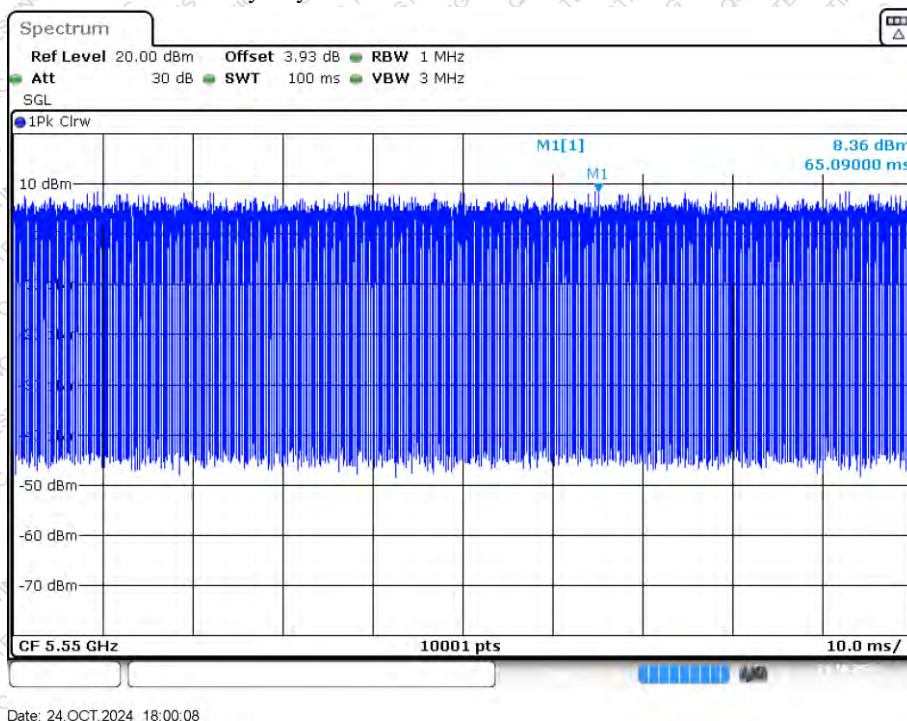
Duty Cycle NVNT ax40 5510MHz Ant1



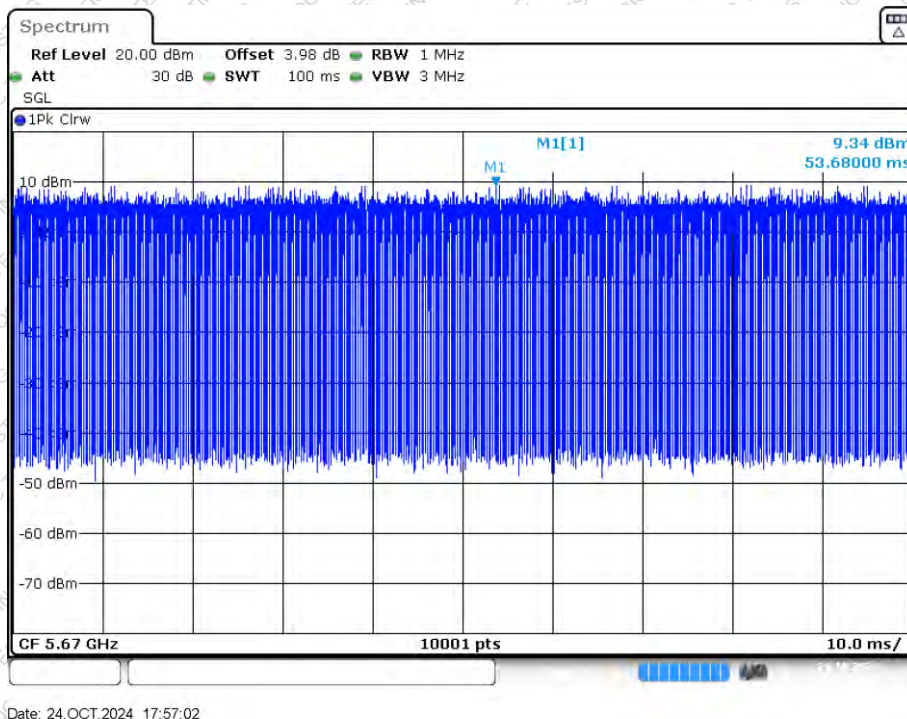
Date: 24.OCT.2024 18:02:31



Duty Cycle NVNT ax40 5550MHz Ant1

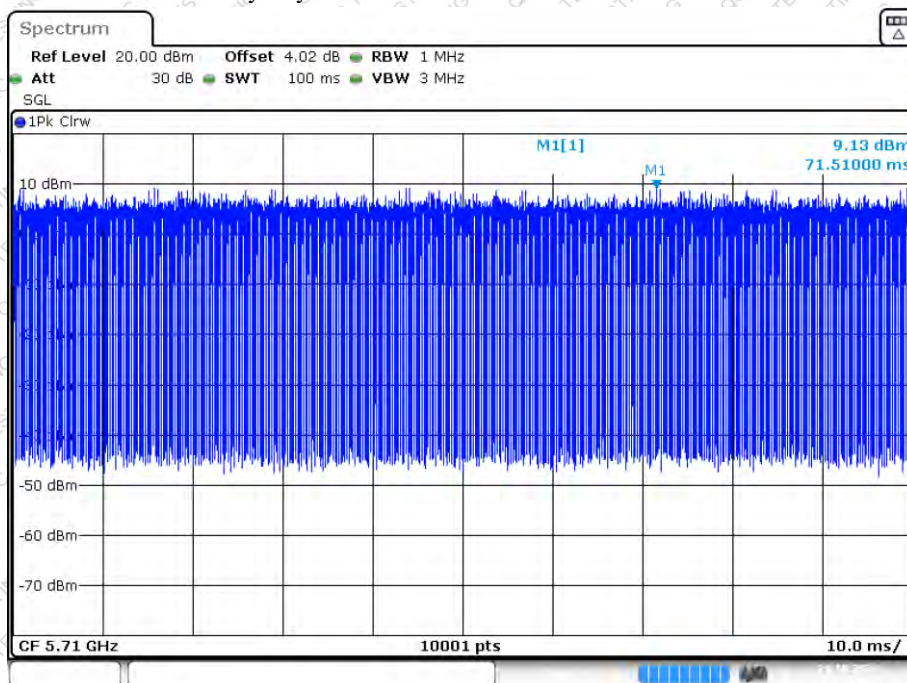


Duty Cycle NVNT ax40 5670MHz Ant1



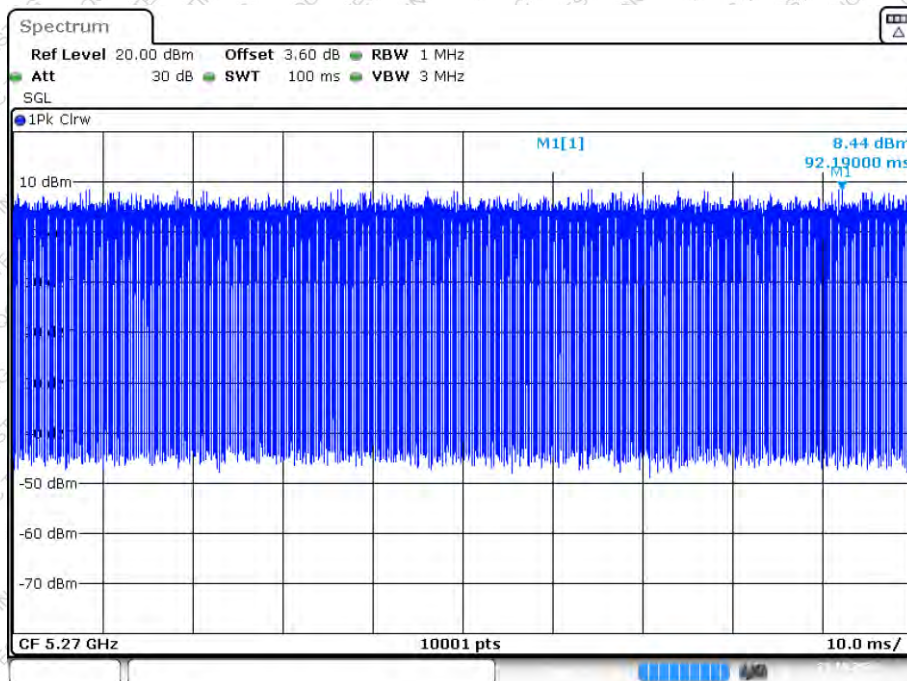


Duty Cycle NVNT ax40 5710MHz Ant1



Date: 24.OCT.2024 17:59:10

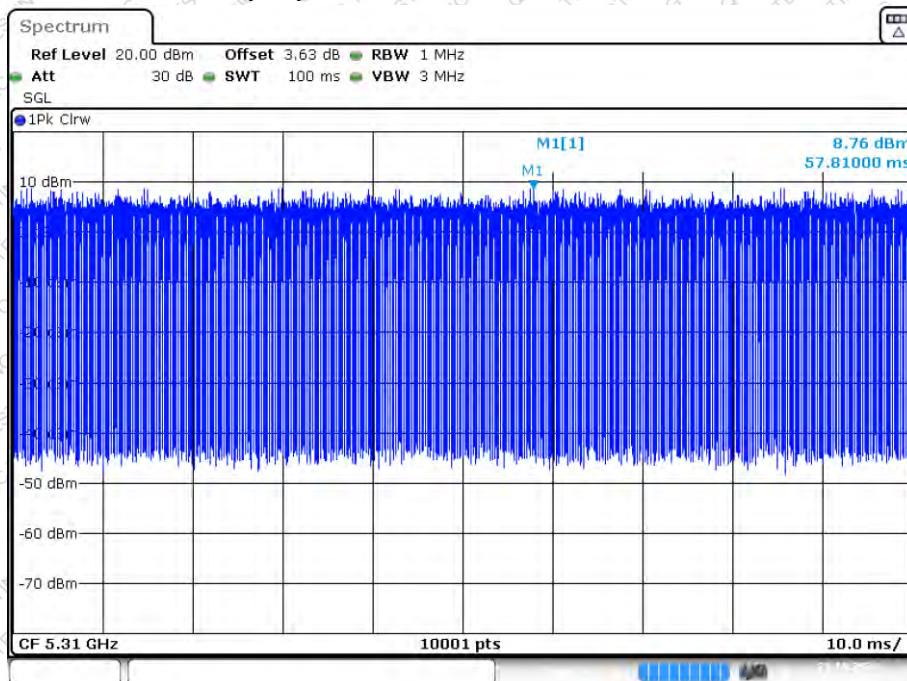
Duty Cycle NVNT ax40 5270MHz Ant2



Date: 23.OCT.2024 19:02:11

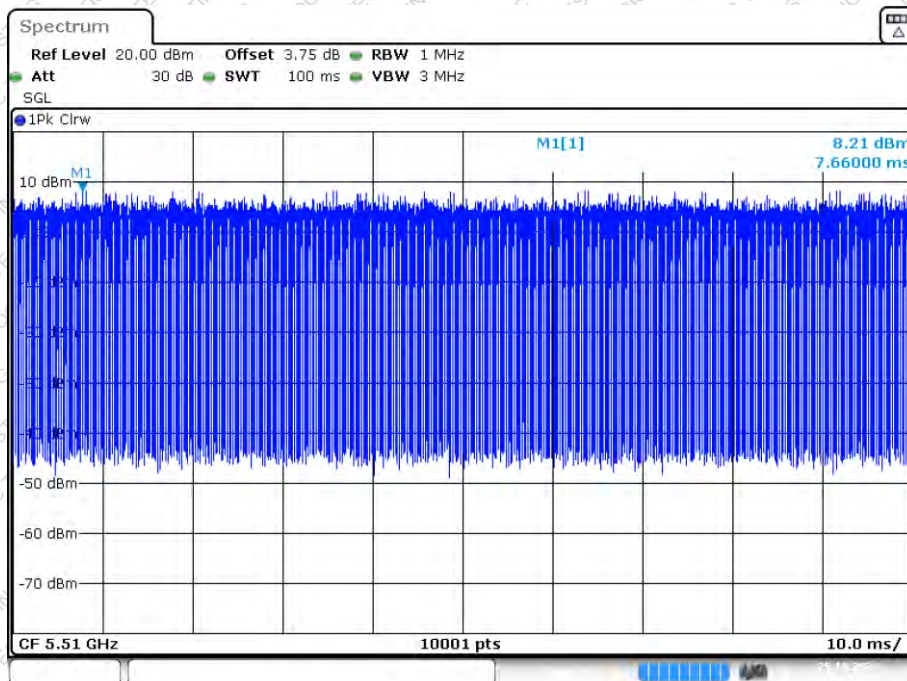


Duty Cycle NVNT ax40 5310MHz Ant2



Date: 23.OCT.2024 19:04:53

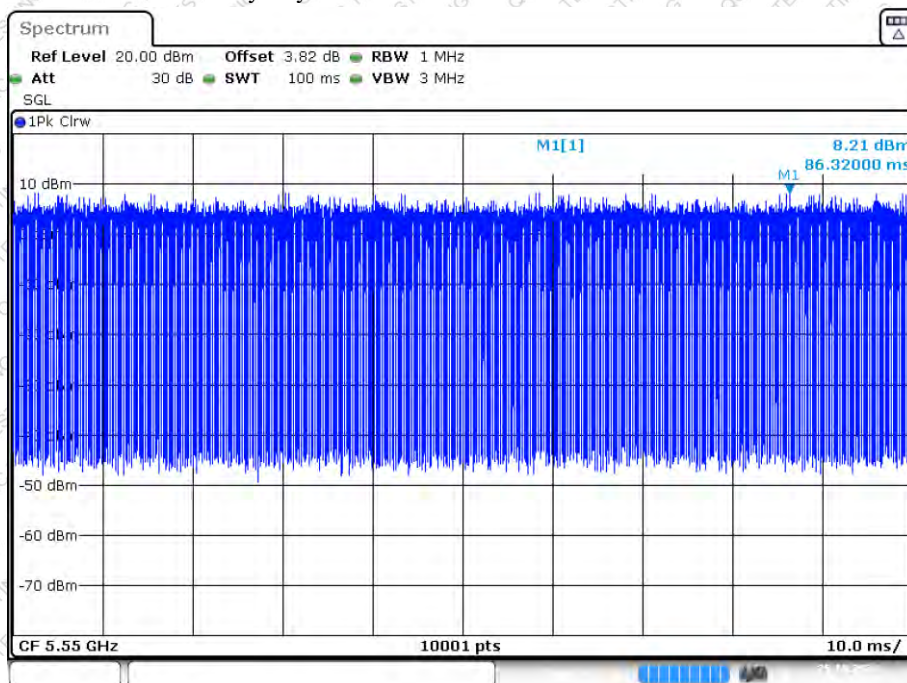
Duty Cycle NVNT ax40 5510MHz Ant2



Date: 25.OCT.2024 09:09:11

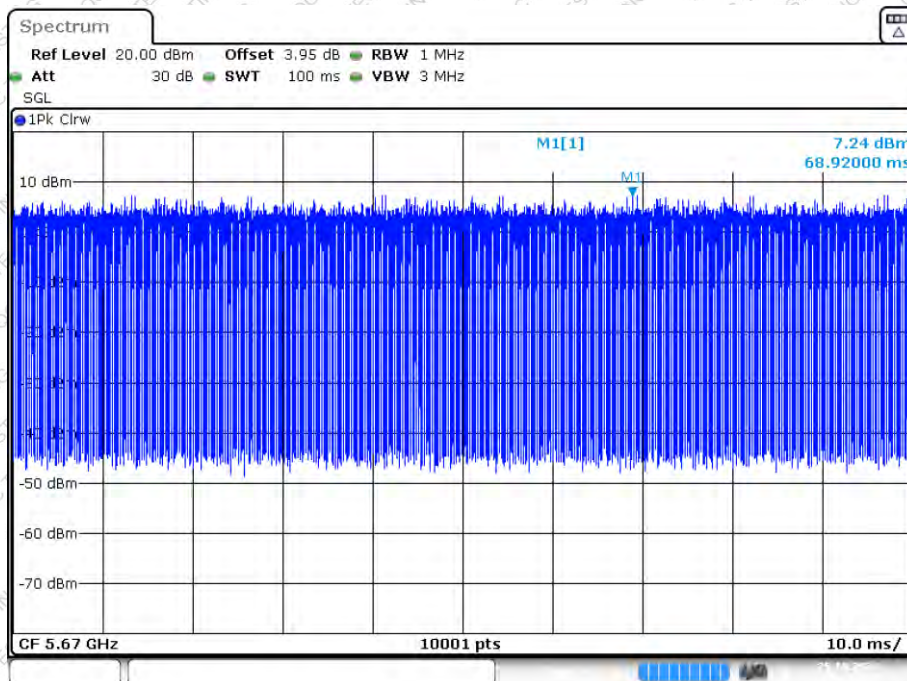


Duty Cycle NVNT ax40 5550MHz Ant2



Date: 25.OCT.2024 09:10:21

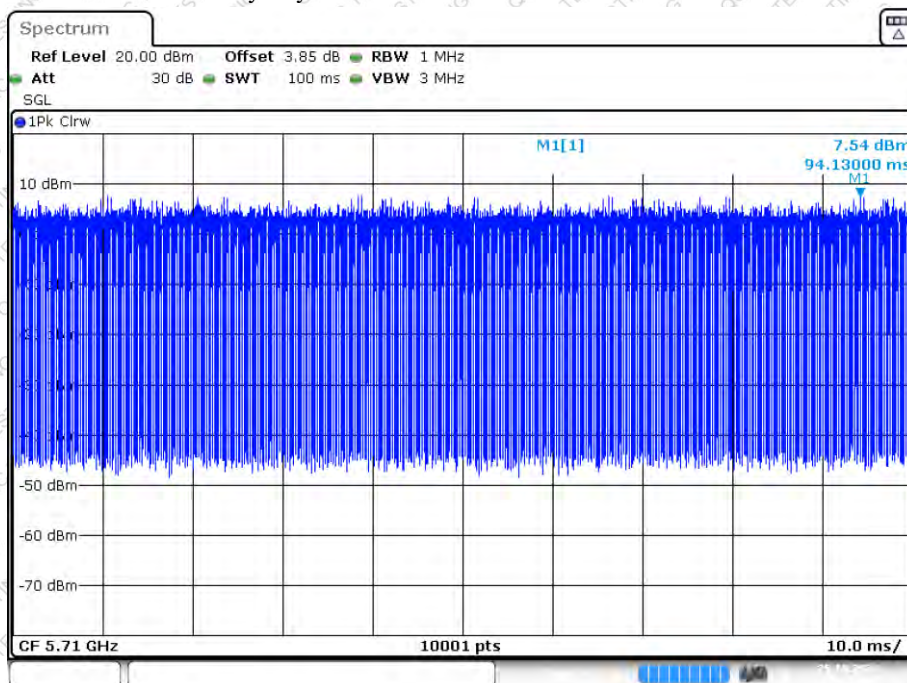
Duty Cycle NVNT ax40 5670MHz Ant2



Date: 25.OCT.2024 09:11:55

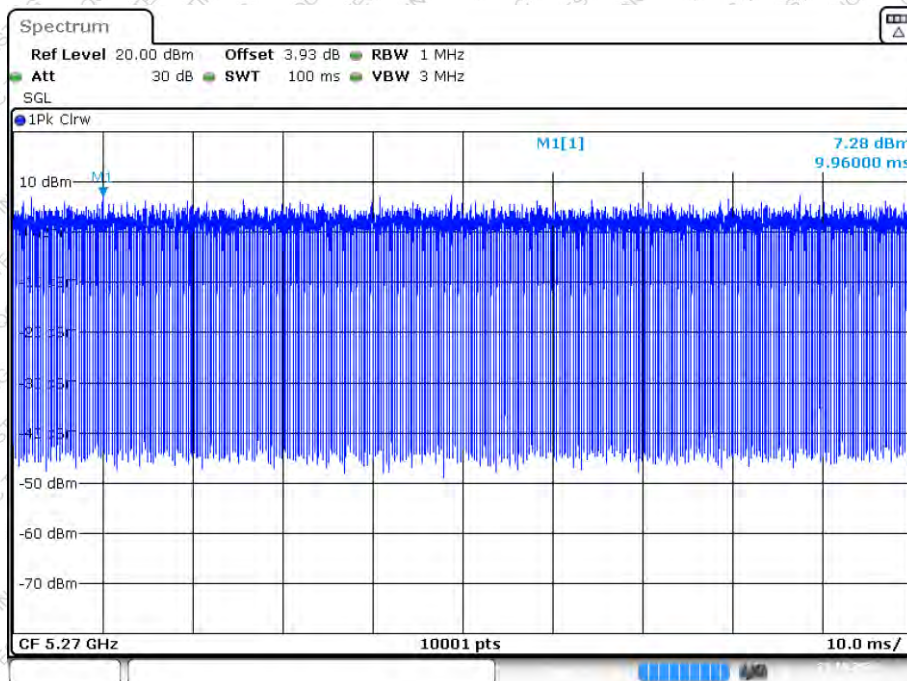


Duty Cycle NVNT ax40 5710MHz Ant2



Date: 25.OCT.2024 09:13:06

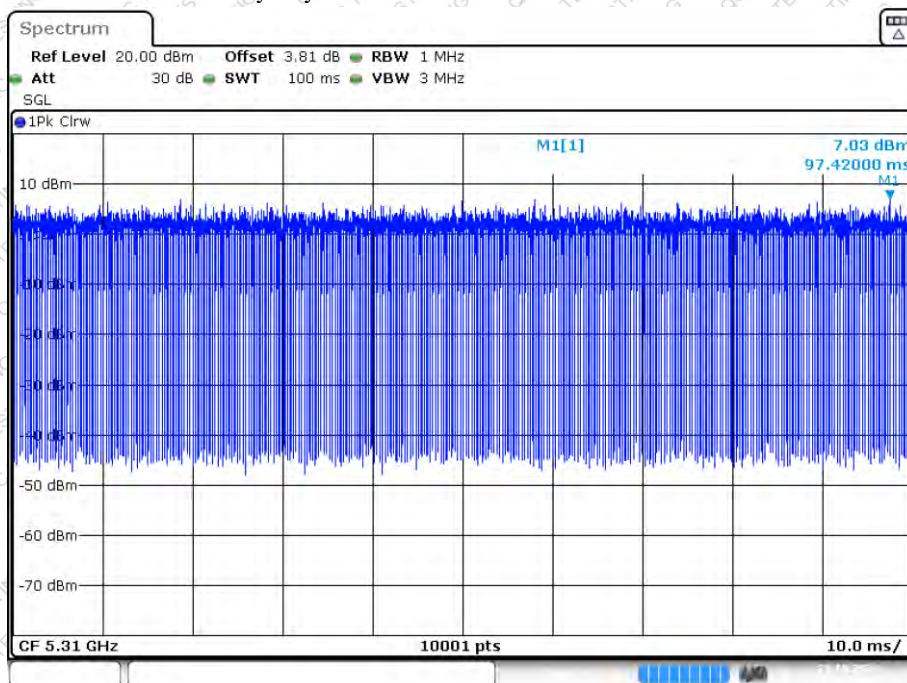
Duty Cycle NVNT ax40 5270MHz Sum



Date: 23.OCT.2024 20:23:27

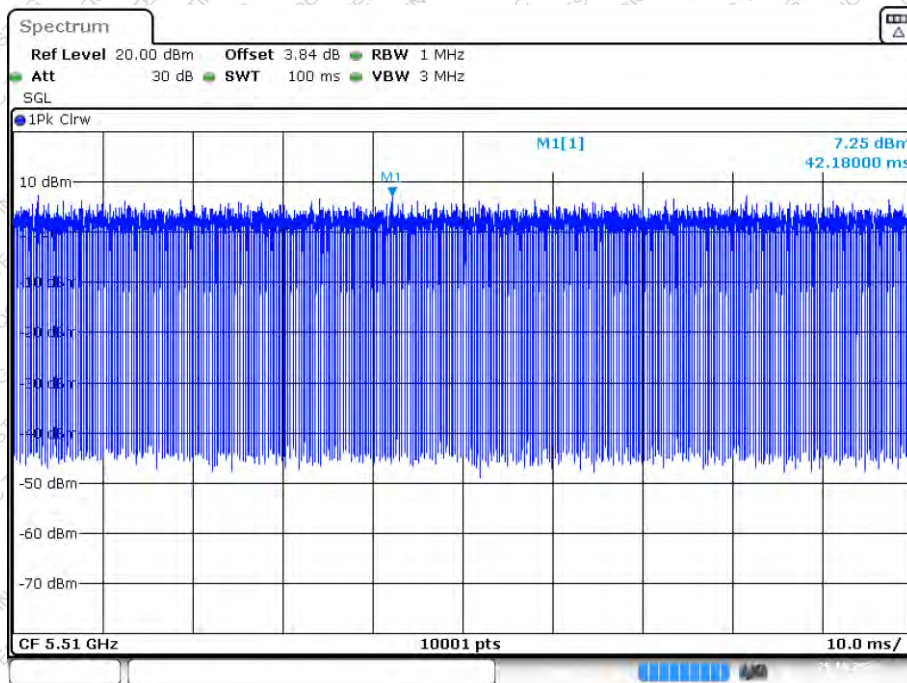


Duty Cycle NVNT ax40 5310MHz Sum



Date: 23.OCT.2024 20:24:47

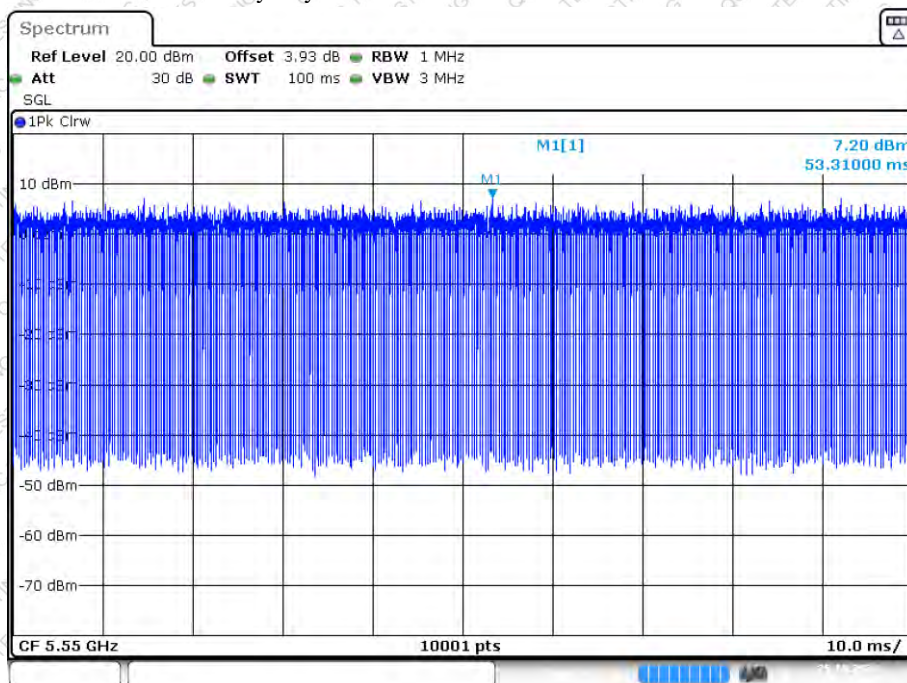
Duty Cycle NVNT ax40 5510MHz Sum



Date: 25.OCT.2024 10:25:52

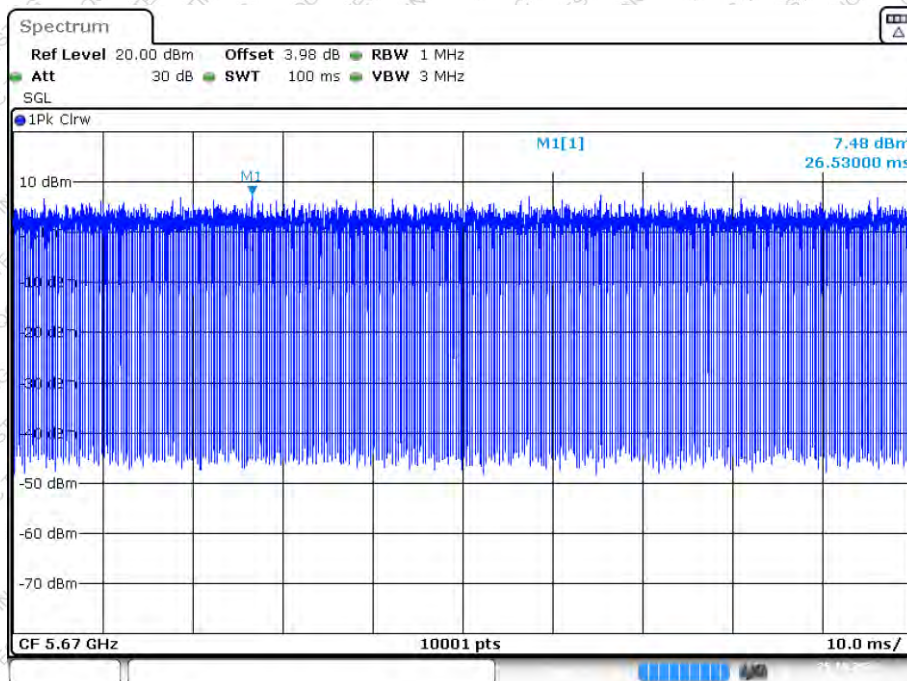


Duty Cycle NVNT ax40 5550MHz Sum



Date: 25.OCT.2024 10:19:05

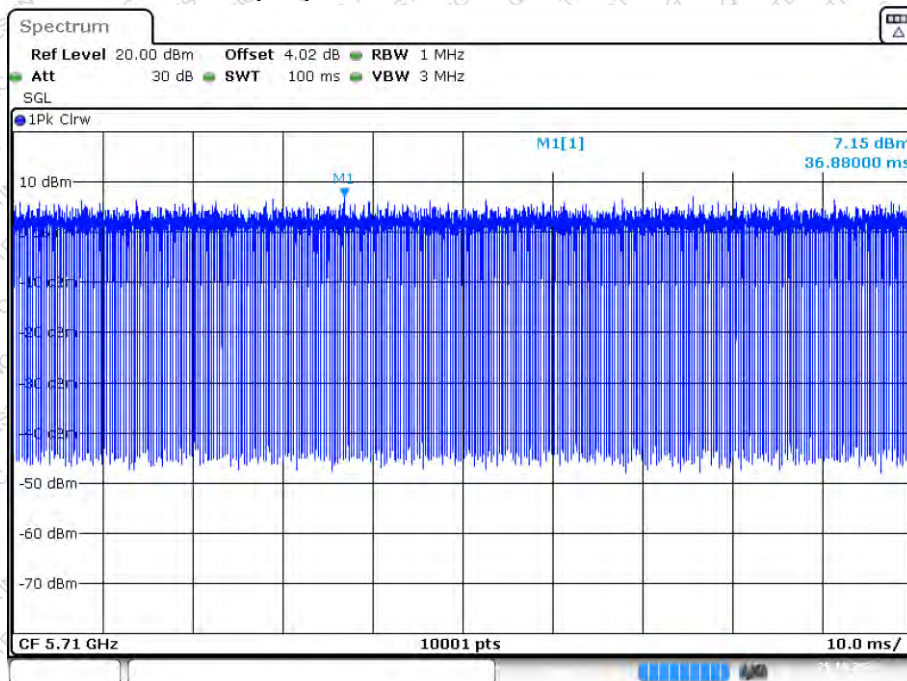
Duty Cycle NVNT ax40 5670MHz Sum



Date: 25.OCT.2024 10:20:30

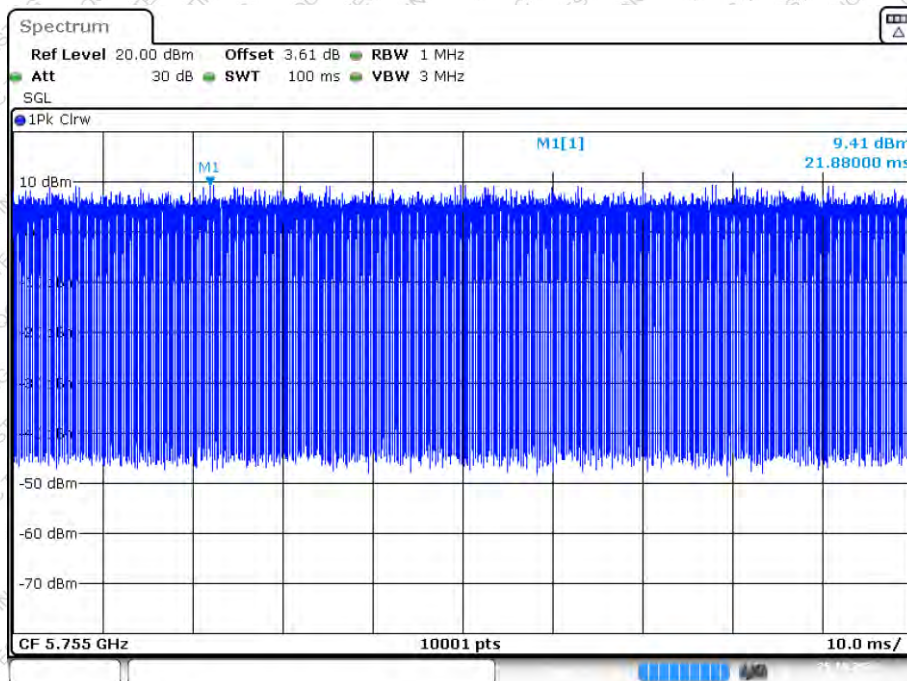


Duty Cycle NVNT ax40 5710MHz Sum



Date: 25.OCT.2024 10:23:12

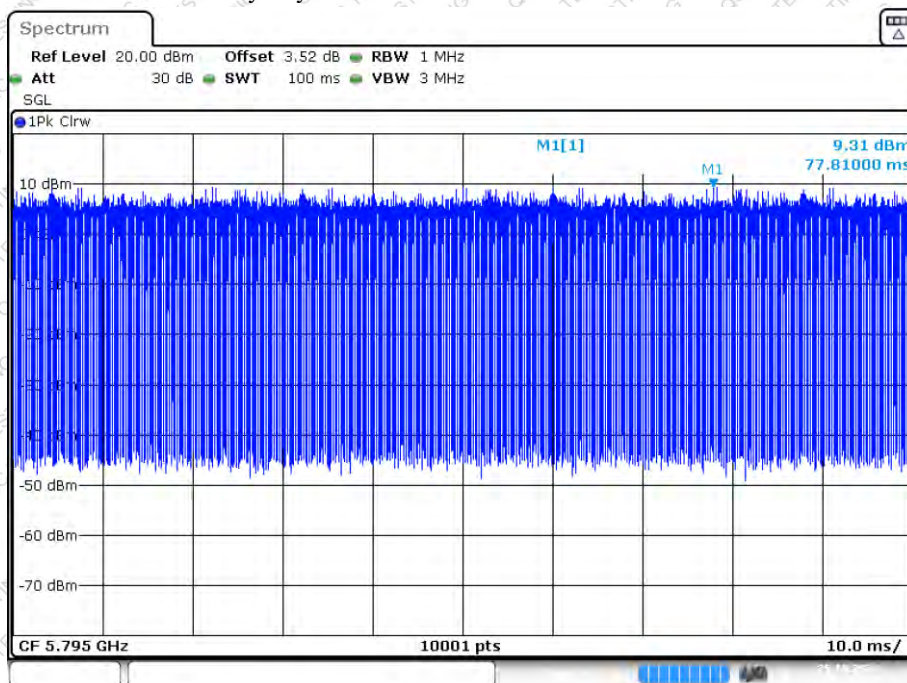
Duty Cycle NVNT ax40 5755MHz Ant1



Date: 25.OCT.2024 11:15:18

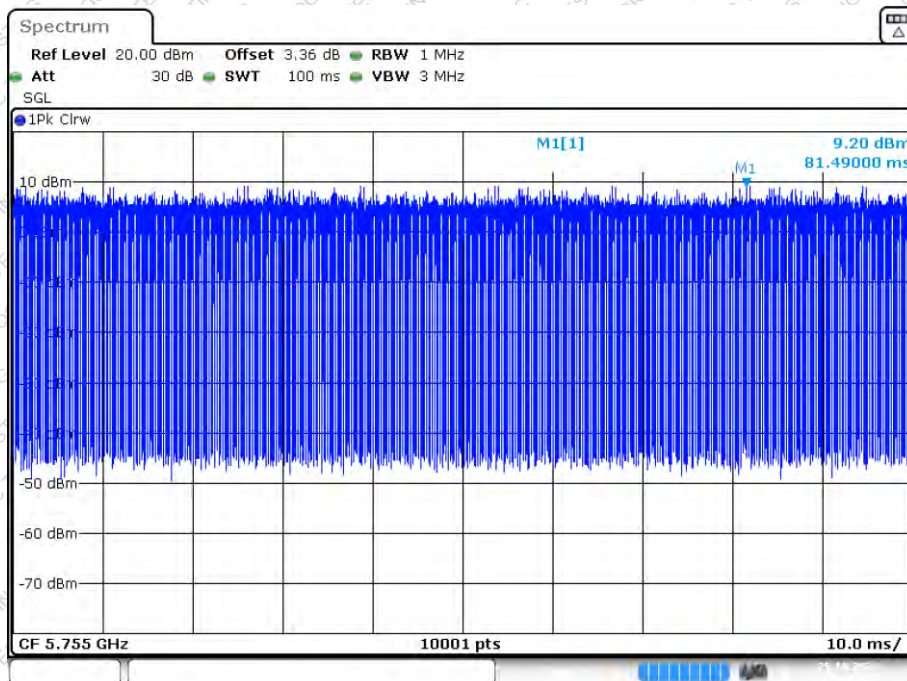


Duty Cycle NVNT ax40 5795MHz Ant1



Date: 25.OCT.2024 11:16:29

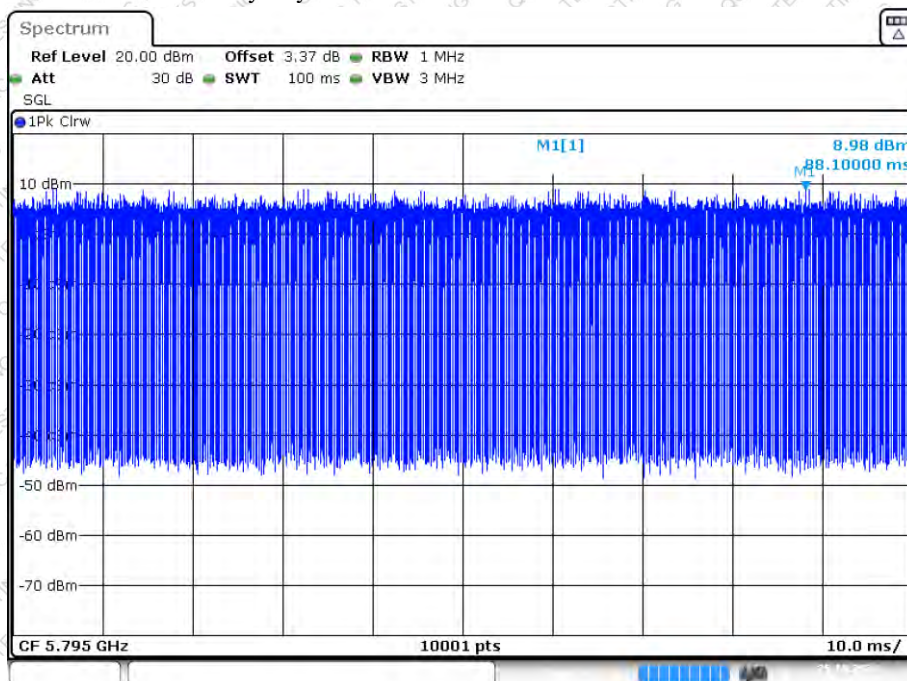
Duty Cycle NVNT ax40 5755MHz Ant2



Date: 25.OCT.2024 14:01:59

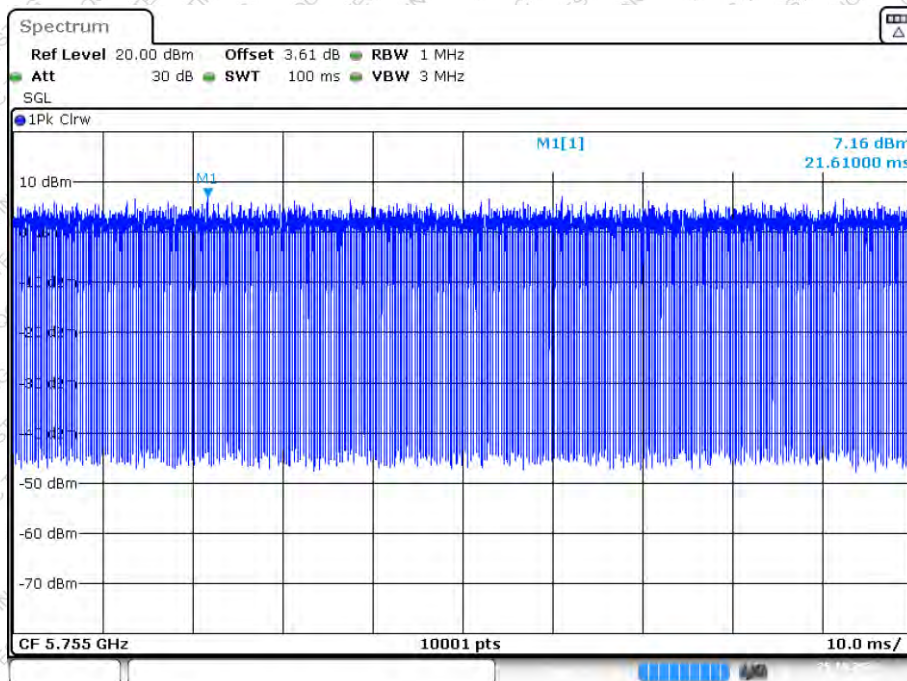


Duty Cycle NVNT ax40 5795MHz Ant2



Date: 25.OCT.2024 14:03:27

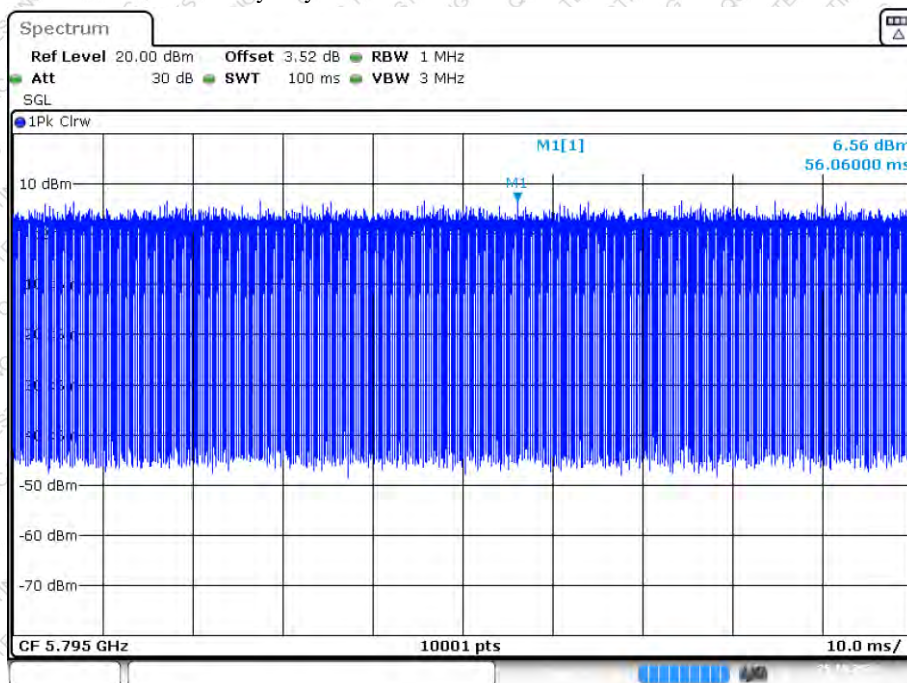
Duty Cycle NVNT ax40 5755MHz Sum



Date: 25.OCT.2024 15:03:49

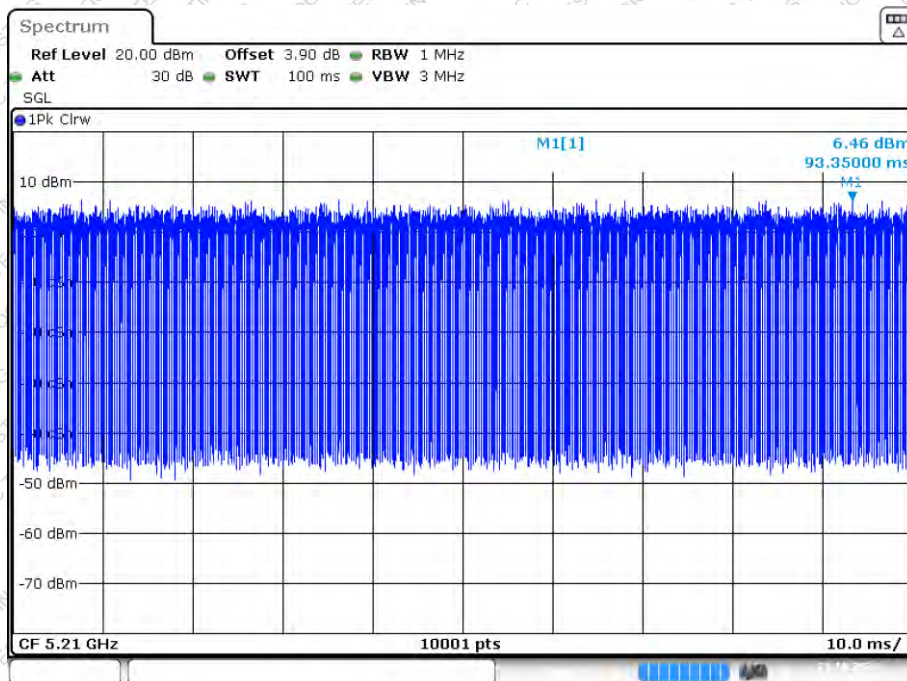


Duty Cycle NVNT ax40 5795MHz Sum



Date: 25.OCT.2024 15:06:59

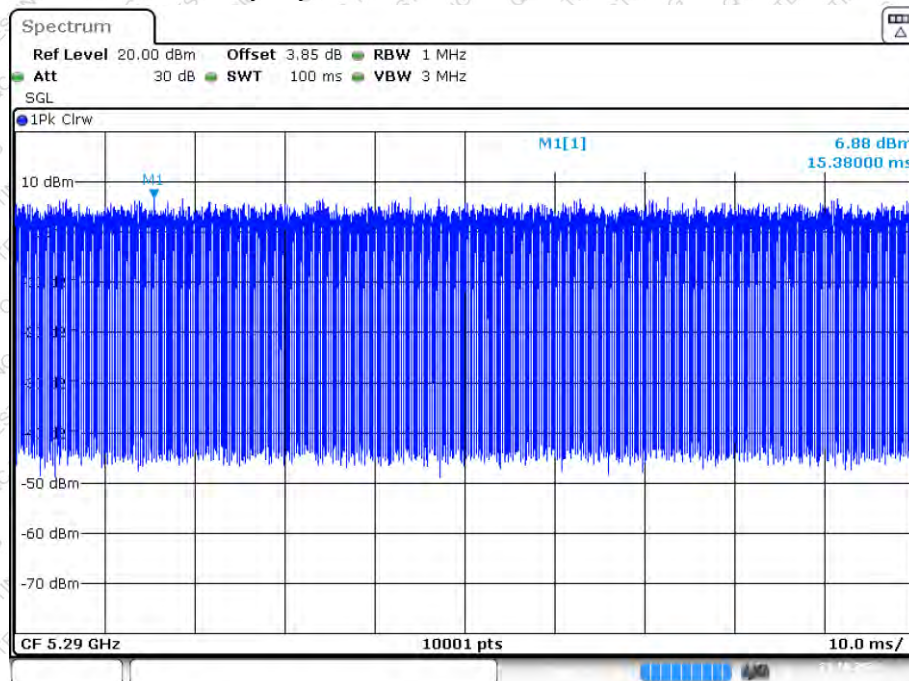
Duty Cycle NVNT ax80 5210MHz Ant1



Date: 23.OCT.2024 10:50:33

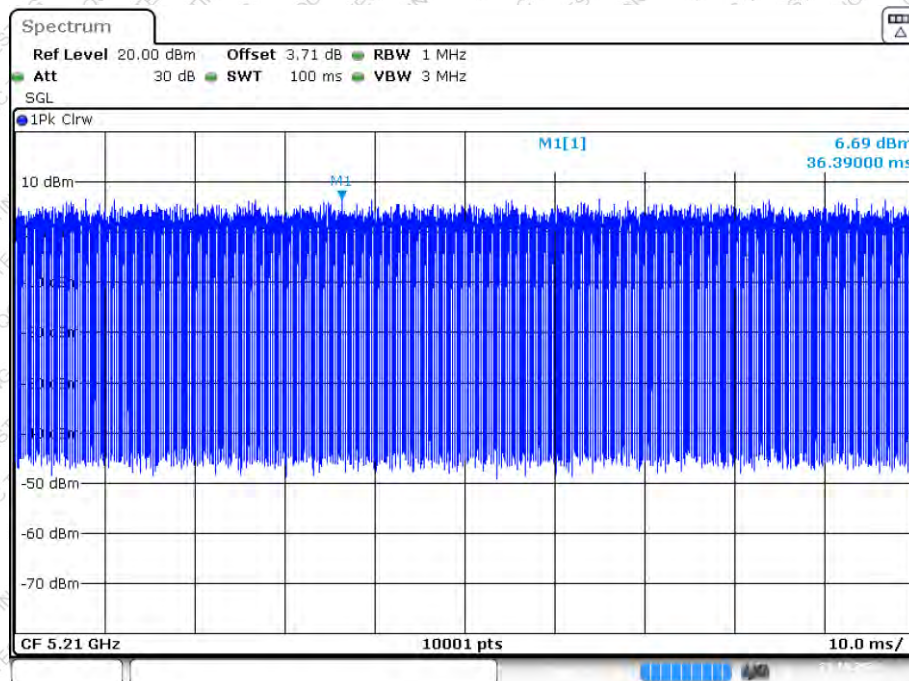


Duty Cycle NVNT ax80 5290MHz Ant1



Date: 23.OCT.2024 10:52:14

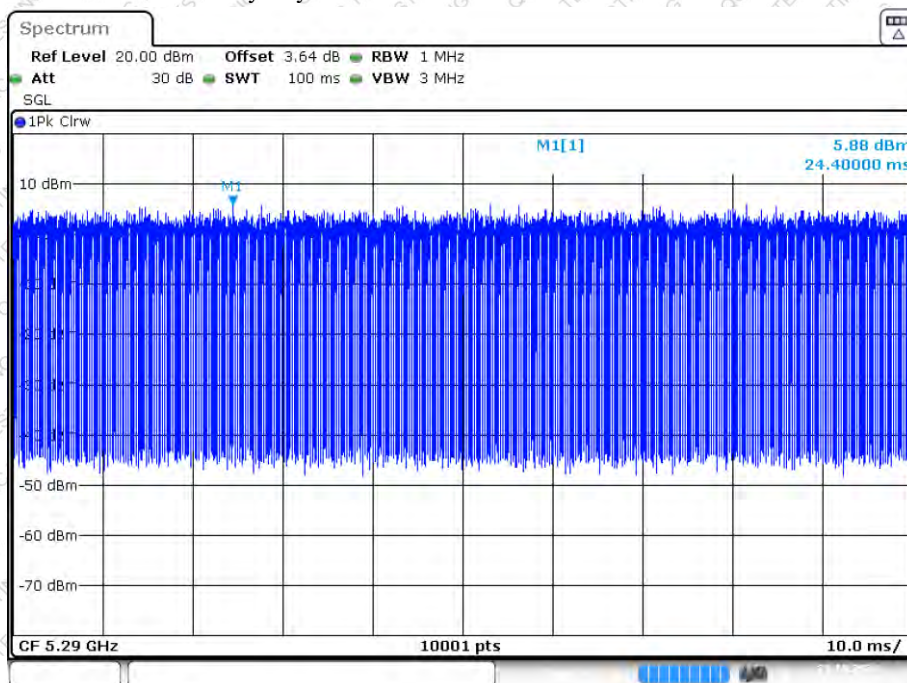
Duty Cycle NVNT ax80 5210MHz Ant2



Date: 23.OCT.2024 11:20:57

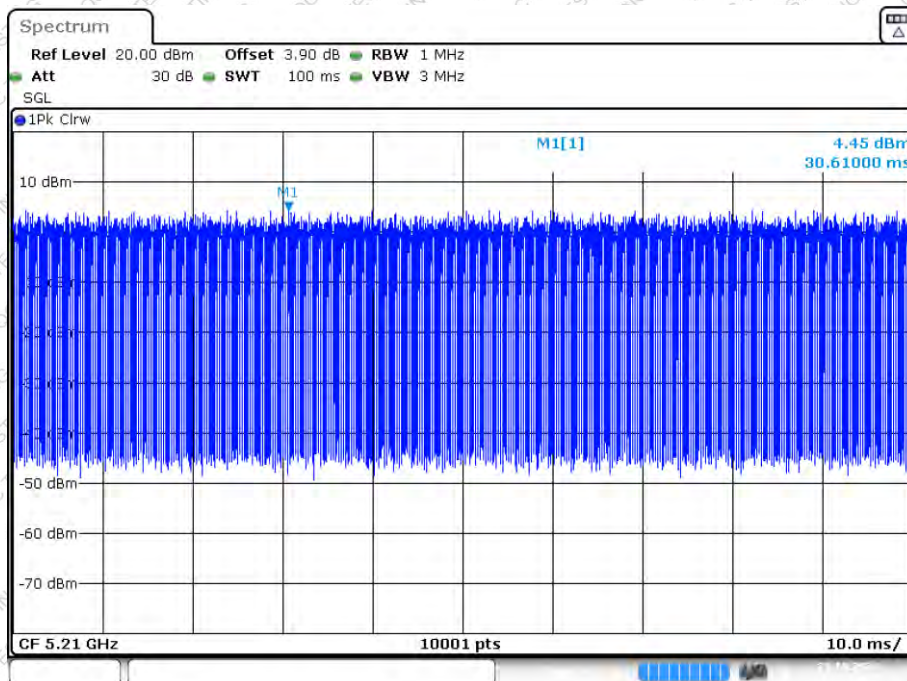


Duty Cycle NVNT ax80 5290MHz Ant2



Date: 23.OCT.2024 11:22:04

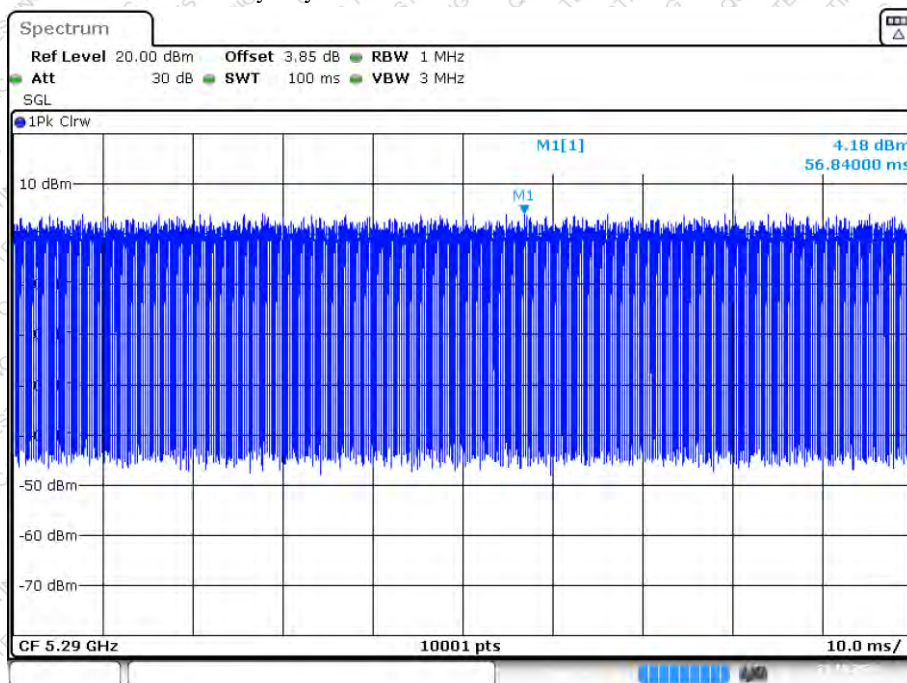
Duty Cycle NVNT ax80 5210MHz Sum



Date: 23.OCT.2024 15:14:42

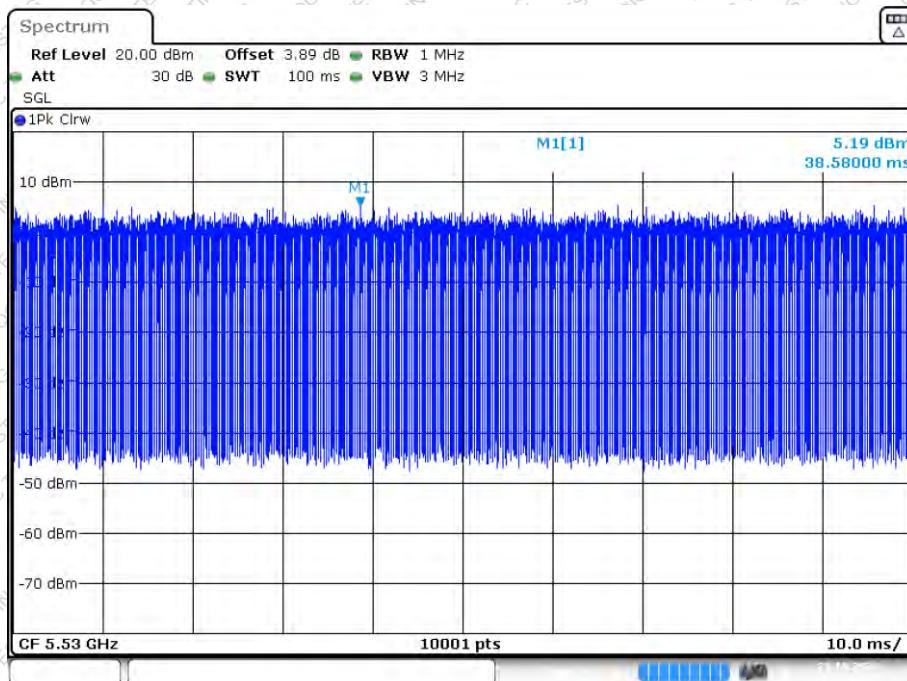


Duty Cycle NVNT ax80 5290MHz Sum



Date: 23.OCT.2024 15:17:34

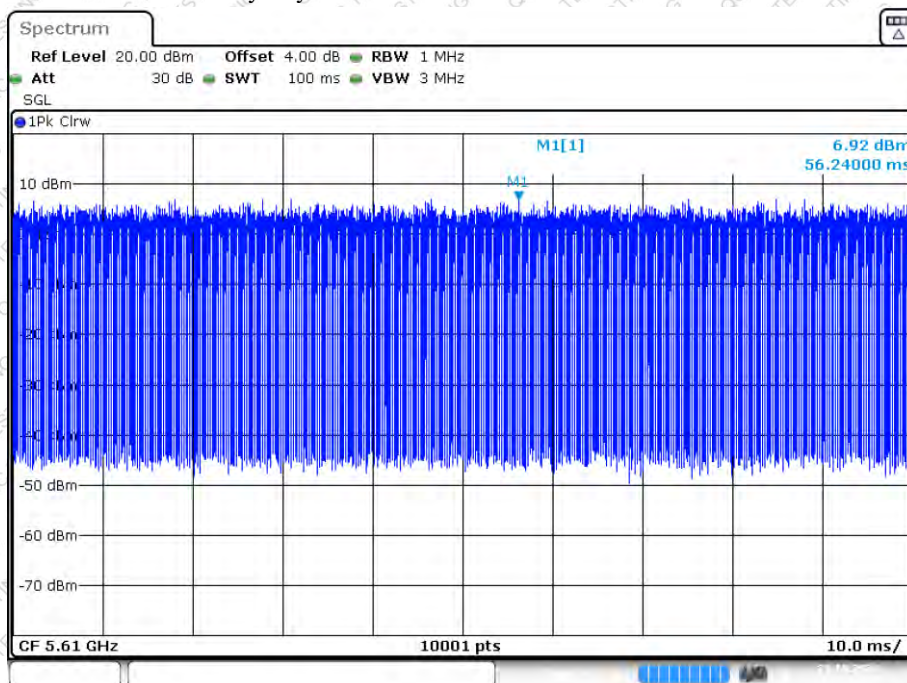
Duty Cycle NVNT ax80 5530MHz Ant1



Date: 23.OCT.2024 17:22:32

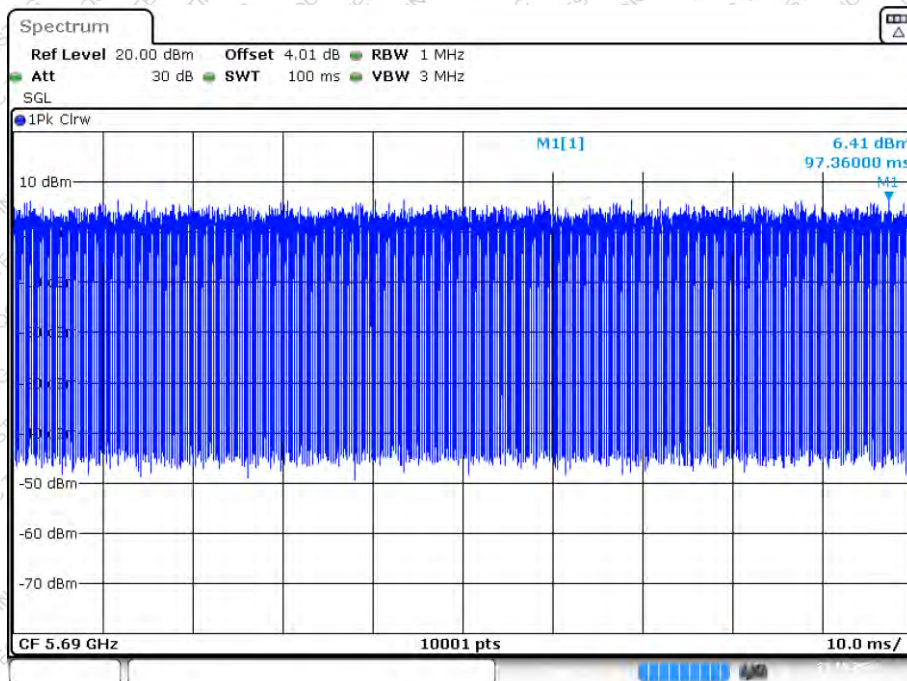


Duty Cycle NVNT ax80 5610MHz Ant1



Date: 23.OCT.2024 17:24:17

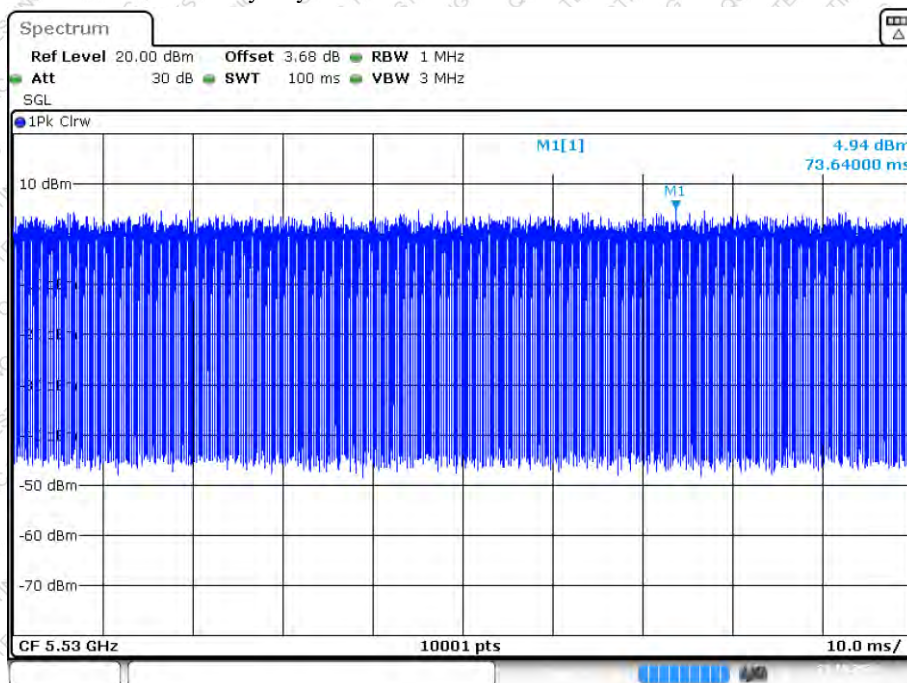
Duty Cycle NVNT ax80 5690MHz Ant1



Date: 23.OCT.2024 17:27:08

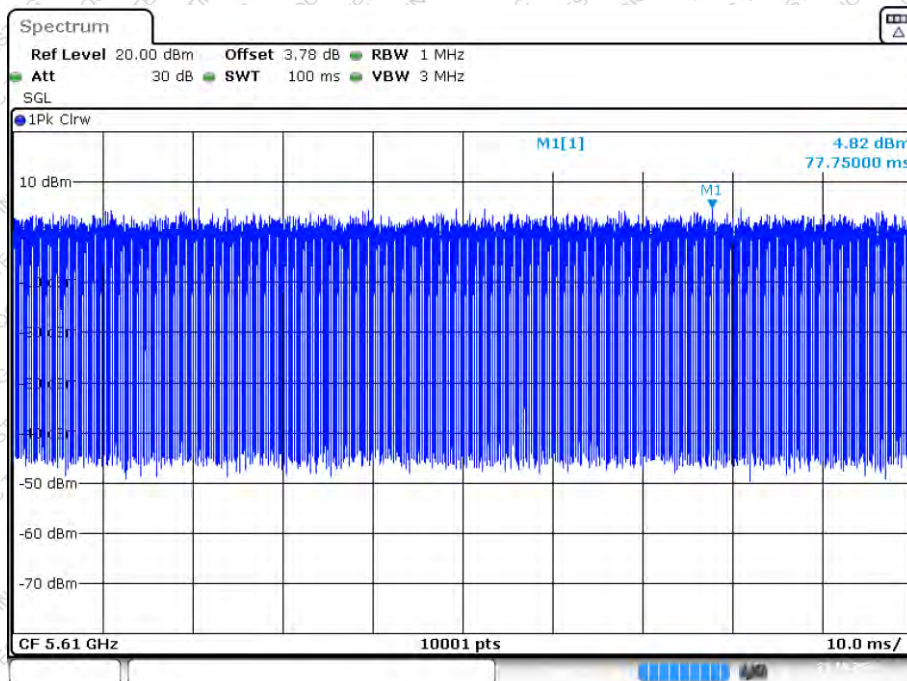


Duty Cycle NVNT ax80 5530MHz Ant2



Date: 23.OCT.2024 19:14:52

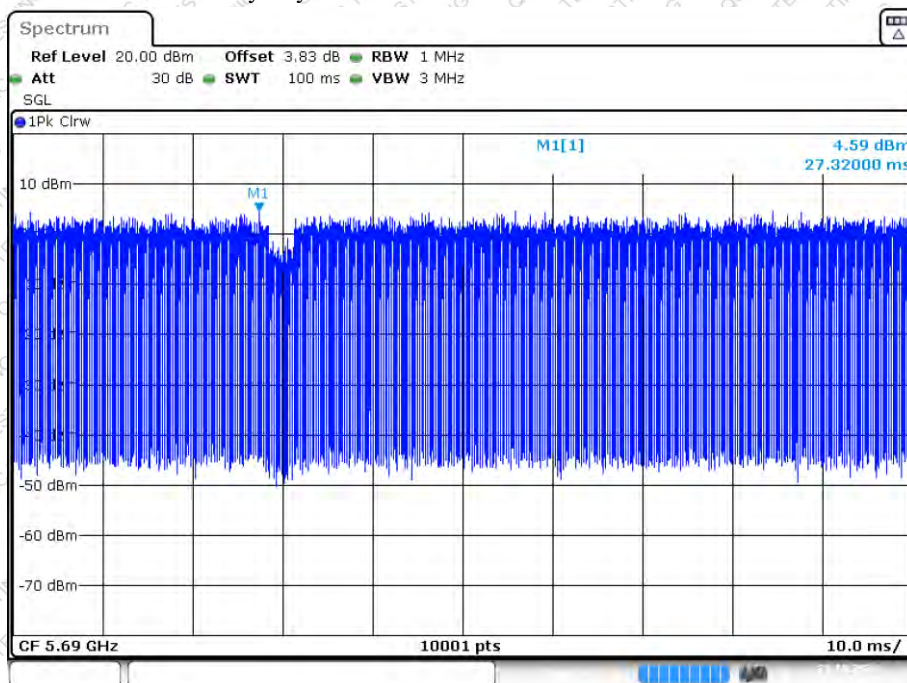
Duty Cycle NVNT ax80 5610MHz Ant2



Date: 23.OCT.2024 19:16:32

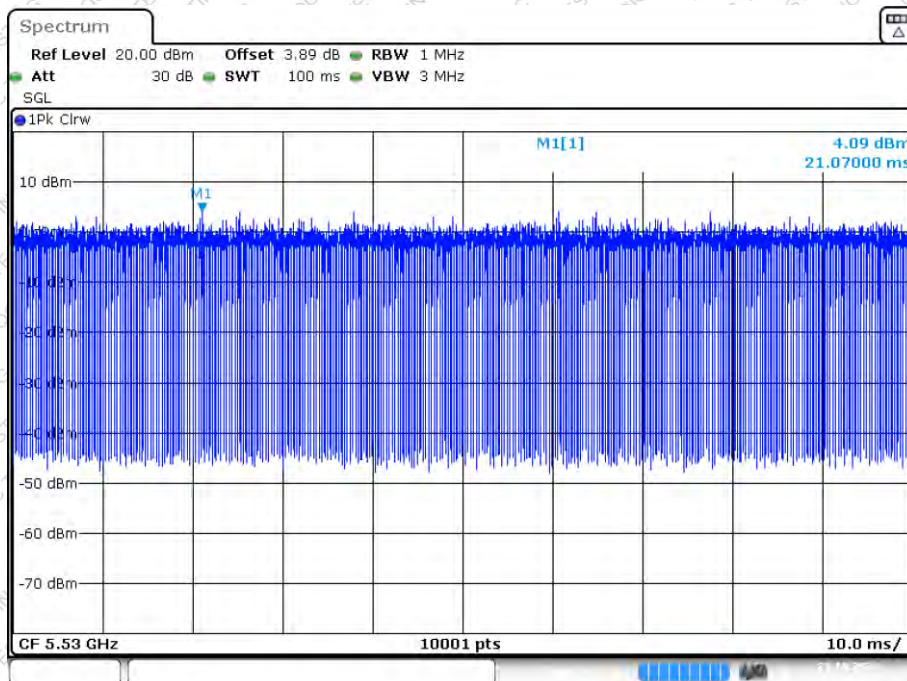


Duty Cycle NVNT ax80 5690MHz Ant2



Date: 23.OCT.2024 19:18:09

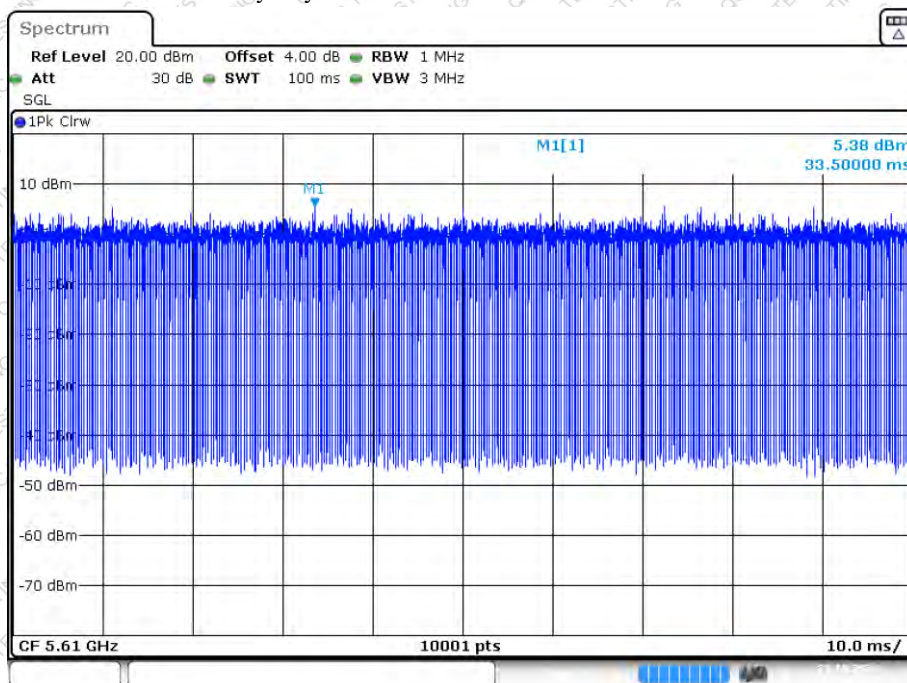
Duty Cycle NVNT ax80 5530MHz Sum



Date: 23.OCT.2024 20:45:01

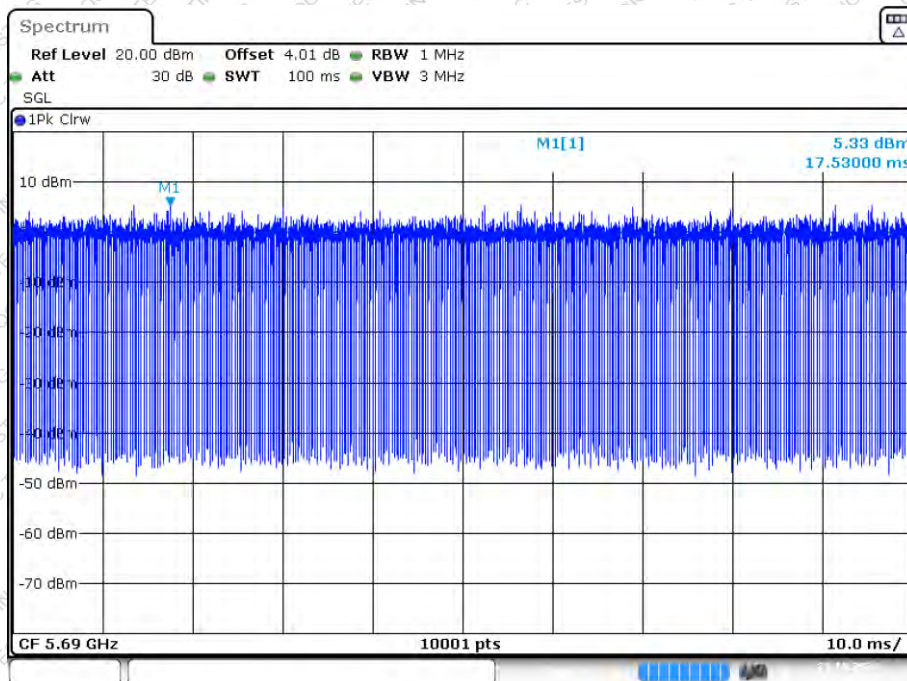


Duty Cycle NVNT ax80 5610MHz Sum



Date: 23.OCT.2024 20:41:11

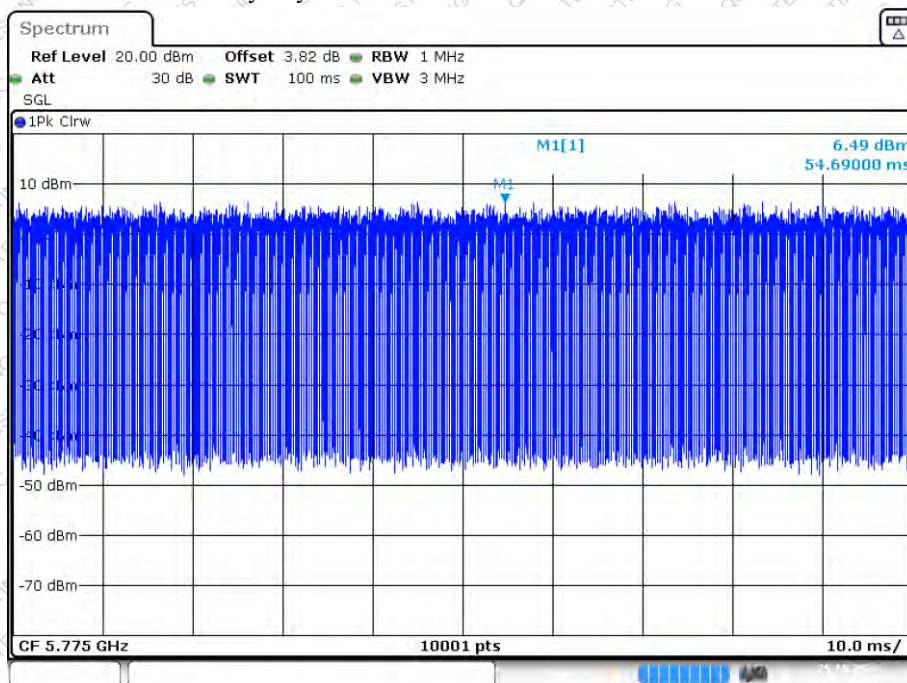
Duty Cycle NVNT ax80 5690MHz Sum



Date: 23.OCT.2024 20:42:52

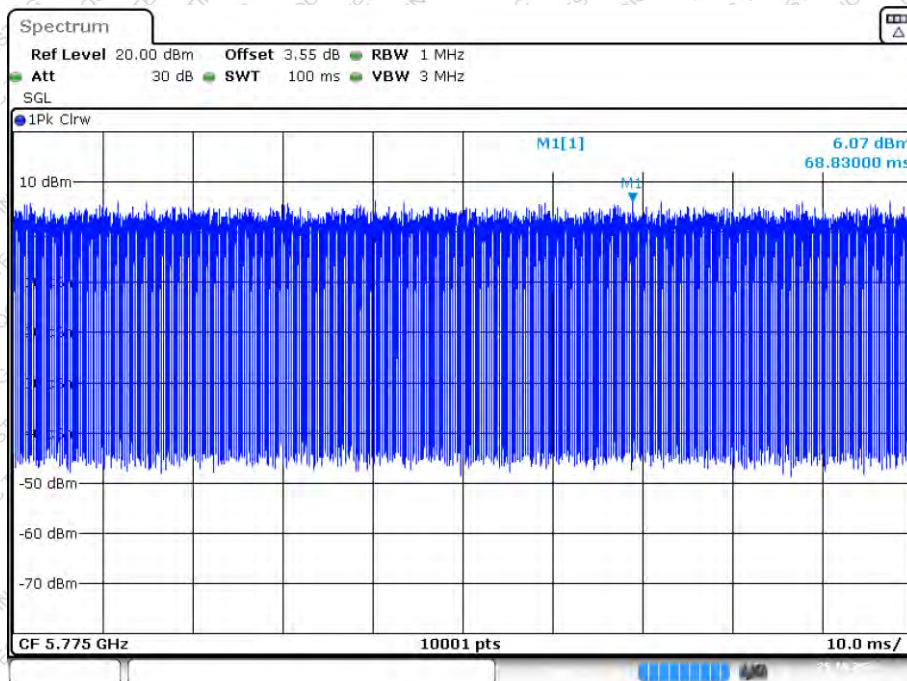


Duty Cycle NVNT ax80 5775MHz Ant1



Date: 25.OCT.2024 11:19:28

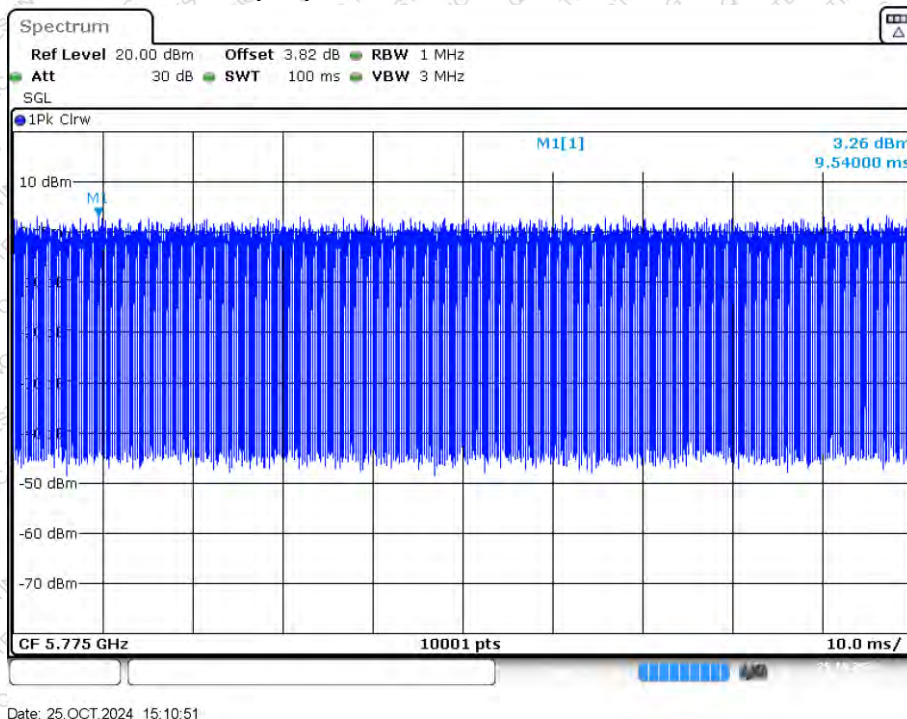
Duty Cycle NVNT ax80 5775MHz Ant2



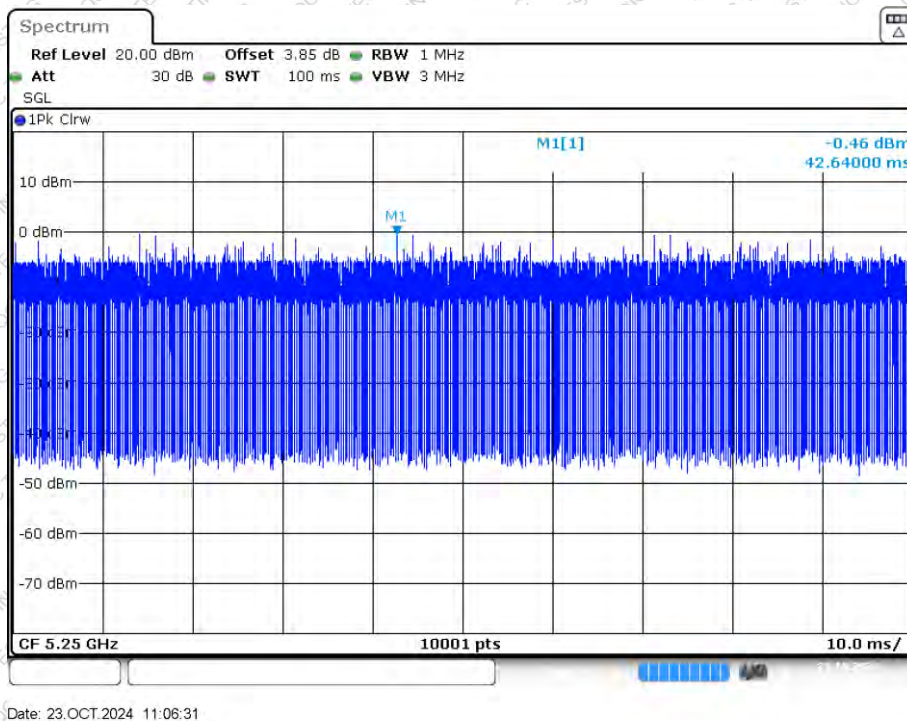
Date: 25.OCT.2024 14:07:07



Duty Cycle NVNT ax80 5775MHz Sum

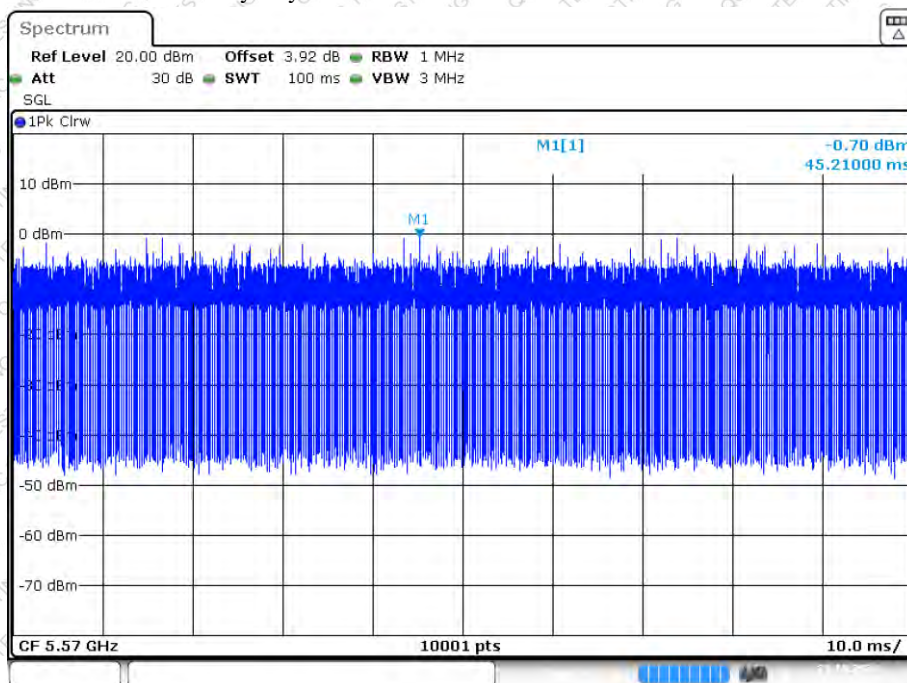


Duty Cycle NVNT ax160 5250MHz Ant1



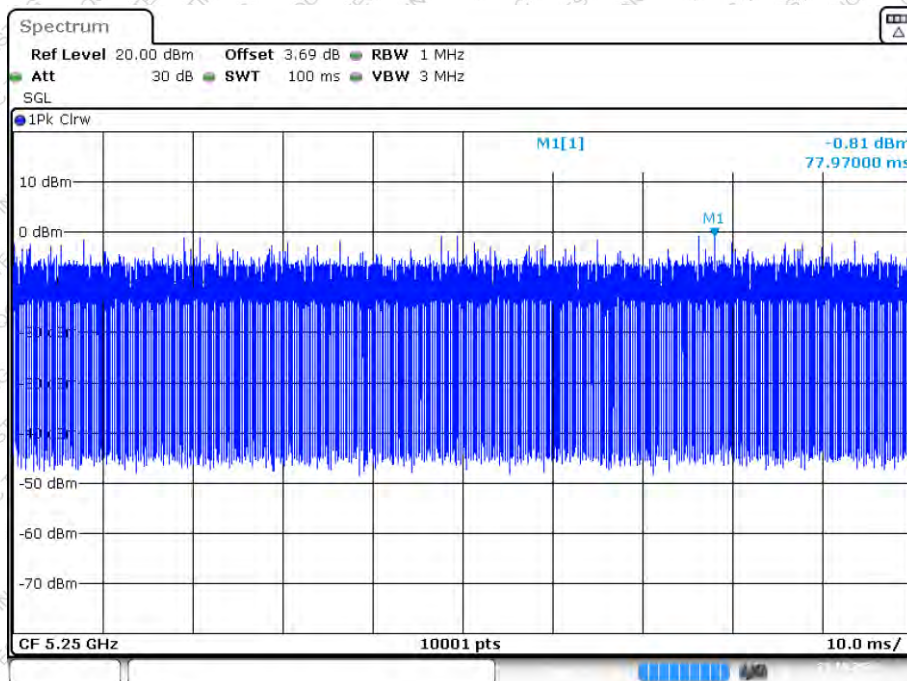


Duty Cycle NVNT ax160 5570MHz Ant1



Date: 23.OCT.2024 11:08:50

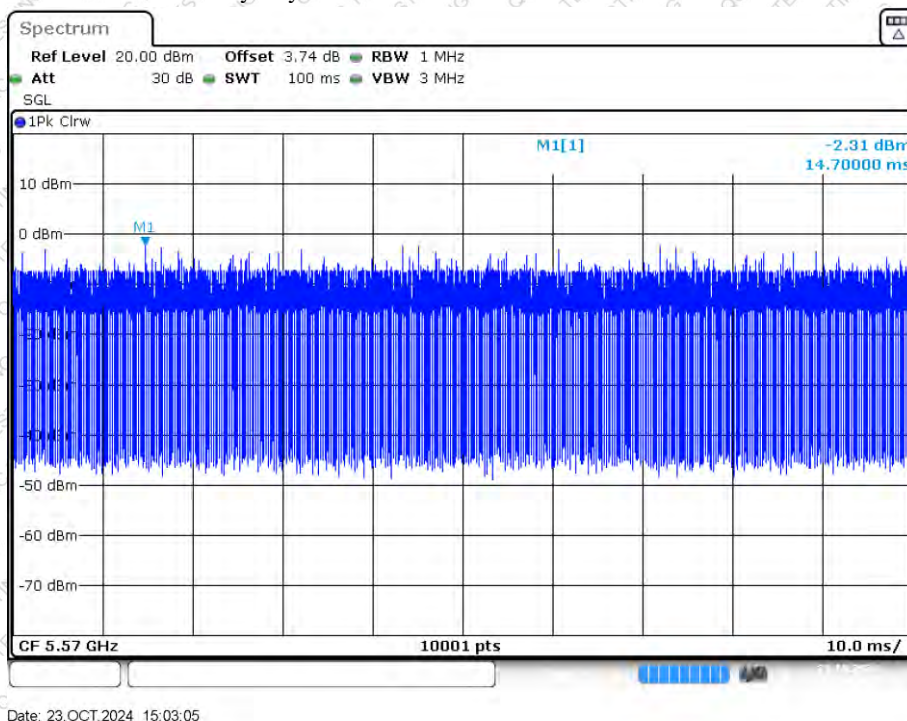
Duty Cycle NVNT ax160 5250MHz Ant2



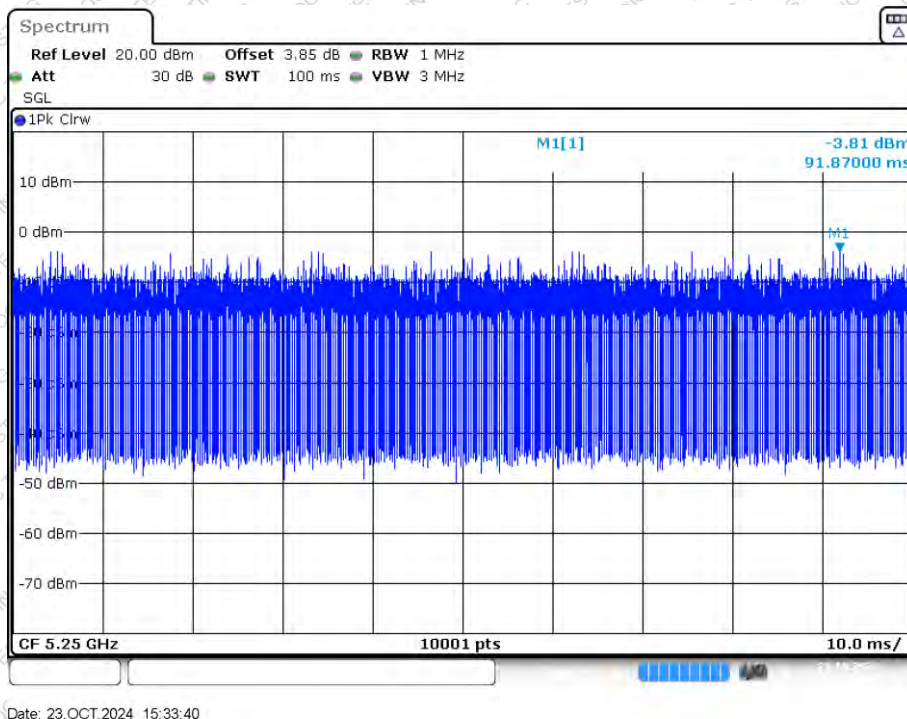
Date: 23.OCT.2024 15:01:20



Duty Cycle NVNT ax160 5570MHz Ant2

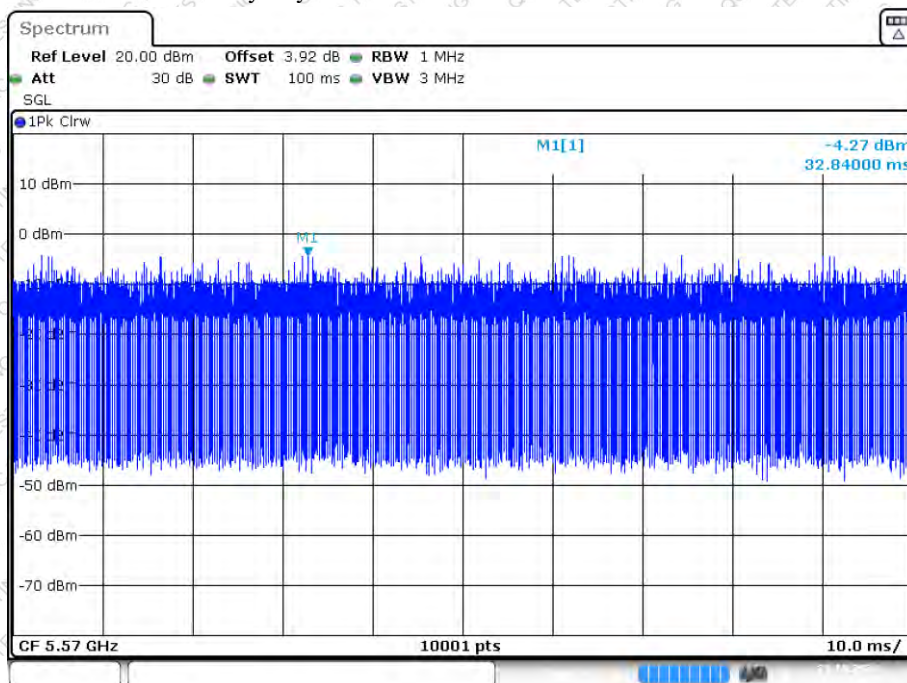


Duty Cycle NVNT ax160 5250MHz Sum





Duty Cycle NVNT ax160 5570MHz Sum



Date: 23.OCT.2024 15:37:48