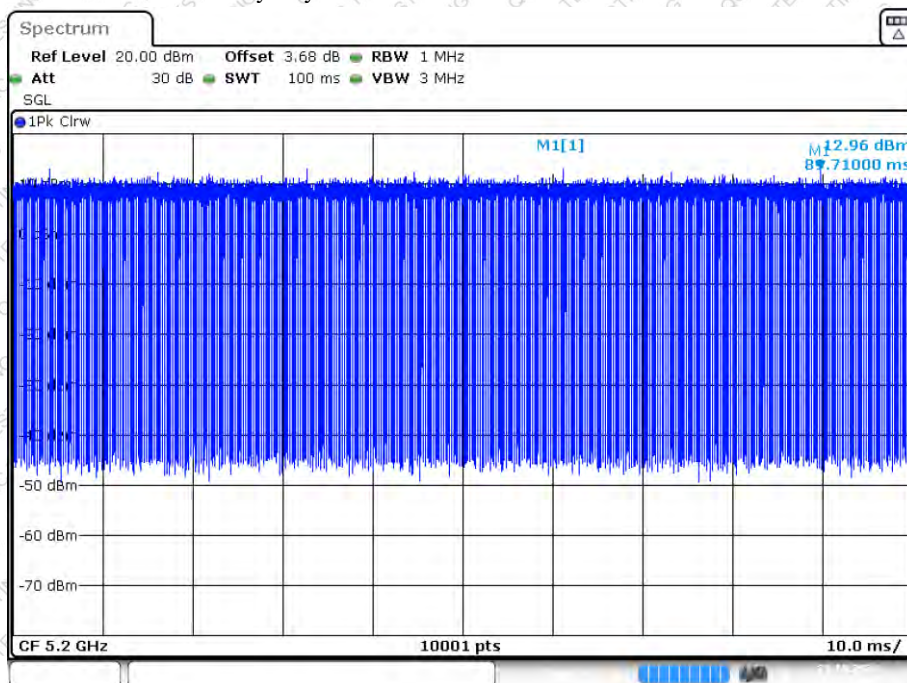


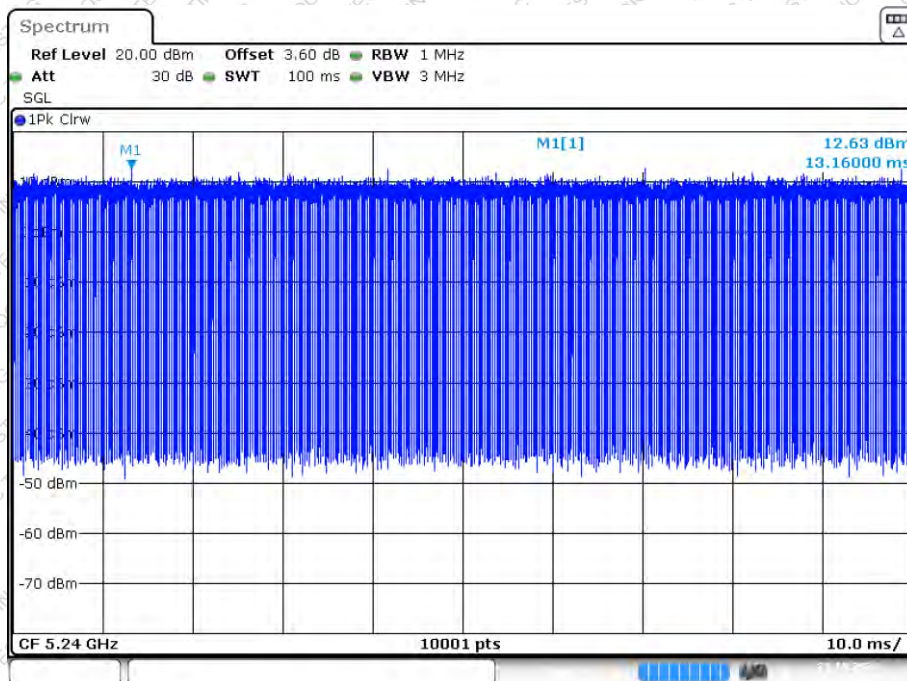


Duty Cycle NVNT n20 5200MHz Ant2



Date: 23.OCT.2024 17:39:46

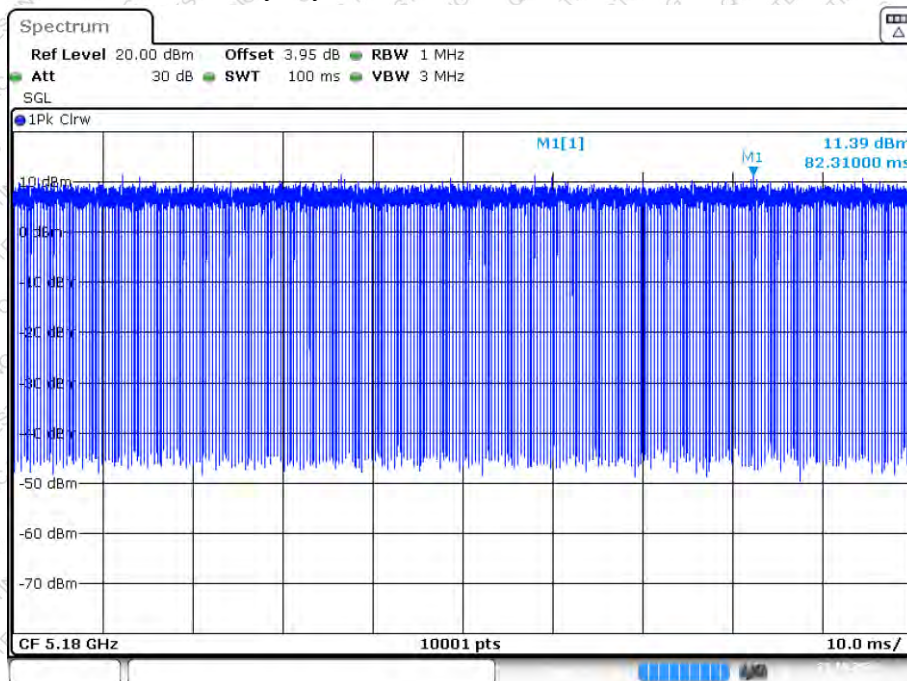
Duty Cycle NVNT n20 5240MHz Ant2



Date: 22.OCT.2024 17:59:24

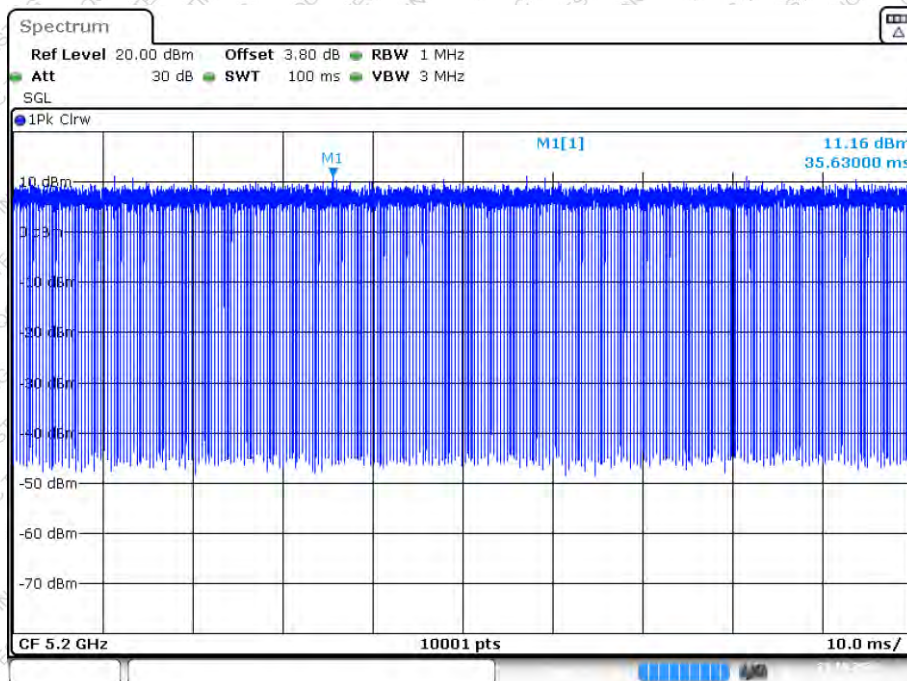


Duty Cycle NVNT n20 5180MHz Sum



Date: 22.OCT.2024 19:27:25

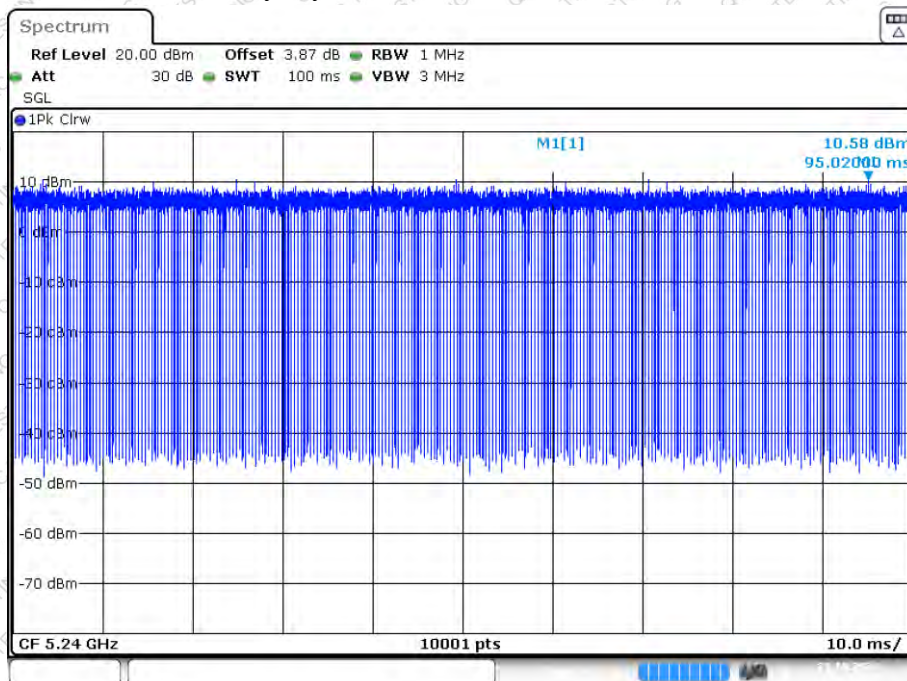
Duty Cycle NVNT n20 5200MHz Sum



Date: 23.OCT.2024 19:33:12

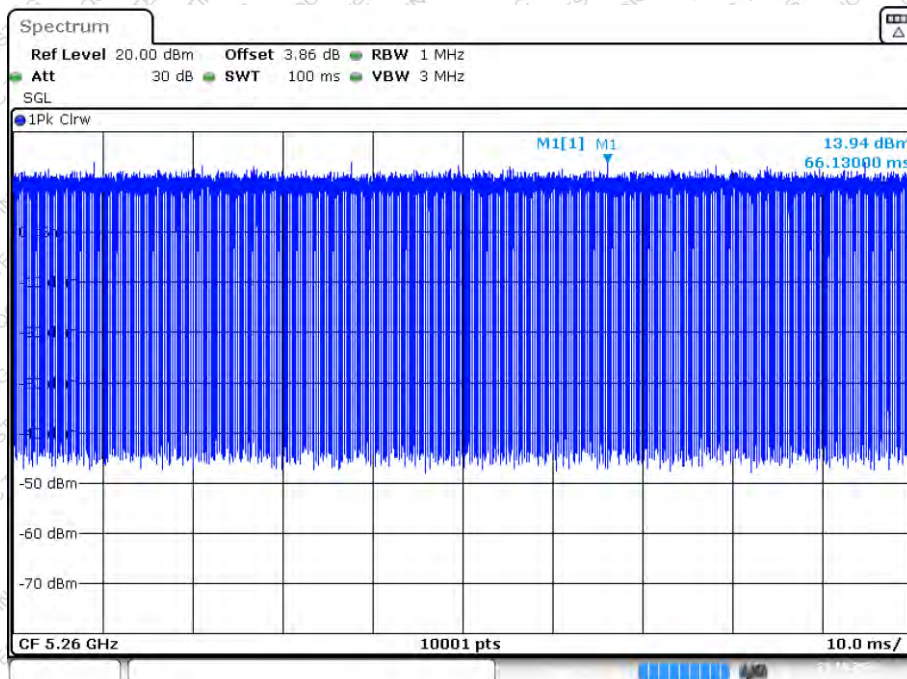


Duty Cycle NVNT n20 5240MHz Sum



Date: 22.OCT.2024 19:31:15

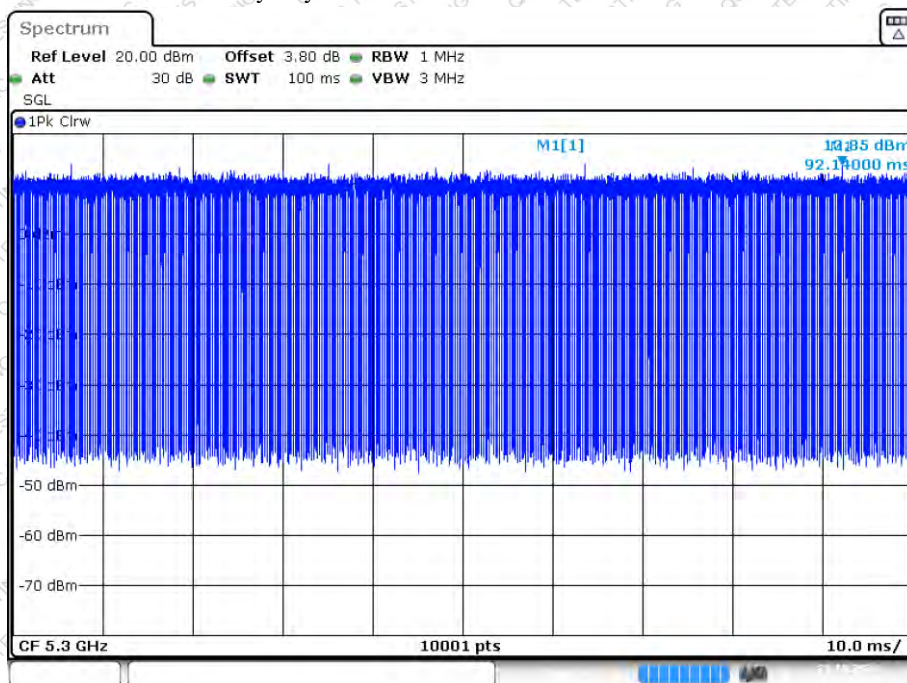
Duty Cycle NVNT n20 5260MHz Ant1



Date: 23.OCT.2024 16:24:13

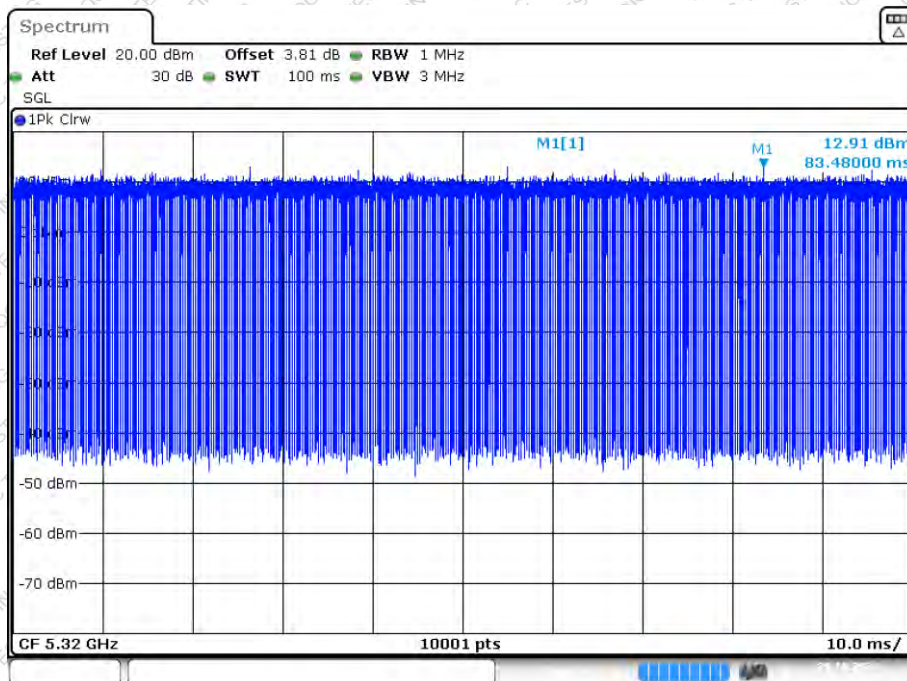


Duty Cycle NVNT n20 5300MHz Ant1



Date: 23.OCT.2024 16:25:16

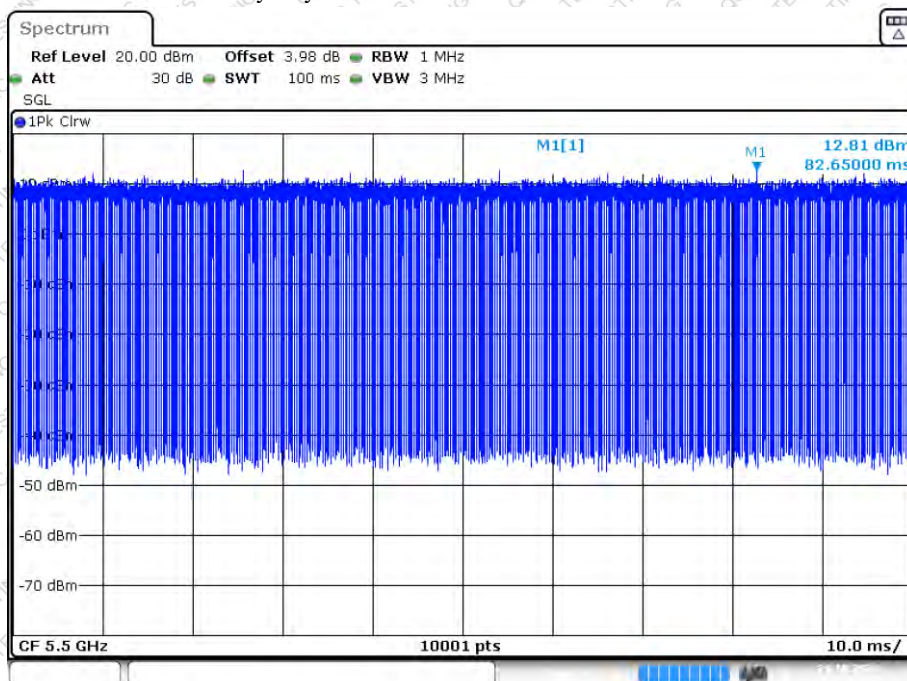
Duty Cycle NVNT n20 5320MHz Ant1



Date: 28.OCT.2024 07:55:57

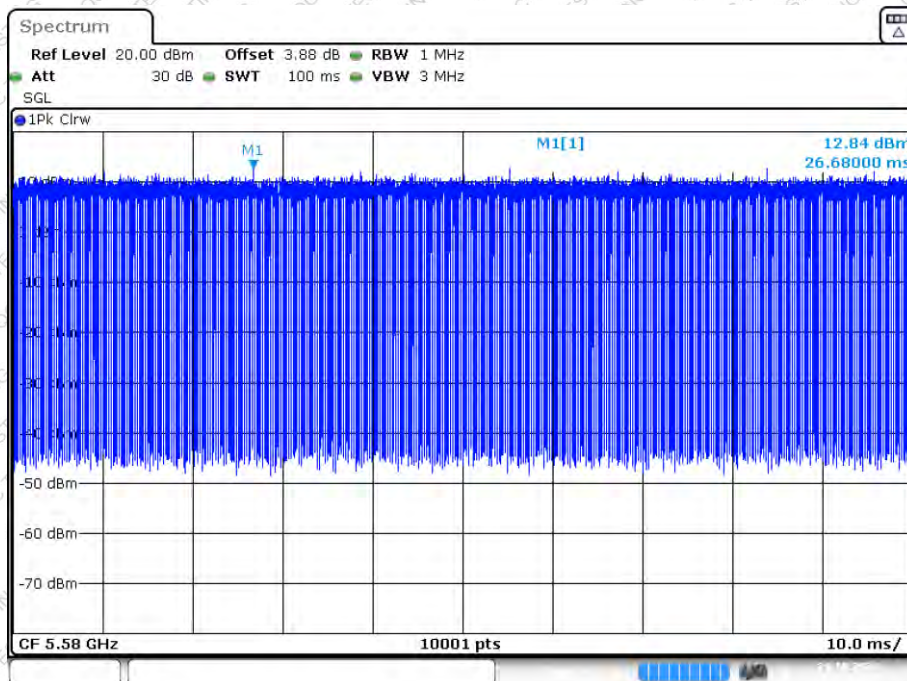


Duty Cycle NVNT n20 5500MHz Ant1



Date: 24.OCT.2024 16:53:26

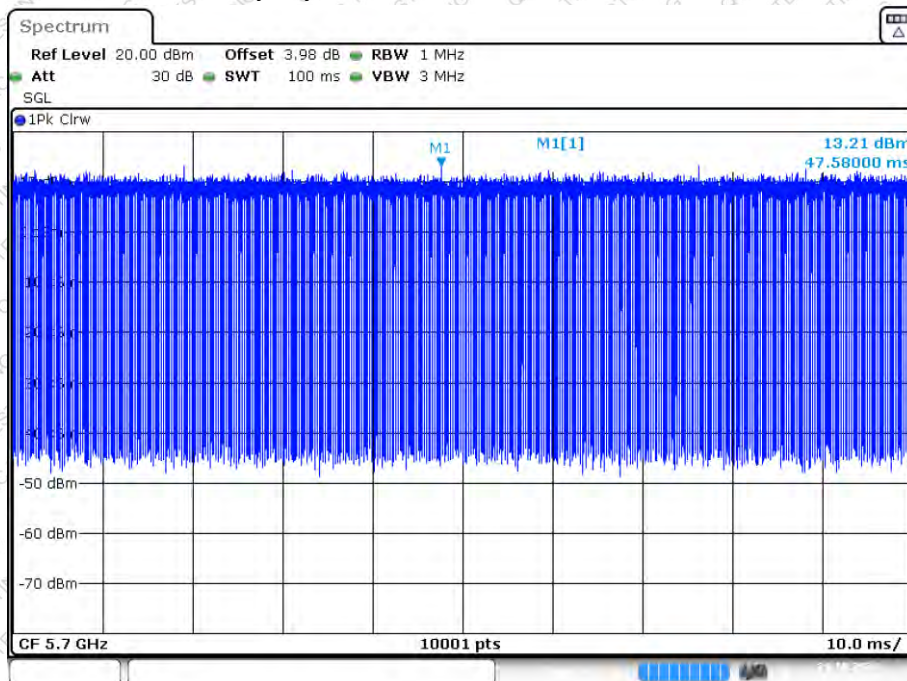
Duty Cycle NVNT n20 5580MHz Ant1



Date: 24.OCT.2024 16:54:55

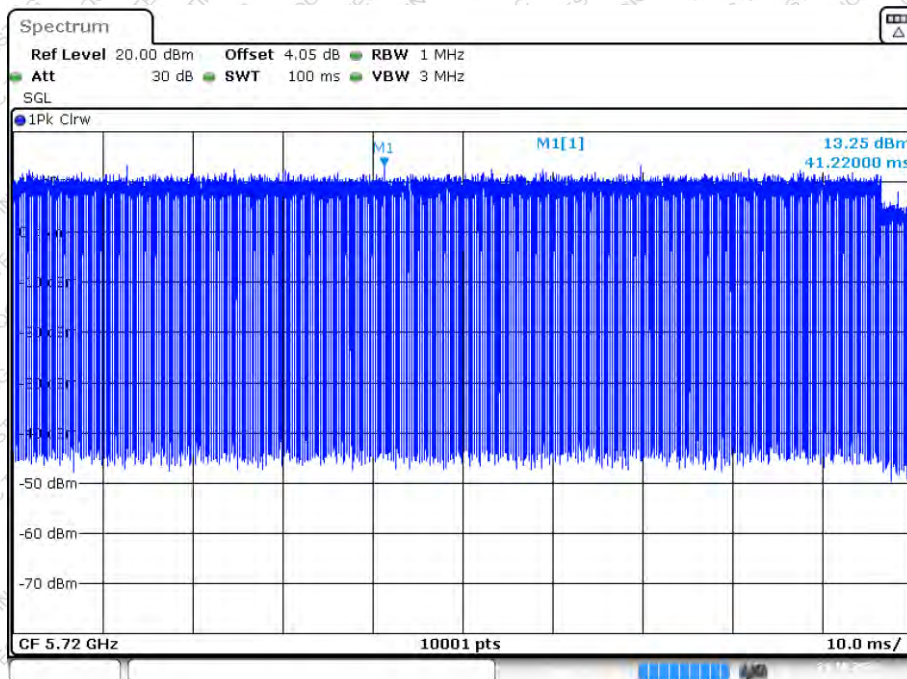


Duty Cycle NVNT n20 5700MHz Ant1



Date: 24.OCT.2024 16:56:54

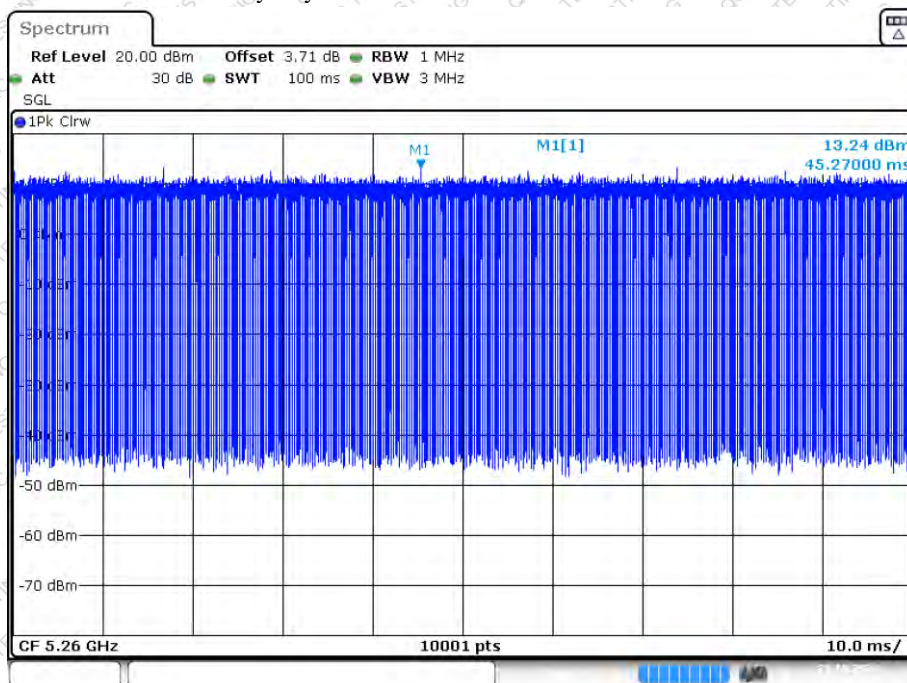
Duty Cycle NVNT n20 5720MHz Ant1



Date: 24.OCT.2024 16:58:21

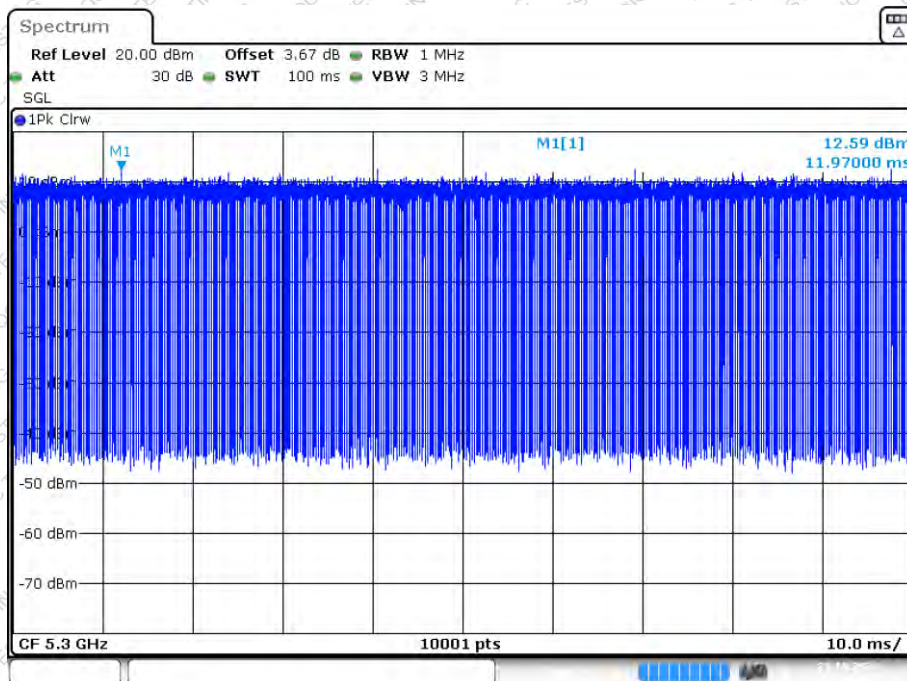


Duty Cycle NVNT n20 5260MHz Ant2



Date: 23.OCT.2024 17:42:02

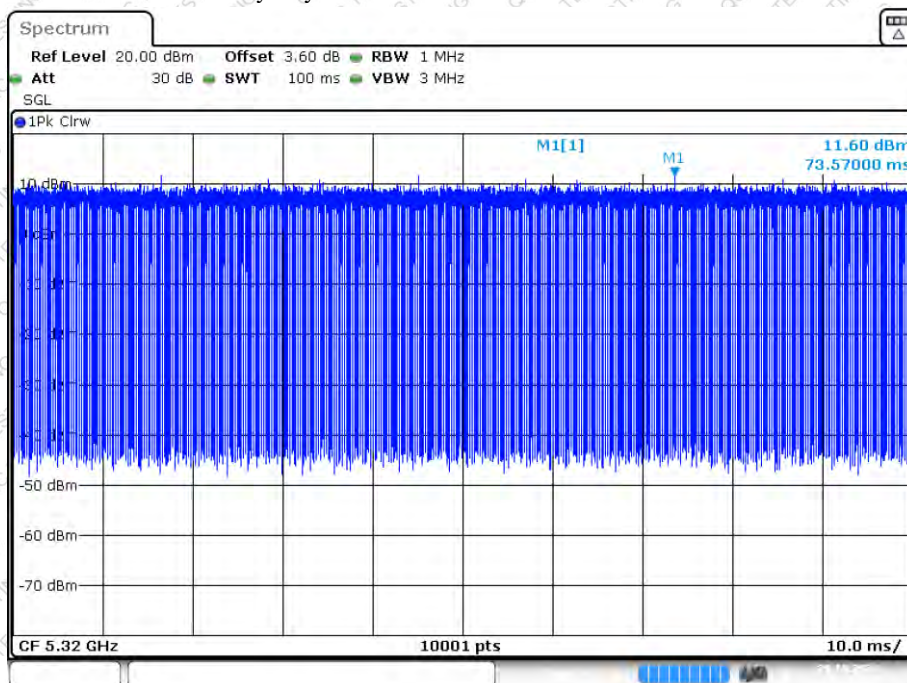
Duty Cycle NVNT n20 5300MHz Ant2



Date: 23.OCT.2024 17:43:03

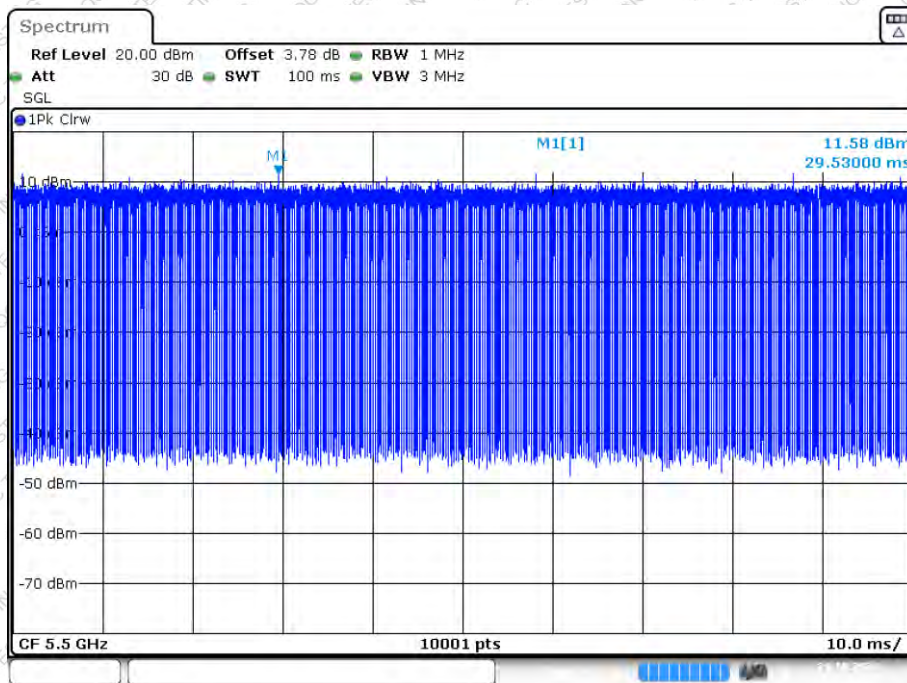


Duty Cycle NVNT n20 5320MHz Ant2



Date: 28.OCT.2024 08:04:15

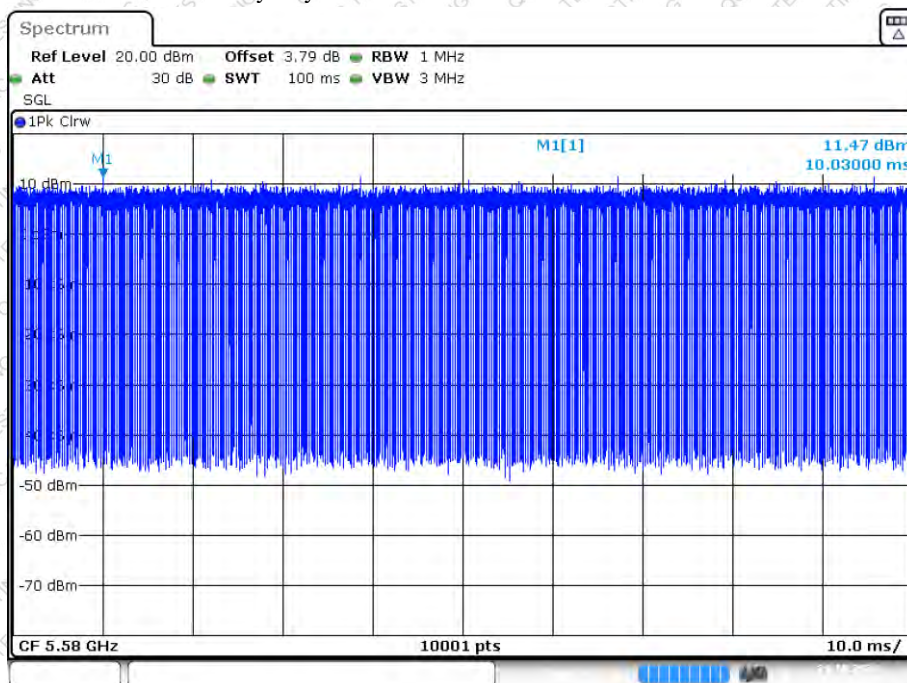
Duty Cycle NVNT n20 5500MHz Ant2



Date: 24.OCT.2024 18:57:30

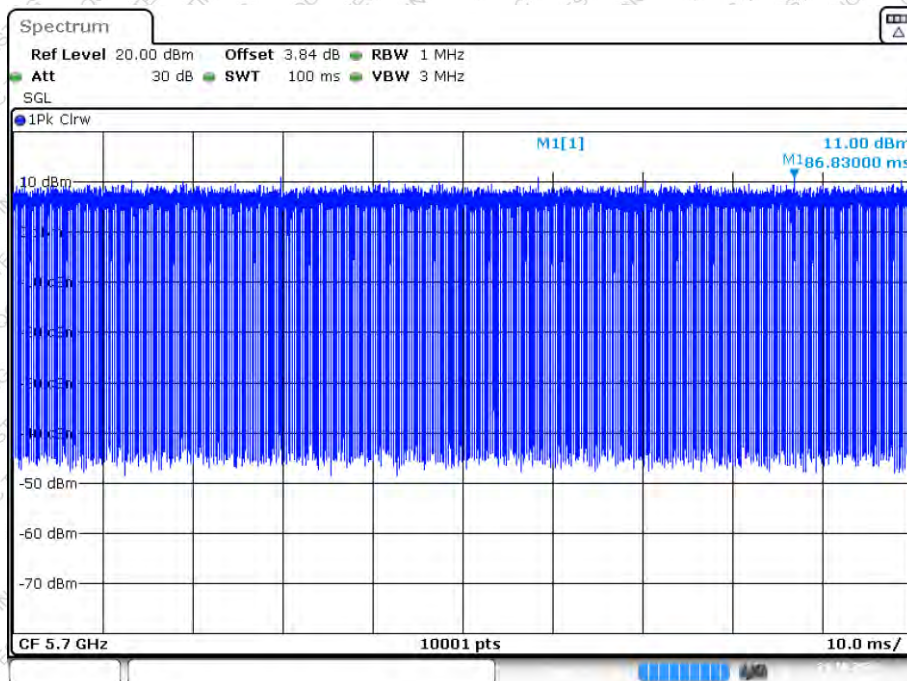


Duty Cycle NVNT n20 5580MHz Ant2



Date: 24.OCT.2024 18:58:34

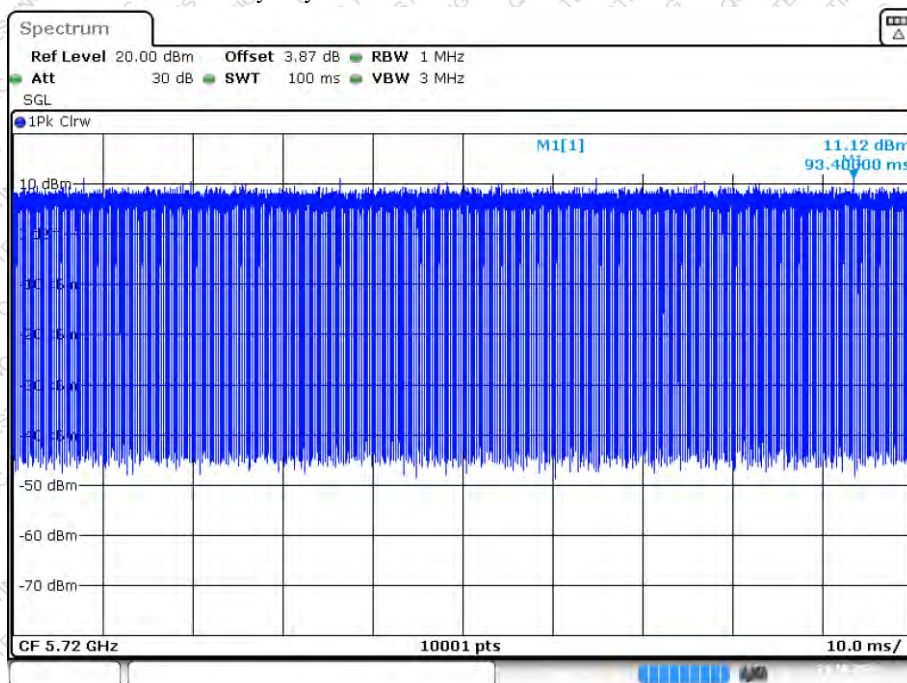
Duty Cycle NVNT n20 5700MHz Ant2



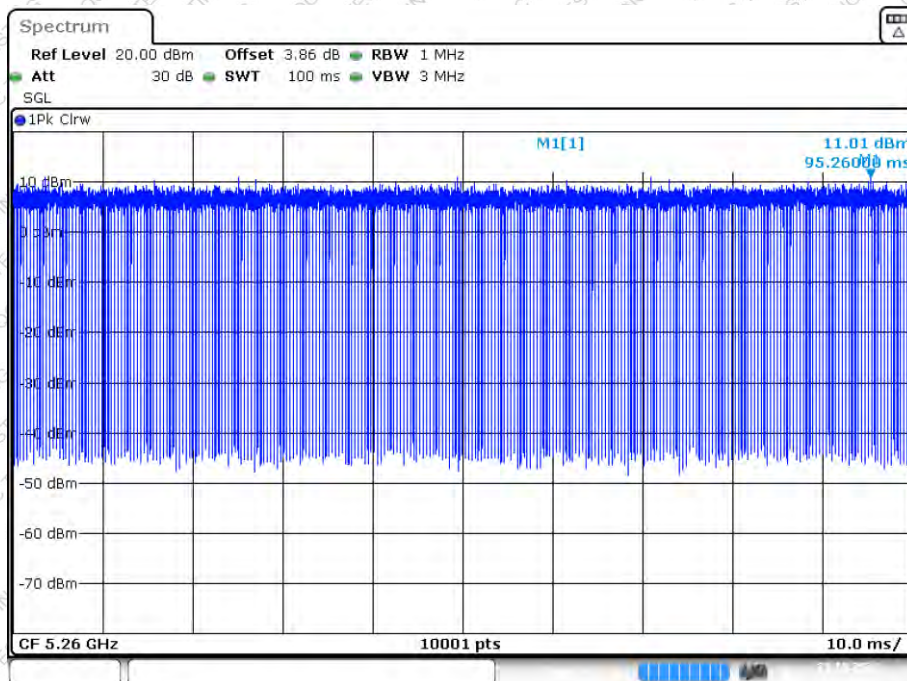
Date: 24.OCT.2024 19:00:47



Duty Cycle NVNT n20 5720MHz Ant2

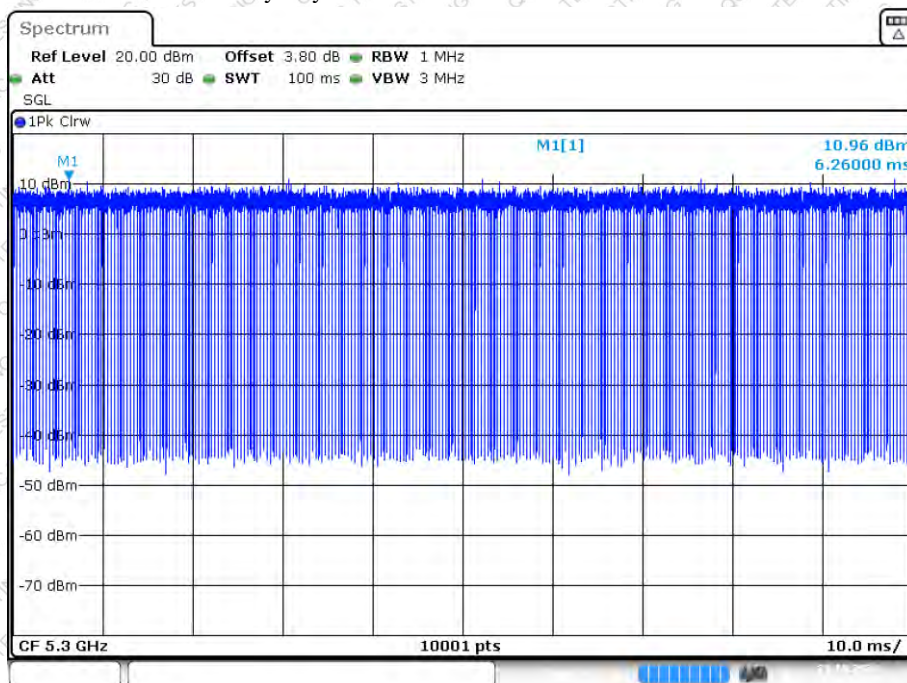


Duty Cycle NVNT n20 5260MHz Sum



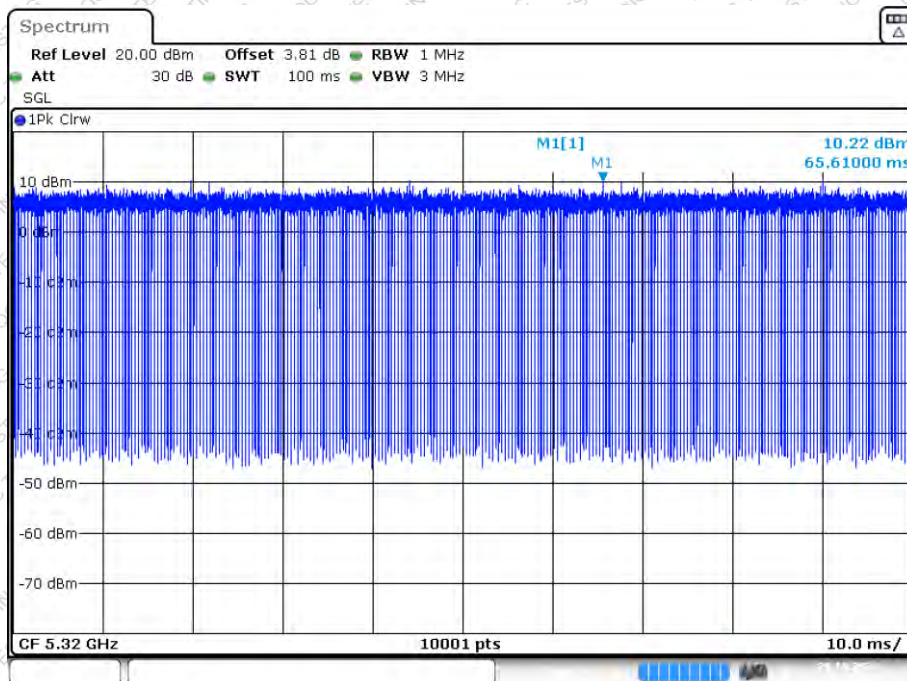


Duty Cycle NVNT n20 5300MHz Sum



Date: 23.OCT.2024 19:36:23

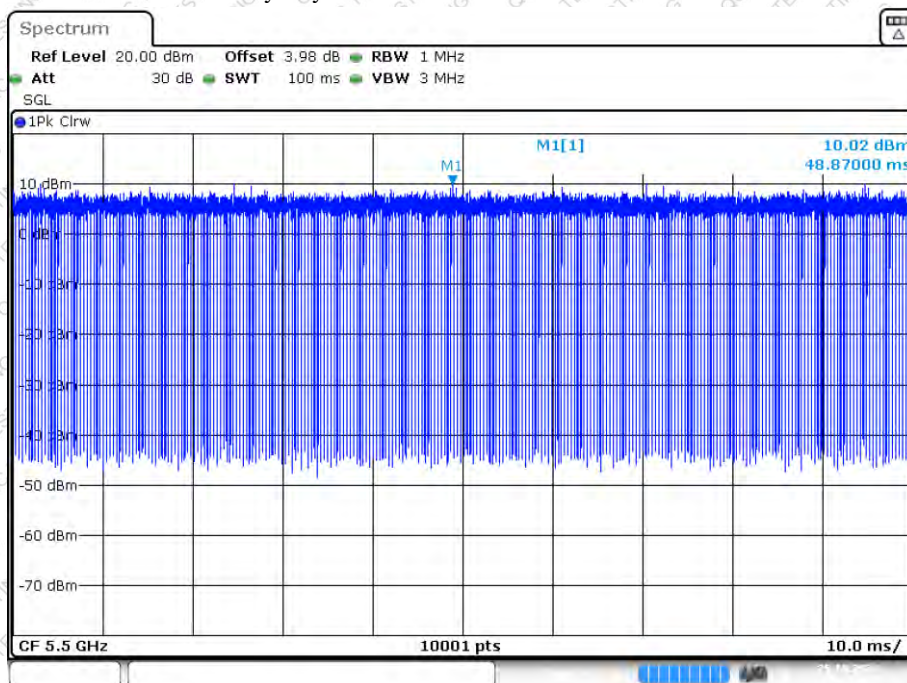
Duty Cycle NVNT n20 5320MHz Sum



Date: 28.OCT.2024 08:25:51

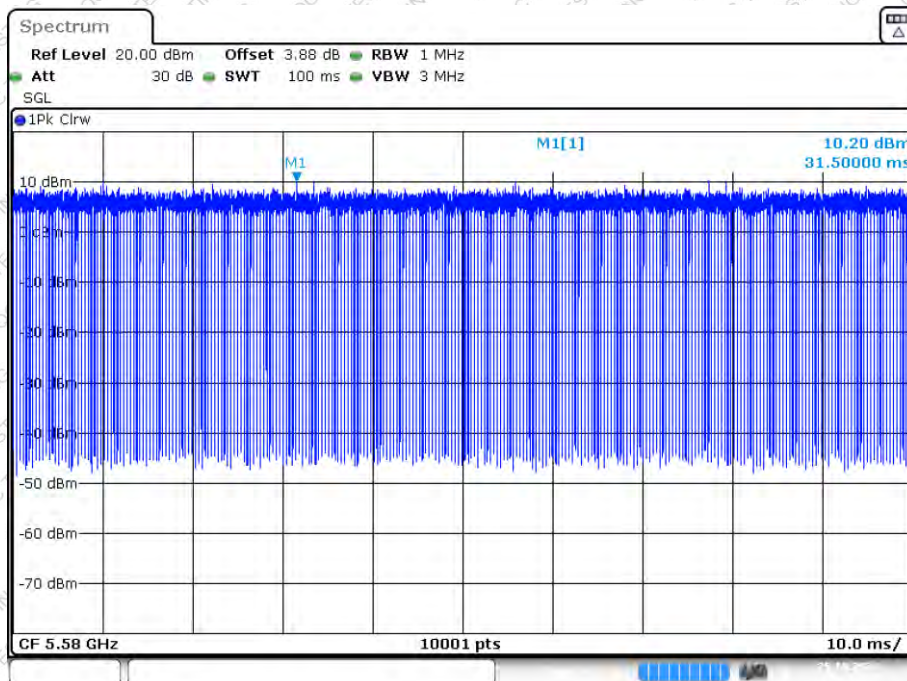


Duty Cycle NVNT n20 5500MHz Sum



Date: 25.OCT.2024 09:23:05

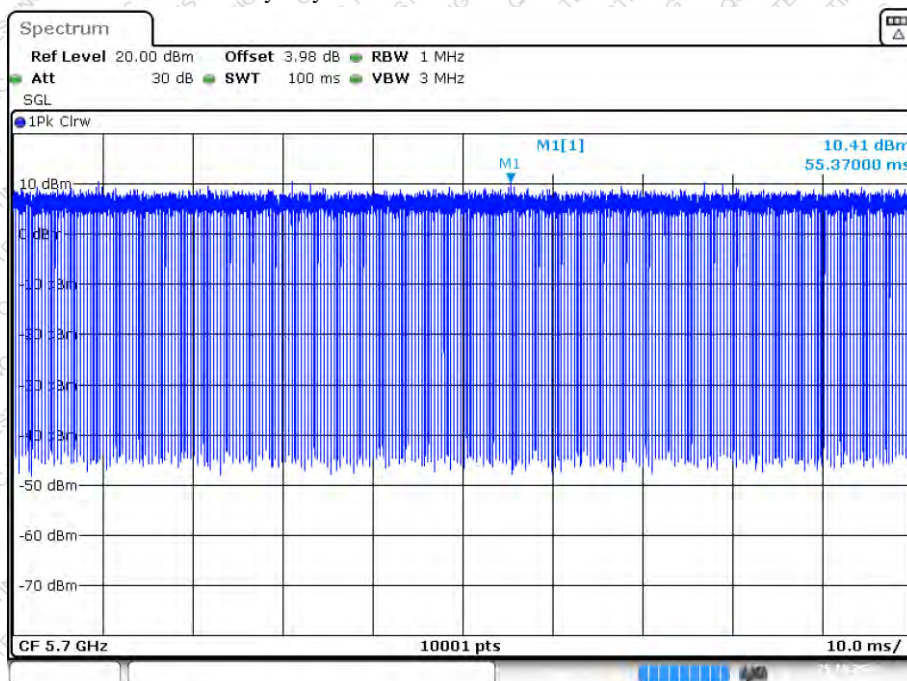
Duty Cycle NVNT n20 5580MHz Sum



Date: 25.OCT.2024 09:24:24

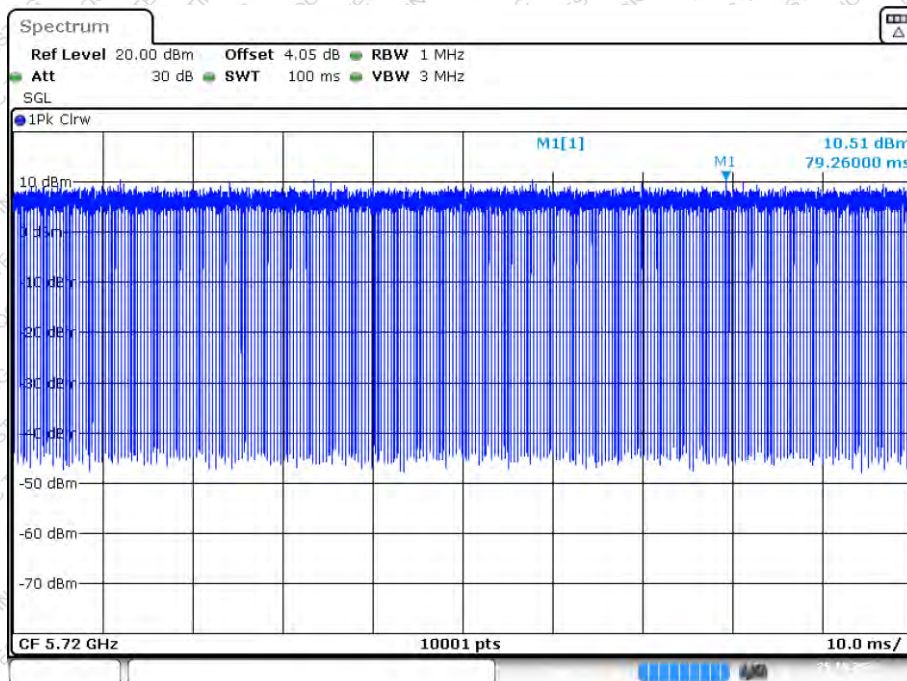


Duty Cycle NVNT n20 5700MHz Sum



Date: 25.OCT.2024 09:25:43

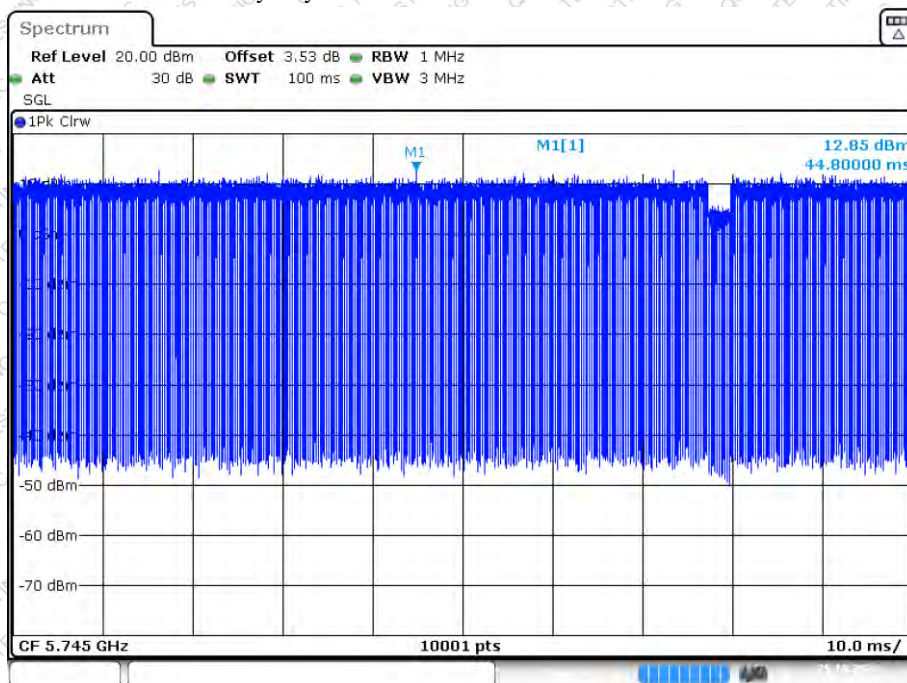
Duty Cycle NVNT n20 5720MHz Sum



Date: 25.OCT.2024 09:27:55

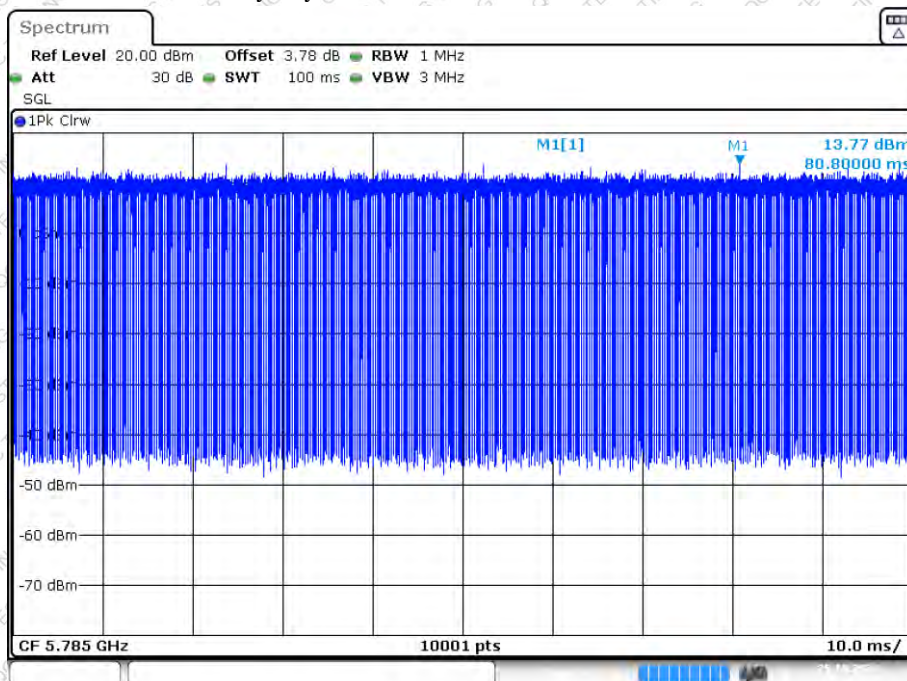


Duty Cycle NVNT n20 5745MHz Ant1



Date: 25.OCT.2024 10:42:12

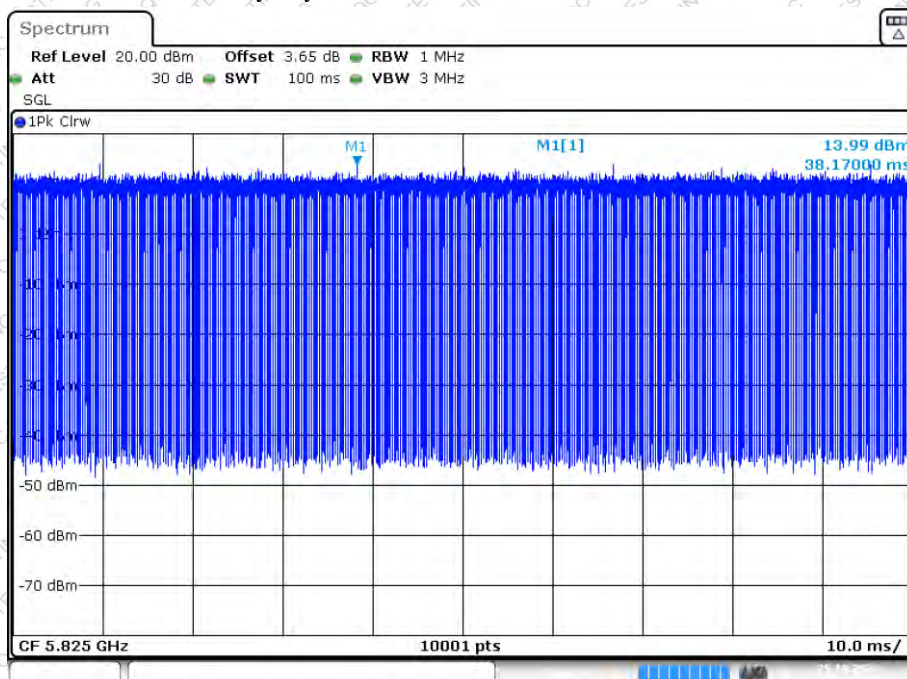
Duty Cycle NVNT n20 5785MHz Ant1



Date: 25.OCT.2024 10:45:32

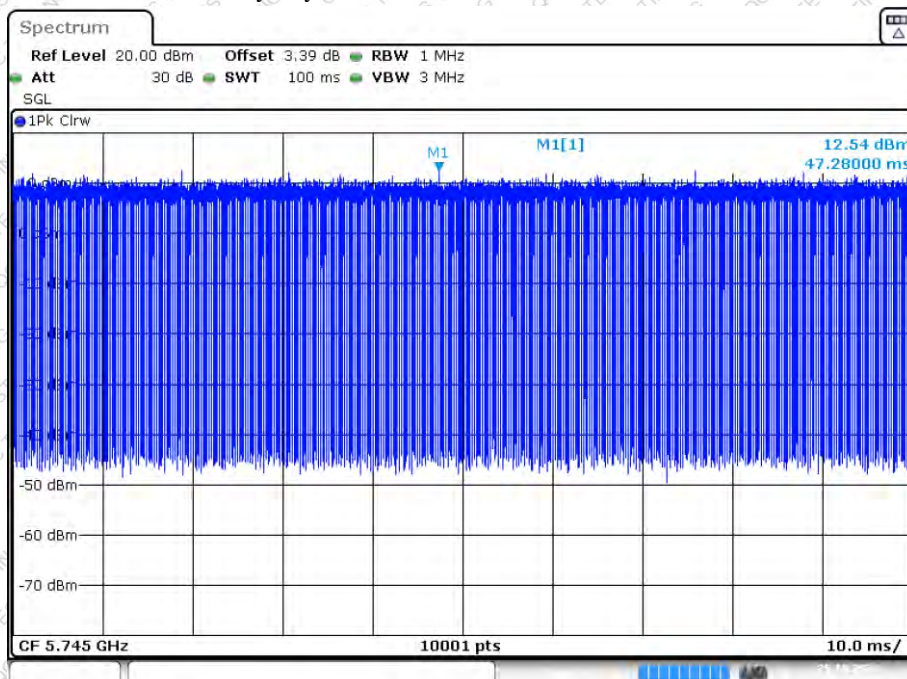


Duty Cycle NVNT n20 5825MHz Ant1



Date: 25.OCT.2024 10:47:53

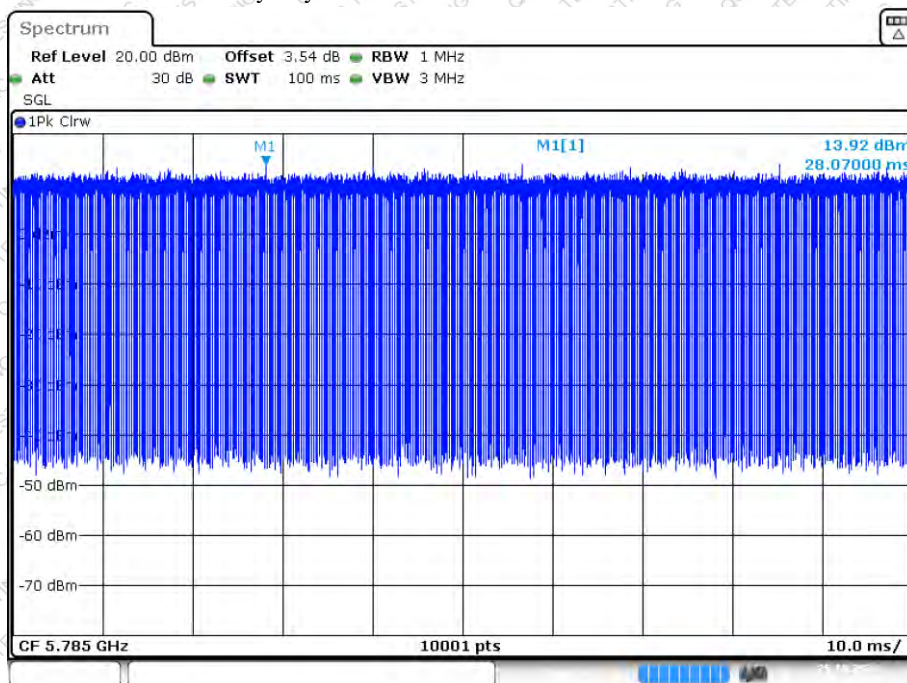
Duty Cycle NVNT n20 5745MHz Ant2



Date: 25.OCT.2024 11:40:05

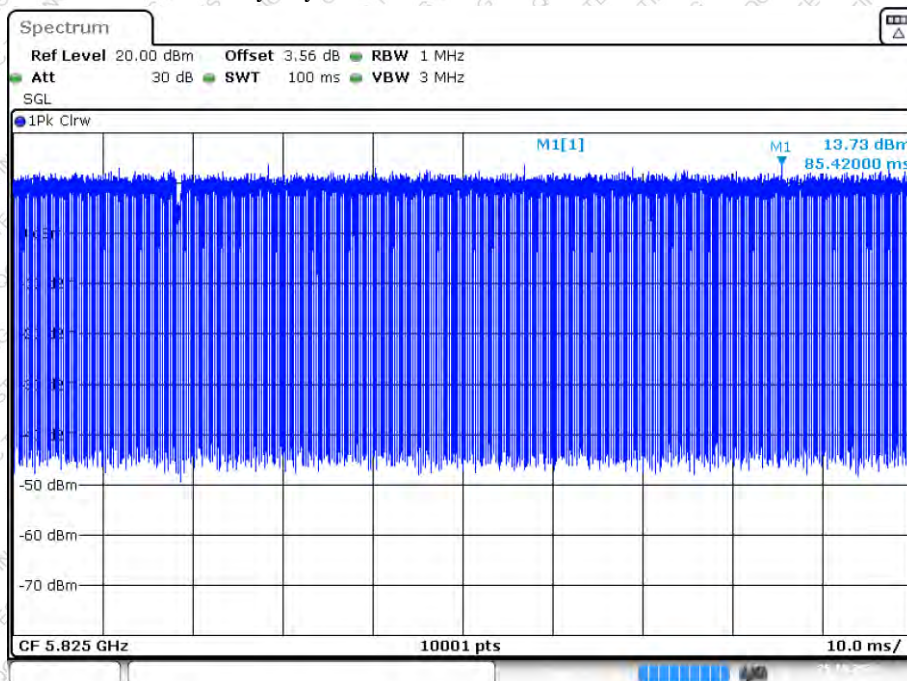


Duty Cycle NVNT n20 5785MHz Ant2



Date: 25.OCT.2024 11:42:33

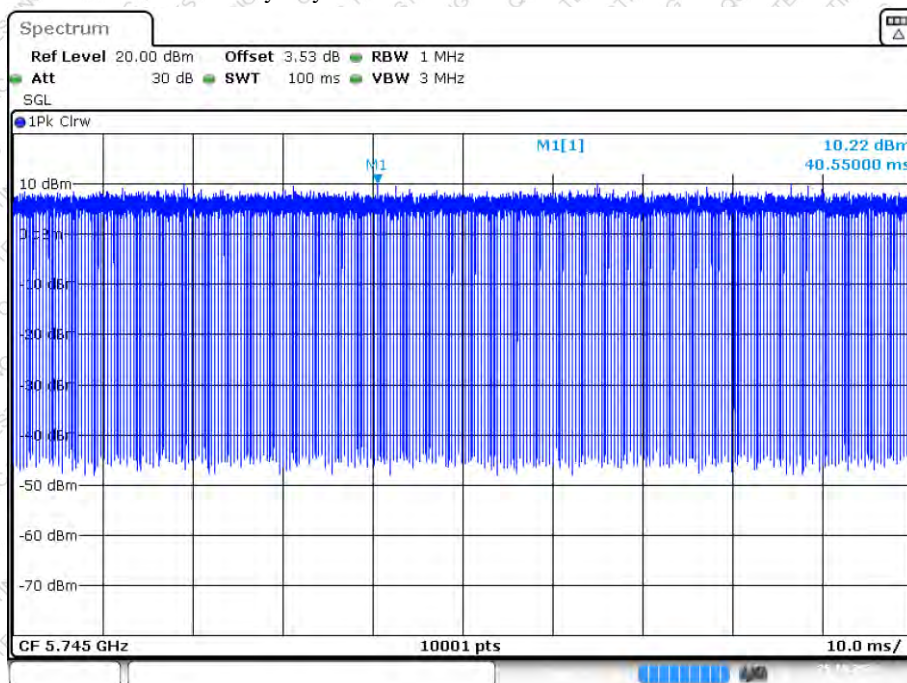
Duty Cycle NVNT n20 5825MHz Ant2



Date: 25.OCT.2024 11:45:45

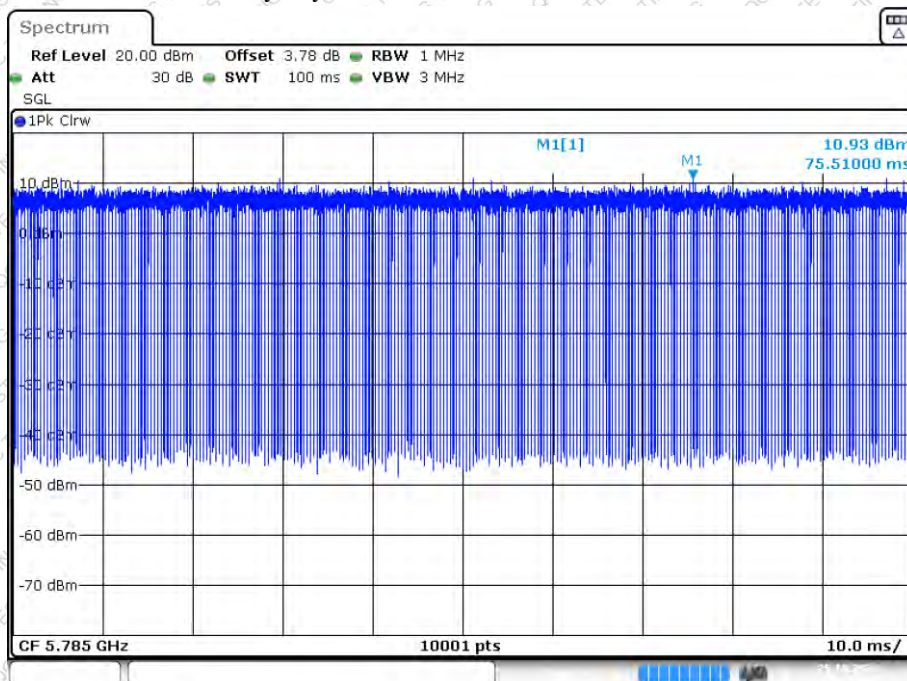


Duty Cycle NVNT n20 5745MHz Sum



Date: 25.OCT.2024 14:23:09

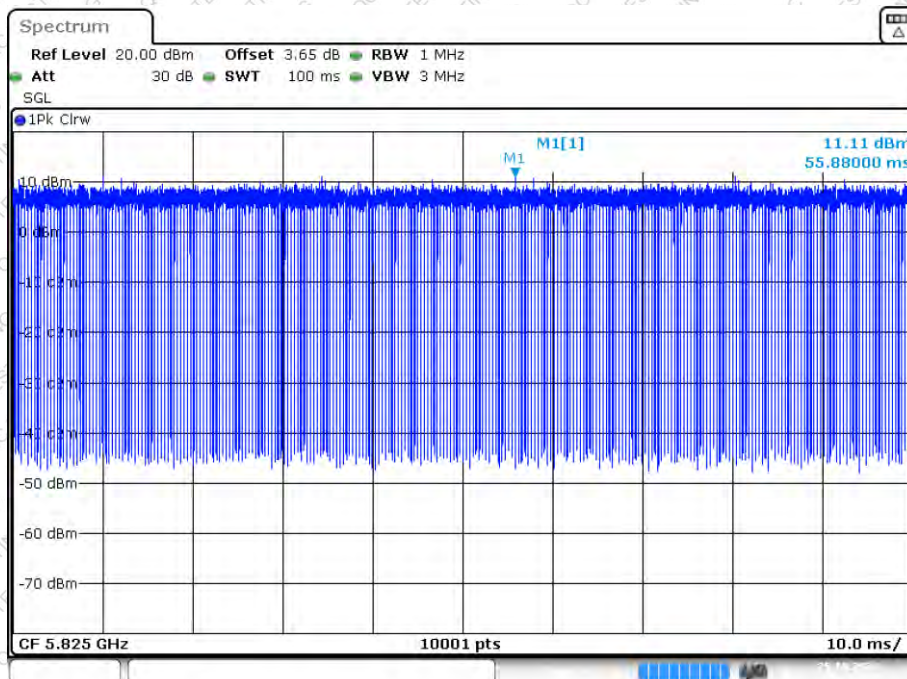
Duty Cycle NVNT n20 5785MHz Sum



Date: 25.OCT.2024 14:30:04

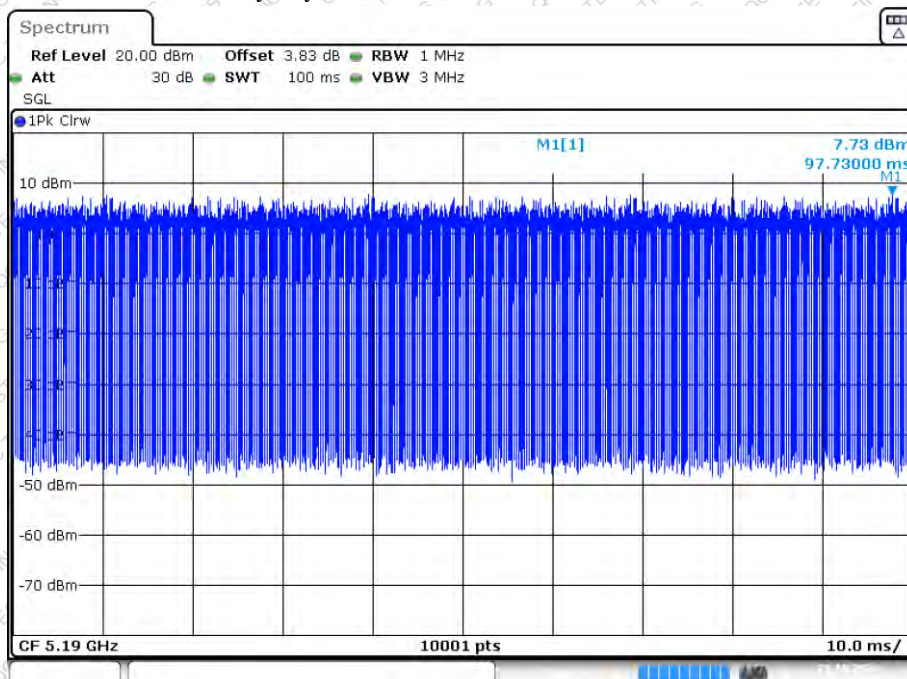


Duty Cycle NVNT n20 5825MHz Sum



Date: 25.OCT.2024 14:32:54

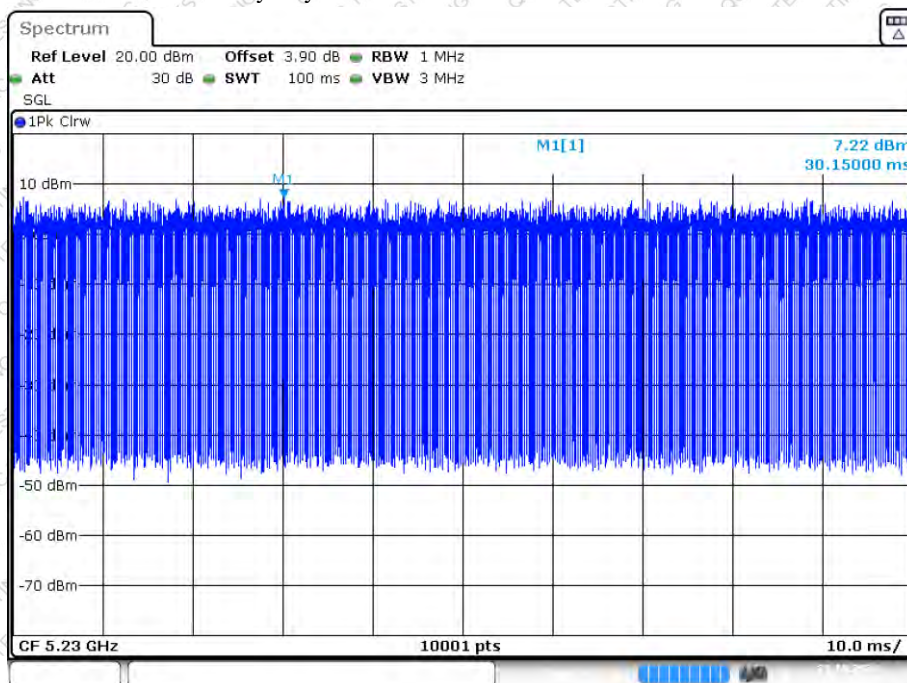
Duty Cycle NVNT n40 5190MHz Ant1



Date: 22.OCT.2024 11:12:15

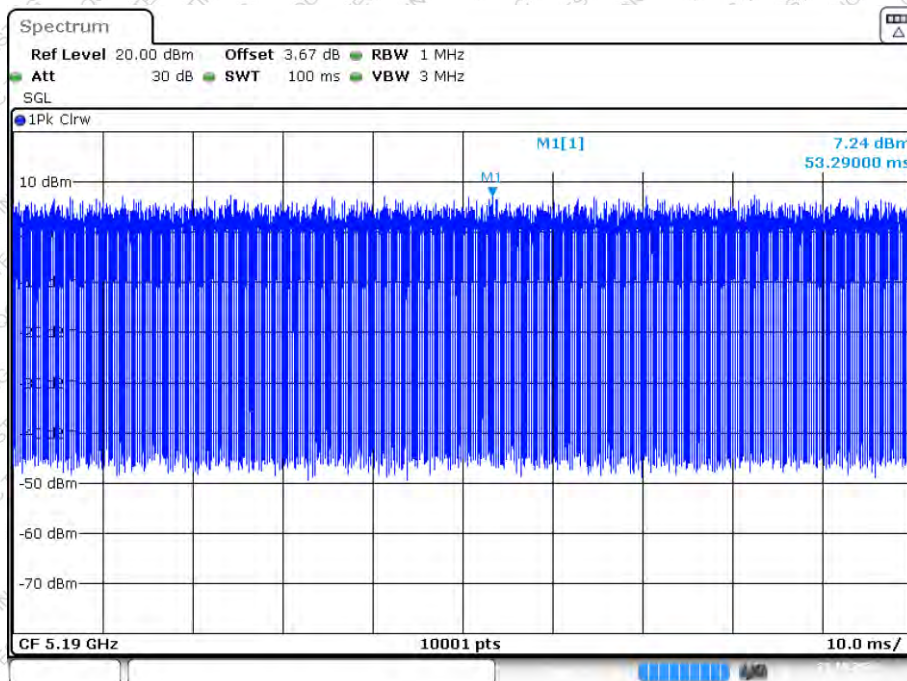


Duty Cycle NVNT n40 5230MHz Ant1



Date: 22.OCT.2024 11:14:22

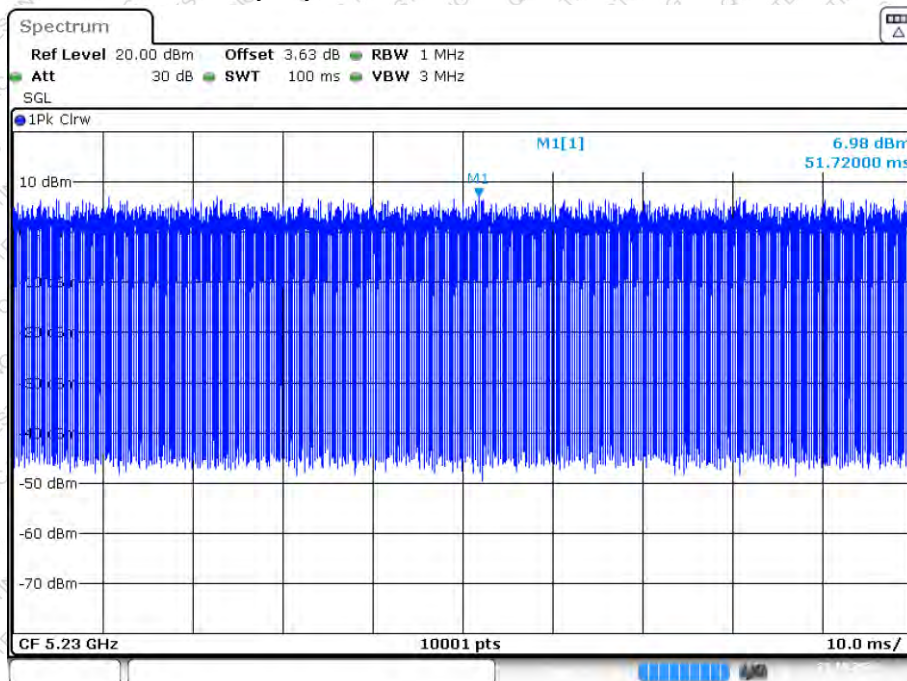
Duty Cycle NVNT n40 5190MHz Ant2



Date: 22.OCT.2024 18:50:23

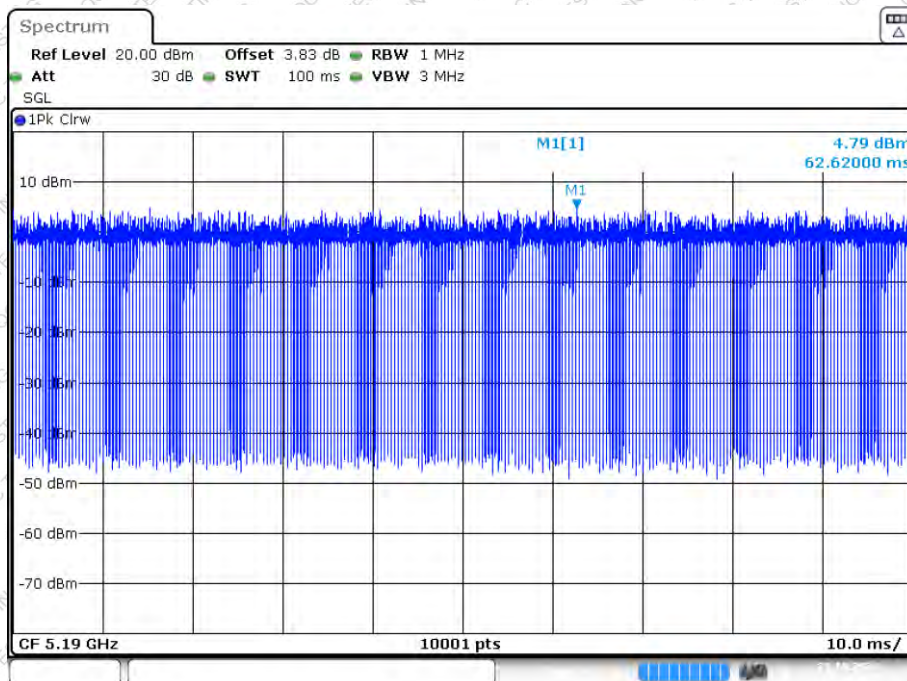


Duty Cycle NVNT n40 5230MHz Ant2



Date: 22.OCT.2024 18:53:02

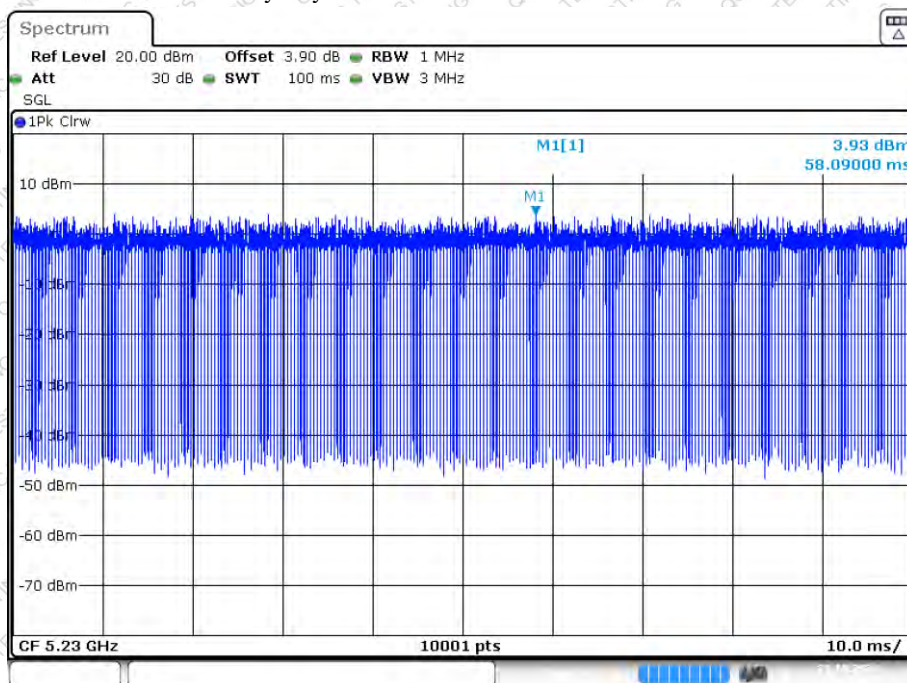
Duty Cycle NVNT n40 5190MHz Sum



Date: 22.OCT.2024 19:49:02

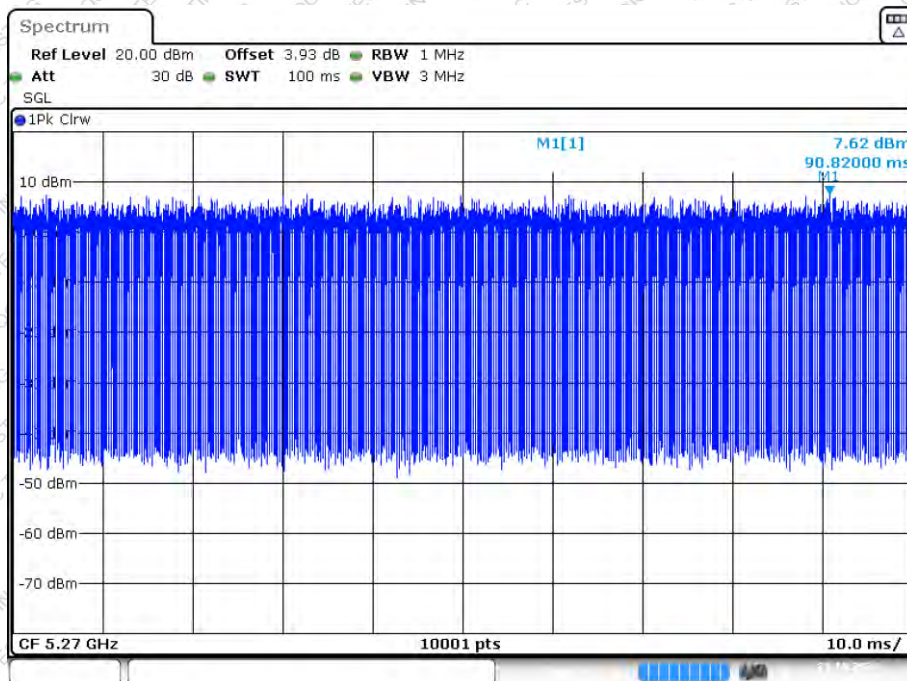


Duty Cycle NVNT n40 5230MHz Sum



Date: 22.OCT.2024 19:50:45

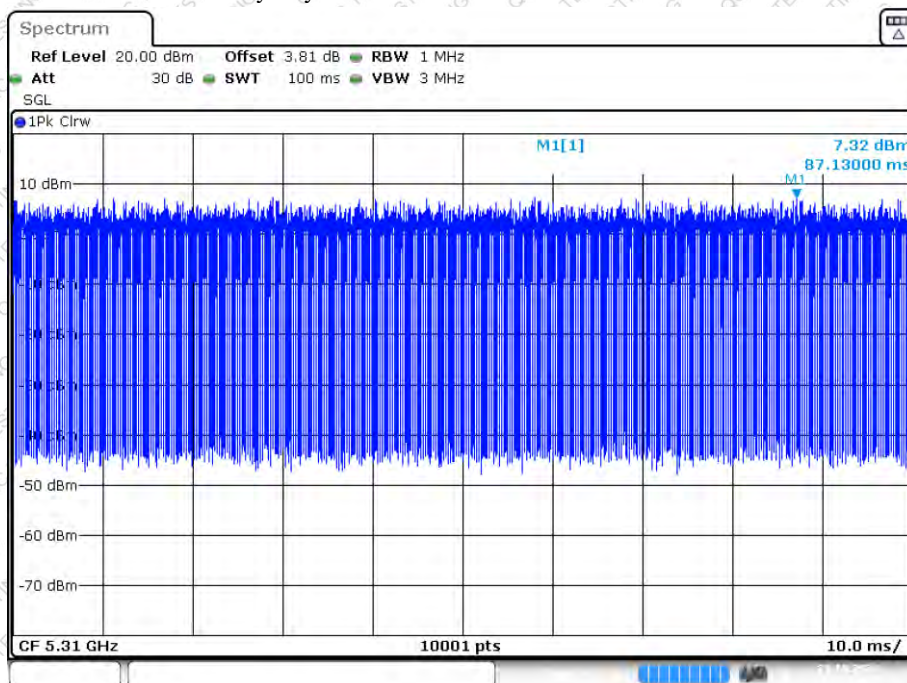
Duty Cycle NVNT n40 5270MHz Ant1



Date: 23.OCT.2024 16:47:23

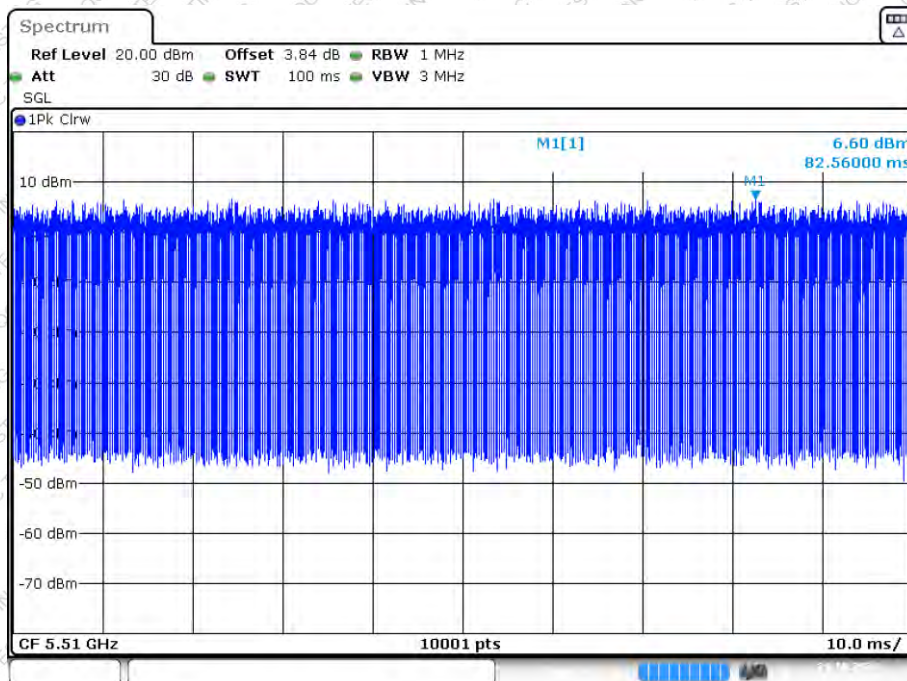


Duty Cycle NVNT n40 5310MHz Ant1



Date: 23.OCT.2024 17:00:02

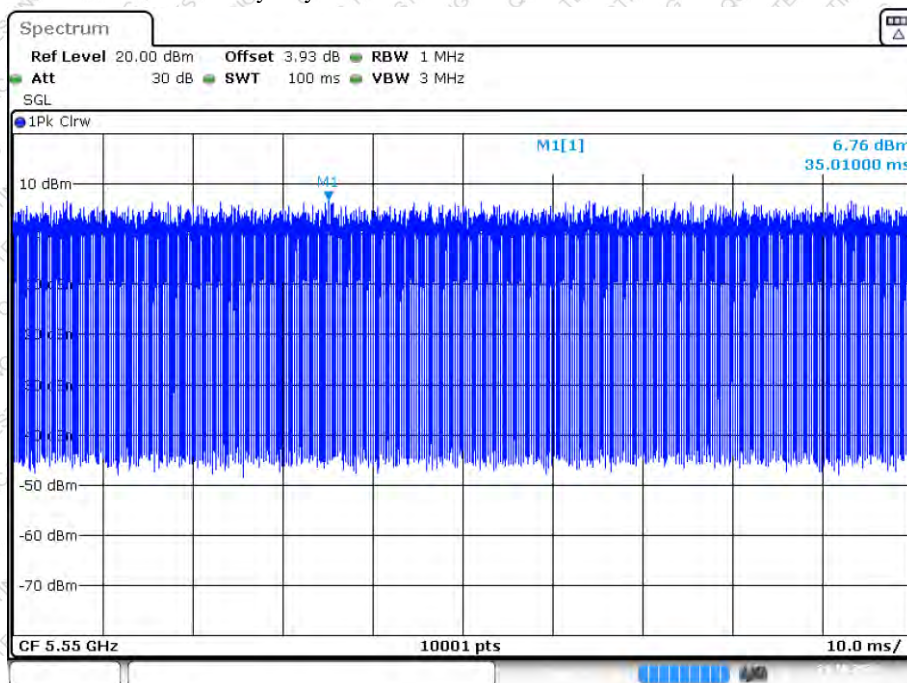
Duty Cycle NVNT n40 5510MHz Ant1



Date: 24.OCT.2024 17:26:30

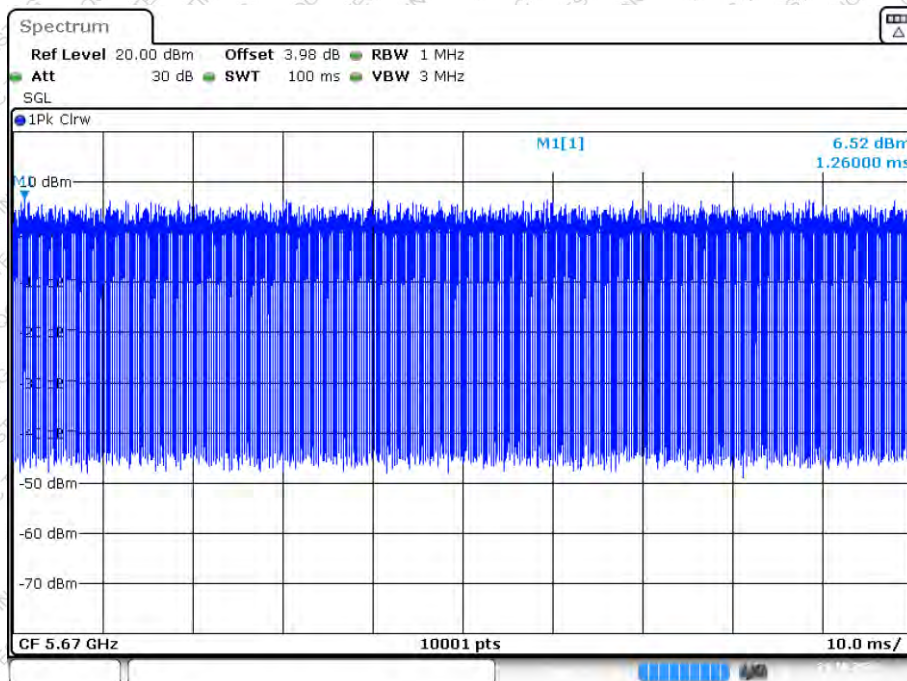


Duty Cycle NVNT n40 5550MHz Ant1



Date: 24.OCT.2024 17:27:51

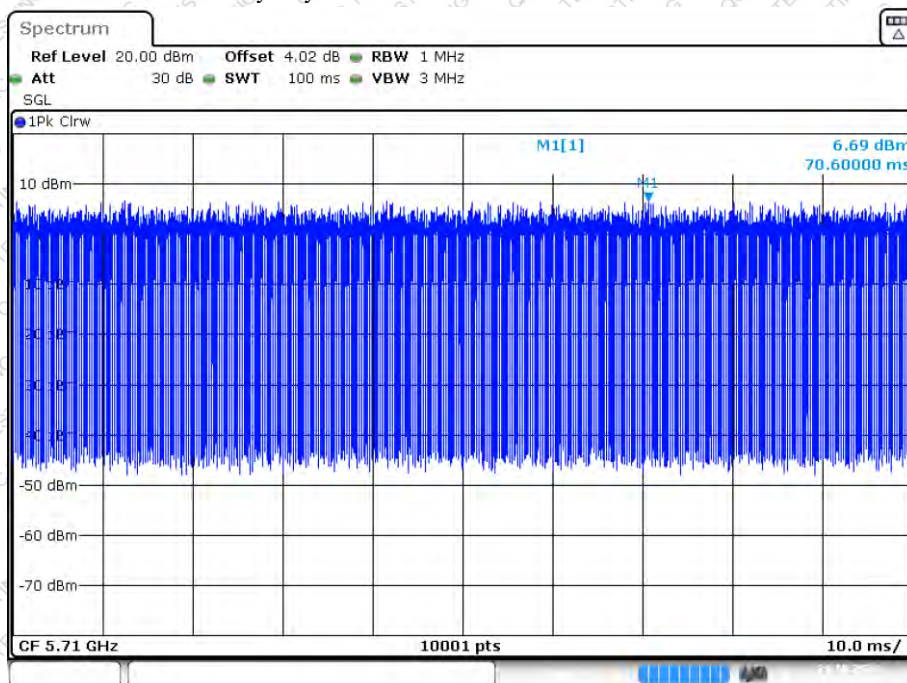
Duty Cycle NVNT n40 5670MHz Ant1



Date: 24.OCT.2024 17:30:20

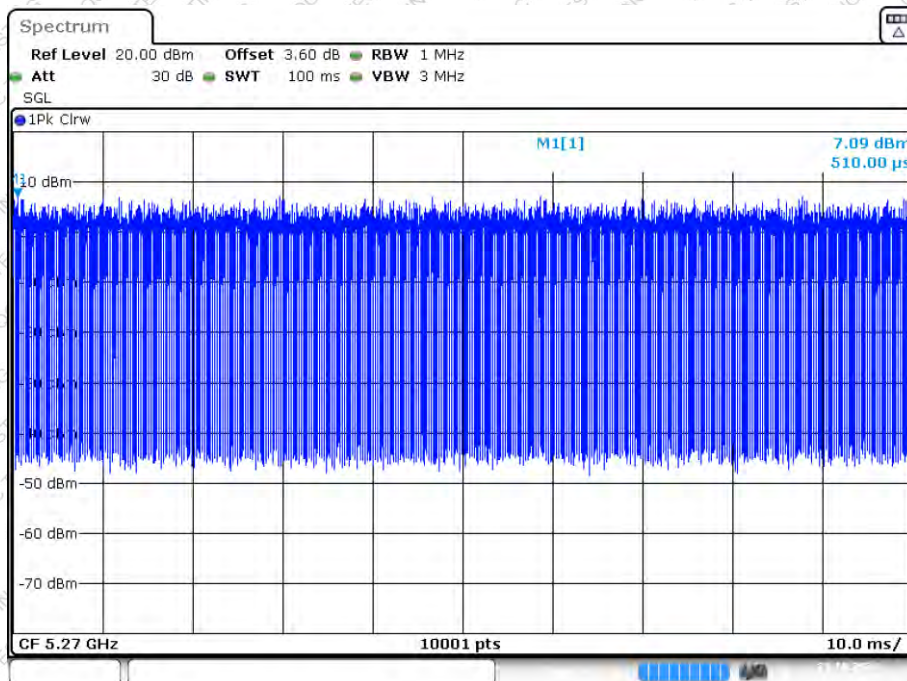


Duty Cycle NVNT n40 5710MHz Ant1



Date: 24.OCT.2024 17:31:23

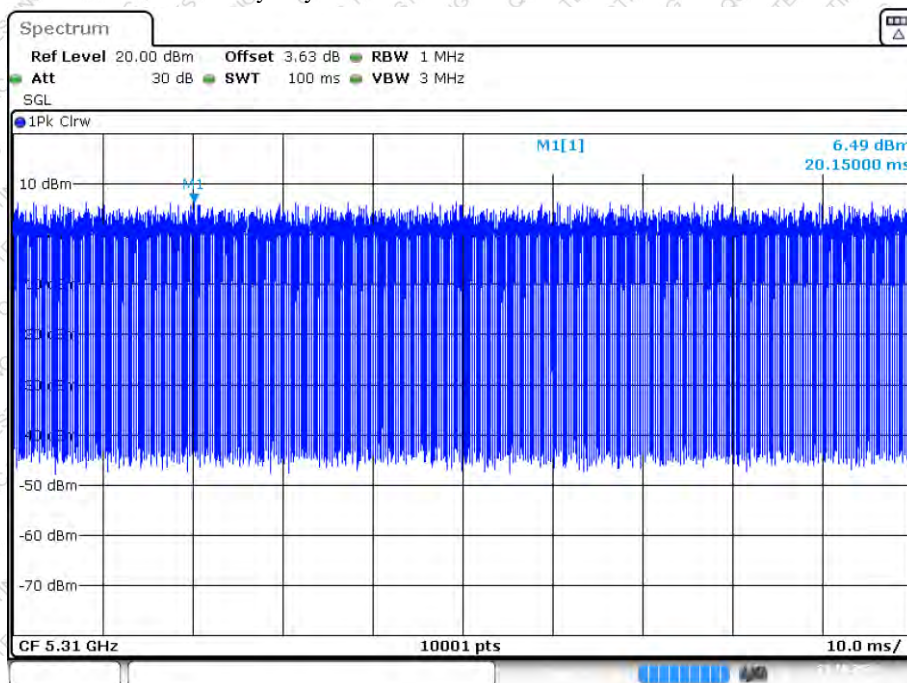
Duty Cycle NVNT n40 5270MHz Ant2



Date: 23.OCT.2024 17:58:00

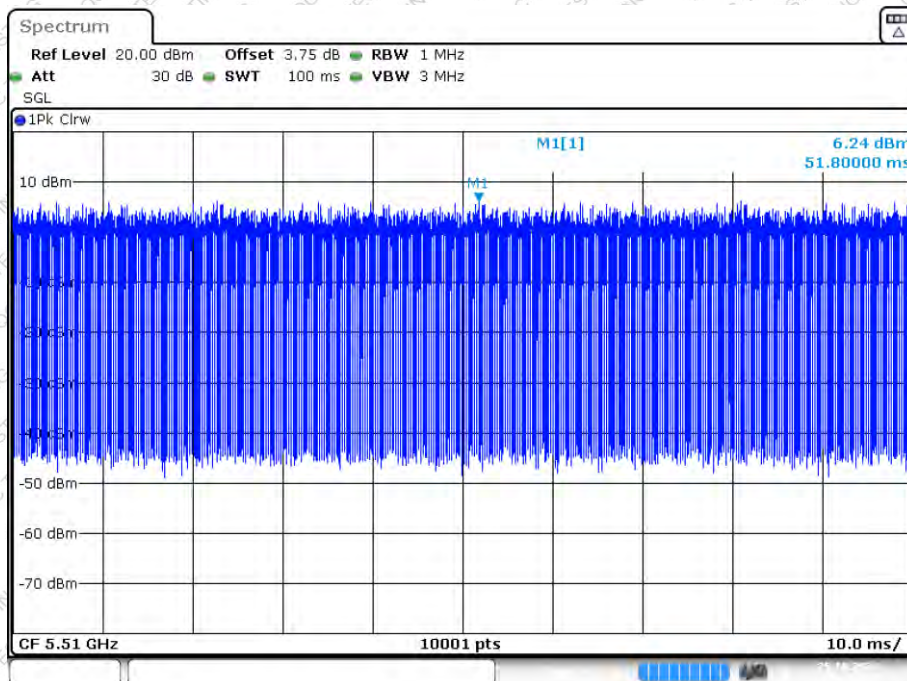


Duty Cycle NVNT n40 5310MHz Ant2



Date: 23.OCT.2024 18:51:06

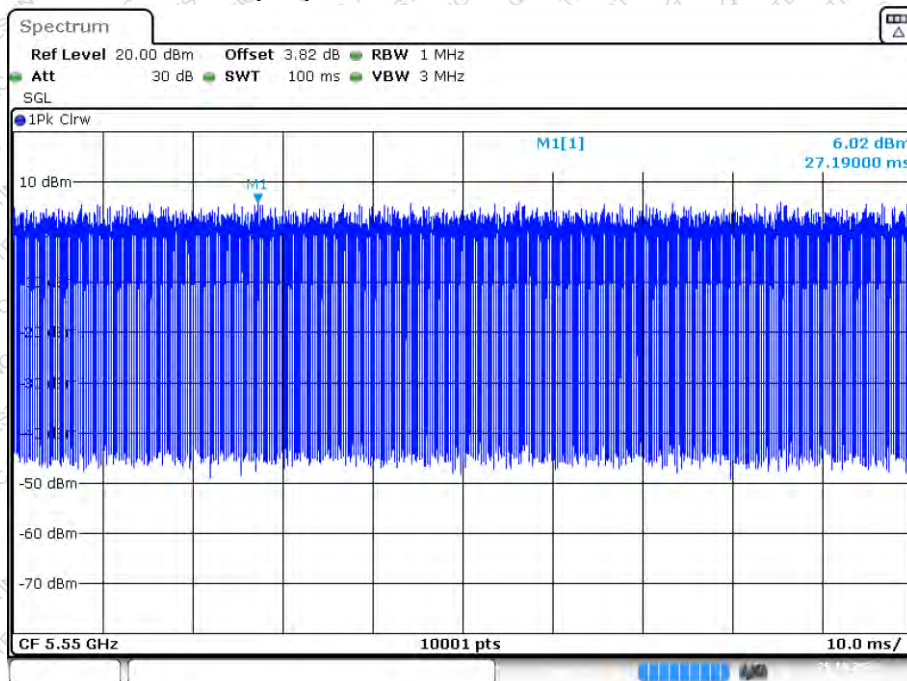
Duty Cycle NVNT n40 5510MHz Ant2



Date: 25.OCT.2024 08:59:17

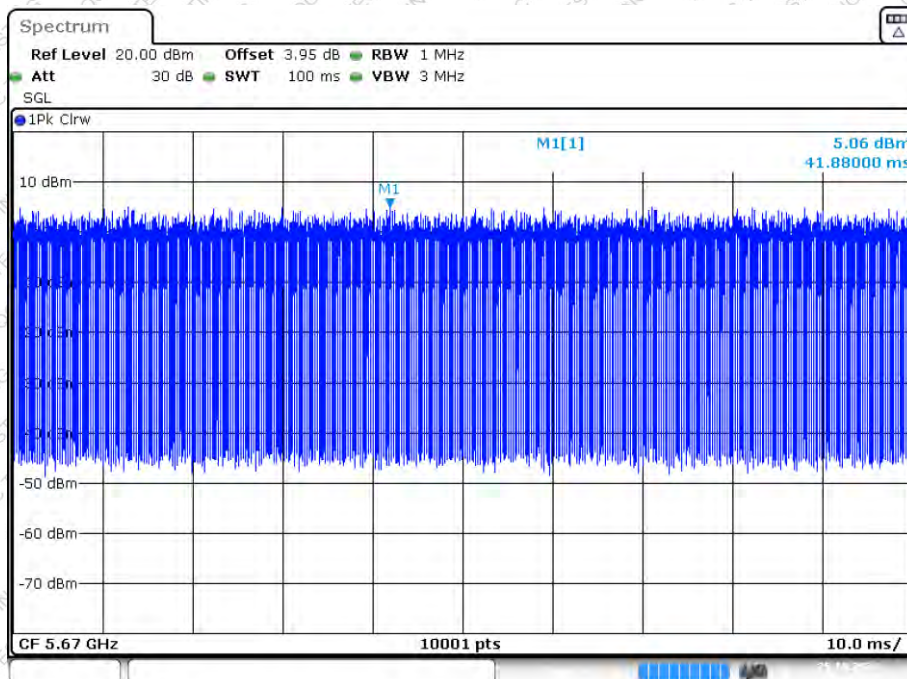


Duty Cycle NVNT n40 5550MHz Ant2



Date: 25.OCT.2024 09:00:28

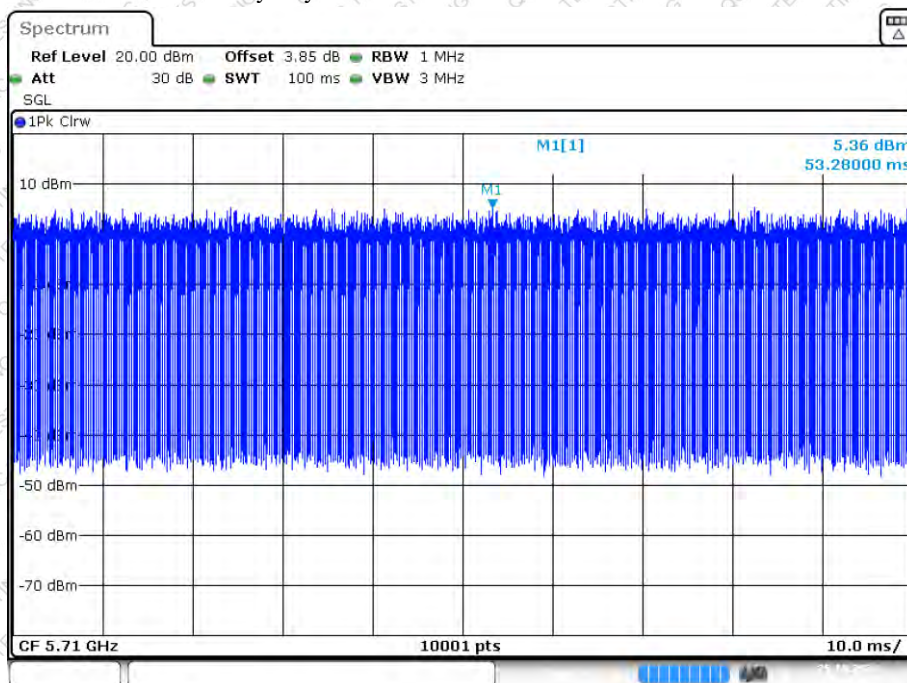
Duty Cycle NVNT n40 5670MHz Ant2



Date: 25.OCT.2024 09:02:13

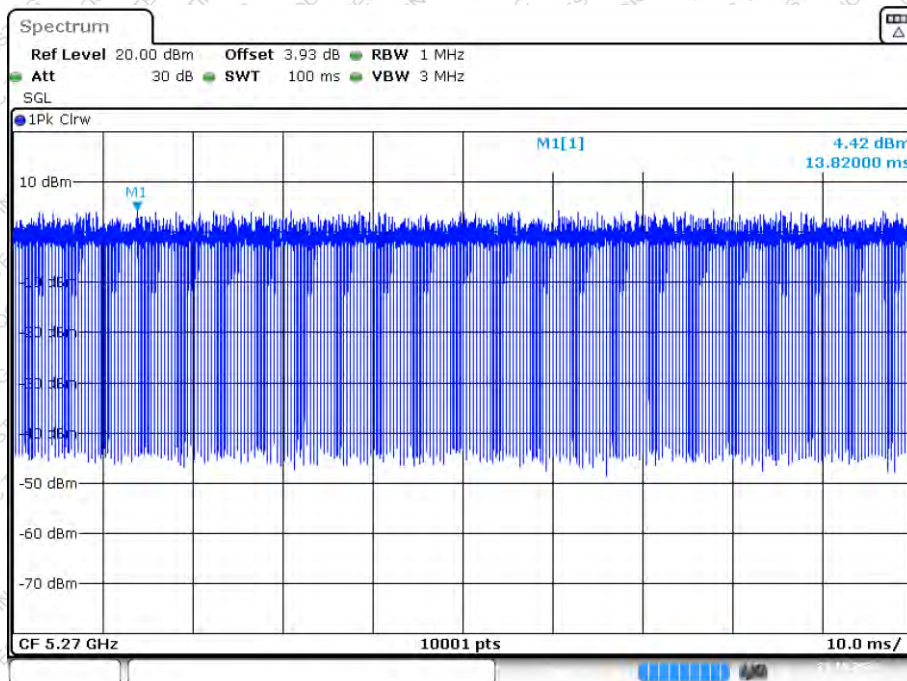


Duty Cycle NVNT n40 5710MHz Ant2



Date: 25.OCT.2024 09:03:09

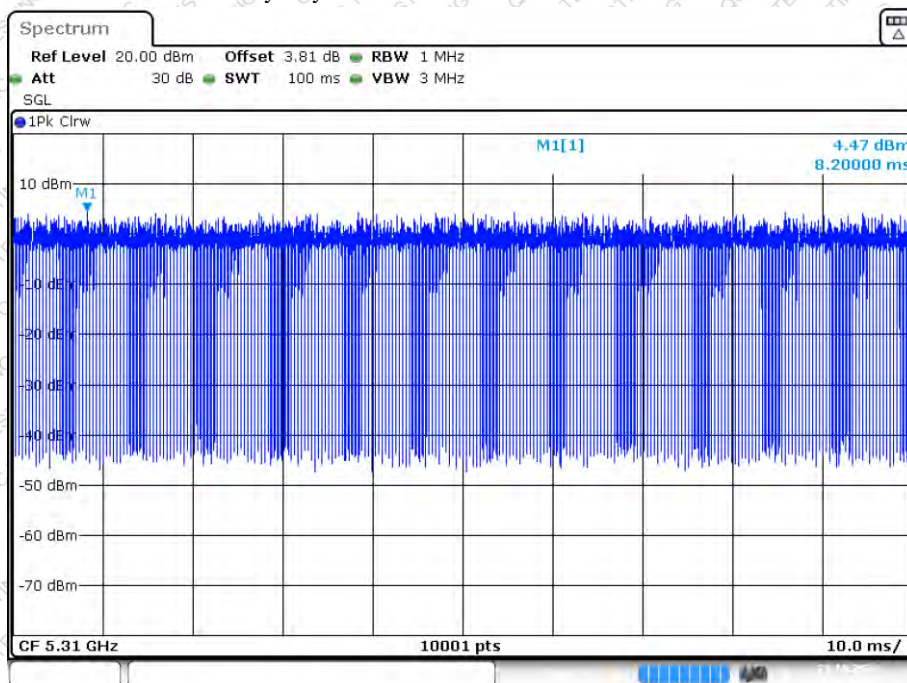
Duty Cycle NVNT n40 5270MHz Sum



Date: 23.OCT.2024 20:12:47

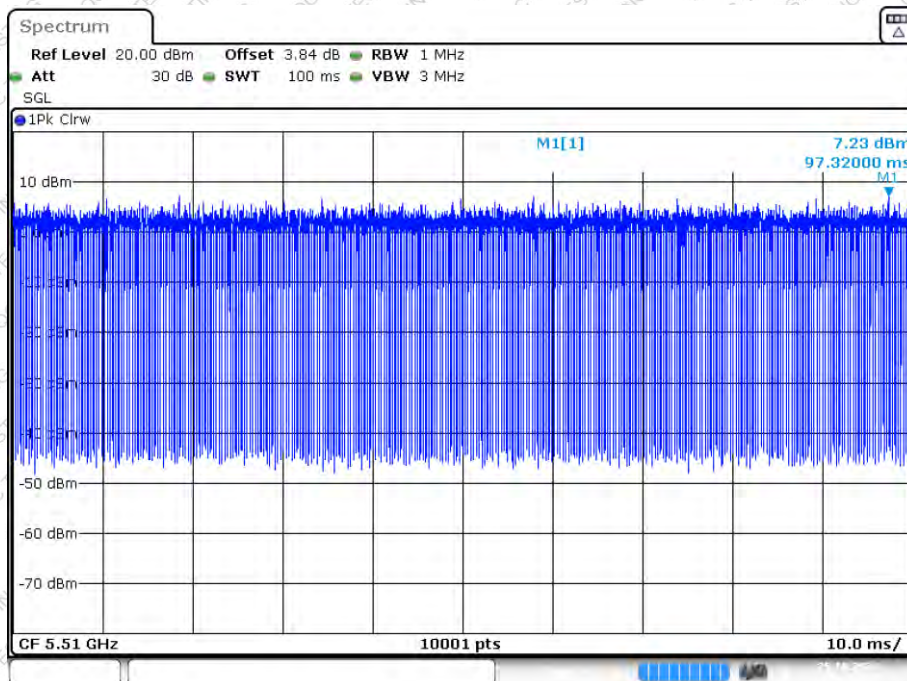


Duty Cycle NVNT n40 5310MHz Sum



Date: 23.OCT.2024 20:11:18

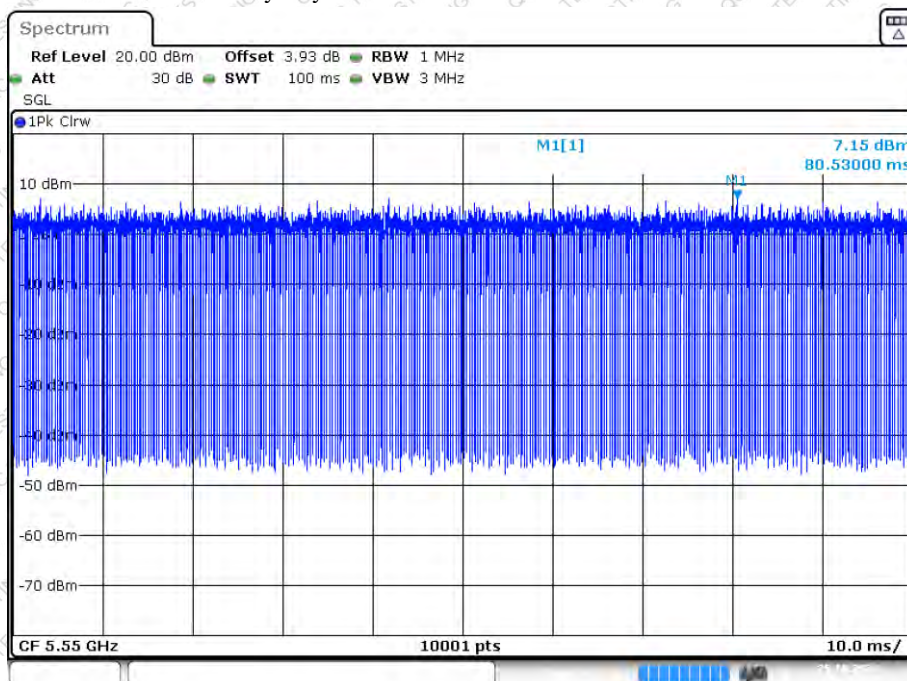
Duty Cycle NVNT n40 5510MHz Sum



Date: 25.OCT.2024 09:53:56

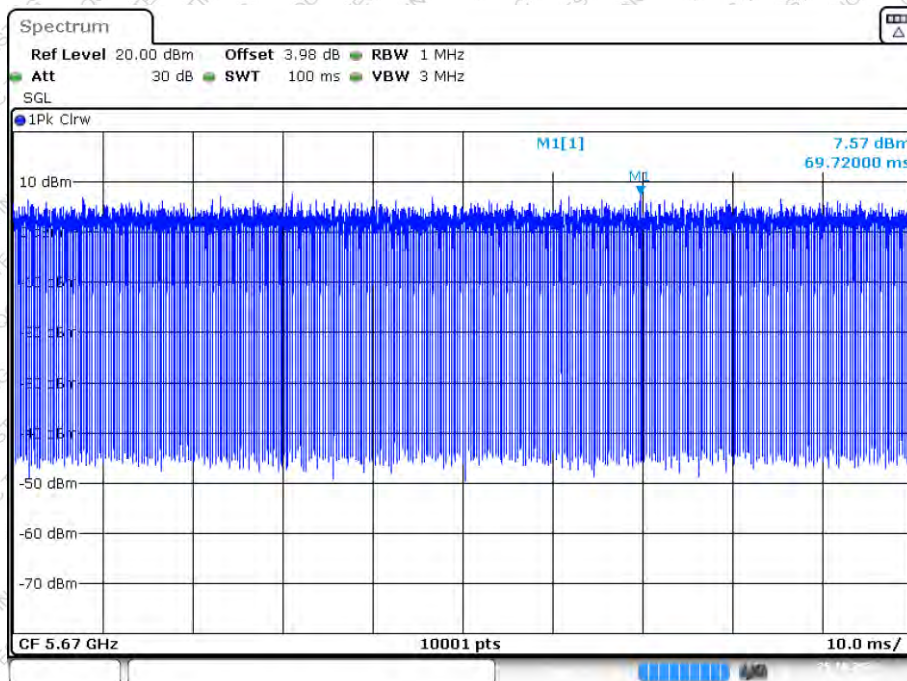


Duty Cycle NVNT n40 5550MHz Sum



Date: 25.OCT.2024 09:55:18

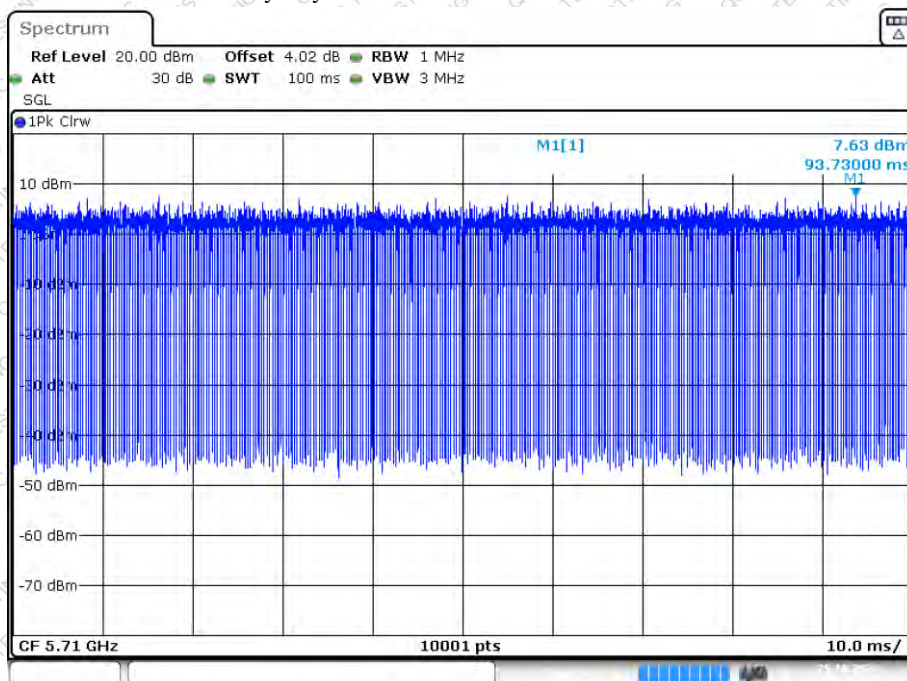
Duty Cycle NVNT n40 5670MHz Sum



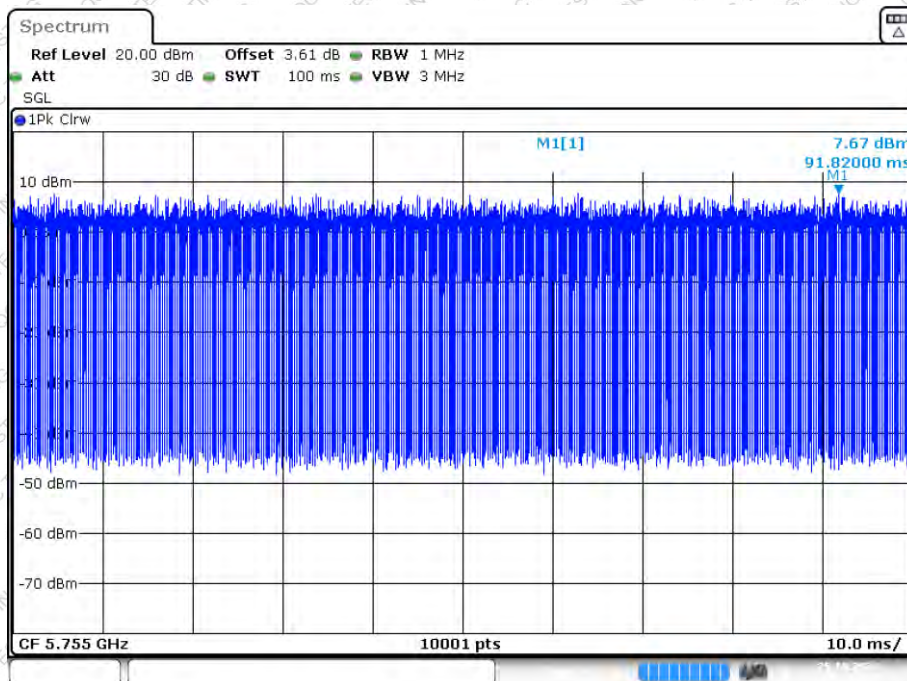
Date: 25.OCT.2024 09:56:46



Duty Cycle NVNT n40 5710MHz Sum

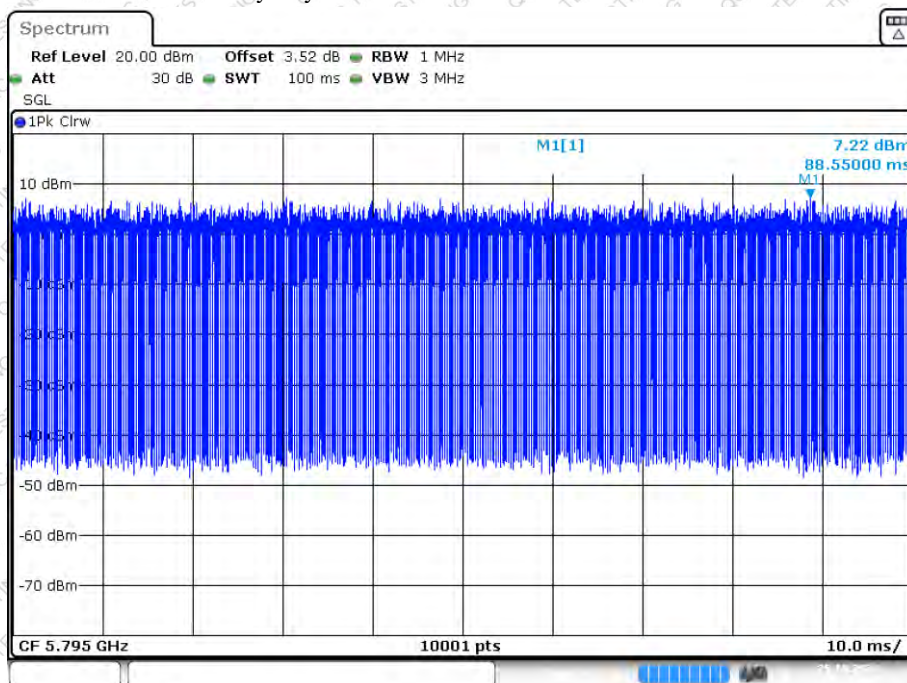


Duty Cycle NVNT n40 5755MHz Ant1



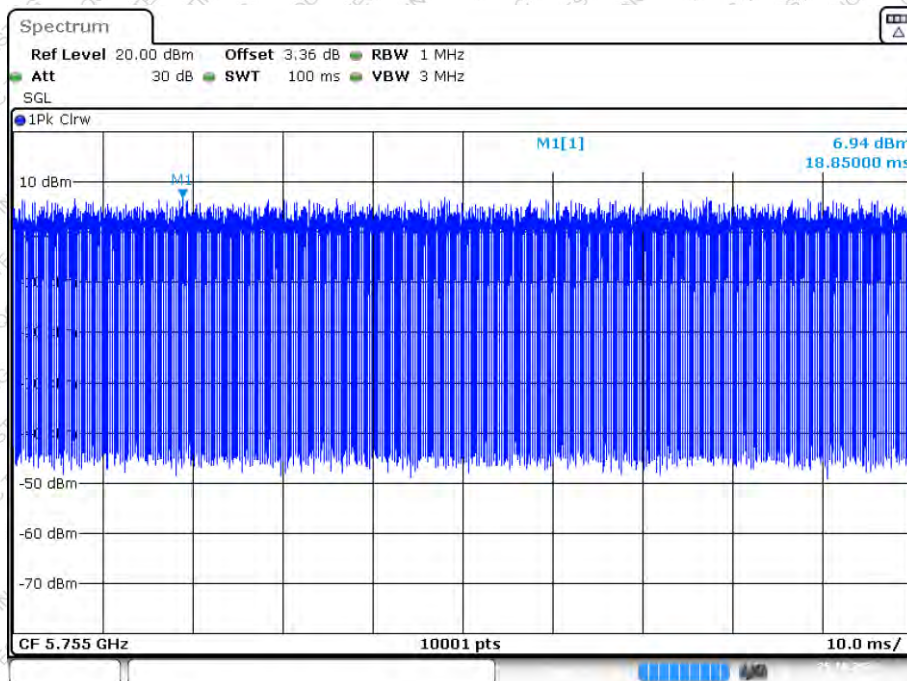


Duty Cycle NVNT n40 5795MHz Ant1



Date: 25.OCT.2024 11:10:47

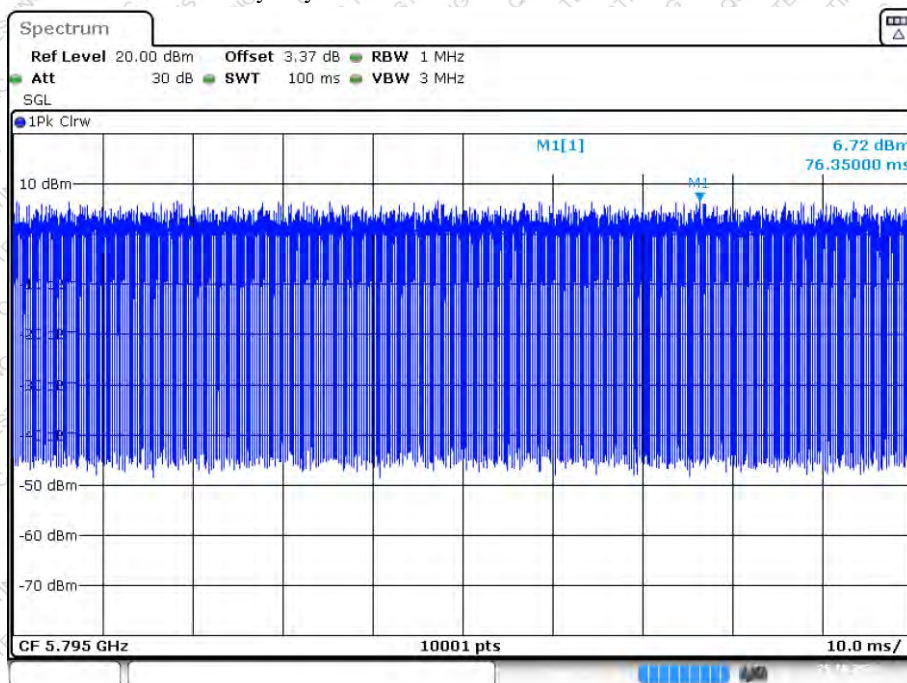
Duty Cycle NVNT n40 5755MHz Ant2



Date: 25.OCT.2024 13:56:40

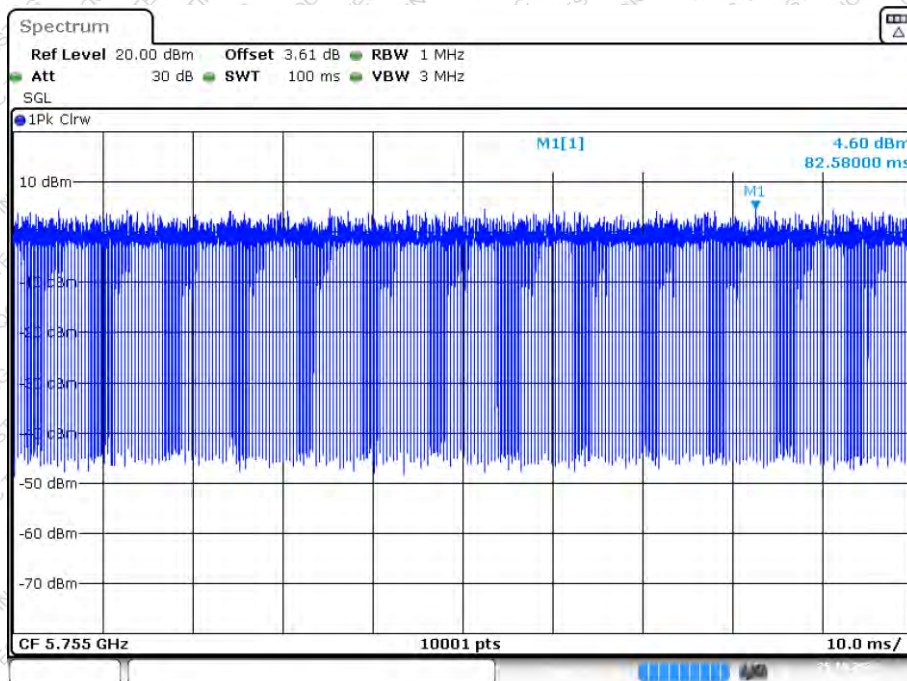


Duty Cycle NVNT n40 5795MHz Ant2



Date: 25.OCT.2024 13:57:52

Duty Cycle NVNT n40 5755MHz Sum



Date: 25.OCT.2024 14:56:49